Abstract—Recent advances in deep learning have enabled researchers across many disciplines to uncover new insights about large datasets. Deep neural networks have shown applicability to image, time-series, textual, and other data, all of which are available in a plethora of research fields. However, their computational complexity and large memory overhead requires advanced software and hardware technologies to train neural networks in a reasonable amount of time. To make this possible, there has been an influx in development of deep learning software that aim to leverage advanced hardware resources. In order to better understand the performance implications of deep learning frameworks over these different resources, we analyze the performance of three different frameworks, Caffe, TensorFlow, and Apache SINGA, over several hardware environments. This includes scaling up and out with single- and multi-node setups using different CPU and GPU technologies. Notably, we investigate the performance characteristics of NVIDIA’s state-of-the-art hardware technology, NVLink, and also Intel’s Knights Landing, the most advanced Intel product for deep learning, with respect to training time and utilization. To our best knowledge, this is the first work concerning deep learning benchmarking with NVLink and Knights Landing. Through these experiments, we provide analysis of the frameworks’ performance over different hardware environments in terms of speed and scaling. As a result of this work, better insight is given towards both using and developing deep learning tools that cater to current and upcoming hardware technologies.

I. INTRODUCTION

Deep learning has continued to thrive as the availability of capable computing resources becomes more common. Many fields are now able to make use of deep learning for their particular applications. These applications include biomedical image analysis, social media analysis, and many more. New deep learning techniques and applications are constantly being developed as they continue to see success through many domains. With this spurring of development, many different software and hardware have been developed that cater towards deep learning workloads. There are a few existing works that focus on the comparison of deep learning hardware performance from speed and scaling perspectives. In order to more effectively compare the variety of software tools, they should be benchmarked with these metrics over various hardware environments. In this work, we aim to do this using three deep learning frameworks, several HPC environments, and state-of-the-art hardware technologies.

The trending development of deep learning tools is providing users with more options that can cater specifically to their needs. Typically, these needs stem from available hardware resources. For instance, a user with access to a large-scale commodity cluster may aim to utilize a different deep learning tool than a user with access to a single multi-GPU machine. This isn’t to say that one single tool isn’t suitable for multiple environments. Rather, the different tools were created to include features that benefit certain hardware setups. Caffe is a popular deep learning framework that excels at single- or multi-GPU training on a single machine, making it more accessible to the general user [1]. On the other hand, Apache SINGA strives to provide scalable deep learning in a distributed environment [2], [3]. Combining multiple ideologies, TensorFlow looks to perform well on a variety of platforms, including scaled-up CPU or GPU machines, scaled-out clusters, and mobile devices [4]. There are several works that benchmark the performance of these and other frameworks with respect to batch training time [5], [6]. Caffe in particular has been thoroughly evaluated. However, works concerning the evaluation and comparison of TensorFlow, SINGA, or other distributed deep learning tools through scaling are limited. We aim to contribute to the already existing benchmarks with scaling results. The three aforementioned frameworks are used in this work. Many other tools have been developed with certain performance goals in mind, and we plan to explore these in future works [7], [8].

Since many of the recently developed software for deep learning were created with specific computing environments in mind, it is also important to consider the ongoing advancement of related hardware technologies and their impact on software performance. The spotlight tends to remain on the advancement of general purpose GPUs because of their high performance vector computation and deep learning’s reliance on such. However, it is still important to visualize CPU and other computing hardware capability with respect to deep learning. It is interesting to see if the recent and upcoming technologies, such as Intel’s Knight’s Landing Xeon Phi processor with Omni-Path interconnect, can compete with GPU in deep learning. On top of this, GPU memory is limited,
and the size of data continues to grow. Hence, CPU or other technologies may see increased use in deep learning. GPU-related technologies are beginning to adapt to overcome this issue and the data transfer bottleneck it imposes on GPU-based training. Previous works have been done to evaluate hardware performance for deep learning, and we aim to add to these with the inclusion of new frameworks and hardware [9], [5], [6]. Notably, our work evaluates the impact of NVIDIA’s recently developed NVLink technology for deep learning, which is an alternative to traditional PCIe Gen3 for CPU to GPU and GPU to GPU data transfer, and also Intel’s Knight Landings (KNL), which was very recently introduced to overcome the problem of limited memory on GPUs while having powerful and fast vector and tensor operations. This technology is unique in that it is Intel’s first Xeon Phi accelerator that can replace a host processor, eliminating the need for PCIe transfer.

The remainder of the paper is organized as follows. First, we describe the deep learning frameworks and hardware used in this work. Following this, we evaluate the different frameworks with various metrics on differing hardware setups. Before concluding, a brief discussion regarding challenges is given. After conclusion, we talk about future works to which we intend to extend this work.

II. DEEP LEARNING FRAMEWORKS

TABLE I: Deep learning frameworks for evaluation.

<table>
<thead>
<tr>
<th>FRAMEWORK</th>
<th>VERSION</th>
<th>URL</th>
</tr>
</thead>
<tbody>
<tr>
<td>Caffe</td>
<td>v1.0</td>
<td><a href="http://caffe.berkeleyvision.org/">http://caffe.berkeleyvision.org/</a></td>
</tr>
<tr>
<td>TensorFlow</td>
<td>v0.12</td>
<td><a href="https://www.tensorflow.org/">https://www.tensorflow.org/</a></td>
</tr>
<tr>
<td>Apache SINGA</td>
<td>v0.30</td>
<td><a href="https://singa.incubator.apache.org/">https://singa.incubator.apache.org/</a></td>
</tr>
</tbody>
</table>

For this work, we selected three frameworks for evaluation: Caffe from Berkeley, TensorFlow from Google, and Apache SINGA [1], [4], [3], [2]. These were selected based on a combination of popularity, performance, and distributed computing capability. Because of the difficulty in compiling the different frameworks over various hardware and developing similar models for benchmarking, we limited ourselves to only these. For example, ensuring correct compilation and execution of each framework over the P100 and Knight’s Landing took much effort. Furthermore, producing code for identical deep learning models and training for each framework is difficult. In a future work, we plan to diversify our selection of frameworks.

A. Caffe

Caffe was developed at the Berkeley Vision and Learning Center and is one of the most popular deep learning frameworks currently available. It is made popular in part by its fast benchmark training time and ease of use for programming novices. Because of its popularity, the large community of users and developers have released many different flavors of Caffe, each attempting to add a new feature or improve an existing one. For instance, Intel released their own version with optimization for Xeon processors. Additionally, there have been several releases that add MPI to the framework, consequently enabling scaled-out deep learning. In this work, we make use of the base version of Caffe from Berkeley for GPU comparisons and for evaluating Intel’s Knights Landing (KNL) we use the Intel version of Caffe.

B. TensorFlow

TensorFlow was originally developed by the Google Brain Team at Google’s Machine Intelligence research organization. It has since then become an open-source project. Deep learning models can be expressed with a data flow graph, where each vertex represents some computation, similar to a neuron in a neural network. Recently, distributed training was added to TensorFlow. This framework was selected for our work because of its fast community growth and distributed training capability. Furthermore, because it is still quite new, there is a lack of validated benchmarks available that measures the performance of TensorFlow over different hardware.

C. Apache SINGA

SINGA is a lesser known deep learning platform designed with distributed training in mind. The lack of popularity is likely due to the fact that it is an Apache project that is still in incubation. However, it offers desirable features in a deep learning tool. The deep learning model definition, which is done by defining layers, is similar to that of Caffe, allowing easy migration of model configurations from one framework to another. More importantly, it enables effortless scaling out and flexible client/server configuration for synchronous or asynchronous training. The authors have published promising results demonstrating the performance of scaling out and both synchronous and asynchronous training [3], [2]. In spite of its lack of popularity, we select SINGA in order to compare the scalability with other frameworks in multiple different distributed HPC environments.

III. HPC ARCHITECTURES

TABLE II: Hardware for deep learning evaluation.

<table>
<thead>
<tr>
<th>HARDWARE</th>
<th>CORES</th>
<th>MEMORY</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel Xeon E5-2680v2</td>
<td>20</td>
<td>64 GB</td>
</tr>
<tr>
<td>IBM Power8 Processor</td>
<td>20</td>
<td>256 GB</td>
</tr>
<tr>
<td>IBM Power8+ Processor with NVLink interface</td>
<td>20</td>
<td>256 GB</td>
</tr>
<tr>
<td>NVIDIA Tesla P100 with NVLink interface</td>
<td>3584</td>
<td>16 GB</td>
</tr>
<tr>
<td>Intel Phi 7230 processor(KNL)</td>
<td>64</td>
<td>96 GB</td>
</tr>
</tbody>
</table>

A. Deep learning with CPU

More often than not, training deep learning models with CPU is not ideal. It is well known that GPUs provide an extreme advantage in cases of vector computation, which makes up the majority of computation during training. In practice, the CPU typically acts as the parameter server, performing scalar
updating of parameters received from GPUs and redistributing the updated values to the GPUs. However, it is still important to evaluate CPU-based training because not all users have access to GPUs with sufficient memory for training larger data. In Table II, the CPUs used in this work are described. The main difference between the Power8 and Power8+ is that the Power8+ supports NVLink, which will be described in the following subsection. We investigate the Intel’s Knight’s Landing performance on various deep learning frameworks. Intel Knight’s Landing is interesting since they it can replace the host CPU, eliminating the need for data transfer over PCIe, while having acceptable number of cores and hyper threads to accommodate the needs of vector operations in deep learning. In addition, it can has access to a larger memory pool by using RAM, enabling training with a larger batch size and model and making faster convergence possible.

B. Deep learning with GPU

Training evaluation using GPU is arguably more important than the CPU evaluation. GPUs outperform CPUs in vector computation, which makes up the majority of computation in deep learning. The GPUs utilized for our experiments are listed in Table II. Of particular interest is the Tesla P100 with NVLink. NVLink is a recently developed technology by NVIDIA that intends to provide faster data sharing between CPU to GPU and GPU to GPU. In the evaluation, we will go into detail on their performance over the P100 and in order to make a clear comparison and find out the effectiveness of the the new NVlink technology. We select and compare NVIDIA Tesla P100 GPUs, one with support of NVLink and the other with PCIe Gen3 connection to CPU.

TABLE III: Deep learning models and datasets for experiments.

<table>
<thead>
<tr>
<th>MODEL</th>
<th>DATASET</th>
<th>URL</th>
</tr>
</thead>
<tbody>
<tr>
<td>AlexNet  [10]</td>
<td>ILSVRC’12</td>
<td>image-net.org/challenges/LSVRC/2012</td>
</tr>
<tr>
<td>GoogLeNet [12]</td>
<td>ILSVRC’12</td>
<td></td>
</tr>
<tr>
<td>ConvNet</td>
<td>CIFAR-10</td>
<td><a href="http://www.cs.toronto.edu/kriz/cifar.html">www.cs.toronto.edu/kriz/cifar.html</a></td>
</tr>
</tbody>
</table>

IV. EVALUATION

We divide the evaluation into three parts. First, we investigate the performance of different CPU, GPU, and Knight’s Landing models on training time. Second, we analyze training performance on a single node while scaling up the number of CPU and GPU. The frameworks evaluated for this are Caffe (both Berkeley and Intel versions) and TensorFlow. Lastly, we look at scaling out with multiple nodes. For this, we focus on TensorFlow, SINGA, and the Intel version of Caffe, since they were designed with scaling in mind, and the base version of Caffe does not scale out. Several neural network models and datasets are used throughout these experiments and are described in Table III. They are all commonly used in deep learning experiments and benchmarks. All timings were averaged over 1000 iterations and ignore the first 100 iterations which often exhibit some startup overhead. In other words, the timings from iterations 101 to 1100 are used.

A. CPU and GPU performance

Before considering scaling up or out, it is important to see the performance of a single compute resource. Our first
experimental consideration is the affect of CPU and GPU model on the time taken to train a batch of images. In Table IV, we provide the training time per batch with various CPU and GPU configurations. These results were gathered using Caffe and the AlexNet model with the ImageNet Large Scale Visual Recognition Competition 2012 (ILSVRC’12) dataset. Each scenario consists of only one worker (whether it be CPU or GPU) and has a batch size of 256. As expected, the two configurations using GPU give the best performance, and the P100 GPU with NVLink vastly outperforms CPU and KNL. Later in this section, we will investigate this performance more closely in order to visualize the effect of NVLink on training time. There is an interesting trend with the CPUs; there is performance degradation with any configuration where simultaneous multi-threading (SMT) is on. In other words, the number of threads exceeding the number of cores hurts performance. As it is shown and expected in IV, GPU and Intel’s Knights landing outperform CPU remarkably, so the paper will concentrate more on accelerator hardware than benchmarking CPU.

Previously, Table IV demonstrated the added benefit of the P100 and NVLink over the PCIe. To analyze the data transfer speedup provided by NVLink more closely, we provide Table V, which describes the data transfer rate of NVLink against PCIe Gen3. The DeepBench toolkit was used to gather these results. We used the All-Reduce technique, which relies on keeping the parameters on all instances of the model across 4 GPUs synchronized by making sure all instances of the model have the same copy of the gradients before taking an optimization step. We measured the time spent to synchronized parameters for four P100 GPUs, with and without NVLink, in one node. As it is shown in Table V, except for the smallest case of 100000 floats, NVLink outperforms PCIe by an approximate factor of two and the difference is much more obvious when the amount of data is increased. As a result, it is obvious that NVLink contributes to faster training and shorter time to completion very effectively.

### TABLE V: Data transfer speeds using P100 with and without NVLink.

<table>
<thead>
<tr>
<th>NUM FLOATS</th>
<th>BYTES</th>
<th>NVLINK</th>
<th>PCIe</th>
</tr>
</thead>
<tbody>
<tr>
<td>100000</td>
<td>400000</td>
<td>0.238(msec)</td>
<td>0.186(msec)</td>
</tr>
<tr>
<td>3097600</td>
<td>12390400</td>
<td>1.427(msec)</td>
<td>2.861(msec)</td>
</tr>
<tr>
<td>4194304</td>
<td>16777216</td>
<td>1.914(msec)</td>
<td>3.858(msec)</td>
</tr>
<tr>
<td>6553600</td>
<td>26214400</td>
<td>3.088(msec)</td>
<td>6.029(msec)</td>
</tr>
<tr>
<td>16777217</td>
<td>67108868</td>
<td>7.519(msec)</td>
<td>16.358(msec)</td>
</tr>
</tbody>
</table>

**B. Scaling up a single node**

After laying the groundwork for different CPU and GPU performance, we will see how well different framework and hardware combinations scale up. In practice, many users utilize a single machine with either multiple CPU cores or GPUs as workers. Here, we will see how different frameworks, Caffe and TensorFlow in particular, scale up with respect to CPU and GPU. Starting with Figure 1, we have the training time for Caffe with Alexnet on P100 GPUs while scaling up the batch size. In the figure, P100(NV) and P100(PC) represent P100 with NVlink and P100 with PCIe connection respectively and the number after (x) shows number of GPU used in the benchmark. We scale up from one to 4 GPUs across one node with batch sizes from 256 to 1024 images. As it is expected, increasing the batch size increases the computation time linearly and increasing the number of GPUs decreases the training time linearly. Figure 1 illustrates Caffe shows very good scalability. Another highlighted feature the Figure 1 is the NVLink speedup. The marked gap shows the difference in training time for P100 with NVlink and P100 with PCIe for

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2 Available at https://github.com/baidu-research/DeepBench
batch size of 1024 images. The mentioned difference is the speed up solely caused by having faster communication link between CPU and GPUs.

Briefly, we give the scaling performance with respect to CPU. Figure 2 has the training time for Caffe with Googlenet with scaled batch size from 32 to 512 images. While the CPU performance is significantly behind GPU in terms of speed, CPU still exhibits good scaling.

As mentioned before, Intel has released Intel’s Knights Landing to overcome the limited GPU memory problem which limits the batch size while having fast vector and matrix operations by providing 64-72 number of cores, depending on the model, and 4 threads per core. In addition, KNL is Intel’s first Xeon Phi accelerator that can replace a host processor eliminating the need for PCIe transfer which in turn contribute to better performance. In Figure 3, we compare time for one training iteration for P100 with NVlink and KNL by scaling up the batch size in AlexNet with Caffe. Figure 3 shows although KNL is showing acceptable speed, it is outperformed by P100 by almost factor of 3 regarding the performance.

To compare Caffe and TensorFlow more closely, we evaluate their training times with two larger, popular networks, VGG-19 and GoogLeNet (Inception V1). Figure 4 gives the time per training iteration using a batch size of 128 with the GoogLeNet (Inception V1) model. Both frameworks exhibit good scaling, but Caffe performs each iteration almost two times faster than TensorFlow. In moving to a larger network, we have VGG-19 results in Figure 5. The frameworks’ performance is much closer in this case, however TensorFlow began to exhibit some scaling issues with four nodes.

![Fig. 4: Scaling up batch size on P100 with NVLink using GoogLeNet with TensorFlow and Caffe.](image)

**C. Scaling out with multiple nodes**

In this section, we will provide some initial analysis of scaled-out deep learning with TensorFlow, SINGA and Intel Caffe over Intel’s Knight Landings using Omni-Path interconnect and P100 using InfiniBand. First, we observe TensorFlow, Caffe, and SINGA results for scaling out GPUs with the LeNet architecture and MNIST dataset in Figure 6. This shows the number of images trained per seconds as more nodes are added. Each node has one P100 GPU worker with a mini-batch size of 64 images. The network interconnect used is 56 Gbps InfiniBand. Performance is depicted in terms of number of images trained per millisecond. At this point, distributed training is a very experimental feature in TensorFlow. Regardless, it provides good scaling as more nodes are added. While SINGA certainly benefits from scaling out, it lags behind the other two frameworks severely in terms of training throughput. The Intel version of Caffe also shows good scalability while providing the best throughput in terms of number of images trained per millisecond.

Since Intel Caffe was designed with particular effort towards utilizing Intel processors and coprocessors (i.e., Knight’s Landing), we investigate its performance with respect to scaling out over nodes with Knight’s Landing (KNL) processors. These results include TensorFlow’s performance in order to have a point of comparison. SINGA had compilation issues, so it is not included in this result. Figure 7 shows TensorFlow and Caffe results for scaling out with LeNet and MNIST mini-batch size of 64 images on each KNL. The number of images trained per millisecond is reported as more KNL machines are added. Each machine has one KNL. The network interconnect...
used is Intel Omni-Path. These results show the benefit of leveraging a particular hardware technology when developing a deep learning tool. Caffe outperforms TensorFlow in terms of image throughput as well as scalability. In fact, TensorFlow exhibits poor scalability, likely due to it not being developed with KNL in mind.

It is evident, based on Figures 6 and 7, that GPU outperforms KNL. We can more directly observe this in Figures 8 and 9, which provide comparisons of scaling out Caffe and TensorFlow, respectively, using KNL with Omni-Path and P100 with InfiniBand. Again, the LeNet model and MNIST dataset are used with a mini-batch size of 64 images. With regards to both Caffe and TensorFlow, the P100 provides drastically more throughput than the KNL.

D. Discussion

Efficiently scaling up single-node training can become challenging, particularly when using GPUs. GPU training introduces an additional overhead in transferring data to and from the CPU or other GPUs. This bottleneck limits the scalability of multiple-GPU machines. In this work, we used a newly developed technology, NVLink, to speed up data transfer. Figure 1 demonstrated the effect of having NVLink by comparing the time between P100 GPU with and without NVLink with the same batch size.

Scalable distributed training is arguably more challenging than scaling up in a single node because it is subject to the same CPU and GPU data transfers but with an additional, more severe bottleneck in the network. However, it is still important to push towards efficient distributed training in order to mitigate larger models and the constant increase in available data for training. In order to make this advancement, deep learning software should make clever use of model and data parallelism, have a strong communication architecture in place, and leverage state-of-the-art technologies.

Certain hardware technologies are already being well leveraged by deep learning tools, as shown in this paper. However, as exhibited by Intel Caffe in Figures 7 and 8, it shows tremendous speedup over TensorFlow when using the Knight’s Landing but remains slower than when using the P100. In other words, Intel Caffe, developed with utilizing Intel Xeon processors and coprocessors, is still faster with P100 over the KNL. This is likely due to the early state of development of deep learning over KNL, especially when CUDA and CUDNN, libraries utilized by Caffe for deep learning over GPU, are much more established.

V. Conclusion

In this work, we evaluated three different deep learning tools, Caffe, TensorFlow, and SINGA, over a variety of hardware setups. Analysis was provided in terms of speed and scaling, promoting a better understanding of these tools and their performance in different scenarios. Of particular interest was the performance analysis using NVIDIA’s NVLink technology over PCIe and using Intel’s Knights Landing hosts with Intel Omni-Path interconnect.

As a result of these experiments, we have the following observations:
The experimental results obtained from the analysis of the different frameworks are as follows:

1) Computation time: We have observed that Caffe is faster than other examined frameworks and it is outperforming TensorFlow. Larger networks, especially VGG-19, show the two frameworks much closer in timing, indicating that TensorFlow’s performance suffers with smaller networks like LeNet. Both Caffe and TensorFlow outperform SINGA by a large margin.

2) Scalability results: All three frameworks, Caffe, TensorFlow, and SINGA, scale-up with multiple GPUs on one node and scale-out with multiple nodes linearly, while again Caffe outperforms TensorFlow, which confirms our previous observation.

However, when utilizing KNL and Omni-Path, TensorFlow does not scale well. This is not unexpected since it was not designed to run over KNL or other similar processors and coprocessors.

To our best knowledge, this is the first work evaluating NVLink and Intel’s Knights Landing for deep learning benchmarks. In future works, we plan to add more frameworks for benchmarking and expand our hardware environment with an aim towards scaling out. As deep learning continues to thrive, it will continue to be necessary to evaluate the tools developed over state-of-the-art hardware.

A. Future work

One aspect in which we plan to extend this work involves adding more novel hardware that may be of interest to evaluate includes other Xeon Phi co-processors (e.g., Knight’s Corner) and FPGAs. On top of adding additional hardware technologies, we plan to include additional popular deep learning models and frameworks. Specifically, we aim to evaluate more frameworks capable of distributed training. With this, further experiments and more detailed analysis towards scaling out deep learning will be done.

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REFERENCES


