

IBM Z: Accessing the LPAR Resource Assignment Task Washington Systems Center

This document can be found on the web, www.ibm.com/support/techdocs
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The new View Partition Resource Assignments task will be available on the HMC and SE. It is a display only task. On the SE, this task can be used in PEDEBUD, SYSPROG and SERVICE modes. On the HMC, this task can be used in PEDEBUD and SERVICE modes.

The View Partition Resource Assignments task targets the CPC and provides a snapshot of all active partitions and associated processor information. The information displayed for each partition includes the HiperDispatch setting and processor information (number, CPU type and polarity) displayed within the Node and Chip locations.

Steps:

1. Log on to the HMC as PEDEBUD or SERVICE modes.

2. Select the System Management view
3. Place a check next to your z14 family processor

Select	Name	Status	Activation
<input checked="" type="radio"/>	QSYS	Operating	DEFAULT
<input type="radio"/>	SSYS	Operating	DEFAULT
<input type="radio"/>	USYS	Operating	DEFAULT
<input type="radio"/>	Unmanaged Systems		

Max Page Size: 500 Total: 4 Filtered: 4 Selected: 1

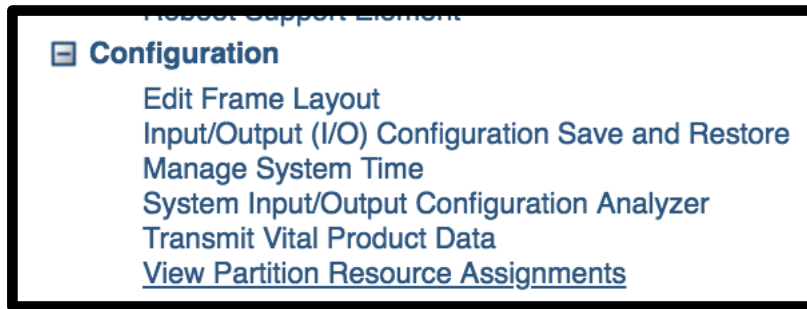
Tasks: QSYS

- System Details
- Toggle Lock
- Daily**
 - Activate
 - Deactivate
 - Hardware Messages
 - Generate System Messages

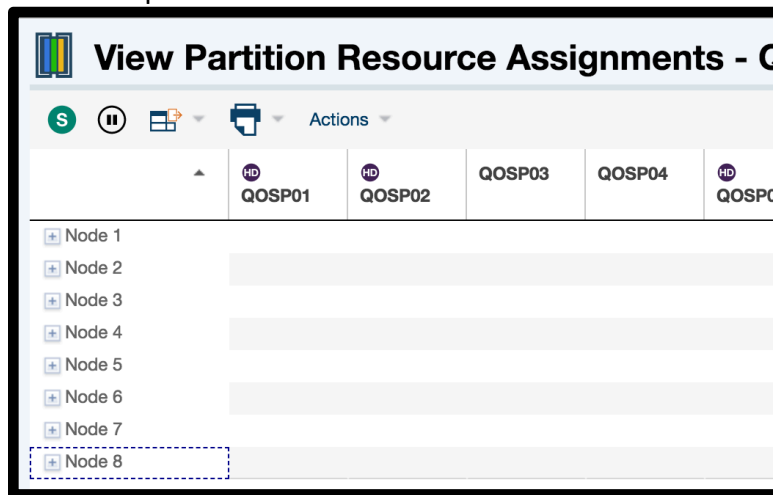
Service

- Archive Security
- Backup Critical Data
- Perform Problem Analysis
- Perform Transfer
- Report a Problem
- Retrieve Backup

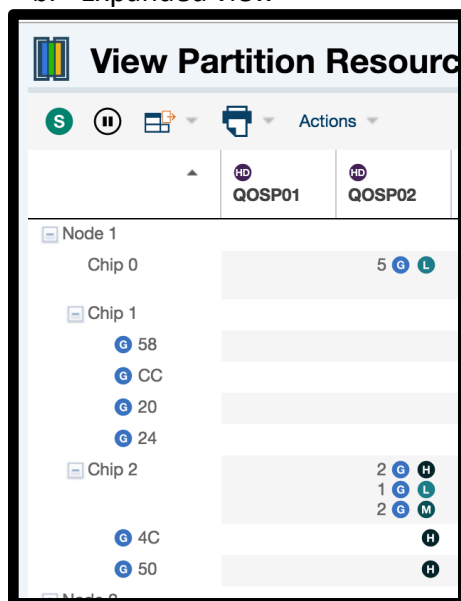
4. Find the task View Partition Resource Assignments under the Configurations task list



























5. Partitions can be expanded or collapsed to show relevant LPAR information
 - a. Collapsed View



- b. Expanded view



Sample Output/Key

		<div> <div>HD</div> <div>HD</div> <div>QOSP03</div> <div>QOSP04</div> <div>HD</div> <div>QOSP05</div> <div>QOSP06</div> <div>HD</div> <div>HD</div> <div>HD</div> <div>QOSP16</div> </div>									
		QOSP01	QOSP02	QOSP03	QOSP04	QOSP05	QOSP06	QOSP07	QOSP09	QOSP0F	QOSP16
Node	1										
Chip	0		2  						3  		
Chip	1		2   4   2  								
Chip	2										
Node	2										
Chip	0				2  				2   3   2  		
Chip	1								6  		
Node	3										
Chip	0									2  	5  
Chip	1										8 