z/OSV2R1
MVS Data Areas
Volume 5 (LDA -SJRSP)

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Note

Before using this information and the product it supports, be sure to read the general information under “Notices” on page 691.

Third Edition, August 2014

This edition applies to Version 2 Release 1 of z/OS (5650-ZOS) and to all subsequent releases and modifications until otherwise indicated in new editions.

Contents

About this information

This information is a graphic presentation of many data areas used by the z/OS operating system and by application programs. The data areas are one or more of the following:

- Programming interfaces
- Needed for debugging or diagnosis.

This information supports z/OS (5694-A01).

Who should use this information

This informatation is for system programmers who diagnose and debug operating system and programming problems. It provides information for debugging installation-provided programs or diagnosing IBM-provided programs. The user of this information should have a working knowledge of the functions and logic of the operating system.

How to use this information

Data areas are sequenced alphanumerically by data area acronym. Each data area has up to four sections:

- Programming Interface Information
- Header
- Data area map
- Cross-reference, if the data area map is long enough

The header

The header includes some or all of the following:

Common Name: The descriptive name of the data area.

Macro ID: The name of the mapping macro for the data area. Mapping macros can be issued in programs to generate a copy of the data area.

DSECT Name: Name of the DSECT (dummy control section) created by the mapping macro.

Owning Component: Component name and component identifier in parentheses.

Eye-Catcher ID: Character string identifier of the eye-catcher (sometimes called the **control block id**) within the mapping macro. The offset and length of the eye-catcher are also included.

Storage Attributes: The storage attributes of the data area, including the following:

- **Main Storage:** Central storage attributes of the data area.
- **Virtual Storage:** Virtual storage attributes of the data area.
- **Auxiliary Storage:** Spool storage attributes of the data area.
- **Subpool and Key:** Subpool is the area of virtual storage that contains the data area. Key is the storage protect key for the storage represented by the data area.

Size: The size of the data area in decimal bytes.

Created by: Module, macro, or component whose use creates the data area.

Pointed to by: Registers or data area fields that contain the address of the data area.

Serialization: Method used to ensure that one user does not update a data area that is being updated or used by another user. The most common methods used for serialization are:

- Lock or locks
- ENQ and DEQ macros
- Compare and Swap (CS) instruction

- Disablement, which is disabling interruptions by setting bits in the program status word (PSW) of the program using the data area

Function: Brief description of the use of the data area.

Data area map

The data area is described field by field. These field descriptions are taken directly from the system code.

The following is an example of the field descriptions for the ANYAREA data area:

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	384	ANYAREA	
0	(0)	CHARACTER		ANYBEGIN	BEGINNING OF ANYAREA
0	(0)	CHARACTER	4	ANYACRO	ACRONYM IN EBCDIC 'ANY '
4	(4)	ADDRESS	4	ANYADDR	ADDRESS OF NEXT ANYAREA ON QUEUE

For each field in the data area, the data area map provides the following information:

Offsets The address of the field, shown in both decimal (DEC) and hexadecimal (HEX in parentheses), relative to the beginning of the data area.

Type The kind of program data defined for this field, as follows:

Type	Description
ADDRESS	Address constant
BITSTRING	Bitstring constant
CHARACTER	Character value
DBL WORD	Double word boundary
FIXED	Arithmetic signed or unsigned value
HEX	Hexadecimal value
SIGNED	Arithmetic signed value
STRUCTURE	Level 1 control block name
UNSIGNED	Unsigned value

Len Size of the field in decimal bytes.

Name (Dim) The name of the field, bit, or mask.

Bit or mask names are preceded by a description of bit position and value, as follows:

```
1... ....      Refers to bit 0.
.... ..11      Refers to bits 6 and 7.
...1 ....      Refers to bit 3.
11.. 1111      Refers to bits 0, 1, 4, 5, 6, and 7.
```

Description A description of the purpose or meaning of the field, bit, or mask.

Cross reference

For each data area with more than 10 fields, the cross reference shows the following:

Name The name of the field, bit, or mask.

Hex Offset The hexadecimal offset of the field into the data area. For bits, the hexadecimal offset of the field containing the bit.

Hex Value Values are shown only for bits, equates, and initialized character strings. For bits, the hexadecimal value shown implies the position of the bit in the field containing the bit.

Bit ANYBIT in the following illustration shows how to use the hexadecimal value. In the Example, cross reference for the ANYBIT bit looks like this:

Name	Hex Offset	Hex Value
ANYBIT	F0	80

In the map of the data area, the ANYBIT bit appears like this:

```
240    (F0)  FIXED      4   ANYWORD     CONTROL WORD
240    (F0)  BITSTRING  1   ANYBYTE     FLAG BYTE

             1... ....      ANYBIT      "X'80'" BIT ON MEANS THIS . . .
```

X'F0' is the offset of field ANYWORD into the data area. ANYWORD is a 4-byte field, which contains a 1-byte field named ANYBYTE. Both ANYWORD and ANYBYTE have the same offset. The first bit in both fields is named ANYBIT. Ignoring the other bits in the field ANYBYTE, if the ANYBIT bit is on, the value of field ANYBYTE would be 1000 0000, which is equivalent to X'80'. This value (X'80') is shown both in the Description in the data area map and in the column of the cross reference.

Programming interface information

This document contains information NOT intended to be used as programming interfaces of z/OS.

This document also contains intended programming interfaces that allow the customer to write programs to obtain the services of z/OS.

This information is identified where it occurs, either by an introductory statement to a chapter or section or by the following marking:

Programming Interface information

End of Programming Interface information

Unless otherwise specified, for data areas classified as programming interfaces, the **MACRO ID** and **DSECT NAME(S)** in the header are part of the programming interface. **ALL** other header information is included for diagnostic purposes **ONLY**.

Since a *data area name* that is designated as part of the programming interface is one of the following:

- MACRO ID
- DSECT NAME
- commonly-used name

before including the *data area name* in a program, refer to the data area header for the applicable **MACRO ID**.

If only certain fields in a data area are intended or not intended for use as a programming interface, the specific field name(s) are differentiated within the data area.

For data areas classified as programming interfaces, "RESERVED FOR USER" fields are part of the interface; all other **"RESERVED ..."** fields are **NOT** part of the interface.

For a field that is part of the programming interface, the only information that is part of the interface for writing programs is:

- field name
- data type
- field length
- description (purpose or allowed values)

INCLUDE ONLY data area: **ONLY** the MACRO ID is the programming interface. The DSECT NAME, constants,and data area itself are **NOT** part of the programming interface.

TOKEN ONLY data area: **ONLY** the address of the data area is a programming interface. The DSECT NAME, constants, and data area itself are **NOT** part of the programming interface.

LDA Information

LDA Heading Information

Common Name: VSM Local Data Area
Macro ID: IHALDA
DSECT Name: LDA
Owning Component: Virtual Storage Manager (SC1CH)
Eye-Catcher ID: LDA
Offset: 0
Length: 4
Storage Attributes: Subpool: 255
Key: 0
Residency: Above 16M
Size: 496 bytes
Created by: IEAIPL14, IGVGCAS
Pointed to by: ASCBLDA, VSWKLDA
Serialization: LOCAL lock
Function: Contains control information about address space related virtual storage and VSM control block pointers.

LDA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	496	LDA	LOCAL DATA AREA
0	(0)	CHARACTER	4	LDAID	CONTROL BLOCK IDENTIFIER
4	(4)	CHARACTER	24	LDAQANC	LSQA queue anchors
4	(4)	ADDRESS	4	LDASQAT	Address of the LSQA SQAT
8	(8)	ADDRESS	4	LDAAQAT	Address of the LSQA AQAT
12	(C)	CHARACTER	16	LDADFEQ	LSQA DFE queue header
12	(C)	ADDRESS	4	LDAADF	Address of first DFE on the LSQA address queue
16	(10)	ADDRESS	4	LDAADL	Address of last DFE on the LSQA address queue
20	(14)	ADDRESS	4	LDASZF	Address of first DFE on LSQA size queue
24	(18)	ADDRESS	4	LDASZL	Address of last DFE on LSQA size queue
28	(1C)	CHARACTER	24	LDAEANC	LSQA queue anchors - Extended
28	(1C)	ADDRESS	4	LDAESQAT	Address of the LSQA SQAT
32	(20)	ADDRESS	4	LDAEAQAT	Address of the LSQA AQAT
36	(24)	CHARACTER	16	LDAEDFEQ	LSQA DFE queue header
36	(24)	ADDRESS	4	LDAEADF	Address of first DFE on the LSQA address queue
40	(28)	ADDRESS	4	LDAEADL	Address of last DFE on the LSQA address queue
44	(2C)	ADDRESS	4	LDAESZF	Address of first DFE on LSQA size queue
48	(30)	ADDRESS	4	LDAESZL	Address of last DFE on LSQA size queue
52	(34)	CHARACTER	16	LDAARD	Address Space Region Descriptor
52	(34)	ADDRESS	4	LDAFBQAF	Address of first FBQE on the ADDRESS SPACE FBQE queue
56	(38)	ADDRESS	4	LDAFBQAL	Address of last FBQE on the ADDRESS SPACE FBQE queue
60	(3C)	ADDRESS	4	LDASTRTA	Low address of Address Space Region
64	(40)	SIGNED	4	LDASIZA	Size of Address Space Region
68	(44)	CHARACTER	16	LDAEARD	Address Space Region Descriptor - Extended
68	(44)	ADDRESS	4	LDAEFBAF	Address of first FBQE on the ADDRESS SPACE FBQE queue
72	(48)	ADDRESS	4	LDAEFBAL	Address of last FBQE on the ADDRESS SPACE FBQE queue
76	(4C)	ADDRESS	4	LDAESTRA	Low address of Address Space Region
80	(50)	SIGNED	4	LDAESIZA	Size of Address Space Region
84	(54)	CHARACTER	16	LDASRD	System Region Descriptor
84	(54)	ADDRESS	4	LDAFBQSF	Address of the first FBQE on the System Region FBQE
88	(58)	ADDRESS	4	LDAFBQSL	Address of the last FBQE on the System Region FBQE
92	(5C)	ADDRESS	4	LDASTRTS	Low address of System Region
96	(60)	SIGNED	4	LDASIZS	Size of System Region
100	(64)	CHARACTER	16	LDAESRD	System Region Descriptor - Extended
100	(64)	ADDRESS	4	LDAEFBSF	Address of the first FBQE on the System Region FBQE
104	(68)	ADDRESS	4	LDAEFBSL	Address of the last FBQE on the System Region FBQE
108	(6C)	ADDRESS	4	LDAESTRS	Low address of System Region
112	(70)	SIGNED	4	LDAESIZS	Size of System Region
116	(74)	CHARACTER	16	LDARRD	V=R Region Descriptor
116	(74)	ADDRESS	4	LDAFBQRF	Address of the first FBQE on the V=R Region FBQE queue
120	(78)	ADDRESS	4	LDAFBQRL	Address of the last FBQE on the V=R Region FBQE queue
124	(7C)	ADDRESS	4	LDASTRTR	Low address of the V=R Region
128	(80)	SIGNED	4	LDASIZR	Size of the V=R Region
132	(84)	CHARACTER	16	LDAERRD	V=R Region Descriptor - Extended
132	(84)	ADDRESS	4	LDAEFBRF	Address of the first FBQE on the V=R Region FBQE queue
136	(88)	ADDRESS	4	LDAEFBRL	Address of the last FBQE on the V=R Region FBQE queue
140	(8C)	ADDRESS	4	LDAESTRR	Low address of the V=R Region

LDA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
144	(90)	SIGNED	4	LDAESIZR	Size of the V=R Region
148	(94)	ADDRESS	4	LDAAQTAD	Address of the first AQAT stack on the AQAT Q-stack. (This is always the persistent AQAT stack.)
152	(98)	ADDRESS	4	LDACRGTP	Current high address of PRIVATE AREA Region
156	(9C)	ADDRESS	4	LDAERGTP	Current high address of PRIVATE AREA Region - Extended
160	(A0)	ADDRESS	4	LDADEFQ	Address of DEFERRED RELEASE queue
164	(A4)	ADDRESS	4	LDAAQTST	Address of the next free AQAT in the AQAT Q-stack.
168	(A8)	CHARACTER	12	LDACPANC	LSQA CELL POOL HEADER
168	(A8)	ADDRESS	4	LDACPADR	Address of LSQA CELL POOL
172	(AC)	SIGNED	4	LDACPCNT	NUMBER OF FREE CELLS IN LSQA CELL POOL
176	(B0)	ADDRESS	4	LDAFCADR	Address of first FREE CELL IN LSQA CELL POOL
180	(B4)	ADDRESS	4	LDAWRKA	Address of LOCAL WORK AREA
184	(B8)	ADDRESS	4	LDAASCB	Address of ASCB FOR THIS ADDRESS SPACE
188	(BC)	ADDRESS	4	LDAPPD	Address of LOCAL PPD queue
192	(C0)	CHARACTER	4	*	Reserved
196	(C4)	CHARACTER	1	LDAFLGS	MISC. FLAGS
		1...		LDADEFER	IF ONE DEFERRED RELEASE CONDITION EXISTS
		.1..		LDADEFED	IF ONE FREE ASSOCIATED PAGE EXISTS
197	(C5)	CHARACTER	3	*	Reserved
200	(C8)	CHARACTER	24	LDASIZES	MISC. sizes
200	(C8)	CHARACTER	1	LDAUFLGS	USER FLAGS
		1...		LDALIMCL	IF ZERO CALL IEALIMIT ROUTINE
		.1..		LDAULIM	IF ZERO DO FBQE CHECK BELOW 16M
		..1.		LDAEULIM	IF ONE DO FBQE CHECK ABOVE 16M
201	(C9)	CHARACTER	3	*	Reserved
204	(CC)	SIGNED	4	LDAREGRQ	Region size REQUESTED
208	(D0)	ADDRESS	4	LDALIMIT	< 16M V=V Region limit value
212	(D4)	ADDRESS	4	LDAVVRG	< 16M V=V Region high value
216	(D8)	ADDRESS	4	LDAELIM	> 16M V=V Region limit value
220	(DC)	ADDRESS	4	LDAEVVRG	> 16M V=V Region high value
224	(E0)	CHARACTER	8	LDANONFM	NON-FREEMAINABLE PRIVATE AREAS
224	(E0)	CHARACTER	8	LDASM	NON-FREEMAINABLE STORAGE MANAGEMENT AREA
224	(E0)	ADDRESS	4	LDASMAD	Address of AREA
228	(E4)	SIGNED	4	LDASMSZ	Size of AREA
232	(E8)	CHARACTER	16	LDAALLOC	ALLOCATION VALUES
232	(E8)	UNSIGNED	4	LDALOAL	< 16M USER Region alloc value
236	(EC)	UNSIGNED	4	LDAHIAL	< 16M AUTH Region alloc value
240	(F0)	UNSIGNED	4	LDAELOAL	> 16M USER Region alloc value
244	(F4)	UNSIGNED	4	LDAEHIAL	> 16M AUTH Region alloc value
248	(F8)	CHARACTER	16	LDASMF	Limit values set by SMF
248	(F8)	UNSIGNED	4	LDASMFL	< 16M V=V SMF LDALIMIT VALUE
252	(FC)	UNSIGNED	4	LDASMFR	< 16M V=V SMF LDAVVRG VALUE
256	(100)	UNSIGNED	4	LDASMFEL	> 16M V=V SMF LDAELIM VALUE
260	(104)	UNSIGNED	4	LDASMFER	> 16M V=V SMF LDAEVVRG VALUE
264	(108)	CHARACTER	24	LDAEAN05	Subpool 205 queue anchors -- Extended
264	(108)	ADDRESS	4	LDASQT05	Subpool 205 SQAT address
268	(10C)	ADDRESS	4	LDAAQT05	Subpool 205 AQAT address
272	(110)	CHARACTER	16	LDADFE05	Subpool 205 DFE queue header
272	(110)	ADDRESS	4	LDAADF05	Address of first DFE on the address queue
276	(114)	ADDRESS	4	LDAADL05	Address of last DFE on the address queue
280	(118)	ADDRESS	4	LDASZF05	Address of first DFE on the size queue
284	(11C)	ADDRESS	4	LDASZL05	Address of last DFE on the size queue
288	(120)	CHARACTER	24	LDAEAN15	Subpool 215 queue anchors -- Extended
288	(120)	ADDRESS	4	LDASQT15	Subpool 215 SQAT address
292	(124)	ADDRESS	4	LDAAQT15	Subpool 215 AQAT address
296	(128)	CHARACTER	16	LDADFE15	Subpool 215 DFE queue header
296	(128)	ADDRESS	4	LDAADF15	Address of first DFE on the ADDRESS queue
300	(12C)	ADDRESS	4	LDAADL15	Address of last DFE on the address queue
304	(130)	ADDRESS	4	LDASZF15	Address of first DFE on the size queue
308	(134)	ADDRESS	4	LDASZL15	Address of last DFE on the size queue
312	(138)	CHARACTER	24	LDAEAN25	Subpool 225 queue anchors -- Extended
312	(138)	ADDRESS	4	LDASQT25	Subpool 225 SQAT address
316	(13C)	ADDRESS	4	LDAAQT25	Subpool 225 AQAT address
320	(140)	CHARACTER	16	LDADFE25	Subpool 225 DFE queue header
320	(140)	ADDRESS	4	LDAADF25	Address of first DFE on the address queue
324	(144)	ADDRESS	4	LDAADL25	Address of last DFE on the address queue
328	(148)	ADDRESS	4	LDASZF25	Address of first DFE on the size queue
332	(14C)	ADDRESS	4	LDASZL25	Address of last DFE on the size queue
336	(150)	ADDRESS	4	LDAA2GFA	Above 2G free area address
340	(154)	ADDRESS	4	LDAA2GAA	Above 2G alloc area address
344	(158)	SIGNED	4	LDAMRG24	Maximum region below 16MB before first job step executes
348	(15C)	SIGNED	4	LDAMRG31	Maximum region above 16MB before first job step executes
352	(160)	CHARACTER	24	LDAQANCR64	LSQA queue anchors R64
352	(160)	ADDRESS	4	LDASQATR64	Address of the LSQA SQAT

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
356	(164)	ADDRESS	4	LDAAQATR64	Address of the LSQA AQAT
360	(168)	CHARACTER	16	LDADFEQR64	LSQA DFE queue header
360	(168)	ADDRESS	4	LDAADFR64	Address of first DFE on the LSQA address queue
364	(16C)	ADDRESS	4	LDAADLR64	Address of last DFE on the LSQA address queue
368	(170)	ADDRESS	4	LDASZFR64	Address of first DFE on LSQA size queue
372	(174)	ADDRESS	4	LDASZLR64	Address of last DFE on LSQA size queue
376	(178)	CHARACTER	24	LDAEANCR64	LSQA queue anchors - Extended R64
376	(178)	ADDRESS	4	LDAESQATR64	Address of the LSQA SQAT
380	(17C)	ADDRESS	4	LDAEAQATR64	Address of the LSQA AQAT
384	(180)	CHARACTER	16	LDAEDFEQR64	LSQA DFE queue header
384	(180)	ADDRESS	4	LDAEADFR64	Address of first DFE on the LSQA address queue
388	(184)	ADDRESS	4	LDAEADLR64	Address of last DFE on the LSQA address queue
392	(188)	ADDRESS	4	LDAESZFR64	Address of first DFE on LSQA size queue
396	(18C)	ADDRESS	4	LDAESZLR64	Address of last DFE on LSQA size queue
400	(190)	CHARACTER	24	LDAEAN05R64	Subpool 205 queue anchors -- Extended R64
400	(190)	ADDRESS	4	LDASQT05R64	Subpool 205 SQAT address
404	(194)	ADDRESS	4	LDAAQT05R64	Subpool 205 AQAT address
408	(198)	CHARACTER	16	LDADFE05R64	Subpool 205 DFE queue header
408	(198)	ADDRESS	4	LDAADF05R64	Address of first DFE on the address queue
412	(19C)	ADDRESS	4	LDAADL05R64	Address of last DFE on the address queue
416	(1A0)	ADDRESS	4	LDASZF05R64	Address of first DFE on the size queue
420	(1A4)	ADDRESS	4	LDASZL05R64	Address of last DFE on the size queue
424	(1A8)	CHARACTER	24	LDAEAN15R64	Subpool 215 queue anchors -- Extended R64
424	(1A8)	ADDRESS	4	LDASQT15R64	Subpool 215 SQAT address
428	(1AC)	ADDRESS	4	LDAAQT15R64	Subpool 215 AQAT address
432	(1B0)	CHARACTER	16	LDADFE15R64	Subpool 215 DFE queue header
432	(1B0)	ADDRESS	4	LDAADF15R64	Address of first DFE on the address queue
436	(1B4)	ADDRESS	4	LDAADL15R64	Address of last DFE on the address queue
440	(1B8)	ADDRESS	4	LDASZF15R64	Address of first DFE on the size queue
444	(1BC)	ADDRESS	4	LDASZL15R64	Address of last DFE on the size queue
448	(1C0)	CHARACTER	24	LDAEAN25R64	Subpool 225 queue anchors -- Extended R64
448	(1C0)	ADDRESS	4	LDASQT25R64	Subpool 225 SQAT address
452	(1C4)	ADDRESS	4	LDAAQT25R64	Subpool 225 AQAT address
456	(1C8)	CHARACTER	16	LDADFE25R64	Subpool 225 DFE queue header
456	(1C8)	ADDRESS	4	LDAADF25R64	Address of first DFE on the address queue
460	(1CC)	ADDRESS	4	LDAADL25R64	Address of last DFE on the address queue
464	(1D0)	ADDRESS	4	LDASZF25R64	Address of first DFE on the size queue
468	(1D4)	ADDRESS	4	LDASZL25R64	Address of last DFE on the size queue
472	(1D8)	CHARACTER	8	LDAPRIVBUF	PrivateBuffer values used for this job
472	(1D8)	UNSIGNED	4	LDAPRIVBUF24	Below 16M
476	(1DC)	UNSIGNED	4	LDAPRIVBUF31	Above 16M
480	(1E0)	CHARACTER	8	LDASMFPRIVBUF	PrivateBuffer values requested by SMF exit IEFUSI - FFFFFFFFx if not set by IEFUSI
480	(1E0)	UNSIGNED	4	LDASMFPRIVBUF24	Below 16M
484	(1E4)	UNSIGNED	4	LDASMFPRIVBUF31	Above 16M
488	(1E8)	CHARACTER	8	*	Reserved
496	(1F0)	CHARACTER	0	LDAEND	END OF LDA

LDA Cross Reference

Name	Hex Offset	Hex Value
LDA	0	
LDAADF	C	
LDAADFR64	168	
LDAADF05	110	
LDAADF05R64	198	
LDAADF15	128	
LDAADF15R64	1B0	
LDAADF25	140	
LDAADF25R64	1C8	
LDAADL	10	
LDAADLR64	16C	
LDAADL05	114	
LDAADL05R64	19C	
LDAADL15	12C	
LDAADL15R64	1B4	
LDAADL25	144	
LDAADL25R64	1CC	
LDAALLOC	E8	
LDAAQAT	8	
LDAAQATR64	164	
LDAAQTAD	94	
LDAAQTST	A4	
LDAAQT05	10C	
LDAAQT05R64	194	
LDAAQT15	124	
LDAAQT15R64	1AC	
LDAAQT25	13C	
LDAAQT25R64	1C4	
LDAARD	34	
LDAASCB	B8	
LDAA2GAA	154	
LDAA2GFA	150	
LDACPADR	A8	
LDACPANC	A8	
LDACPCNT	AC	
LDACRGTP	98	

LDA Cross Reference

Name	Hex Offset	Hex Value
LDADEFED	C4	40
LDADEFER	C4	80
LDADEFQ	A0	
LDADFEQ	C	
LDADFEQR64	168	
LDADFE05	110	
LDADFE05R64	198	
LDADFE15	128	
LDADFE15R64	1B0	
LDADFE25	140	
LDADFE25R64	1C8	
LDAEADF	24	
LDAEADFR64	180	
LDAEADL	28	
LDAEADLR64	184	
LDAEANC	1C	
LDAEANCR64	178	
LDAEAN05	108	
LDAEAN05R64	190	
LDAEAN15	120	
LDAEAN15R64	1A8	
LDAEAN25	138	
LDAEAN25R64	1C0	
LDAEAQAT	20	
LDAEAQATR64	17C	
LDAEARD	44	
LDAEDFEQ	24	
LDAEDFEQR64	180	
LDAEFBAF	44	
LDAEFBAL	48	
LDAEFBRF	84	
LDAEFBRL	88	
LDAEFBSF	64	
LDAEFBSL	68	
LDAEHIAL	F4	
LDAELIM	D8	
LDAELOAL	F0	
LDAEND	1F0	
LDAERGTP	9C	
LDAERRD	84	
LDAESIZA	50	
LDAESIZR	90	
LDAESIZS	70	
LDAESQAT	1C	
LDAESQATR64	178	
LDAESRD	64	
LDAESTRA	4C	
LDAESTRR	8C	
LDAESTRS	6C	
LDAESZF	2C	
LDAESZFR64	188	
LDAESZL	30	
LDAESZLR64	18C	
LDAEULIM	C8	20
LDAEVVRG	DC	
LDAFBQAF	34	
LDAFBQAL	38	
LDAFBQRF	74	
LDAFBQRL	78	
LDAFBQSF	54	
LDAFBQSL	58	
LDAFCADR	B0	
LDAFLGS	C4	
LDAHIAL	EC	
LDAID	0	
LDALIMCL	C8	80
LDALIMIT	D0	
LDALOAL	E8	
LDAMRG24	158	
LDAMRG31	15C	
LDANONFM	E0	
LDAPPD	BC	
LDAPRIVBUF	1D8	
LDAPRIVBUF24	1D8	
LDAPRIVBUF31	1DC	
LDAQANC	4	
LDAQANCR64	160	
LDAREGRQ	CC	
LDARRD	74	
LDASIZA	40	
LDASIZES	C8	
LDASIZR	80	
LDASIZS	60	
LDASM	E0	
LDASMAD	E0	
LDASMF	F8	
LDASMFEL	100	
LDASMFER	104	
LDASMFL	F8	
LDASMFPRIVBUF	1E0	
LDASMFPRIVBUF24	1E0	
LDASMFPRIVBUF31	1E4	
LDASMFR	FC	
LDASMSZ	E4	
LDASQAT	4	
LDASQATR64	160	
LDASQT05	108	
LDASQT05R64	190	
LDASQT15	120	
LDASQT15R64	1A8	
LDASQT25	138	
LDASQT25R64	1C0	
LDASRD	54	
LDASTRTA	3C	
LDASTRTR	7C	
LDASTRTS	5C	
LDASZF	14	
LDASZFR64	170	
LDASZF05	118	
LDASZF05R64	1A0	
LDASZF15	130	
LDASZF15R64	1B8	
LDASZF25	148	
LDASZF25R64	1D0	
LDASZL	18	
LDASZLR64	174	
LDASZL05	11C	
LDASZL05R64	1A4	
LDASZL15	134	
LDASZL15R64	1BC	
LDASZL25	14C	
LDASZL25R64	1D4	
LDAUFLGS	C8	
LDAULIM	C8	40
LDAVVRG	D4	
LDAWRKA	B4	

LGE Information

LGE Heading Information

Common Name: Logical Group Entry
Macro ID: ILRLGE
DSECT Name: LGE
Owning Component: Auxiliary Storage Manager (SC1CW)
Eye-Catcher ID: None
Storage Attributes: Virtual Storage: YES
Subpool: 245
Key: 0
Data Space: NO
Residency: Above 16 Megabytes virtual
Size: 24 Bytes
Created by: ILRGOS
Pointed to by: ASHLGEQ field of the ASMHD data area
LGENEXT field of the LGE data area
LGVELGEP field of the LGVTE data area
ASPLGE field of the ASPCT data area
ACELGE field of the ACE data area
AIALGE field of the AIA data area
Serialization: The ASM class lock of the owning address space is used to serialize the LGE.
Function: ASM's focal point for controlling all operations of a logical group.

LGE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	32	LGE	Logical Group Element
0	(0)	CHARACTER	8	LGEPROCQ	The LGE process queue. This is a double-threaded queue containing AIAs or ACEs for all operations started or pending execution for the logical group
0	(0)	ADDRESS	4	LGEPROCF	Address of first AIA/ACE on process queue
4	(4)	ADDRESS	4	LGEPROCL	Address of last AIA/ACE on process queue
8	(8)	BITSTRING	1	LGEFLAG1	LGE flag field
		1...		LGEWRKPD	Work pending flag. 1 = At least one requested operation is pending execution, 0 = No operations are pending
		.1..		LGEGRINP	Group operation in progress flag. 1 = Group operation in progress, 0 = Group operation not in progress
		..1.		LGERELLG	Release LG requested. 1 = Release LG has been requested, reject all future requests to LG, 0 = Release LG has not been requested.
		...1		LGESAVRQ	Save request queued. 1 = Save LG/LGN or save LG (if LGERELLG=1) request has been queued for LG, 0 = No save requests queued.
		.... 1...		*	Reserved
		.... .1..		LGEPGDEL	PAGEDEL in process flag. 1 = PAGEDEL is processing this logical group, 0 = PAGEDEL not processing this logical group.
		.... ..1.		LGENOSAV	No saved copy flag. 1 = ASPCT saved copy was erased due to an error in PAGEDEL processing
		1		LGERSV5	Reserved
9	(9)	CHARACTER	1	*	Reserved
10	(A)	CHARACTER	2	*	Reserved
12	(C)	ADDRESS	4	LGEASPCT	Address of ASPCT for this logical group
16	(10)	ADDRESS	4	LGENEXT	Address of next LGE on process queue
20	(14)	SIGNED	4	LGELGID	Logical group identifier for this LGE
24	(18)	SIGNED	4	LGESLTCT	Number of slots assigned to this address space or freed during group operation processing
28	(1C)	CHARACTER	4	*	Reserved
32	(20)	CHARACTER	0	*	

LGE Cross Reference

Name	Hex Offset	Hex Value
LGE	0	
LGEASPCT	C	
LGEFLAG1	8	
LGEGRINP	8	40
LGELGID	14	
LGENEXT	10	
LGENOSAV	8	02
LGEPGDEL	8	04
LGEPROCF	0	
LGEPROCL	4	
LGEPROCQ	0	
LGERELLG	8	20
LGERSV5	8	01
LGESAVRQ	8	10
LGESLTCT	18	
LGEWRKPD	8	80

LGVT Information

LGVT Heading Information

Common Name: ASM Logical Group Vector Table
Macro ID: ILRLGVT
DSECT Name: LGVT
Owning Component: Auxiliary Storage Manager (SC1CW)
Eye-Catcher ID: LGVT
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: YES
Subpool: 245
Key: 0
Data Space: NO
Residency: Above 16 Megabytes virtual
Size: Variable because of extensions
Created by: ILRASRIM
Pointed to by: ASMLGVT field of the ASMVT data area
LGVLGVEP field of the LGVT data area
(points to an LGVTE)
LGVENEXT field of the LGVTE data area
(points to an LGVTE)
Serialization: The ASMGL lock is used to serialize the available LGVTE queue, LGVTE's, and the expansion of the LGVT.
Function: LGVT is a collection of information about logical groups for use by ASM. It contains the address of the LGE for the logical group and the address of the ASCB for the address space owning the logical group.

LGVT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	LGVT	Logical Group Vector Table
0	(0)	CHARACTER	4	LGVIDENT	Control block identifier, always set to C'LGVT'
4	(4)	ADDRESS	4	LGVLGVEP	Pointer to first available LGVTE
8	(8)	SIGNED	4	LGVMAXLG	Highest LGN supported by current size of LGVT
12	(C)	SIGNED	4	LGVSIZE	Current size of LGVT in bytes
16	(10)	SIGNED	4	LGVUSECT	Count of LGVTEs currently in use
20	(14)	CHARACTER	16	*	Reserved
36	(24)	CHARACTER	12	LGVENTRS (*)	LGVT entries

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	12	LGVTE	Logical Group Vector Table entry
0	(0)	CHARACTER	12	LGVLGVTE	LGVTE. The number of contiguous LGVTEs is specified by the LGVMAXLG field.
0	(0)	BITSTRING	1	LGVEFLGS	LGVTE flags.
		1...		LGVEUSE	LGVTE in use flag.
		.1..		LGVENCVT	LGVTE no-convert flag. If on, this LG was ASSIGNed after PAGEDEL data sets were made read-only, so it cannot contain migrated pages.
		..11 1111		*	Reserved
1	(1)	CHARACTER	3	LGVERSVD	Reserved
4	(4)	ADDRESS	4	LGVELGEP	Address of LGE for this LG
4	(4)	ADDRESS	4	LGVENEXT	Address of next available LGVTE if this LGVTE is available
8	(8)	ADDRESS	4	LGVEASCB	Address of ASCB to which logical group is assigned
8	(8)	SIGNED	4	LGVELGID	If this LGVTE is available, the LGN of the logical group this LGVTE represents

LGVT Cross Reference

Name	Hex Offset	Hex Value
LGVEASCB	8	
LGVEFLGS	0	
LGVELGEP	4	
LGVELGID	8	
LGVENCVT	0	40
LGVENEXT	4	
LGVENTRS	24	
LGVERSVD	1	
LGVEUSE	0	80
LGVIDENT	0	
LGVLGVEP	4	
LGVLGVTE	0	
LGVMAXLG	8	
LGVSIZE	C	
LGVT	0	
LGVTE	0	
LGVUSECT	10	

LKPT Information

LKPT Heading Information

Common Name: LOCK MANAGER PARMAMETER LIST TABLE
Macro ID: IHALKPT
DSECT Name: LKPT
Owning Component: Supervisor Control (SC1C5)
Eye-Catcher ID: None
Storage Attributes: Subpool: Nucleus resident
Key: Nucleus resident
Size: 400 bytes
Created by: IEAVELIT - THE LOCK INTERFACE TABLE
Pointed to by: PSALKPTT
Serialization: Disablement if using the mapping macro in conjunction with the 'SETLOCK (TEST) TYPE (HIER)' option and testing for a disabled spin lock, also no disablement required.
Function: To be used with the 'SETLOCK (TEST) TYPE (HIER)' request to determine if a lock higher than a user specified lock is held.

LKPT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	400	LKPT	SETLOCK'S PARAMETER LIST
0	(0)	CHARACTER	16	LKPTDISP	DISPATCHER LOCK
0	(0)	SIGNED	4	LKPTDSPC	CLHT OFFSET
4	(4)	UNSIGNED	4	LKPTDSPO	OBTAIN MASK
8	(8)	UNSIGNED	4	LKPTDSPH	HIERARCHY MASK
12	(C)	UNSIGNED	4	LKPTDSPR	RELEASE MASK
16	(10)	CHARACTER	16	LKPTUCB	IOSUCB LOCK
16	(10)	SIGNED	4	LKPTUCBC	CLHT OFFSET
20	(14)	UNSIGNED	4	LKPTUCBO	OBTAIN MASK
24	(18)	UNSIGNED	4	LKPTUCBH	HIERARCHY MASK
28	(1C)	UNSIGNED	4	LKPTUCBR	RELEASE MASK
32	(20)	CHARACTER	16	LKPTSYN	IOSYNCH LOCK
32	(20)	SIGNED	4	LKPTSYNC	CLHT OFFSET
36	(24)	UNSIGNED	4	LKPTSYNO	OBTAIN MASK
40	(28)	UNSIGNED	4	LKPTSYNH	HIERARCHY MASK
44	(2C)	UNSIGNED	4	LKPTSYNR	RELEASE MASK
48	(30)	CHARACTER	16	LKPTNCB	TPNCB LOCK
48	(30)	SIGNED	4	LKPTNCBC	CLHT OFFSET
52	(34)	UNSIGNED	4	LKPTNCBO	OBTAIN MASK
56	(38)	UNSIGNED	4	LKPTNCBH	HIERARCHY MASK
60	(3C)	UNSIGNED	4	LKPTNCBR	RELEASE MASK
64	(40)	CHARACTER	16	LKPTDNC	TPDNCB LOCK
64	(40)	SIGNED	4	LKPTDNCC	CLHT OFFSET
68	(44)	UNSIGNED	4	LKPTDNCO	OBTAIN MASK
72	(48)	UNSIGNED	4	LKPTDNCH	HIERARCHY MASK
76	(4C)	UNSIGNED	4	LKPTDNCR	RELEASE MASK
80	(50)	CHARACTER	16	LKPTACB	TPACBDEB LOCK
80	(50)	SIGNED	4	LKPTACBC	CLHT OFFSET
84	(54)	UNSIGNED	4	LKPTACBO	OBTAIN MASK
88	(58)	UNSIGNED	4	LKPTACBH	HIERARCHY MASK
92	(5C)	UNSIGNED	4	LKPTACBR	RELEASE MASK
96	(60)	CHARACTER	16	LKPTASM	ASM LOCK
96	(60)	SIGNED	4	LKPTASMC	CLHT OFFSET
100	(64)	UNSIGNED	4	LKPTASMO	OBTAIN MASK
104	(68)	UNSIGNED	4	LKPTASMH	HIERARCHY MASK
108	(6C)	UNSIGNED	4	LKPTASMR	RELEASE MASK
112	(70)	CHARACTER	16	LKPTSALL	SALLOC LOCK
112	(70)	SIGNED	4	LKPTSALC	CLHT OFFSET
116	(74)	UNSIGNED	4	LKPTSALO	OBTAIN MASK
120	(78)	UNSIGNED	4	LKPTSALH	HIERARCHY MASK
124	(7C)	UNSIGNED	4	LKPTSALR	RELEASE MASK
128	(80)	CHARACTER	16	LKPTSRM	SRM LOCK
128	(80)	SIGNED	4	LKPTSRMC	CLHT OFFSET
132	(84)	UNSIGNED	4	LKPTSRMO	OBTAIN MASK
136	(88)	UNSIGNED	4	LKPTSRMH	HIERARCHY MASK
140	(8C)	UNSIGNED	4	LKPTSRMR	RELEASE MASK
144	(90)	CHARACTER	16	LKPTLOCL	LOCAL LOCK
144	(90)	SIGNED	4	LKPTLCLC	CLHT OFFSET
148	(94)	UNSIGNED	4	LKPTLCLO	OBTAIN MASK

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
152	(98)	UNSIGNED	4	LKPTLCLH	HIERARCHY MASK
156	(9C)	UNSIGNED	4	LKPTLCLR	RELEASE MASK
160	(A0)	CHARACTER	16	LKPTCML	CML LOCK
160	(A0)	SIGNED	4	LKPTCMLC	CLHT OFFSET
164	(A4)	UNSIGNED	4	LKPTCMLO	OBTAIN MASK
168	(A8)	UNSIGNED	4	LKPTCMLH	HIERARCHY MASK
172	(AC)	UNSIGNED	4	LKPTCMLR	RELEASE MASK
176	(B0)	CHARACTER	16	LKPTCMS	CMS LOCK
176	(B0)	SIGNED	4	LKPTCMSC	CLHT OFFSET
180	(B4)	UNSIGNED	4	LKPTCMSO	OBTAIN MASK
184	(B8)	UNSIGNED	4	LKPTCMSH	HIERARCHY MASK
188	(BC)	UNSIGNED	4	LKPTCMSR	RELEASE MASK
192	(C0)	CHARACTER	16	LKPTRACE	TRACE LOCK
192	(C0)	SIGNED	4	LKPTRCEC	CLHT OFFSET
196	(C4)	UNSIGNED	4	LKPTRCEO	OBTAIN MASK
200	(C8)	UNSIGNED	4	LKPTRCEH	HIERARCHY MASK
204	(CC)	UNSIGNED	4	LKPTRCER	RELEASE MASK
208	(D0)	CHARACTER	16	LKPTVPAG	VSMPAG LOCK
208	(D0)	SIGNED	4	LKPTVSPC	CLHT OFFSET
212	(D4)	UNSIGNED	4	LKPTVSPO	OBTAIN MASK
216	(D8)	UNSIGNED	4	LKPTVSPH	HIERARCHY MASK
220	(DC)	UNSIGNED	4	LKPTVSPR	RELEASE MASK
224	(E0)	CHARACTER	16	LKPTRSM	RSM LOCK
224	(E0)	SIGNED	4	LKPTRSMC	CLHT OFFSET
228	(E4)	UNSIGNED	4	LKPTRSMO	OBTAIN MASK
232	(E8)	UNSIGNED	4	LKPTRSMH	HIERARCHY MASK
236	(EC)	UNSIGNED	4	LKPTRSMR	RELEASE MASK
240	(F0)	CHARACTER	16	LKPTRSMA	RSMAD LOCK
240	(F0)	SIGNED	4	LKPTRADC	CLHT OFFSET
244	(F4)	UNSIGNED	4	LKPTRADO	OBTAIN MASK
248	(F8)	UNSIGNED	4	LKPTRADH	HIERARCHY MASK
252	(FC)	UNSIGNED	4	LKPTRADR	RELEASE MASK
256	(100)	CHARACTER	16	LKPTRSMX	RSMXM LOCK
256	(100)	SIGNED	4	LKPTRXMC	CLHT OFFSET
260	(104)	UNSIGNED	4	LKPTRXMO	OBTAIN MASK
264	(108)	UNSIGNED	4	LKPTRXMH	HIERARCHY MASK
268	(10C)	UNSIGNED	4	LKPTRXMR	RELEASE MASK
272	(110)	CHARACTER	16	LKPTRSMS	RSMST LOCK
272	(110)	SIGNED	4	LKPTRSTC	CLHT OFFSET
276	(114)	UNSIGNED	4	LKPTRSTO	OBTAIN MASK
280	(118)	UNSIGNED	4	LKPTRSTH	HIERARCHY MASK
284	(11C)	UNSIGNED	4	LKPTRSTR	RELEASE MASK
288	(120)	CHARACTER	16	LKPTASMG	ASMGL LOCK
288	(120)	SIGNED	4	LKPTASGC	CLHT OFFSET
292	(124)	UNSIGNED	4	LKPTASGO	OBTAIN MASK
296	(128)	UNSIGNED	4	LKPTASGH	HIERARCHY MASK
300	(12C)	UNSIGNED	4	LKPTASGR	RELEASE MASK
304	(130)	CHARACTER	16	LKPTVFIX	VSMFIX LOCK
304	(130)	SIGNED	4	LKPTVSFC	CLHT OFFSET
308	(134)	UNSIGNED	4	LKPTVSFO	OBTAIN MASK
312	(138)	UNSIGNED	4	LKPTVSFH	HIERARCHY MASK
316	(13C)	UNSIGNED	4	LKPTVSFR	RELEASE MASK
320	(140)	CHARACTER	16	LKPTRSMG	RSMGL LOCK
320	(140)	SIGNED	4	LKPTRGLC	CLHT OFFSET
324	(144)	UNSIGNED	4	LKPTRGLO	OBTAIN MASK
328	(148)	UNSIGNED	4	LKPTRGLH	HIERARCHY MASK
332	(14C)	UNSIGNED	4	LKPTRGLR	RELEASE MASK
336	(150)	CHARACTER	16	LKPTCPU	CPU LOCK
336	(150)	SIGNED	4	LKPTCPUC	CLHT OFFSET
340	(154)	UNSIGNED	4	LKPTCPUO	OBTAIN MASK
344	(158)	UNSIGNED	4	LKPTCPUH	HIERARCHY MASK
348	(15C)	UNSIGNED	4	LKPTCPUR	RELEASE MASK
352	(160)	CHARACTER	16	LKPTRCM	RSMCM LOCK
352	(160)	SIGNED	4	LKPTRCMC	CLHT OFFSET
356	(164)	UNSIGNED	4	LKPTRCMO	OBTAIN MASK
360	(168)	UNSIGNED	4	LKPTRCMH	HIERARCHY MASK
364	(16C)	UNSIGNED	4	LKPTRCMR	RELEASE MASK
368	(170)	CHARACTER	16	LKPTRSMD	RSMDS LOCK
368	(170)	SIGNED	4	LKPTRDSC	CLHT OFFSET
372	(174)	UNSIGNED	4	LKPTRDSO	OBTAIN MASK
376	(178)	UNSIGNED	4	LKPTRDSH	HIERARCHY MASK
380	(17C)	UNSIGNED	4	LKPTRDSR	RELEASE MASK
384	(180)	CHARACTER	16	LKPTIOS	IOS LOCK
384	(180)	SIGNED	4	LKPTIOSC	CLHT OFFSET

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
388	(184)	UNSIGNED	4	LKPTIOSO	OBTAIN MASK
392	(188)	UNSIGNED	4	LKPTIOSH	HIERARCHY MASK
396	(18C)	UNSIGNED	4	LKPTIOSR	RELEASE MASK

LKPT Cross Reference

Name	Hex Offset	Hex Value
LKPT	0	
LKPTACB	50	
LKPTACBC	50	
LKPTACBH	58	
LKPTACBO	54	
LKPTACBR	5C	
LKPTASGC	120	
LKPTASGH	128	
LKPTASGO	124	
LKPTASGR	12C	
LKPTASM	60	
LKPTASMC	60	
LKPTASMG	120	
LKPTASMH	68	
LKPTASMO	64	
LKPTASMR	6C	
LKPTCML	A0	
LKPTCMLC	A0	
LKPTCMLH	A8	
LKPTCMLO	A4	
LKPTCMLR	AC	
LKPTCMS	B0	
LKPTCMSC	B0	
LKPTCMSH	B8	
LKPTCMSO	B4	
LKPTCMSR	BC	
LKPTCPU	150	
LKPTCPUC	150	
LKPTCPUH	158	
LKPTCPUO	154	
LKPTCPUR	15C	
LKPTDISP	0	
LKPTDNC	40	
LKPTDNCC	40	
LKPTDNCH	48	
LKPTDNCO	44	
LKPTDNCR	4C	
LKPTDSPC	0	
LKPTDSPH	8	
LKPTDSPO	4	
LKPTDSPR	C	
LKPTIOS	180	
LKPTIOSC	180	
LKPTIOSH	188	
LKPTIOSO	184	
LKPTIOSR	18C	
LKPTLCLC	90	
LKPTLCLH	98	
LKPTLCLO	94	
LKPTLCLR	9C	
LKPTLOCL	90	
LKPTNCB	30	
LKPTNCBC	30	
LKPTNCBH	38	
LKPTNCBO	34	
LKPTNCBR	3C	
LKPTRACE	C0	
LKPTRADC	F0	
LKPTRADH	F8	
LKPTRADO	F4	
LKPTRADR	FC	
LKPTRCEC	C0	
LKPTRCEH	C8	
LKPTRCEO	C4	
LKPTRCER	CC	
LKPTRCM	160	
LKPTRCMC	160	
LKPTRCMH	168	
LKPTRCMO	164	
LKPTRCMR	16C	
LKPTRDSC	170	
LKPTRDSH	178	
LKPTRDSO	174	
LKPTRDSR	17C	
LKPTRGLC	140	
LKPTRGLH	148	
LKPTRGLO	144	
LKPTRGLR	14C	
LKPTRSM	E0	
LKPTRSMA	F0	
LKPTRSMC	E0	
LKPTRSMD	170	
LKPTRSMG	140	
LKPTRSMH	E8	
LKPTRSMO	E4	
LKPTRSMR	EC	
LKPTRSMS	110	
LKPTRSMX	100	
LKPTRSTC	110	
LKPTRSTH	118	
LKPTRSTO	114	
LKPTRSTR	11C	
LKPTRXMC	100	
LKPTRXMH	108	
LKPTRXMO	104	
LKPTRXMR	10C	
LKPTSALC	70	
LKPTSALH	78	
LKPTSALL	70	
LKPTSALO	74	
LKPTSALR	7C	
LKPTSRM	80	
LKPTSRMC	80	
LKPTSRMH	88	
LKPTSRMO	84	
LKPTSRMR	8C	
LKPTSYN	20	
LKPTSYNC	20	
LKPTSYNH	28	
LKPTSYNO	24	
LKPTSYNR	2C	
LKPTUCB	10	
LKPTUCBC	10	
LKPTUCBH	18	
LKPTUCBO	14	
LKPTUCBR	1C	
LKPTVFIX	130	
LKPTVPAG	D0	
LKPTVSFC	130	
LKPTVSFH	138	
LKPTVSFO	134	
LKPTVSFR	13C	
LKPTVSPC	D0	
LKPTVSPH	D8	
LKPTVSPO	D4	
LKPTVSPR	DC	

LLCB Information

LLCB Heading Information

Common Name: LNKLST Lookaside Control Block
Macro ID: IHALLCB
DSECT Name: LLCB
Owning Component: Contents Supervisor (SC1CJ)
Eye-Catcher ID: LLCB
Offset: 0
Length: 4
Storage Attributes: Subpool: read/write nucleus
Key: 0
Residency: Above 16M
Size: 44 bytes
Created by: nucleus
Pointed to by: CVTLLCB of the CVT data area
Serialization: None
Function: The LLCB contains data needed by the Library Lookaside search routines, by their callers and by LLA routines in the LLA Address Space.

LLCB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	44	LLCB	Library Lookaside Control Block
0	(0)	CHARACTER	4	LLCBID	Control block ID ("LLCB")
4	(4)	UNSIGNED	1	LLCBLVEL	Level number of this LLCB
5	(5)	UNSIGNED	1	LLCBFLGS	Flag byte
		1...		LLCBAVAL	The LLA search services are available. LLCBAVAL is turned on by LLA directory build to activate a new directory. It is initially off, and LLA turns it off when it terminates. LLA's recovery exit CSVLESCH and BLDL's ESTAE IGCT0018 turn off LLCBAVAL if serious errors occur in the LLA search service.
		.1..		LLCBSTOP	LLA was terminated by the operator. This flag inhibits automatic re-START processing.
		..1.		LLCBFAIL	LLA's memory termination resource manager turns on LLCBFAIL when it issues an internal START command to automatically re-START LLA. LLA turns off LLCBFAIL when it completes building a new directory. LLA's memory termination resource manager turns off LLCBFAIL if the re-START fails.
		...1		*	Was LLCBX1OK
		.... 1...		LLCBTLNK	The LNKLST concatenation was truncated when it was opened during NIP processing. CSVLLCRE re-informs the operator and turns off this flag.
		.... .1..		LLCBTLPA	The LPALST concatenation was truncated when it was opened during NIP processing. CSVLLCRE re-informs the operator and turns off this flag.
		.... ..1.		LLCBLNKL	LLA is managing entire LNKLST
		1		LLCBRSV2	Reserved
6	(6)	BITSTRING	2	LLCB_CS_FLAGS	
		1...		LLCBX1_CHECKFOREXITROUTINE	Recheck EXIT1 to see if it has any exit routines that can be called
		.1..		LLCBX1_EXITROUTINETOCALL	EXIT1 has an exit routine that can be called
		..1.		LLCBX1_CSVLLIX1ADDED	Added CSVLLIX1
		...1		LLCBX1_EXITDEFINED	Exit has been defined
		.... 1...		LLCBX2_CHECKFOREXITROUTINE	Recheck EXIT2 to see if it has any exit routines that can be called
		.... .1..		LLCBX2_EXITROUTINETOCALL	EXIT2 has an exit routine that can be called
		.... ..1.		LLCBX2_CSVLLIX2ADDED	Added CSVLLIX2
		1		LLCBX2_EXITDEFINED	Exit has been defined
7	(7)	1111 111.		*	
		1		LLCB_ADDEDCSVLLDYX	CSVLLDYX added
8	(8)	UNSIGNED	4	LLCBRSCD	Component reason code. Copied from SDWAHRC if SDWARCF is on. Valid only if LLCBRSOK is on. Serialized by LLCBRSOK.
12	(C)	ADDRESS	4	LLCBASCB	Address of the ASCB of the LLA address space. Used to denote ownership of the LLCB and to abnormally terminate LLA if there is an error while accessing the LLA directory from another address space.

LLCB Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
16	(10)	ADDRESS	4	LLCBTCB	Address of TCB of LLA's jobstep task. Used to abnormally terminate LLA if an error occurs during LLA's directory search processing.
20	(14)	UNSIGNED	4	*	Reserved
24	(18)	UNSIGNED	4	*	Reserved
28	(1C)	ADDRESS	4	LLCBNXT1	Address of instruction after the MVCK instruction which stores data from the LLA directory into the user's parameter list. Used to determine whether program checks in CSVLLS01 are due to invalid user parameters passed in to BLDL
32	(20)	CHARACTER	4	LLCBDOM	WTO id used to delete LLA's error messages (CSV218E or CSV226E) from the operator's screen.
36	(24)	CHARACTER	4	LLCBABCC	Abend completion code copied from SDWAABCC or ASCBMCC.
36	(24)	CHARACTER	1	LLCBCMPF	Flags in completion code
		1111 1...		*	
		.... .1..		LLCBRSOK	LLCBRSCD is valid
		11		*	
37	(25)	CHARACTER	3	LLCBCMPC	System completion code (1st 12 bits) and user completion code (2nd 12 bits).
40	(28)	CHARACTER	2	LLCBXXLN	Suffix of the "LNKLSTnn" parmlib member being processed when NIP truncated the LNKLST. LLCBTLNK is also turned on.
42	(2A)	CHARACTER	2	LLCBXXLP	Suffix of the "LPALSTnn" parmlib member being processed when NIP truncated the LPALST. LLCBTLNK is also turned on.
44	(2C)	CHARACTER	0	LLCBEND	End+1 of LLCB.

LLCB Cross Reference

Name	**Hex Offset**	**Hex Value**
LLCB	0	
LLCB_ADDEDCSVLLDYX		
	7	01
LLCB_CS_FLAGS		
	6	
LLCBABCC	24	
LLCBASCB	C	
LLCBAVAL	5	80
LLCBCMPC	25	
LLCBCMPF	24	
LLCBDOM	20	
LLCBEND	2C	
LLCBFAIL	5	20
LLCBFLGS	5	
LLCBID	0	
LLCBLNKL	5	02
LLCBLVEL	4	
LLCBNXT1	1C	
LLCBRSCD	8	
LLCBRSOK	24	04
LLCBRSV2	5	01
LLCBSTOP	5	40
LLCBTCB	10	
LLCBTLNK	5	08
LLCBTLPA	5	04
LLCBXXLN	28	
LLCBXXLP	2A	
LLCBX1_CHECKFOREXITROUTINE		
	6	80
LLCBX1_CSVLLIX1ADDED		
	6	20
LLCBX1_EXITDEFINED		
	6	10
LLCBX1_EXITROUTINETOCALL		
	6	40
LLCBX2_CHECKFOREXITROUTINE		
	6	08
LLCBX2_CSVLLIX2ADDED		
	6	02
LLCBX2_EXITDEFINED		
	6	01
LLCBX2_EXITROUTINETOCALL		
	6	04

LLE Information

LLE Programming Interface information

Programming Interface information

LLE

End of Programming Interface information

LLE Heading Information

Common Name: Load List Element
Macro ID: IHALLE
DSECT Name: LLE
Owning Component: Contents Supervisor (SC1CJ)
Eye-Catcher ID: None
Storage Attributes: Subpool: 255
Key: 0
Size: 12 bytes
Created by: Contents Supervor (CSVSBRTN)
Pointed to by: TCBLLS field of the TCB data area (last LLE)
LLECHN field of the LLE data area (next LLE)
Serialization: Local Lock
Function: An LLE controls the loading and deleting (specifically, the LOAD and DELETE functions of Contents Supervision) of a particular load module on an entry point name basis.

LLE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	LLE	
0	(0)	SIGNED	4	LLECHN	- ADDRESS OF NEXT ELEMENT ON LOAD LIST
4	(4)	SIGNED	4	LLECDPT	- ADDRESS OF CDE FOR MODULE
8	(8)	SIGNED	2	LLECOUNT	- RESPONSIBILITY COUNT. THE TOTAL NUMBER OF REQUESTS FOR THE MODULE VIA THE LOAD MACRO INSTRUCTION.
10	(A)	SIGNED	2	LLESYSCT	- SYSTEM RESPONSIBILITY COUNT. THE TOTAL NUMBER OF SYSTEM REQUESTS FOR THE MDOULE VIA THE LOAD MACRO INSTRUCTION.

LLPM Information

LLPM Heading Information

Common Name: Library Lookaside Parameter List (LLPM)
Macro ID: IHALLPM
DSECT Name: LLPM
Owning Component: Contents Supervisor (SC1CJ)
Eye-Catcher ID: None
Storage Attributes: Subpool: N/A
Key: N/A
Residency: N/A
Size: 20 bytes
Function:
The LLPM is passed in to the LLA search service routine, CSVLLSCH. It contains parameters and a work area for CSVLLSCH.

LLPM Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	LLPM	LLA data access parameter list.
0	(0)	ADDRESS	4	LLPMPDS2	Target area address for requested directory entry. (either PDS2 or SMDE)
4	(4)	SIGNED	4	LLPMCOPY	Bytes to copy (excludes length of name).
8	(8)	SIGNED	4	LLPMWORK	Work area for LLA
12	(C)	CHARACTER	1	LLPMKEY	For authorized callers, bits 0-3 contain the storage key for LLA to use when copying the directory entry into the BLDL list (LLPMPDS2).
13	(D)	UNSIGNED	1	LLPMLVL	Level number of LLPM
14	(E)	BITSTRING	1	LLPMFLAG	Flags
		1...		LLPMPDSE	Indicates module in PDSE format library.
		.1..		LLPMBUFF	Indicates the module has been buffered. (Used by DFP)
		..11 1111		*	Reserved
15	(F)	CHARACTER	1	*	Reserved
16	(10)	ADDRESS	4	LLPMLLPX	Address of extended LLA parameters.
20	(14)	CHARACTER	0	LLPMEND	End+1 of LLPM.

LLPM Constants

Len	Type	Value	Name	Description
1	DECIMAL	3	LLPMNUM	Current Level number for LLPMLVL.

LLPM Cross Reference

Name	Hex Offset	Hex Value
LLPM	0	
LLPMBUFF	E	40
LLPMCOPY	4	
LLPMEND	14	
LLPMFLAG	E	
LLPMKEY	C	
LLPMLLPX	10	
LLPMLVL	D	
LLPMPDSE	E	80
LLPMPDS2	0	
LLPMWORK	8	

LLP1 Information

LLP1 Programming Interface Information

Programming Interface Information

LLP1

End of Programming Interface Information

LLP1 Heading Information

Common Name: Library Lookaside Fetch Installation Exit Parameters (LLP1)
Macro ID: IHALLP1
DSECT Name: LLP1
Owning Component: Contents Supervisor (SC1CJ)
Eye-Catcher ID: Offset: 0
Length: 4
Storage Attributes: Subpool: LLA Fetch's dynamic storage
Key: 0
Size: 152 bytes
Created by: LLA Fetch
Pointed to by: Register 1 on entry to CSVLLIX1
Serialization: None
Function: LLA fetch passes the LLP1 to its installation exit CSVLLIX1.
LLA fetch calls CSVLLIX1 after fetching an LLA managed module.
The LLP1 contains statistics and a copy of the BLDL format
PDS directory entry of the just completed fetch request.

LLP1 Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	LLP1	LLA Fetch exit parameter list.
				Comment	
				----------Header----------	
				End of Comment	
0	(0)	CHARACTER	4	LLP1ID	LLP1 id = "LLP1".
4	(4)	SIGNED	1	LLP1LEVL	LLP1 level number = 2.
5	(5)	CHARACTER	1	LLP1RSV1	Reserved.
6	(6)	SIGNED	2	LLP1LEN	Length of the LLP1.
				Comment	
				----------Body----------	
				End of Comment	
8	(8)	SIGNED	4	LLP1USER	Contains a 31-bit address pointer to a four byte user data area which is reserved for CSVLLIX1 to use. The four byte user data area is aligned on a full word boundary. It is initially zero and subsequently contains whatever value CSVLLIX1 stores in it. CSVLLIX1 can optionally use the four byte user data area to pass a parameter (or the address of a parameter list) to itself on subsequent invocations. CSVLLIX1 must manage the serialization of the four byte user data area, pointed to by the LLP1USER field. (Compare-and-swap (CS) is a potential serialization method.) If LLA is restarted, the four byte user area will not be reset to zero. It will contain the last value stored by CSVLLIX1.
12	(C)	CHARACTER	8	LLP1DUR	Time (DURation) in CPU TOD clock units used to fetch the module.
20	(14)	CHARACTER	4	LLP1PROV	Provider of the module indicated by a four byte acronym: "LLAF" indicates LLA Fetch satisfied the fetch request by utilizing the LLA staged copy of the module. "PGMF" indicates LLA Fetch utilized DFP Program Fetch to obtain a copy of the module from its home location.
24	(18)	SIGNED	4	LLP1AVUI	Time averaged system high real storage unreferenced interval count (UIC). Units of UIC are in seconds.
28	(1C)	SIGNED	4	LLP1AVMG	Time averaged expanded storage migration age in seconds.
32	(20)	CHARACTER	76	LLP1PDS2	BLDL format PDS directory entry for this entry point. (Mapped by IHAPDS.)
108	(6C)	CHARACTER	44	LLP1DSN	The data set name for this entry point (padded on the right with blanks)
108	(6C)	X'98'	0	LLP1END	"*" End+1 of the LLP1.
				Comment	
				----------Level number----------	
				End of Comment	
108	(6C)	X'2'	0	LLP1LNUM	"2" Current level of LLP1
				Comment	
				----------Return and reason codes----------	
				End of Comment	
108	(6C)	X'0'	0	#RCIX1_DEFAULT	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
108	(6C)	X'4'	0	#RCIX1_OVERRIDE	"0" CSVLLIX1 return code in register 15 which directs LLA Fetch to use the default threshold to trigger LLA module staging. "4" CSVLLIX1 return code in register 15 which directs LLA Fetch either to trigger staging or to not trigger staging. The reason code in register 0 specifies the required action.
108	(6C)	X'0'	0	#RSIX1_DEFAULT	"0" CSVLLIX1 reason code in register 0 when the return code is RCIX1_DEFAULT meaning that LLA Fetch must use the default threshold to trigger LLA module staging.
108	(6C)	X'1'	0	#RSIX1_TRIGGER	"1" CSVLLIX1 reason code in register 0 when the return code is RCIX1_OVERRIDE meaning that LLA Fetch must trigger staging.
108	(6C)	X'2'	0	#RSIX1_NO_TRIGGER	"2" CSVLLIX1 reason code in register 0 when the return code is RCIX1_OVERRIDE meaning that LLA Fetch must not trigger staging.

LLP1 Cross Reference

Name	Hex Offset	Hex Value
#RCIX1_DEFAULT	6C	0
#RCIX1_OVERRIDE	6C	4
#RSIX1_DEFAULT	6C	0
#RSIX1_NO_TRIGGER	6C	2
#RSIX1_TRIGGER	6C	1
LLP1	0	
LLP1AVMG	1C	
LLP1AVUI	18	
LLP1DSN	6C	
LLP1DUR	C	
LLP1END	6C	98
LLP1ID	0	
LLP1LEN	6	
LLP1LEVL	4	
LLP1LNUM	6C	2
LLP1PDS2	20	
LLP1PROV	14	
LLP1RSV1	5	
LLP1USER	8	

LLP2 Information

LLP2 Programming Interface information

Programming Interface information

LLP2

End of Programming Interface information

LLP2 Heading Information

Common Name: LLA Staging Installation Exit Parameters
Macro ID: IHALLP2
DSECT Name: LLP2
Owning Component: Contents Supervisor (SC1CJ)
Eye-Catcher ID: LLP2
Offset: 0
Length: 4
Storage Attributes: Subpool: 0
Key: 0
Residency: 24 Bit
Size: 8 character header.
Variable number of LLP2X's (length is in LLP2LEN)
Created by: Module CSVLLST2
Pointed to by: LLASX2WA
Serialization: None
Function: Provides a mapping for the statistics for the staged modules for which recent fetch statistics exist to be passed to LLA exits.

LLP2 Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	LLP2	LLA Staging exit parameters.
				Comment	
----------Header----------					
				End of Comment	
0	(0)	CHARACTER	4	LLP2ID	LLP2 id = "LLP2".
4	(4)	SIGNED	1	LLP2LEVL	LLP2 level = 2.
5	(5)	CHARACTER	1	LLP2RSV1	Reserved.
6	(6)	SIGNED	2	LLP2LEN	Length of the LLP2 including all its entry point sections (LLP2EP) and its extension LLP2X. There are LLP2EPCT entry point sections.
				Comment	
----------Inputs and outputs to control LLA Staging-------					
				End of Comment	
8	(8)	SIGNED	4	LLP2USER	Contains a 31-bit address pointer to a four byte user data area which is reserved for CSVLLIX2 to use. The four byte user data is aligned on a full word boundary. It is initially zero and subsequently contains any value stored in it by CSVLLIX2. CSVLLIX2 can optionally use the four byte user data area to pass a parameter (or the address of a parameter list) to itself on subsequent invocations. The four byte field will be reset to zero when LLA is restarted.
12	(C)	SIGNED	4	LLP2VALU (4)	Factors of value, each in the range of -10,000 to +10,000, which indicate the relative value to the system of LLA providing this module to users. See also LLP2WGTS.
12	(C)	SIGNED	4	LLP2VRSP	Response time value (input). Derived from observed fetch durations and relative activity.
16	(10)	SIGNED	4	LLP2VCTN	Contention value (input). Derived from the degree of variability in response times for program fetch and LLA fetch
20	(14)	SIGNED	4	LLP2VSTO	Processor storage value (input). Derived from module size and response time savings if staged.
24	(18)	SIGNED	4	LLP2VUSR	User defined value (output). Default is zero.
28	(1C)	SIGNED	4	LLP2WGTS (4)	Weighting factors in the range 0-100 used by LLA Staging to determine the relative importance to the installation of LLA providing this module (input and output). See also LLP2VALU.
28	(1C)	SIGNED	4	LLP2WRSP	Response time weighting factor.
32	(20)	SIGNED	4	LLP2WCTN	Contention weighting factor.
36	(24)	SIGNED	4	LLP2WSTO	Storage weighting factor.
40	(28)	SIGNED	4	LLP2WUSR	User defined weighting factor.
				Comment	
----------Historical data----------					
				End of Comment	
44	(2C)	SIGNED	4	LLP2AVUC	System high real storage unreferenced interval count(UIC). Units of UIC are in seconds.
48	(30)	SIGNED	4	LLP2AVMG	Expanded storage migration age in seconds.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
52	(34)	SIGNED	4	LLP2AVCT	10,000 times the average number of fetches of this module in recent statistics samples.
56	(38)	SIGNED	4	LLP2NTVL (2)	Average length of recent statistics samples in CPU TOD clock units.
64	(40)	SIGNED	4	LLP2PEMN (2)	Minimum program fetch elapsed time in CPU TOD clock units. '7FFFFFFF'X if never program fetched. (e.g. if staged via a deleted alias)
72	(48)	SIGNED	4	LLP2LEMN (2)	Minimum LLA fetch elapsed time in CPU TOD clock units. '7FFFFFFF'X if never LLA fetched.
80	(50)	SIGNED	4	LLP2EPCT	Count of entry point names for this module. There are LLP2EPCT LLP2EP sections for this LLP2.
84	(54)	SIGNED	4	LLP2EPTR	Pointer to the first entry in the entry point section
88	(58)	SIGNED	4	LLP2EPLN	Length of each entry point section.
92	(5C)	SIGNED	4	LLP2XPTR	Pointer to the extension section, LLP2X.
96	(60)	SIGNED	4	LLP2X1US	Contains a 31-bit address pointer to a four byte user data area which is reserved for CSVLLIX1 to use. The four byte user data is aligned on a full word boundary. It is initially zero and subsequently contains any value stored in it by CSVLLIX1. CSVLLIX1 can optionally use the four byte user data area to pass a parameter (or the address of a parameter list) to itself on subsequent invocations. The four byte field will be reset to zero when LLA is restarted. It is passed to CSVLLIX2 to allow the exits to pass information to each other.
96	(60)	X'64'	0	LLP2END1	"*" End+1 of LLP2 base section.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	LLP2EP	Data for this entry point from the most recent statistics sample. There is one of these sections for each entry point in the module. There are LLP2EPCT entry points in this module. The LLP2EP sections follow the LLP2 section.
0	(0)	SIGNED	4	LLP2PFCT	Program fetch count.
4	(4)	SIGNED	4	LLP2LFCT	LLA fetch count.
8	(8)	SIGNED	4	LLP2PEMX (2)	Maximum program fetch elapsed time in CPU TOD clock units. '00000000'X if no data exists.
16	(10)	SIGNED	4	LLP2LEMX (2)	Maximum LLA fetch elapsed time in CPU TOD clock units. '00000000'X if no data exists.
24	(18)	SIGNED	4	LLP2IPDV (2)	Sum of deltas for the sample of program fetch duration minus minimum program fetch, in TOD units.
32	(20)	SIGNED	4	LLP2ILDV (2)	Sum of deltas for the sample of LLA fetch duration minus minimum LLA fetch, in TOD units.
40	(28)	SIGNED	4	LLP2HPCD (2)	PGMF contention delta for the name. The contention delta is the rolling average of LLP2IPDV divided by LLP2PFCT
48	(30)	SIGNED	4	LLP2HLCD (2)	LLAF contention delta for the name. The contention delta is the rolling average of LLP2ILDV divided by LLP2LFCT
56	(38)	CHARACTER	76	LLP2PDS2	BLDL format PDS directory entry for this entry point. (Mapped by IHAPDS.)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	LLP2X	The extension to the LLP2 parameter list. The LLP2X is located in contiguous storage following the last entry of the LLP2EP array. On entry to CSVLLIX2, its address is in LLP2XPTR.
0	(0)	CHARACTER	44	LLP2XDSN	The data set name for the entry points in the LLP2EP array. (Padded on the right with blanks)
44	(2C)	SIGNED	4	LLP2VSTA	The staging threshhold for this library. Modules whose value is above this threshhold are added to the staging candidate list. See prolog for more information about this threshhold.
48	(30)	SIGNED	4	LLP2VDES	The deactivating threshhold for this library. Modules whose value is below this threshhold and are currently staged are deactivated. See prolog for more information about this threshhold.
52	(34)	SIGNED	4	LLP2LPCD (2)	Average PGM contention delta for the library in TOD units per byte
60	(3C)	SIGNED	4	LLP2LLCD (2)	Average LLA contention delta for the library in TOD units per byte

Comment

----------Level number----------

End of Comment

60	(3C)	X'2'	0	LLP2LNUM	"2" Value for LLP2LEVL.

LLP2 Cross Reference

Offsets Dec	Offsets Hex	Type/Value	Len	Name (Dim)	Description
Comment					
----------Return and reason codes----------					
End of Comment					
60	(3C)	X'0'	0	#RCIX2_EVALUATE	"0" CSVLLIX2 return code in register 15 which directs LLA Staging to use the calculated LLA value of this module to decide whether or not to stage the module.
60	(3C)	X'4'	0	#RCIX2_OVERRIDE	"4" CSVLLIX2 return code in register 15 which directs LLA Staging to stage or unstage the module according to the reason code in register 0.
60	(3C)	X'0'	0	#RSIX2_EVALUATE	"0" CSVLLIX2 reason code in register 0 when the return code is RCIX2_EVALUATE meaning that LLA Staging must use the calculated LLA value of this module to decide whether or not to stage the module.
60	(3C)	X'1'	0	#RSIX2_MUSTSTAGE	"1" CSVLLIX2 reason code in register 0 when the return code is RCIX2_OVERRIDE meaning that LLA Staging must stage this module.
60	(3C)	X'2'	0	#RSIX2_MUSTNTSTAGE	"2" CSVLLIX2 reason code in register 0 when the return code is RCIX2_OVERRIDE meaning that LLA Staging must not use a staged copy of the module.

LLP2 Cross Reference

Name	Hex Offset	Hex Value
#RCIX2_EVALUATE	3C	0
#RCIX2_OVERRIDE	3C	4
#RSIX2_EVALUATE	3C	0
#RSIX2_MUSTNTSTAGE	3C	2
#RSIX2_MUSTSTAGE	3C	1
LLP2	0	
LLP2AVCT	34	
LLP2AVMG	30	
LLP2AVUC	2C	
LLP2END1	60	64
LLP2EP	0	
LLP2EPCT	50	
LLP2EPLN	58	
LLP2EPTR	54	
LLP2HLCD	30	
LLP2HPCD	28	
LLP2ID	0	
LLP2ILDV	20	
LLP2IPDV	18	
LLP2LEMN	48	
LLP2LEMX	10	
LLP2LEN	6	
LLP2LEVL	4	
LLP2LFCT	4	
LLP2LLCD	3C	
LLP2LNUM	3C	2
LLP2LPCD	34	
LLP2NTVL	38	
LLP2PDS2	38	
LLP2PEMN	40	
LLP2PEMX	8	
LLP2PFCT	0	
LLP2RSV1	5	
LLP2USER	8	
LLP2VALU	C	
LLP2VCTN	10	
LLP2VDES	30	
LLP2VRSP	C	
LLP2VSTA	2C	
LLP2VSTO	14	
LLP2VUSR	18	
LLP2WCTN	20	
LLP2WGTS	1C	
LLP2WRSP	1C	
LLP2WSTO	24	
LLP2WUSR	28	
LLP2X	0	
LLP2XDSN	0	
LLP2XPTR	5C	
LLP2X1US	60	

LLT Information

LLT Programming Interface information

Programming Interface information

LLT

End of Programming Interface information

LLT Heading Information

Common Name: Link List Table
Macro ID: IHALLT
DSECT Name: LLT LLTAPFTB
Owning Component: Contents Supervisor (SC1CJ)
Eye-Catcher ID: LLT
Offset: 0
Length: 4
Storage Attributes: Subpool: 245
Key: 0
Residency: Above 16M
Size: 8 character header.
Variable number of 45 character entries
Created by: Modules IEAVNPE5, CSVDLPR
Pointed to by: DLCBLLT@ field of the DLCB data area
Serialization: None
Function: Provides a mapping for the table of data sets that comprise the link list concatenation.

LLT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	LLT	The basing expression was changed because CVTLLTA does not point to the true LLT any longer. Users should either use ASSBDLCB (IHAASSB) -> DLCBLLT@ (CSVDLCB) or specify CVTLLTA themselves.
0	(0)	CHARACTER	8	LLTHEAD	TABLE HEADER
0	(0)	CHARACTER	4	LLTID	TABLE ID 'LLT '
4	(4)	SIGNED	4	LLTCOUNT	NUMBER OF ENTRIES IN TABLE
8	(8)	CHARACTER	45	LLTENTRY (*)	ENTRIES IN TABLE
8	(8)	UNSIGNED	1	LLTDSLTH	LENGTH OF DATASET NAME
9	(9)	CHARACTER	44	LLTDSN	DATASET NAME

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	LLTAPFTB	LNKLST APF libraries table.
0	(0)	CHARACTER	1	LLTANTRY (*)	LNKLST data set entries
0	(0)	CHARACTER	1	LLTAFLGS	Flag byte
		1...		LLTAPFIN	Library is in APF table
		.111 1111		LLTARSV1	Reserved

LLT Cross Reference

Name	Hex Offset	Hex Value
LLT	0	
LLTAFLGS	0	
LLTANTRY	0	
LLTAPFIN	0	80
LLTAPFTB	0	
LLTARSV1	0	7F
LLTCOUNT	4	
LLTDSLTH	8	
LLTDSN	9	
LLTENTRY	8	
LLTHEAD	0	
LLTID	0	

LPAL Information

LPAL Heading Information

Common Name: LPA Device Support Module List
Macro ID: IOSDLPAL
Owning Component: I/O Supervisor (SC1C3)
Eye-Catcher ID: LPAL
Offset: 0
Length: 4
Storage Attributes: Subpool and Key: Built in the IPL work space. Copied into the extended SQA for NIP processing
Size: Variable length
Created by: IEAIPL40 (IRIM to Identify the Device Support Modules)
Pointed to by: IVTLPALP field of the IVT data area during IPL processing
NVTLPALP field of the NVT data area during NIP processing
Serialization: None
Function: The LPA Device Support Module List contains the list of the LPA device support modules that are required to support the devices in the current I/O configuration.

LPAL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	LPAL	LPA Device Support Module List
0	(0)	CHARACTER	8	LPALHEAD	LPA List header.
0	(0)	CHARACTER	4	LPALID	LPA List indentifier ('LPAL')
4	(4)	SIGNED	4	LPALCNT	Count of module names in list
8	(8)	CHARACTER	8	LPALNAME (*)	List of LPA module names in ascending alpha-numeric order

LPAL Constants

Len	Type	Value	Name	Description
		Comment		
		The following constant is used to place an identifer in the LPA Device Support Module List (LPALID field).		
		End of Comment		
4	CHARACTER	LPAL	LPALIDNM	LPA List identifier

LPAT Information

LPAT Heading Information

Common Name: LPALST Table
Macro ID: IHALPAT
DSECT Name: LPAT
Owning Component: Contents Supervision (SC1CJ)
Eye-Catcher ID: LPAT
Offset: 0
Length: 4
Storage Attributes: Key: 0
Residency: LPA, Above 16M
Size: 8 character header.
Variable number of 45 character data set name entries.
Variable number of 6 character volid entries.
Created by: IEAVNPC5
Pointed to by: CVTEPLPS field of the CVT data area
Serialization: None
Function: The LPAT lists the data sets that are included in the LPALST concatenation.
The table consists of a header, followed by an array of the data set names of the libraries included in the LPALST concatenation, followed by an array of the volids that correspond 1:1 to the data set names (volid N goes with dsname N).

LPAT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	LPAT	
0	(0)	CHARACTER	8	LPATHDR	Header section
0	(0)	CHARACTER	4	LPATID	Table id 'LPAT'
4	(4)	SIGNED	4	LPATCNT	Number of entries in table
8	(8)	CHARACTER	*	LPATDATA	Start of table entries
8	(8)	CHARACTER	45	LPATNTRY (*)	Table entry
8	(8)	UNSIGNED	1	LPATDSLN	Length of data set name
9	(9)	CHARACTER	44	LPATDSN	Data set name

LPBT Information

LPBT Heading Information

Common Name: TABLE OF LOGICAL PATH CONTROL BLOCKS
Macro ID: IRALPBT
DSECT Name: LPBT
Owning Component: SYSTEMS RESOURCE MANAGER (SC1CX)
Eye-Catcher ID: LPBT
Offset: 0
Length: 4
Storage Attributes: Subpool: 245
Key: 0
Residency: ABOVE 16M LINE
Size: 16 + 32 X (NUMBER OF LPBS)
Created by: IEAVNP1F
Pointed to by: THE ADDRESS OF THE LPBT IS CONTAINED IN THE -CMCTLPBT- FIELD OF THE CHANNEL MEASUREMENT CONTROL TABLE
Serialization: SRM LOCK
Function: THE LPBT IS A CONTIGUOUS STORAGE AREA USED BY SYSTEM RESOUCES MANAGER TO CONTAIN THE LPB'S.

LPBT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	12	LPBT	
0	(0)	CHARACTER	12	LPBTHDR	
0	(0)	CHARACTER	4	LPBTNAME	ACRONYM 'LPBT'
4	(4)	SIGNED	4	LPBTSIZE	NO. OF BYTES IN LPBT
8	(8)	SIGNED	2	LPBTLAST	OFFSET TO LAST USED LPB
10	(A)	CHARACTER	2	LPBTRSV1	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	36	LPB	LOGICAL PATH BLOCK
0	(0)	BITSTRING	8	LPBID	LOGICAL PATH BLOCK IDENTIFIER MASK
8	(8)	UNSIGNED	4	LPBWORK	WORK AREA
12	(C)	SIGNED	2	LPBCPUT	LPB UTILIZATION, DERIVED FROM CPID UTILIZATIONS IN PERCENT TIMES 100
14	(E)	SIGNED	2	LPBCONNP	PERCENT CONNECTION TIME FOR ALL DEVICES USING THIS LPB IN PERCENT TIMES 100
16	(10)	UNSIGNED	1	LPBCLASS	DEVICE CLASS INDEX TO SELECT LPB THRESHOLDS
17	(11)	BITSTRING	1	LPBFLG	FLAGS
		1...		LPBDAREQ	CHPID DATA REQUESTED
		.1..		LPBOUTIL	LPB IS OVERUTILIZED
		..1.		LPBUUTIL	LPB IS UNDERUTILIZED
		...1		LPBDAVAL	DEVICE ALLOCATION DATA (LPB UTILIZATION) IS VALID
		.... 1...		LPBLBVAL	LOAD BALANCER DATA (PERCENT CONNECTION TIME) IS VALID
		.... .111		*	RESERVED
18	(12)	SIGNED	2	LPBRVUF	LPB UTILIZATION FACTOR FOR COMPUTING RECOMMENDATION VALUES
20	(14)	SIGNED	2	LPBCPIDO (4294967304:0)	ARRAY OF 8, 2-BYTE ENTRIES HAVING OFFSETS INTO THE CPMT (0 VALUE MEANS NO ENTRY)

LPBT Cross Reference

Name	Hex Offset	Hex Value
LPB	0	
LPBCLASS	10	
LPBCONNP	E	
LPBCPIDO	14	
LPBCPUT	C	
LPBDAREQ	11	80
LPBDAVAL	11	10
LPBFLG	11	
LPBID	0	
LPBLBVAL	11	08
LPBOUTIL	11	40
LPBRVUF	12	
LPBT	0	
LPBTHDR	0	
LPBTLAST	8	
LPBTNAME	0	
LPBTRSV1	A	
LPBTSIZE	4	
LPBUUTIL	11	20
LPBWORK	8	

LPDE Information

LPDE Heading Information

Common Name: Link Pack Directory Entry
Macro ID: IHALPDE
DSECT Name: LPDE
Owning Component: Contents Supervisor (SC1CJ)
Eye-Catcher ID: None
Storage Attributes: Residency: LPA, Below 16M
Size: 40 bytes
Created by: Contents Supervisor RIM (IEAVNPC5)
Pointed to by: CVTLPDIR field of the CVT data area
LPDECHN field of the LPDE data area(next LPDE)
LLECDPT field of the LLE data area
RBCDE1 field of the RB data area
RBCDE field of the SVRB data area
RBCDE field of the PRB data area
LPDEMJP field of the LPDE data area
Serialization: None
Function: Each LPDE represents a particular load module which is loaded into the pageable link pack area.

LPDE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	40	LPDE	
0	(0)	ADDRESS	4	LPDECHN	ADDRESS OF NEXT LPDE IN CHAIN OF LPDE SYNONYMS
4	(4)	ADDRESS	4	LPDERBP	RESERVED
8	(8)	CHARACTER	8	LPDENAME	EITHER MODULE NAME OR ALIAS NAME
16	(10)	ADDRESS	4	LPDENTP	RELOCATED ENTRY POINT ADDRESS
		1...		LPDEMODE	ROUTINE RUNS IN 31 BIT MODE
16	(10)	BITSTRING	3	*	
19	(13)	1		LPDEAM64	ROUTINE RUNS IN 64 BIT MODE
20	(14)	ADDRESS	4	LPDEMJP	POINTER TO THE MAJOR LPDE IF THIS IS A MINOR LPDE, OTHERWISE ZERO
24	(18)	ADDRESS	2	LPDEUSE	COUNT FIELD - COUNT IS 1
26	(1A)	CHARACTER	1	LPDEATTB	ATTRIBUTE FLAGS
		1...		LPDEOM	END OF MEMORY OPTION FOR A CDE - MUST BE ZERO FOR AN LPDE
		.111		*	RESERVED
		.... 1...		LPDELPDE	LPDE IDENTIFIER BIT - MUST BE ON IN AN LPDE AND OFF IN A CDE
		.... .111		*	RESERVED
27	(1B)	CHARACTER	1	LPDESP	SUBPOOL FIELD FOR A CDE - MUST BE ZERO FOR AN LPDE
28	(1C)	CHARACTER	1	LPDEATTR	ATTRIBUTE FLAGS
		1...		LPDENIP	MODULE LOADED BY NIP
		.1..		LPDENIC	NOT IN CORE BIT FOR A CDE - MUST BE ZERO FOR AN LPDE
		..1.		LPDEREN	MODULE IS REENTERABLE
		...1		LPDESER	MODULE IS SERIALLY REUSABLE
		.... 1...		LPDENFN	NON FUNCTIONAL INDICATOR FLAG FOR A CDE - MUST BE ZERO FOR AN LPDE
		.... .1..		LPDEMIN	THIS IS A MINOR LPDE
		.... ..1.		LPDEJPA	JOB PACK AREA MODULE INDICATOR - MUST BE ZERO FOR AN LPDE
		1		LPDENLR	NOT LOADABLE ONLY
29	(1D)	CHARACTER	1	LPDEATT2	SECOND ATTRIBUTE FLAG BYTE
		1...		LPDESPZ	INDICATES A MODULE LOADED BY THE AOS LOADER - MUST BE ZERO FOR AN LPDE
		.1..		LPDEREL	INDICATES A MODULE IS INACTIVE AND MAY BE RELEASED, MUST BE ZERO FOR AN LPDE
		..1.		LPDEXLE	EXTENT LIST BUILT - MAIN STORAGE OCCUPIED BY MODULE IS DESCRIBED THEREIN
		...1		LPDERLC	LPDE CONTAINS A RELOCATED ALIAS ENTRY POINT ADDRESS
		.... 1...		LPDEANYM	ROUTINE RUNS IN ANY MODE
		.... .1..		LPDEOLY	MODULE IS IN OVERLAY FORMAT - MUST BE ZERO FOR AN LPDE
		.... ..1.		LPDESYSL	AUTHORIZED LIBRARY MODULE
		1		LPDEAUTH	PROGRAM AUTHORIZATION FLAG ICB360
30	(1E)	CHARACTER	1	LPDEATT3	3rd attribute byte
		1111		*	RESERVED
		.... 1...		LPDELPOK	LongParms OK
		.... .1..		LPDEDYNL	Dynamic LPA. CDE bit. Never on within LPDE
		.... ..1.		LPDEFIX	Page-Fixed. CDE bit. Never on within LPDE
		1		LPDEPROT	Page-Protected. CDE bit, Never on within LPDE
31	(1F)	CHARACTER	1	LPDEATT4	Flags

LPDE Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
32	(20)	CHARACTER	8	LPDEMJNM	MAJOR LPDE ENTRY POINT NAME WHEN LPDEMIN=1 OR 8-BYTE EXTENT LIST IF LPDEMIN=0
32	(20)	SIGNED	4	LPDEXTLN	LENGTH OF MAIN STORAGE BLOCK IN WHICH MODULE RESIDES
36	(24)	ADDRESS	4	LPDEXTAD	ADDRESS OF MAIN STORAGE BLOCK IN WHICH MODULE RESIDES ("Load Point") */

LPDE Cross Reference

Name	**Hex Offset**	**Hex Value**
LPDE	0	
LPDEAM64	13	01
LPDEANYM	1D	08
LPDEATTB	1A	
LPDEATTR	1C	
LPDEATT2	1D	
LPDEATT3	1E	
LPDEATT4	1F	
LPDEAUTH	1D	01
LPDECHN	0	
LPDEDYNL	1E	04
LPDEFIX	1E	02
LPDEJPA	1C	02
LPDELPDE	1A	08
LPDELPOK	1E	08
LPDEMIN	1C	04
LPDEMJNM	20	
LPDEMJP	14	
LPDEMODE	10	80
LPDENAME	8	
LPDENFN	1C	08
LPDENIC	1C	40
LPDENIP	1C	80
LPDENLR	1C	01
LPDENTP	10	
LPDEOLY	1D	04
LPDEOM	1A	80
LPDEPROT	1E	01
LPDERBP	4	
LPDEREL	1D	40
LPDEREN	1C	20
LPDERLC	1D	10
LPDESER	1C	10
LPDESP	1B	
LPDESPZ	1D	80
LPDESYSL	1D	02
LPDEUSE	18	
LPDEXLE	1D	20
LPDEXTAD	24	
LPDEXTLN	20	

LQB Information

LQB Programming Interface Information

Programming Interface Information

LQB

End of Programming Interface Information

LQB Heading Information

Common Name: Language Query Block Mapping Macro
Macro ID: CNLMLQB
DSECT Name: LQB LQBLNGEN
Owning Component: MVS Message Service (SCMMS)
Eye-Catcher ID: LQB
Offset: 0
Length: 4
Storage Attributes: Subpool: of caller
Key: of caller
Residency: of caller
Size: 56 bytes plus 38 bytes for each language entry block.
Created by: Caller of Message Query Language service (QRYLANG)
Pointed to by: LQB_PTR
Serialization: None required.
Function: Used to map the Language Query Block (LQB) used for input and output by Message Query Language user functions. The fixed part is updated using CNLMQLNG macro. The variable part of the LQB consists of one or more language entry information structures updated by CNLUQLNG function called using CNLMQLNG macro.

LQB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	LQB	LANGUAGE QUERY BLOCK
0	(0)	CHARACTER	4	LQBACRN	ACRONYM "LQB"
4	(4)	BITSTRING	1	LQBVRSN	LQB VERSION NUMBER
5	(5)	CHARACTER	3		RESERVED
8	(8)	SIGNED	4	LQBSIZE	SIZE OF THIS LQB
12	(C)	CHARACTER	24	LQBINLNG	LANGUAGE TO BE QUERY
36	(24)	SIGNED	4	LQBCOUNT	NUMBER OF LANGUAGE ENTRIES
40	(28)	SIGNED	4	LQBOFFST	OFFSET TO BE 1ST LANGUAGE ENTRY
44	(2C)	CHARACTER	8		RESERVED
52	(34)	SIGNED	4	LQBVDATL	LENGTH OF THE LQB VARIABLE AREA
56	(38)	CHARACTER	1	LQBVDAT (0)	LQB VARIABLE DATA SECTION

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	LQBLNGEN	LANGUAGE ENTRY BLOCK
0	(0)	CHARACTER	3	LQBLNGCD	LANGUAGE CODE
3	(3)	BITSTRING	1	LQBLNGFL	LANGUAGE FLAGS
		1...		LQBDBCS	"X'80'" DOUBLE BYTE LANGUAGE INDICATOR
4	(4)	CHARACTER	24	LQBLNGNM	PREFERRED LANGUAGE NAME
28	(1C)	CHARACTER	10		RESERVED
28	(1C)	X'26'	0	LQBEBL	"*-LQBLNGEN" LENGTH OF LANGUAGE ENTRY BLOCK

LQB Cross Reference

Name	Hex Offset	Hex Value
LQB	0	
LQBACRN	0	
LQBCOUNT	24	
LQBDBCS	3	80
LQBEBL	1C	26
LQBINLNG	C	
LQBLNGCD	0	
LQBLNGEN	0	
LQBLNGFL	3	
LQBLNGNM	4	
LQBOFFST	28	
LQBSIZE	8	
LQBVDAT	38	
LQBVDATL	34	
LQBVRSN	4	

LRB Information

LRB Heading Information

Common Name: LOGREC Buffer
Macro ID: IHALRB
DSECT Name: LRB
Owning Component: Machine Check Handler (BB1CT)
Eye-Catcher ID: None
Storage Attributes: Subpool: 239 when created by Machine Check Handler, 245 when created by MIH or DDR
Key: 0
Residency: Above 16M line
Size: Variable
Created by: MCH - modules IGFRIM00, IGFPBUCR
Pointed to by: PCCALRBR field of the PCCA data area
PCCALRBV field of the PCCA data area
RVTLRBPT field of the RVT data area
Serialization: MIH and DDR serialize dynamic storage subpool 245.
Function: Holds log record information that is put on SYS1.LOGREC.

LRB Map

Offsets Dec	Offsets Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	0	LRB	
				Comment	
		COMMON HEADER SECTION			
				End of Comment	
0	(0)	BITSTRING	1	LRBHTYPE	TYPE OF RECORD
				Comment	
		RECORD TYPE EQUATES			
				End of Comment	
		..1. ..11		LRBHSLH	"X'23'" SUBCHANNEL LOGOUT RECORD
		..1. .1.1		LRBHCRW	"X'25'" CHANNEL REPORT WORD RECORD
		.11.		LRBHREC	"X'60'" DDR RECORD
		1..1		LRBHMDR	"X'90'" MDR RECORD
		.111 ...1		LRBHMIH	"X'71'" MIH RECORD
		...1 ..11		LRBHMCH	"X'13'" MCH RECORD
		1... ...1		LRBHTER	"X'81'" SYSTEM TERMINATION RECORD
		1... .1..		LRBHSRS	"X'84'" SYSTEM RESTARTABLE WAIT
		1.1.		LRBHMCF	"X'A0'" MCH FRAME RECORD
		1.11		LRBHCCF	"X'B0'" CCH FRAME RECORD
		.1..		LRBHSFW	"X'40'" 4X TYPE RECORDS ARE SOFTWARE TYPE MAPPED BY IHAHDR
		.1.. 1111		LRBHSFR	"X'4F'" " "
1	(1)	BITSTRING	1	LRBHREL	RELEASE NUMBER
1	(1)	X'1'	0	LRBHSYS	"LRBHREL" SYSTEM TYPE
				Comment	
		EQUATES FOR LRBHSYS			
				End of Comment	
		1...		LRBHVS2	"X'80'" OS/VS2 SYSTEM
2	(2)	BITSTRING	1	LRBHSW0	INDEPENDENT SWITCH BYTE
				Comment	
		EQUATES FOR LRBHSW0			
				End of Comment	
		1...		LRBHMORE	"X'80'" MULTIPLE RECORDS
		.1..		LRBHNS	"X'40'" STCK USED / NS MACHINE
		..1.		LRBNOLOG	"X'20'" NO LOG RECORD FLAG.
		...1		LRBHEAB	"X'10'" EXTENDED ADDRESSING

LRB Map

Offsets Dec	Hex	Type/Value	Len	Name (Dec)	Description
		.... 1...		LRBHTMC	"X'08'" TIME MACRO USED
3	(3)	BITSTRING	1	LRBHSW1	DEPENDENT SWITCH BYTE 0
				Comment	
		DDR EQUATES FOR LRBHSW1			
				End of Comment	
		1...		LRBRPRIM	"X'80'" DDR PRIMARY STORAGE RECONFIG
		.1..		LRBRSEC	"X'40'" DDR SEC STORAGE RECONFIG
		..1.		LRBROPER	"X'20'" DDR OPERATOR REQUEST RECONFIG
		...1		LRBRSYSI	"X'10'" DDR PERMANENT ERROR REQUEST
				Comment	
		MCH EQUATES FOR LRBHSW1			
				End of Comment	
		1...		LRBMNOIO	"X'80'" 1: IOSRMCH TELLING IGFPTSIG NOT TO PERFORM ANY I/O
		.1..		LRBMNVF	"X'40'" LRB MAY NOT BE VALID
		..1.		LRBMSYST	"X'20'" SYSTEM TERMINATED
		...1		LRBTRACE	"X'10'" SET TO 1 BY IGFPMCIH BEFORE ALTRTRC SUSPEND AND SET TO 0 AFTER
		.... 1...		LRBDAT	"X'08'" SET TO 1 BY IGFPMCIH BEFORE LOADING THE DATON PSW TO GOTO IGFPMAIN
		.... .1..		LRBMRECV	"X'04'" SET TO 1 WHEN AN ERROR IS COMPLETELY RECOVERED
		.... ..1.		LRBMDEG	"X'02'" SET TO 1 WHEN A RESOURCE IS TAKEN OFFLINE BUT NO WORK IS ABENDED
		1		LRBMFA	"X'01'" SET TO 1 AFTER A MALFUNCTION ALERT
4	(4)	BITSTRING	1	LRBHSW2	DEPENDENT SWITCH BYTE 1
4	(4)	X'4'	0	LRBMACT	"LRBHSW2" MCH BUFFER ACTIVE FLAG
5	(5)	BITSTRING	1	LRBHSW3	DEPENDENT SWITCH BYTE 2
5	(5)	X'5'	0	LRBMCLB	"LRBHSW3" LOGREC CLOBBER FLAG (INDICATES LOGREC BUFFER OVERLAYED)
6	(6)	BITSTRING	1	LRBHCNT	PHYSICAL RECORDS PER LOGICAL REC CNT
7	(7)	BITSTRING	1		RESERVED
8	(8)	BITSTRING	4	LRBHDATE	DATE
12	(C)	BITSTRING	4	LRBHTIME	TIME
16	(10)	DBL WORD	8	(0)	
16	(10)	BITSTRING	8	LRBHCPID (0)	. STIDP OPERAND FIELD
16	(10)	BITSTRING	1		. RESERVED
17	(11)	BITSTRING	3	LRBHCSER	. CPU SERIAL NUMBER
20	(14)	BITSTRING	2	LRBHMDL	. CPU MODEL NUMBER
22	(16)	BITSTRING	2		. RESERVED
24	(18)	CHARACTER	1	LRBBASE (0)	. END OF HEADER
				Comment	
		MACHINE CHECK HANDLER RECORD			
				End of Comment	
24	(18)	SIGNED	4	LRBMLNH	. LENGTH OF LOGREC RECORD
28	(1C)	BITSTRING	4	LRBMWSC	. WAIT STATE CODE
32	(20)	BITSTRING	4	LRBMCEIA (0)	. MACHINE CHECK ERROR INDICATOR AREA
32	(20)	BITSTRING	1	LRBMTERM	. TERMINAL ERROR FLAGS
		..1.		LRBMTTHR	"X'20'" HARD ERROR THRESHOLD FLAG
		...1		LRBMTSEC	"X'10'" . SECONDARY ERROR FLAG
		.... 1...		LRBMTCKS	"X'08'" . CHECK STOP FLAG
		1		LRBMTINV	"X'01'" . INVALID LOGOUT FLAG (SET WHEN LRBMCIC=0 OR WHEN A STORE-STATUS-AT-ADDRESS HAS FAILED AFTER A MALFUNCTION ALERT)
33	(21)	BITSTRING	1	LRBMHARD	. HARD MACHINE ERROR FLAGS
		1...		LRBMHHRD	"X'80'" . ASSUMED HARD ERROR FLAG WAS LRBMHVS, X'20' NOW RESERVED
		...1		LRBMHSD	"X'10'" . SYSTEM DAMAGE FLAG
		.... 1...		LRBMHINV	"X'08'" . REGISTER OR PSW INVALID FLAG
		.... .1..		LRBMHSTO	"X'04'" . HARD STORAGE FAILURE FLAG
		.... ..1.		LRBMHSPF	"X'02'" . HARD PROTECTION KEY ERROR FLAG
		1		LRBMHIPD	"X'01'" . INSTRUCTION PROCESSING DAMAGE FLAG
34	(22)	BITSTRING	1	LRBMINTM	. INTERMEDIATE ERROR FLAGS
		1...		LRBMIPSD	"X'80'" PRIMARY SYNC DAMAGE
		.1..		LRBMIAFD	"X'40'" ETR ATTACHMENT DAMAGE
		..1.		LRBMISWL	"X'20'" SWITCH TO LOCAL

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		LRBMISYC	"X'10'" ETR SYNC CHECK
		.... 1...		LRBMITOD	"X'08'" . TOD CLOCK ERROR FLAG
		.... .1..		LRBMICKC	"X'04'" . CLOCK COMPARATOR ERROR FLAG
		.... ..1.		LRBMICTM	"X'02'" . CPU TIMER ERROR FLAG WAS LRBMIVTE, X'01' NOW RESERVED
35	(23)	BITSTRING	1	LRBMSOFT	. SOFT MACHINE ERROR FLAGS
		1...		LRBMSSFT	"X'80'" . ASSUMED SOFT ERROR FLAG
		.1..		LRBMSSPD	"X'40'" SERVICE PROCESSOR DAMAGE WAS LRBMSVF, X'20' NOW RESERVED
		...1		LRBMDBSE	"X'10'" DOUBLE BIT STORAGE ERROR FLAG
		.... 1...		LRBMSTSL	"X'08'" . SYNC CHECK THRESHOLD EXCEEDED
		.... .1..		LRBMSECC	"X'04'" . ECC CORRECTED STORAGE ERROR FLAG
		.... ..1.		LRBMSHIR	"X'02'" . HIR CORRECTED PROCESSOR ERROR FLAG
		1		LRBMSDG	"X'01'" . DG MACHINE CHECK OCCURRED
36	(24)	BITSTRING	1	LRBMPDAR	. PDAR DATA (SUPPLIED BY RTM)
		...1		LRBMINVP	"X'10'" . STORAGE RECONFIGURED - PAGE INVALIDATED
		.... 1...		LRBMRSRC	"X'08'" . STORAGE RECONFIGURATION STATUS AVAILABLE (FOLLOWING TWO BYTES ARE MEANINGFUL)
		.... .1..		LRBMRSRF	"X'04'" . STORAGE RECONFIGURATION NOT ATTEMPTED
37	(25)	BITSTRING	2	LRBMRSRS	. STORAGE RECONFIGURATION STATUS
39	(27)	BITSTRING	1	LRBMPWL	. PHYSICAL WORD LENGTH (CHECKING BLOCK SIZE)
40	(28)	BITSTRING	8		. Reserved
48	(30)	BITSTRING	280	LRBMFLO (0)	. MACHINE CHECK FIXED LOGOUT AREA (MOVED FROM STORAGE LOCATIONS 232-511 PRE-ESAME or 232-255 and 4608-5119 partial ESAME)
48	(30)	BITSTRING	8	LRBMCIC (0)	. MACHINE CHECK INTERRUPT CODE (MOVED FROM STORAGE LOCATIONS 232-239)
48	(30)	BITSTRING	1		. 1ST BYTE OF LRBMCIC
		1...		LRBMFSD	"X'80'" . SYSTEM DAMAGE
		.1..		LRBMFPD	"X'40'" . PROCESSING DAMAGE
		..1.		LRBMFSR	"X'20'" . SYSTEM RECOVERY
		.... 1...		LRBMFCD	"X'08'" . CLOCK DAMAGE
		.... .1..		LRBMFED	"X'04'" . EXTERNAL DAMAGE WAS LRBMFVF,X'02' NOW RESERVED
		1		LRBMFDG	"X'01'" . DEGRADATION
49	(31)	BITSTRING	1		. 2ND BYTE OF LRBMCIC
		1...		LRBMFWN	"X'80'" . POWER WARNING
		.1..		LRBMFCP	"X'40'" AN AVAILABLE CRW IS PENDING
		..1.		LRBMFSPD	"X'20'" SERVICE PROCESSOR DAMAGE
		...1		LRBMFCK	"X'10'" CHANNEL SUBSYSTEM DAMAGE WAS LRBMFVS, X'04' NOW RESERVED
		.... ..1.		LRBMIBU	"X'02'" . BACK UP INDICATOR
50	(32)	BITSTRING	1		. 3RD BYTE OF LRBMCIC
		1...		LRBMFSE	"X'80'" . STORAGE ERROR
		.1..		LRBMFSC	"X'40'" . STORAGE ERROR CORRECTED
		..1.		LRBMFKE	"X'20'" . KEY ERROR
		...1		LRBMFDS	"X'10'" DOUBLE BIT STORAGE ERROR
		.... 1...		LRBMVWP	"X'08'" . PSW EMWP VALIDITY
		.... .1..		LRBMVMS	"X'04'" . PSW MASKS AND KEY VALIDITY
		.... ..1.		LRBMVPM	"X'02'" . PROGRAM MASKS AND CONDITION CODE VALIDITY
		1		LRBMVIA	"X'01'" . INSTRUCTION ADDRESS VALIDITY
51	(33)	BITSTRING	1		. 4TH BYTE OF LRBMCIC
		1...		LRBMVFA	"X'80'" . FAILING STORAGE ADDR VALIDITY
		.1..		LRBMVZ1	"X'40'" . Z1 Validity
		..1.		LRBMVED	"X'20'" . EXTERNAL DAMAGE CODE VALIDITY
		...1		LRBMVFP	"X'10'" . FLOATING POINT REG VALIDITY
		.... 1...		LRBMVGR	"X'08'" . GENERAL PURPOSE REG VALIDITY
		.... .1..		LRBMVCR	"X'04'" . CONTROL REG VALIDITY
		1		LRBMVST	"X'01'" . STORAGE LOGICAL VALIDITY
52	(34)	BITSTRING	1		. 5TH BYTE OF LRBMCIC
		.1..		LRBMARV	"X'40'" . ACCESS REGISTER VALIDITY.
		..1.		LRBMDAE	"X'20'" DELAYED ACCESS EXCEPTION
		...1		LRBMCSLO	"X'10'" CSLO OCCURRED
		1		LRBMSYC	"X'01'" ETR SYNC CHECK
53	(35)	BITSTRING	1		. 6TH BYTE OF LRBMCIC
		...1		LRBMVAFP	"X'10'" FPCR validity
		...1		LRBMVFPC	"X'10'" FPCR validity
		.... 1...		LRBMVAP	"X'08'" ANCILLARY REPORT CONDITION BIT 44
		.... ..1.		LRBMVPT	"X'02'" . PROCESSOR TIMER VALIDITY
		1		LRBMVCC	"X'01'" . CLOCK COMPARATOR VALIDITY
54	(36)	BITSTRING	2		. RESERVED
56	(38)	BITSTRING	4		. DATA FROM 240-243
60	(3C)	BITSTRING	4	LRBMEDCD (0)	. DATA FROM 244-247, EXTERNAL DAMAGE CODE
60	(3C)	BITSTRING	1	LRBMEDC	. DATA FROM 244 BITS 0:7
61	(3D)	BITSTRING	1	LRBMEDC1	. DATA FROM 245 BITS 8:15
		1...		LRBMEDXN	"X'80'" . EXTENDED STORAGE NOT OPERATIONAL
		.1..		LRBMEDXF	"X'40'" . EXTENDED STORAGE CONTROL FAILURE

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
62	(3E)	BITSTRING	1	LRBMEDC2	. DATA FROM 246 BITS 16:23
		1...		LRBMEDPS	"X'80'" . PRIMARY-SYNC DAMAGE.
		.1..		LRBMEDAD	"X'40'" . ATTACHMENT FACILITY DAMAGE.
		..1.		LRBMEDSL	"X'20'" . SWITCH TO LOCAL.
		...1		LRBMEDSC	"X'10'" . ETR SYNC CHECK.
		.... 1...		LRBMEDEC	"X'08'" . SIDE CONTROL ELEMENT/SIDE ID CHANGE
63	(3F)	BITSTRING	1	LRBMEDC3	. Data from 247, bits 24:31
		1...		LRBMEDSS	"X'80'" . STP Sync Check
		.1..		LRBMEDSI	"X'40'" . STP Island Condition
		..1.		LRBMEDCC	"X'20'" . STP Configuration Change
		...1		LRBMEDCS	"X'10'" . STP Clock Source Error
64	(40)	ADDRESS	4	LRBMFSA	. FAILING STORAGE ADDRESS (MOVED FROM STORAGE LOCATIONS 248-251) (PRE-ESAME)
68	(44)	BITSTRING	4		. DATA FROM 252:255 (PRE-ESAME)
64	(40)	CHARACTER	1	LRBMFSAE (0)	. FSA moved from 248-255 (ESAME)
64	(40)	ADDRESS	4	LRBMFSAH	. FSA high half
68	(44)	ADDRESS	4	LRBMFSAL	. FSA low half
72	(48)	CHARACTER	1	LRBFLA (0)	. "FIXED LOGOUT AREA" from 256-271 (PRE- ESAME) or 4864-4879 (ESAME)
72	(48)	BITSTRING	16	LRBSSPSW	. STORE STATUS PSW, DATA FROM 256:263 3
88	(58)	BITSTRING	16	LRBMFLOR	. DATA FROM 272:287
104	(68)	BITSTRING	64	LRBAREGS	. DATA FROM 288:351, ACCESS REGISTERS
168	(A8)	BITSTRING	32	LRBFREGS	. DATA FROM 352:383, FLOATING POINT REGS 0,2,4,6
200	(C8)	BITSTRING	64	LRBGREGS	. DATA FROM 384:447, GENERAL PURPOSE REGISTERS
264	(108)	BITSTRING	64		. DATA FROM 448:511, So do not use
328	(148)	BITSTRING	1	LRBMEVIA	. EVENT INDICATOR AREA
		1...		LRBMISEC	"X'80'" . SIDE CONTROL ELEMENT/SIDE ID CHANGED
		.1..		LRBMETRA	"X'40'" . MCH TIMER SLIH REQUESTED ABEND OF THE EXTERNAL TIMER SLIH.
329	(149)	BITSTRING	1	LRBMMFLG	. MISCELLANEOUS FLAGS
		1...		LRBMAPR	"X'80'" . APR IS POSSIBLE 1
330	(14A)	BITSTRING	1	LRBMINT2	. Intermeadiate error flags
		1...		LRBMISS	"X'80'" . STP Sync Check
		.1..		LRBMISI	"X'40'" . STP Island Condition
		..1.		LRBMICC	"X'20'" . STP Configuration Change
		...1		LRBMICS	"X'10'" . STP Clock Source Error
		.... ..1.		LRBMSTPC	"X'02'" . STP Clock Source Error reached
		1		LRBMSTPR	"X'01'" . STP Sync check threshold exceeded
331	(14B)	BITSTRING	1		. Reserved
332	(14C)	CHARACTER	100	LRBAFPR (0)	Save area for FPRs 1,3,5,7-15, FPCR
332	(14C)	CHARACTER	8	LRBFPR1	FPR 1
340	(154)	CHARACTER	8	LRBFPR3	FPR 3
348	(15C)	CHARACTER	8	LRBFPR5	FPR 5
356	(164)	CHARACTER	8	LRBFPR7	FPR 7
364	(16C)	CHARACTER	8	LRBFPR8	FPR 8
372	(174)	CHARACTER	8	LRBFPR9	FPR 9
380	(17C)	CHARACTER	8	LRBFPR10	FPR 10
388	(184)	CHARACTER	8	LRBFPR11	FPR 11
396	(18C)	CHARACTER	8	LRBFPR12	FPR 12
404	(194)	CHARACTER	8	LRBFPR13	FPR 13
412	(19C)	CHARACTER	8	LRBFPR14	FPR 14
420	(1A4)	CHARACTER	8	LRBFPR15	FPR 15
428	(1AC)	SIGNED	4	LRBFPCR	FPCR
432	(1B0)	BITSTRING	8		. Reserved
440	(1B8)	CHARACTER	64	LRBG64H	Save area for bits 0-31 of GPRs
504	(1F8)	CHARACTER	128	LRBC64S	Save area for ESAME CRs
632	(278)	CHARACTER	16	LRBMOPSW16	Machine check old PSWE (from 352-367)
648	(288)	CHARACTER	512	LRBZ1	IHAZONEO
1160	(488)	CHARACTER	1	LRBEND (0)	END OF HEADER + MCH SECTION

Comment

RECONFIGURATION (DDR) RECORD

End of Comment

24	(18)	CHARACTER	8	LRBRJOB	'FROM' DEVICE USER'S JOB NAME
32	(20)	CHARACTER	6	LRBRVOL1	VOLUME MOUNTED ON 'FROM' DEVICE
38	(26)	CHARACTER	6	LRBRVOL2	VOLUME MOUNTED ON 'TO' DEVICE
44	(2C)	CHARACTER	1	LRBRPH1	PHYSICAL ID OF DEVICE
45	(2D)	CHARACTER	3	LRBRCUA1	PRIMARY CUA OF 'FROM' DEVICE
48	(30)	CHARACTER	4	LRBRDEV1	'FROM' DEVICE TYPE
52	(34)	CHARACTER	1	LRBRPH2	PHYSICAL ID OF 'TO' DEVICE
53	(35)	CHARACTER	3	LRBRCUA2	PRIMARY CUA OF 'TO' DEVICE
56	(38)	CHARACTER	4	LRBRDEV2	'TO' DEVICE TYPE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
					Comment
		SYSTEM TERMINATION RECORD			
					End of Comment
24	(18)	SIGNED	4	LRBTLNH	LOGREC RECORD LENGTH
28	(1C)	BITSTRING	4	LRBTWSC	WAIT STATE CODE
32	(20)	BITSTRING	1	LRBTUSR (0)	USER DATA FIELD-NOTE THE VALUE IN LRBTLNH IS THE TOTAL LENGTH INCLUDING THE LRBTUSR, EXCLUDING THE HEADER.

LRB Cross Reference

Name	Hex Offset	Hex Value
LRB	0	
LRBAFPR	14C	
LRBAREGS	68	
LRBBASE	18	
LRBC64S	1F8	
LRBDAT	3	8
LRBEND	488	
LRBFLA	48	
LRBFPCR	1AC	
LRBFPR1	14C	
LRBFPR10	17C	
LRBFPR11	184	
LRBFPR12	18C	
LRBFPR13	194	
LRBFPR14	19C	
LRBFPR15	1A4	
LRBFPR3	154	
LRBFPR5	15C	
LRBFPR7	164	
LRBFPR8	16C	
LRBFPR9	174	
LRBFREGS	A8	
LRBGREGS	C8	
LRBG64H	1B8	
LRBHCCF	0	B0
LRBHCNT	6	
LRBHCPID	10	
LRBHCRW	0	25
LRBHCSER	11	
LRBHDATE	8	
LRBHEAB	2	10
LRBHMCF	0	A0
LRBHMCH	0	13
LRBHMDL	14	
LRBHMDR	0	90
LRBHMIH	0	71
LRBHMORE	2	80
LRBHNS	2	40
LRBHREC	0	60
LRBHREL	1	
LRBHSFR	0	4F
LRBHSFW	0	40
LRBHSLH	0	23
LRBHSRS	0	84
LRBHSW0	2	
LRBHSW1	3	
LRBHSW2	4	
LRBHSW3	5	
LRBHSYS	1	1
LRBHTER	0	81
LRBHTIME	C	
LRBHTMC	2	8
LRBHTYPE	0	
LRBHVS2	1	80
LRBMACT	4	4
LRBMAPR	149	80
LRBMARV	34	40
LRBMCEIA	20	
LRBMCIC	30	
LRBMCLB	5	5
LRBMCSLO	34	10
LRBMDAE	34	20
LRBMDBSE	23	10
LRBMDEG	3	2
LRBMEDAD	3E	40
LRBMEDC	3C	
LRBMEDCC	3F	20
LRBMEDCD	3C	
LRBMEDCS	3F	10
LRBMEDC1	3D	
LRBMEDC2	3E	
LRBMEDC3	3F	
LRBMEDEC	3E	8
LRBMEDPS	3E	80
LRBMEDSC	3E	10
LRBMEDSI	3F	40
LRBMEDSL	3E	20
LRBMEDSS	3F	80
LRBMEDXF	3D	40
LRBMEDXN	3D	80
LRBMETRA	148	40
LRBMEVIA	148	
LRBMFA	3	1
LRBMFCD	30	8
LRBMFCK	31	10
LRBMFCP	31	40
LRBMFDG	30	1
LRBMFDS	32	10
LRBMFED	30	4
LRBMFKE	32	20
LRBMFLO	30	
LRBMFLOR	58	
LRBMFPD	30	40
LRBMFSA	40	
LRBMFSAE	40	
LRBMFSAH	40	
LRBMFSAL	44	
LRBMFSC	32	40
LRBMFSD	30	80
LRBMFSE	32	80
LRBMFSPD	31	20
LRBMFSR	30	20
LRBMFWN	31	80
LRBMHARD	21	
LRBMHHRD	21	80
LRBMHINV	21	8
LRBMHIPD	21	1
LRBMHSD	21	10
LRBMHSPF	21	2
LRBMHSTO	21	4
LRBMIAFD	22	40
LRBMIBU	31	2
LRBMICC	14A	20
LRBMICKC	22	4
LRBMICS	14A	10
LRBMICTM	22	2

LRB Cross Reference

Name	Hex Offset	Hex Value
LRBMINTM	22	
LRBMINT2	14A	
LRBMINVP	24	10
LRBMIPSD	22	80
LRBMISEC	148	80
LRBMISI	14A	40
LRBMISS	14A	80
LRBMISWL	22	20
LRBMISYC	22	10
LRBMITOD	22	8
LRBMLNH	18	
LRBMMFLG	149	
LRBMNOIO	3	80
LRBMNVF	3	40
LRBMOPSW16	278	
LRBMPDAR	24	
LRBMPWL	27	
LRBMRECV	3	4
LRBMRSRC	24	8
LRBMRSRF	24	4
LRBMRSRS	25	
LRBMSDG	23	1
LRBMSECC	23	4
LRBMSHIR	23	2
LRBMSOFT	23	
LRBMSSFT	23	80
LRBMSSPD	23	40
LRBMSTPC	14A	2
LRBMSTPR	14A	1
LRBMSTSL	23	8
LRBMSYC	34	1
LRBMSYST	3	20
LRBMTCKS	20	8
LRBMTERM	20	
LRBMTINV	20	1
LRBMTSEC	20	10
LRBMTTHR	20	20
LRBMVAFP	35	10
LRBMVAP	35	8
LRBMVCC	35	1
LRBMVCR	33	4
LRBMVED	33	20
LRBMVFA	33	80
LRBMVFP	33	10
LRBMVFPC	35	10
LRBMVGR	33	8
LRBMVIA	32	1
LRBMVMS	32	4
LRBMVPM	32	2
LRBMVPT	35	2
LRBMVST	33	1
LRBMVWP	32	8
LRBMVZ1	33	40
LRBMWSC	1C	
LRBNOLOG	2	20
LRBRCUA1	2D	
LRBRCUA2	35	
LRBRDEV1	30	
LRBRDEV2	38	
LRBRJOB	18	
LRBROPER	3	20
LRBRPH1	2C	
LRBRPH2	34	
LRBRPRIM	3	80
LRBRSEC	3	40
LRBRSYSI	3	10
LRBRVOL1	20	
LRBRVOL2	26	
LRBSSPSW	48	
LRBTLNH	18	
LRBTRACE	3	10
LRBTUSR	20	
LRBTWSC	1C	
LRBZ1	288	

LXAT Information

LXAT Heading Information

Common Name: LINKAGE INDEX ALLOCATION TABLE
Macro ID: IHALXAT
DSECT Name: LXAT
Owning Component: PC/AUTH (SCXMS)
Eye-Catcher ID: LXAT
Offset: 0
Length: 4
Storage Attributes: Subpool: 229
Key: 0
Residency: Above 16M line
Size: 8 BYTES PLUS 256 BYTES FOR EACH 32 LX'S
Created by: THE LXAT IS CREATED BY IEAVXMAS DURING NIP RIM PROCESSING. THE LINKAGE INDEX RESERVE SERVICE(IEAVXLRE) WILL EXPAND THE LXAT IN MULTIPLES OF 32 ENTRIES TO CORRESPOND WITH THE EXPANSION IN THE SIZE OF A LINKAGE TABLE WHEN MORE LINKAGE INDEXES ARE REQUIRED BY THE SYSTEM. THE LXAT IS IN THE PAGEABLE PRIVATE STORAGE OF THE PC/AUTH ADDRESS SPACE.
Pointed to by: THE CROSS MEMORY DIRECTORY FIELD XMDLXAT.
Serialization: LOCAL LOCK OF THE PC/AUTH SERVICES ADDRESS SPACE.
Function: CONTAINS INFORMATION ON WHICH LINKAGE INDEXES ARE IN USE.

LXAT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	LXAT	LINKAGE INDEX ALLOCATION TABLE
0	(0)	CHARACTER	32	LXATHDR	LXAT HEADER
0	(0)	CHARACTER	4	LXATLXAT	LXAT ACRONYM
4	(4)	UNSIGNED	2	LXATHISLTLXI	HIGHEST LX CONTAINED IN SLT
6	(6)	UNSIGNED	2	LXATMSLXI	Maximum system LX index in LXAT. This equals the actual LX value
		Comment			
		| LXATINCR = 32 = NUMBER OF ENTRIES PER LXAT EXPANSION. | LXATMAX = 2047 = MAXIMUM LX VALUE SUPPORTED.			
		End of Comment			
8	(8)	SIGNED	4	LXATHISLTBLXI	HIGHEST Big LX contained in LXAT. When the ASN-and-LX-Reuse facility is not installed, this equals LXATHISLTLXI
12	(C)	SIGNED	4	LXATMSBLXI	Maximum system Big LX index in LXAT. This does not equal the actual LX value. When the ASN-and-LX-Reuse facility is facility is not installed, this equals LXATMSLXI.
		Comment			
		| LXATINCR = 32 = NUMBER OF ENTRIES PER LXAT EXPANSION. | LXATBMAX = 32767 = maximum big LX value supported			
		End of Comment			
16	(10)	SIGNED	4	LXATHILXI_USED	
20	(14)	SIGNED	4	LXATHIBLXI_USED	
24	(18)	ADDRESS	4	LXATLXATX	Address of LXATx
28	(1C)	CHARACTER	4	*	Reserved
32	(20)	CHARACTER	16	LXATINDX (*)	ARRAY OF LINKAGE INDEXES
32	(20)	UNSIGNED	2	LXATASID	ASID OWNING THIS INDEX (VALID ONLY WHEN LXATOWND IS ON)
34	(22)	UNSIGNED	2	LXATBIND	COUNT OF ADDRESS SPACES USING THIS INDEX. (FOR A SYSTEM LX THAT WAS EVER CONNECTED THIS VALUE WILL BE X'FFFF')
36	(24)	UNSIGNED	2	LXATETCT	COUNT OF ENTRY TABLES CONNECTED TO THIS INDEX. (FOR A SYSTEM LX WHICH IS CONNECTED THIS VALUE WILL BE X'FFFF')
38	(26)	BITSTRING	1	LXATFLGS	FLAGS
		1...		LXATRIP	RESERVE IN PROCESS FOR THIS LX
		.1..		LXATOWND	THIS LX IS RESERVED (OWNED)
		..1.		LXATSYS	THIS IS A SYSTEM LX
		...1		LXATDORM	THIS SYSTEM LX IS DORMANT
		.... 1...		LXATREUS	THIS IS A REUSABLE LX
		.... .1..		LXATWASA	This LX was assigned

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
39	(27)	UNSIGNED	1	LXATRSV2	RESERVED
40	(28)	UNSIGNED	4	LXATSEQNUM	Used for LX reuse purposes
44	(2C)	CHARACTER	4	LXATRSV3	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	LXATX	
0	(0)	CHARACTER	32	LXATXHDR	
0	(0)	SIGNED	4	LXATXTSIZ	Size of LXAT + LXATX
32	(20)	CHARACTER	16	LXATXENTRY (*)	
32	(20)	CHARACTER	8	LXATXJOBNAME	Jobname associated with LX when it was assigned. Not zeroed when dormant.
40	(28)	UNSIGNED	2	LXATXOASID	Original ASID, when it was assigned. Not zeroed when dormant.
42	(2A)	CHARACTER	6	*	Reserved

LXAT Constants

Len	Type	Value	Name	Description
4	DECIMAL	32	LXATINCR	NUMBER OF ENTRIES PER LXAT EXPANSION
4	DECIMAL	2047	LXATMAX	MAXIMUM LX VALUE SUPPORTED
4	DECIMAL	32767	LXATBMAX	Maximum Big LX value supported.
4	DECIMAL	6144	LXAT_LOWEST_BIGLX_PCNUM	This is the actual LX part of a PC number
4	DECIMAL	2048	LXAT_LOWEST_BIGLX_LXATINDEX	This is the 0-origin index into the LXAT

LXAT Cross Reference

Name	Hex Offset	Hex Value
LXAT	0	
LXATASID	20	
LXATBIND	22	
LXATDORM	26	10
LXATETCT	24	
LXATFLGS	26	
LXATHDR	0	
LXATHIBLXI_USED	14	
LXATHILXI_USED	10	
LXATHISLTBLXI	8	
LXATHISLTLXI	4	
LXATINDX	20	
LXATLXAT	0	
LXATLXATX	18	
LXATMSBLXI	C	
LXATMSLXI	6	
LXATOWND	26	40
LXATREUS	26	08
LXATRIP	26	80
LXATRSV2	27	
LXATRSV3	2C	
LXATSEQNUM	28	
LXATSYS	26	20
LXATWASA	26	04
LXATX	0	
LXATXENTRY	20	
LXATXHDR	0	
LXATXJOBNAME	20	
LXATXOASID	28	
LXATXTSIZ	0	

MCA Information

MCA Programming Interface information

Programming Interface information

MCA

ONLY the following fields are part of the programming interface information:

- MCAALCNT
- MCAAVAIL
- MCADLANG

End of Programming Interface information

MCA Heading Information

Common Name: Message Communication Area Mapping Macro
Macro ID: CNLMMCA
DSECT Name: MCA
Owning Component: MVS Message Service (SCMMS)
Eye-Catcher ID: 'MCA '
Offset: 0
Length: 4
Storage Attributes: Subpool: 228
Key: 0
Residency: Above 16 MB in extended CSA
Size: 60 bytes
Created by: CNLSINIT
Pointed to by: SCVTMCA field of the Secondary Communication Vector Table
Serialization: Compare and Swap logic
Function: Used to map the Message Communication Area which contains global control information for the MVS Message Service
MCA contains a pointer to Message Anchor Block (MAB).

MCA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MCA	MESSAGE COMMUNICATION AREA
0	(0)	CHARACTER	4	MCAACRN	MCA ACRONYM "MCA"
4	(4)	BITSTRING	1	MCAVRSN	MCA VERSION NUMBER
		1		$MCA_VERSION	"X'01'" CURRENT MCA VERSION NUMBER
5	(5)	BITSTRING	1	MCAFLAGS	MCA FLAGS
		1...		MCAAVAIL	"X'80'" MMS IS AVAILABLE
		.1..		MCASDTA	"X'40'" SET/DISPLAY TASK ACTIVE
		..1.		MCAINIT	"X'20'" CNLSINIT is executing and will eventually call CNLSSDT
6	(6)	CHARACTER	2		RESERVED
8	(8)	ADDRESS	4	MCAMABP	ADDRESS OF MAB
12	(C)	SIGNED	4	MCASPCNO	SET MMS PC NUMBER
16	(10)	SIGNED	4	MCADPCNO	DISPLAY MMS PC NUMBER
20	(14)	SIGNED	4	MCATPCNO	TRANSLATE MESSAGE PC NUMBER
24	(18)	SIGNED	4	MCAQPCNO	QUERY LANGUAGE PC NUMBER
28	(1C)	CHARACTER	3	MCADLANG	MCA DEFAULT LANGUAGE CODE
31	(1F)	CHARACTER	1		RESERVED
32	(20)	SIGNED	4	MCAALCNT	ACTIVE LANGUAGE COUNT
36	(24)	SIGNED	4		RESERVED
40	(28)	SIGNED	4	MCAXMTOK	CROSS MEMORY ENTRY TABLE TOKEN
44	(2C)	CHARACTER	16		RESERVED

MCA Cross Reference

Name	Hex Offset	Hex Value
$MCA_VERSION	4	1
MCA	0	
MCAACRN	0	
MCAALCNT	20	
MCAAVAIL	5	80
MCADLANG	1C	
MCADPCNO	10	
MCAFLAGS	5	
MCAINIT	5	20
MCAMABP	8	
MCAQPCNO	18	
MCASDTA	5	40
MCASPCNO	C	
MCATPCNO	14	
MCAVRSN	4	
MCAXMTOK	28	

MCHEAD Information

MCHEAD Programming Interface information

Programming Interface information

MCHEAD

INCLUDE ONLY

End of Programming Interface information

MCHEAD Heading Information

Common Name: Monitor Call Routing Table Head (MCHEAD)
Macro ID: MCHEAD
DSECT Name: MCHEAD
Owning Component: GTF (SC111)
Eye-Catcher ID: MCHEAD
Offset: 0
Length: 8
Storage Attributes: Main Storage: 72 bytes
Virtual Storage: 72 bytes
Auxiliary Storage: 0 bytes
Subpool: None
Key: 0
Data Space: None
Residency: Nucleus
Size: 60 bytes
Created by: IEAVNP17 initializes the address of SETEVENT entries during NIP.
Pointed to by: CVTGTFA which is set up by a VCON when the nucleus is linkedited. MCHEAD is contained in AHLMCIH.
Serialization: None.
Function: Describe the monitor call routine service queue head, the beginning of all tables for monitor call event routing.

MCHEAD Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MCHEAD	
0	(0)	SIGNED	4	(0)	
0	(0)	CHARACTER	8	MCHIDEN	ECBDIC IDENTIFIER - MCHEAD
8	(8)	ADDRESS	4	MCHCUR	PTR TO CURRENT MCCE
12	(C)	ADDRESS	4	MCHCNT	NO. OF ROUTINES USING CURRENT MCCE
16	(10)	BITSTRING	4	MCHCTL (0)	CURRENT MASK OF ACTIVE CLASSES IN CR8
16	(10)	BITSTRING	2	MCHCTLLH	Left halfword containing the enhanced monitor call masks
16	(10)	BITSTRING	0	MCHCT7E	"X'0100'" Enhanced monitor call mask for class 7 - the COUNT class
18	(12)	BITSTRING	2	MCHCTLRH (0)	Right halfword containing the monitor call masks
18	(12)	BITSTRING	0	MCHCT0#6	Monitor call mask for classes 0-6
18	(12)	BITSTRING	0	MCHCT7	"X'0100'" Monitor call mask for class 7 - the COUNT class
19	(13)	BITSTRING	1	MCHCT8#F	Monitor call mask for classes 8-F
20	(14)	ADDRESS	4	MCHDIS	PTR USED TO DISABLE MCEE/MCCLE
24	(18)	SIGNED	4	MCHFLGS (0)	FLAG BYTES
24	(18)	BITSTRING	1	MCHFLG1	FLAG BYTE 1
		1...		MCHACT	"X'80'" MC ROUTING ACTIVE INDICATOR
		.1..		MCHTERM	"X'40'" MC ROUTING TERMINATION INDICATOR
25	(19)	BITSTRING	1	MCHFLG2	Flag byte 2
		1...		MCHUSR	"X'80'" At least one GTF has TRACE=USR (with no filtering)
26	(1A)	BITSTRING	2		
28	(1C)	SIGNED	4	(0)	SKIP TO NEXT WORD
28	(1C)	ADDRESS	4	MCHSETE	PTR TO ENABLED ENTRY TO AHLSETEV
32	(20)	ADDRESS	4	MCHSETD	PTR TO DISABLED ENTRY TO AHLSETEV
36	(24)	ADDRESS	4	MCHMCER	ADDRESS OF MC ROUTER AHLMCER
40	(28)	ADDRESS	4	MCHFRRAD	ADDR OF AHLMCIH RECOVERY CODE
44	(2C)	ADDRESS	4	MCHMAXGT	MAXIMUM AMOUNT OF GTRACE DATA
48	(30)	ADDRESS	4	MCHUSRFT	Address of composite USR filter table
52	(34)	ADDRESS	4	MCHMCIHC	ADDR OF AHLMCIHC ENTRY IN AHLMCIH
56	(38)	ADDRESS	4	MCHUTEST	ADDRESS OF SERVICE IN AHLMCIH TO DETERMINE IF SPECIFIC USER EID WAS SPECIFIED ON START OF GTF
60	(3C)	ADDRESS	4	MCHSETDE	ENDING ADDRESS OF THE LOAD MODULE AHLSETD
64	(40)	ADDRESS	4	MCHUTES0	Address of service in AHLMCIH to determine if specific user EID was specified on start of GTF (GTRACE TEST=YES,DISABLED=YES)
68	(44)	ADDRESS	4	MCHFTSTG	Address of storage for composite USR filter table

MCHEAD Cross Reference

Name	Hex Offset	Hex Value
MCHACT	18	80
MCHCNT	C	
MCHCTL	10	
MCHCTLLH	10	
MCHCTLRH	12	
MCHCT0#6	12	
MCHCT7	12	100
MCHCT7E	10	100
MCHCT8#F	13	
MCHCUR	8	
MCHDIS	14	
MCHEAD	0	
MCHFLGS	18	
MCHFLG1	18	
MCHFLG2	19	
MCHFRRAD	28	
MCHFTSTG	44	
MCHIDEN	0	
MCHMAXGT	2C	
MCHMCER	24	
MCHMCIHC	34	
MCHSETD	20	
MCHSETDE	3C	
MCHSETE	1C	
MCHTERM	18	40
MCHUSR	19	80
MCHUSRFT	30	
MCHUTEST	38	
MCHUTES0	40	

MCSCSA Information

MCSCSA Programming Interface information

Programming Interface information

MCSCSA

The following field is **NOT** programming interface information:

- MCSCOEXT

End of Programming Interface information

MCSCSA Heading Information

Common Name: MCS Extended Console Status Area
Macro ID: IEAVG131
DSECT Name: MCSCSA
Owning Component: Consoles (SC1CK)
Eye-Catcher ID: MCSC
Offset: 0
Length: 4
Storage Attributes: Subpool: N/A
Key: N/A
Residency: Message data space for the address space which owns the Extended MCS Console.
Size: MCSCSA -- X'0034' bytes
Created by: IEAVH603
Pointed to by: MCSCPTR - Pointer maintained in users dynamic
MCSCALET - ALET maintained in users dynamic
Serialization: CS for MCSCFlgs_CS
Function: The MCS Extended Console Status Area Contains the Status of the Messages in a Extended Consoles Message Data Space.

MCSCSA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MCSCSA	
0	(0)	CHARACTER	4	MCSCID	Acronym 'MCSC'
4	(4)	BITSTRING	1	MCSCVER	Version level
5	(5)	BITSTRING	1	MCSCFLGS	Flags byte
				Comment	
					Bit definitions:
				End of Comment	
		1...		MCSCPOST	"X'80'" A post was done on the Alert ECB
6	(6)	SIGNED	2	MCSCSTOR	ALWAYS ZERO - CONTAINS NO VALID DATA
8	(8)	SIGNED	4	MCSCCNID	Console ID of message owner
12	(C)	ADDRESS	4	MCSCNUSE	ALWAYS ZERO - CONTAINS NO VALID DATA
16	(10)	SIGNED	4	MCSCTDEP	Total Message Queue Depth
20	(14)	SIGNED	4	MCSCUDEP	Message Queue Depth for Unsolicited messages
24	(18)	SIGNED	4	MCSCDDEP	Message Queue Depth for Delivered (In Use) messages
28	(1C)	SIGNED	4	MCSCPDEP	Maximum message queue depth permitted
32	(20)	BITSTRING	1	MCSCMFRM	Message format - (Note: the bit offsets correspond to the UCMDISP2 field in the UCM)
				Comment	
					Bit definitions:
				End of Comment	
		1...		MCSCDTIM	"X'80'" Display timestamp
		.1..		MCSCDJOB	"X'40'" Display jobname
		.... .1..		MCSCDSYS	"X'04'" Display system name
		.... ..1.		MCSCDX	"X'02'" Don't display system name and jobname

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					The next four fields indicate the status of queuing at the time when the ALERT ecb was posted. The value one will be stored into each field for which the following queuing condition exists: 1. Memory Limit - no more cells in the data space. Queueing will be halted. 2. Queue Depth Limit - the console's message queue has reached the maximum depth. Queueing will be halted. 3. Internal Error - an error occurred while manipulating the message queues. Queueing will be halted. 4. Alert Percentage - the number of messages on the queue has reached a certain percentage of the maximum queue depth, as defined by the ALERT percentage. Queueing will continue. The next field after these four will be used to request that the extended console be deactivated. The value one will be stored in the MCSCSUSP field. 5. Suspend Operator - the console is considered suspended by the system. The extended console should be deactivated.
					End of Comment
33	(21)	CHARACTER	4	MCSCQSTA	Queuing Status
33	(21)	BITSTRING	1	MCSCMLIM	Queuing Stopped by Memory Limit
34	(22)	BITSTRING	1	MCSCDLIM	Queuing Stopped by Queue Depth Limit
35	(23)	BITSTRING	1	MCSCINTR	Queuing Stopped by Internal Error
36	(24)	BITSTRING	1	MCSCALRT	Queuing Reached Alert percentage
37	(25)	BITSTRING	1	MCSCSUSP	Request to suspend the operator
38	(26)	CHARACTER	6		Reserved
44	(2C)	SIGNED	4	MCSCFLGS_CS	Flags field manipulated via Compare and Swap. Field will be initialized to zero when MCSCSA gets created in MCSOPER activation
44	(2C)	BITSTRING	1	MCSCFLGS_CS1	Byte 1
					Comment
					Bit definitions:
					End of Comment
		1...		MCSCMESSAGEECBISPOSTED	"X'80'" A post was done on the Message ECB in EMCS queuer processing. It will be reset in cross memory Post Exit processing
45	(2D)	BITSTRING	1	MCSCFLGS_CS2	Byte 2
46	(2E)	BITSTRING	1	MCSCFLGS_CS3	Byte 3
47	(2F)	BITSTRING	1	MCSCFLGS_CS4	Byte 4
48	(30)	ADDRESS	4	MCSCOEXT	Pointer to O.C.O extension
52	(34)	CHARACTER	1	MCSCEND (0)	End of MCSCSA non-O.C.O portion
52	(34)	X'C3E2C3'	0	MCSCACRN	"C'MCSC'" Acronym 'MCSC'
52	(34)	X'1'	0	MCSCVERS	"1" Current version
52	(34)	X'1'	0	MCSC410	"1" Version level for SP4.1.0
52	(34)	X'34'	0	MCSCSA_LEN	"*-MCSCSA"

MCSCSA Cross Reference

Name	Hex Offset	Hex Value
MCSCACRN	34	C3E2C3
MCSCALRT	24	
MCSCCNID	8	
MCSCDDEP	18	
MCSCDJOB	20	40
MCSCDLIM	22	
MCSCDSYS	20	4
MCSCDTIM	20	80
MCSCDX	20	2
MCSCEND	34	
MCSCFLGS	5	
MCSCFLGS_CS	2C	
MCSCFLGS_CS1	2C	
MCSCFLGS_CS2	2D	
MCSCFLGS_CS3	2E	
MCSCFLGS_CS4	2F	
MCSCID	0	
MCSCINTR	23	
MCSCMESSAGEECBISPOSTED	2C	80
MCSCMFRM	20	
MCSCMLIM	21	
MCSCNUSE	C	
MCSCOEXT	30	
MCSCPDEP	1C	
MCSCPOST	5	80
MCSCQSTA	21	
MCSCSA	0	
MCSCSA_LEN	34	34
MCSCSTOR	6	

MCSCSA Cross Reference

Name	Hex Offset	Hex Value
MCSCSUSP	25	
MCSCTDEP	10	
MCSCUDEP	14	
MCSCVER	4	
MCSCVERS	34	1
MCSC410	34	1

MCSOP Information

MCSOP Programming Interface information

Programming Interface information

MCSOP

End of Programming Interface information

MCSOP Heading Information

Common Name: MCSOPER OPERPARM Mapping
Macro ID: IEZVG111
DSECT Name: MCSOPPRM, MCSOTBL, MCSOMAP
Owning Component: CONSOLE (SC1CK)
Eye-Catcher ID: None
Storage Attributes: Subpool: Determined by invoker of MCSOPER macro
Key: 0-7
Residency: Any, determined by invoker of MCSOPER macro
Size: MCSOPPRM - 60 bytes
MCSOTBL - maximum systems in sysplex * 8 + 4
Created by: Invoker of MCSOPER
Pointed to by: Invoker of MCSOPER
Serialization: None
Function: Mapping of the Operator Data area
referenced by MCSOPER ACTIVATE processing
via the OPERPARM parameter value.

MCSOP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MCSOPPRM	OPERPARM Attributes
0	(0)	SIGNED	4	(0)	Start on a fullword boundry
				Comment	

				Message Data Space Size - The Maximum size for the Message data space, in MegaBytes.	

				End of Comment	
0	(0)	SIGNED	2	MCSOSTOR	Limit Value
				Comment	

				Authority Level - Two bit flags reprsenting the Authority levels. MASTER, ALL, (SYS,IO,CONS), and INFO are mutually exclusive values. SYS, IO, and CONS can be mixed.	

				End of Comment	
2	(2)	BITSTRING	2	MCSOAUTH (0)	AUTHORITY LEVEL
2	(2)	BITSTRING	1	MCSOATH1	Authority flag 1
		1...		MCSOMSTR	"X'80'" MASTER
		.1..		MCSOAALL	"X'40'" ALL (SYS,IO, AND CONS)
		..1.		MCSOASYS	"X'20'" SYS
		...1		MCSOAIO	"X'10'" I/O
		.... 1...		MCSOCONS	"X'08'" CONS
		.... .1..		MCSOINFO	"X'04'" INFO (DEFUALT)
3	(3)	BITSTRING	1	MCSOATH2	Authority flag 2 - reserved
				Comment	

				Message Form - Indicates how a message is displayed.	

				End of Comment	
4	(4)	BITSTRING	2	MCSOMFRM (0)	OPERATORS MESSAGE FORM
4	(4)	BITSTRING	1	MCSOMFM1	Message Form flag 1
		1...		MCSOMFT	"X'80'" Display with a TIME STAMP
		.1..		MCSOMFS	"X'40'" Display with the SYSTEM NAME
		..1.		MCSOMFJ	"X'20'" Display with JOB ID/NAME
		...1		MCSOMFM	"X'10'" Display without SYSTEM,TIME, or JOB (DEFAULT)
		.... 1...		MCSOMFX	"X'08'" Suppress SYSTEM and JOB names
5	(5)	BITSTRING	1	MCSOMFM2	Message Form flag 2

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
				Comment	

					Message Level - The level of messages to be received by the console.

				End of Comment	
6	(6)	BITSTRING	2	MCSOMLVL (0)	MESSAGE LEVEL
6	(6)	BITSTRING	1	MCSOMLV1	Message Level flag 1
		1...		MCSOMLR	"X'80'" Receive WTORs
		.1..		MCSOMLI	"X'40'" Receive IMMEDIATE ACTION messages
		..1.		MCSOMLCE	"X'20'" Receive CRITICAL EVENTUAL ACTION msgs
		...1		MCSOMLE	"X'10'" Receive EVENTUAL ACTION messages
		.... 1...		MCSOMLIN	"X'08'" Receive INFORMATIONAL messages
		.... .1..		MCSOMLBC	"X'04'" Receive BROADCAST messages
		.... ..1.		MCSOMLAL	"X'02'" Receive ALL message levels (DEFAULT)
		1		MCSOMLNO	"X'01'" Receive NO message levels
7	(7)	BITSTRING	1	MCSOMLV2	Message Level flag 2
				Comment	

					Message Type - This is the MONITOR value. It indicates what events the console will monitor.

				End of Comment	
8	(8)	BITSTRING	2	MCSOMSGT (0)	MESSAGE TYPE
8	(8)	BITSTRING	1	MCSOMTP1	Message Type flag 1
		1...		MCSOMTJN	"X'80'" Monitor JOB NAMES
		.1..		MCSOMTJT	"X'40'" Monitor JOB NAMES, display w/time
		..1.		MCSOMTSS	"X'20'" Monitor SESSIONS
		...1		MCSOMTST	"X'10'" Monitor SESSIONS, display w/time
		.... 1...		MCSOMTS	"X'08'" Monitor STATUS of freed data sets
9	(9)	BITSTRING	1	MCSOMTP2	Message Type flag 2
				Comment	

					Routing Codes - A 128 bit string where each bit represents a Route Code. A flag is included for ALL and NONE.

				End of Comment	
10	(A)	CHARACTER	17	MCSORCDT (0)	Routing Code data
10	(A)	BITSTRING	1	MCSORCFL	Routing Code flag
		1...		MCSORCAL	"X'80'" ALL Routing Codes
		.1..		MCSORCNO	"X'40'" NO Routing Codes (DEFAULT)
11	(B)	CHARACTER	16	MCSORTCD	ROUTING CODES (If not ALL or NONE)
				Comment	

					Log Command Response - Should the Command Response of a console be logged in the MCS Hardcopy Log.

				End of Comment	
27	(1B)	BITSTRING	1	MCSOLOGC	LOG COMMAND RESPONSE Value
		1...		MCSOLOGS	"X'80'" SYSTEM - Log the response (DEFAULT)
		.1..		MCSOLOGN	"X'40'" NO - Do not log the response
				Comment	

					This byte was MCSOMIG (Migration ID flags). It is now reserved.

				End of Comment	
28	(1C)	BITSTRING	1	MCSORSV3	RESERVED - Was MCSOMIG

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment

					DOM - Indicates what type, if any, of Delete Operator Message (DOM) the console will receive. Normal will queue DOMs by the message queuing criteria. All will queue all DOMs. None will keep and DOMs from being sent to the console.

					End of Comment
29	(1D)	BITSTRING	1	MCSODOM	DOM Value
		1...		MCSODOMN	"X'80'" NORMAL (DEFAULT)
		.1..		MCSODOMA	"X'40'" ALL
		..1.		MCSODOMX	"X'20'" NONE
					Comment

					Key - The eight byte character name used to associate groups of consoles.

					End of Comment
30	(1E)	CHARACTER	8	MCSOKEY	Key assigned to console entry
					Comment

					Command System - The system where all commands from this console will be sent to execute. A ' ' will be converted to the currently executing system name.

					End of Comment
38	(26)	CHARACTER	8	MCSOCSNM	Command System Name
					Comment

					ALTGRP - IS RESERVED AS OF HBB7730

					End of Comment
46	(2E)	CHARACTER	8	MCSOALGP	Reserved as of HBB7730
					Comment

					MSCOPE data - The systems for which this console is eligible to receive messages from. If the console is to be scoped to all systems, then the user sets MCSOSALL on. If a specific list of system names is to be specified, then MCSOSLST is set on and MCSOMSPT is set to the address of a structure containing a list of systems. This structure is mapped by the DSECT MCSOTBL.

					End of Comment
54	(36)	BITSTRING	1	MCSOMSFG	MSCOPE flags
		1...		MCSOSALL	"X'80'" *ALL specified for MSCOPE
		.1..		MCSOSLST	"X'40'" List of MSCOPE values specified
55	(37)	BITSTRING	1		reserved for alignment
56	(38)	ADDRESS	4	MCSOMSPT	Pointer to a list of MSCOPE values
60	(3C)	BITSTRING	1	MCSOMISC	Miscellaneous Routing Information 3
		1...		MCSORSV1	"X'80'" Reserved. Was MCSOUDY.
		.1..		MCSORSV2	"X'40'" Reserved. Was MCSOUDN.

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
				Comment	

		Automation - Should the Console be sent automatable messages			

				End of Comment	
		..1.		MCSOAUTY	"X'20'" YES - Queue automatable messages
		...1		MCSOAUTN	"X'10'" No - Don't queue automatable messages
				Comment	

		Hardcopy - Should the Console be sent the hardcopy message set			

				End of Comment	
		.... 1...		MCSOHDCY	"X'08'" YES - receive hardcopy message set
		.... .1..		MCSOHDCN	"X'04'" No - Don't receive hardcopy message set DEFAULT
				Comment	

		INTIDS - Should the Console be sent messages directed to console ID zero			

				End of Comment	
		.... ..1.		MCSOINTY	"X'02'" YES - receive CNID zero messages
		1		MCSOINTN	"X'01'" No - Don't receive CNID zero messages DEFAULT
61	(3D)	BITSTRING	1	MCSOFLAG	Flags byte
				Comment	

		Overriding of security product: The following will be the order of processing for OPERPARMs as determined by the following bits:			
		MCSOVRDY = ON : Yes - Override security product Processing will use this data area (IEZVG111) to set the extended console's attributes.			
		MCSOVRDN = ON : No - Don't override security product (DEFAULT) Processing will first search security security product for an OPERPARM segment. If no segment exists, processing will then use this data area to set the extended console's attributes			

				End of Comment	
		1...		MCSOVRDY	"X'80'" Yes - Override security product
		.1..		MCSOVRDN	"X'40'" No - Don't override security product (DEFAULT)
				Comment	

		Bypassing of MVS.MCSOPER.consname check: The following bits will determine whether MCSOPER processing will perform a check against the users access of the MVS.MCSOPER.consname profile.			
		MCSOBYPY = ON : Yes - The authorized caller of MCSOPER has made all appropriate checks, and has insured that the user should be allowed to activate the console. The check against MVS.MCSOPER.consname will be bypassed by MCSOPER processing.			
		MCSOBYPN = ON : No - The check should not be bypassed (DEFAULT) Note that the MCSOBYPY setting is honored if both bits are set.			
				End of Comment	

MCSOP Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.... 1...		MCSOBYPY	"X'08'" Yes - Bypass security check
		.... .1..		MCSOBYPN	"X'04'" No - Don't bypass security check
62	(3E)	BITSTRING	1	MCSOMSC2	Miscellaneous Routing Information #2
		Comment			

		UNKNIDS - Should the Console be sent messages directed to unknown console IDs			

		End of Comment			
		1...		MCSOUNKY	"X'80'" YES - receive unknown CNID messages
		.1..		MCSOUNKN	"X'40'" No - Don't receive unknown CNID messages DEFAULT
63	(3F)	BITSTRING	5		Reserved
63	(3F)	X'44'	0	MCSOPLEN	"*-MCSOPPRM"

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MCSOTBL	Table pointed to by MCSPMSPT
0	(0)	SIGNED	4	MCSOMSNM	Number of MSCOPE values specified
4	(4)	CHARACTER	8	MCSOTSYS (8)	Storage for system names

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MCSOMAP	Template to be mapped over MCSOTSYS
0	(0)	SIGNED	4	(0)	Put on word boundary
0	(0)	CHARACTER	8	MCSOSYSE	System name entry

MCSOP Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
MCSOAALL	2	40	MCSOMISC	3C	
MCSOAIO	2	10	MCSOMLAL	6	2
MCSOALGP	2E		MCSOMLBC	6	4
MCSOASYS	2	20	MCSOMLCE	6	20
MCSOATH1	2		MCSOMLE	6	10
MCSOATH2	3		MCSOMLI	6	40
MCSOAUTH	2		MCSOMLIN	6	8
MCSOAUTN	3C	10	MCSOMLNO	6	1
MCSOAUTY	3C	20	MCSOMLR	6	80
MCSOBYPN	3D	4	MCSOMLVL	6	
MCSOBYPY	3D	8	MCSOMLV1	6	
MCSOCONS	2	8	MCSOMLV2	7	
MCSOCSNM	26		MCSOMSC2	3E	
MCSODOM	1D		MCSOMSFG	36	
MCSODOMA	1D	40	MCSOMSGT	8	
MCSODOMN	1D	80	MCSOMSNM	0	
MCSODOMX	1D	20	MCSOMSPT	38	
MCSOFLAG	3D		MCSOMSTR	2	80
MCSOHDCN	3C	4	MCSOMTJN	8	80
MCSOHDCY	3C	8	MCSOMTJT	8	40
MCSOINFO	2	4	MCSOMTP1	8	
MCSOINTN	3C	1	MCSOMTP2	9	
MCSOINTY	3C	2	MCSOMTS	8	8
MCSOKEY	1E		MCSOMTSS	8	20
MCSOLOGC	1B		MCSOMTST	8	10
MCSOLOGN	1B	40	MCSOPLEN	3F	44
MCSOLOGS	1B	80	MCSOPPRM	0	
MCSOMAP	0		MCSORCAL	A	80
MCSOMFJ	4	20	MCSORCDT	A	
MCSOMFM	4	10	MCSORCFL	A	
MCSOMFM1	4		MCSORCNO	A	40
MCSOMFM2	5		MCSORSV1	3C	80
MCSOMFRM	4		MCSORSV2	3C	40
MCSOMFS	4	40	MCSORSV3	1C	
MCSOMFT	4	80	MCSORTCD	B	
MCSOMFX	4	8	MCSOSALL	36	80

Name	Hex Offset	Hex Value
MCSOSLST	36	40
MCSOSTOR	0	
MCSOSYSE	0	
MCSOTBL	0	
MCSOTSYS	4	
MCSOUNKN	3E	40
MCSOUNKY	3E	80
MCSOVRDN	3D	40
MCSOVRDY	3D	80

MCT Information

MCT Heading Information

Common Name: System Resource Manager Storage Management Control Table
Macro ID: IRAMCT
DSECT Name: MCT (unless DSECT=NO is coded)
Owning Component: System Resource Manager (SC1CX)
Eye-Catcher ID: MCT
Offset: 0
Length: CHAR(4)
Storage Attributes: Subpool: Nucleus
Key: 0
Residency: Nucleus (above 16M line)
Size: 904 bytes
Created by: Assembled into nucleus module IRARMCNS
Pointed to by: RMCTMCT field of the RMCT data area
Serialization: SRM lock
Function: Contains storage management control information for use by SRM storage management modules

MCT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	936	MCT	STORAGE CONTROL TABLE
0	(0)	CHARACTER	4	MCTMCT	ACRONYM IN EBCDIC -MCT-
				Comment	
				STORAGE CONTROL CONSTANTS POINTERS TO SHORTAGE MESSAGES	
				End of Comment	
4	(4)	ADDRESS	4	MCCMS100	SQA SHORTAGE MESSAGE ADDRESS
8	(8)	ADDRESS	4	MCCMS101	CRITICAL SQA SHORTAGE MSG ADDR
12	(C)	ADDRESS	4	MCCMS102	SQA SHORTAGE RELIEVED MSG ADDR
16	(10)	UNSIGNED	8	MCTSLOTSALLOCATED	Number of allocated slots at the last STASM invocation
24	(18)	BITSTRING	8	MCTIRA265IISSUETIME	When the last IRA265I message got issued (RmctxToc format)
				Comment	
				Preferred Storage Fields	
				End of Comment	
32	(20)	BITSTRING	1	MCTPREFFLAGS	Preferred storage related flags
		1...		MCTPREFENF55ISSUED	ENF 55 pref. shortage signal issued
		.111 111.		*	reserved
		1		MCTPREFSHORTAGE	Currently in a preferred storage shortage
33	(21)	UNSIGNED	1	MCTPREFSHORTAGETYPE	Shortage type of the preferred storage shortage
34	(22)	UNSIGNED	1	MCTPREFSHORTAGECOUNT	Number of RM1 Cycles we are in a preferred storage shortage
35	(23)	UNSIGNED	1	*	reserved
36	(24)	SIGNED	4	*	reserved
40	(28)	BITSTRING	8	MCTPREFSHORTAGETIME	Time when the preferred storage shortage occurred
48	(30)	ADDRESS	4	MCCMS500	SWAP IN FAIL USER MESSAGE ADDRESS
52	(34)	ADDRESS	4	MCCMS501	PTR TO SWAP IN MSG
				Comment	
				STORAGE CONTROL CONSTANTS	
				End of Comment	
56	(38)	SIGNED	2	MCCPLUS	AVAILABLE FRAME QUEUE DELTA FOR STEALING

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					Field MCCSTLCT was only used in COMPAT Mode and hence is made to a reserved field now
					End of Comment
58	(3A)	SIGNED	2	*	Reserved
60	(3C)	SIGNED	2	*	Reserved
62	(3E)	SIGNED	2	MCCSIPRT	TIME BETWEEN PAGE-IN RATE CALCULATIONS
64	(40)	SIGNED	2	MCCDFRPC	DOUBLE FRAME REPLENISH VALUE
66	(42)	SIGNED	2	MCCDFREC	DOUBLE FRAME RELEASE VALUE
68	(44)	SIGNED	2	MCCSPCPT	System Paging Cost Percentage threshold for determining if any address spaces should be monitored
70	(46)	SIGNED	2	MCCAPCPT	Address Space Page Cost Percent threshold for determining if this address space should be monitored
72	(48)	ADDRESS	4	MCCDUMP	DUMPSRV OUCB ADDRESS
					Comment
					Lengths used by SMF ----------
					End of Comment
76	(4C)	UNSIGNED	2	MCCICSPL	Length of IRAICSP.ICSP
78	(4E)	UNSIGNED	2	MCCRQSVL	Length of IRARQSRV.RQSV
80	(50)	UNSIGNED	2	MCCE39PL	Length of IRAE39P.E39P
82	(52)	UNSIGNED	2	MCCRSDL	Length of IRARSD.RSD
					Comment

					End of Comment
84	(54)	UNSIGNED	2	MCCOUCBL	Length of IRAOUCB.OUCB
86	(56)	SIGNED	2	MCTDIRECTPOFACTOR	Alot of memory factor for DirectPo
88	(58)	SIGNED	2	MCCFXBPR	Fixed Below High Percentage Threshold
					Comment
					Field MCCSIGRS was only used in COMPAT Mode and hence is made to a reserved field now
					End of Comment
90	(5A)	SIGNED	2	*	Reserved
					Comment
					AUX Storage Monitoring Fields
					End of Comment
92	(5C)	UNSIGNED	2	*	Reserved
94	(5E)	UNSIGNED	1	MCTVIRTSHORTAGELEVEL	Virtual storage shortage level
95	(5F)	BITSTRING	1	MCTASMFL	Auxilliary Storage Flags
		1...		MCTVIRTCULPRITPROCESSINGNEEDED	Virtual storage culprit processing needed
		.1..		MCTDASDW	DASD Warning level
		..1.		MCTDASD1	DASD 1. level shortage
		...1		MCTDASD2	DASD 2. level shortage. Not yet used
		 1111		*	Reserved
96	(60)	SIGNED	4	MCTAVERAGEVIRTINCREASE	Average virtual storage increase
100	(64)	SIGNED	4	*	Reserved
104	(68)	SIGNED	2	MCCASMT1	FIRST AUX SHORTAGE THRESHOLD
106	(6A)	SIGNED	2	MCCASMT2	SECOND AUX SHORTAGE THRESHOLD
108	(6C)	CHARACTER	0	MCCEND	END OF MCT CONSTANTS
					Comment
					STORAGE CONTROL VARIABLES
					End of Comment

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
108	(6C)	BITSTRING	1	MCVSIFLG	STORAGE ISOLATION FLG
		1...		MCVSIPG	ADS STG ISOL IN EFFECT
		.1..		MCVSICM	CMN STG ISOL IN EFFECT
		..1.		MCVSIWS	CMN STORAGE PROTECTED BY WORKING SET SIZE
		...1		MCVSIPI	CMN STORAGE PROTECTED BY PAGE IN RATE
		 1111		*	RESERVED
109	(6D)	UNSIGNED	1	MCCLIPT	Large increase percentage threshold for fixed below pages
110	(6E)	UNSIGNED	1	MCCASPCT	% OF AUX STORAGE TO RECOMMEND
111	(6F)	UNSIGNED	1	MCCRSPCT	% OF REAL STORAGE TO RECOMMEND
112	(70)	SIGNED	4	MCVSTGPT	TIME OF PREVIOUS STGTEST CALL
116	(74)	SIGNED	2	MCVSIPL	CMN LOW PAGE-IN RATE
118	(76)	SIGNED	2	MCVSIPH	CMN HIGH PAGE-IN RATE
120	(78)	SIGNED	4	MCVSIBP	CMN BASE PAGE-IN CNT
124	(7C)	UNSIGNED	4	MCVSIBT	Base TOD value for page-in rate calculations. In PR1 we will check, if current time-MCVSIBT is less than MCCSIPRT. IF yes, page-in rates will be recalculated @64BITSRM
128	(80)	SIGNED	2	MCVSIPR	CMN RECENT PAGEIN RATE
130	(82)	SIGNED	2	*	Reserved
132	(84)	SIGNED	2	MCVSTCRI	HIGHEST SYSTEM UIC
134	(86)	SIGNED	2	MCVFRCNT	SAVED AVAILABLE EXPANDED STORAGE OK THRESHOLD (RCEAECOK)
136	(88)	SIGNED	4	MCVAVQC	COUNT OF AVQLOWS
140	(8C)	UNSIGNED	4	MCVNWSMB	policy interval base for rcenwsf
144	(90)	UNSIGNED	4	MCVWSMB	policy interval base for rcewsdne

Comment

Percentage of frames of MCCAFCLO/OK that is "reserved"
for frames below 16MB (or below 2GB). Used in IRASETNP
and IRARMRMR.

End of Comment

148	(94)	UNSIGNED	1	MCCBELOWPERCENTAGE	Percentage of frames of MCCAFCLO/OK that is "reserved" for frames below 16MB
149	(95)	UNSIGNED	1	MCCABOVEPERCENTAGE	Percentage of frames of MCCAFCLO/OK that is "reserved" for frames below 2GB
150	(96)	UNSIGNED	1	*	Reserved
151	(97)	UNSIGNED	1	MCTAFQCOUNT	Number of RM1 Cycles we have an AFQ shortage
152	(98)	UNSIGNED	4	MCVESWB	policy interval base for rceeswrt

Comment

Fixed threshold values for the number of frames below 16MB(2GB)
If the actual number of frames falls below this threshold,
RM1 (in IRARMRMR) will count that as a "shortage samples" (below)
If an appropriate number of such samples is found in an RM2
interval, RM2 will bump up RCE thresholds that will cause RSM to
exchange frames

End of Comment

156	(9C)	UNSIGNED	2	MCCBELOWSHORTAGETHRESHOLD	Below 16MB = 50
158	(9E)	UNSIGNED	2	MCCABOVESHORTAGETHRESHOLD	Between 16MB,2GB=100

Comment

Number of samples taken in an RM2 interval where we found available
frames below 16MB (2GB) below threshold above.
The ShortageSamplesLM fields count the # of shortage samples
during the last minute. If SRM doesnt find any samples
in one minute, we will reduce the RCE thresholds again in
RM2 (IRARMRMR)

End of Comment

160	(A0)	SIGNED	2	MCVBELOWSHORTAGESAMPLES	Number of samples with amount of available frames below MCCBelowShortageThreshold value
162	(A2)	SIGNED	2	MCVBELOWSHORTAGESAMPLESLM	Count of RM2 intervals with samplecount above=0
164	(A4)	SIGNED	2	MCVABOVESHORTAGESAMPLES	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Number of samples with amount of available frames below MCCAboveShortageThreshold value
166	(A6)	SIGNED	2	MCVABOVESHORTAGESAMPLESLM	Count of RM2 intervals with samplecount above=0
				Comment	
				MEMORY CONTROL FLAGS	
				End of Comment	
168	(A8)	BITSTRING	1	MCTSFLGS	FLAGS MODIFIED UNDER SALLOC LCK
		1...		MCTSQA1	SQA FIRST LEVEL SHORTAGE
		.1..		MCTSQA2	SQA SECOND LEVEL SHORTAGE
		..1.		MCTAVQ1	AVQ BELOW LIMIT
		...1		MCTASMW	ASM Warning level
		.... 1...		MCTAFQENF55ISSUED	ENF 55 AFQ shortage issued
		.... .11.		*	RESERVED @ME24614C
		1		MCTHIGHPAGINGRATE	RADRV found a high paging rate @ME24614A
169	(A9)	BITSTRING	1	MCTOFLGS	FLAGS MODIFIED UNDER SRM LOCK
		1...		MCTASM1	ASM FIRST LEVEL SHORTAGE
		.1..		MCTASM2	ASM SECOND LEVEL SHORTAGE
		..1.		MCTASMA	ASM Appl. Warning
		...1		MCTAMS2	Obsolete use MCTASM2
		.... 1...		MCTSMS1	SQA FIRST LEVEL MESSAGE
		.... .1..		MCTSMS2	SQA SECOND LEVEL MESSAGE
		.... ..1.		MCTFX1	FIX PG 1ST LEVEL MSG
		1		MCTFX2	FIX PG 2ND LEVEL MSG
170	(AA)	BITSTRING	1	MCTOFLG1	MORE FLAGS - SRM LOCK
		1...		MCTFXA	FIX Appl. Warning
		.1..		MCTNDISP	Set nsw address spaces non dispatchable in case of a pageable storage shortage
		..1.		MCTWTOR	Present a WTOR, when the system is in a critical pageable / AUX shortage
		...1		MCTSQAE	SQA EXPANDED MSG
		.... 1...		MCTFXMPL	REDUCE MPL TO RELIEVE SHORTAGE
		.... .1..		MCTWTORAUTO	Present 20 messages instead of 5 on the IRA420I/IRA220D message
		.... ..1.		MCTLGAVQ	LOGICAL AVQLOW LEVEL 1
		1		MCTSCBT	STOLE CMN BELOW THRES
171	(AB)	BITSTRING	1	MCTCFLGS	FLAGS TURNED ON UNDER SALLOC LOCK & OFF UNDER SRM LOCK
		1...		MCTSHORT	A shortage exists
		.1..		MCTRLSHT	All of real pageable storage shortage.
		..1.		MCTB16SH	Below the line pageable storage shortage.
		...1		MCTDRSHT	All of real and DREF pageable storage shortage
		.... 1...		MCTPVTI	PVT FIELDS INITIALIZED
		.... .1..		MCTRSVB5	reserved
		.... ..1.		MCTRSVB6	reserved
		1		MCTB2GSH	Between 16M and 2G pageable storage shortage @64BITSRM
172	(AC)	ADDRESS	4	MCCMS103	SQA EXPANDED MSG ADDR
				Comment	
				TIME INTERVAL VALUES FOR PR1 INVOCATION	
				End of Comment	
176	(B0)	UNSIGNED	4	MCVTMINQ	TIME PR1 LAST RAN IN QUEUE
180	(B4)	SIGNED	2	MCCFXUIC	FIXED FRAME SHORTAGE UIC THRESHOLD
182	(B6)	SIGNED	2	MCVCHUIC	HIGHEST UIC FOR CURRENTLY ALLOCATED COMMON AREA FRAMES
184	(B8)	SIGNED	2	MCVPVTRI	HIGHEST UIC - PVT AREA
186	(BA)	SIGNED	2	MCVHUICE	UIC of oldest frame in expanded
188	(BC)	SIGNED	2	MCCLSWUP	TSO LOGICAL SWAP WORKING SET ADJUSTMENT FACTOR
190	(BE)	SIGNED	2	MCVDFPGC	DEFERRED PAGE REQ CT
192	(C0)	SIGNED	4	MCVOLDEO	Value of RCEAECOK, with amount by which RCEAECOK has been raised for swap-in subtracted out. Maintained in RM2 so that dynamic adjustment for expanded storage thresholds can be performed correctly.
196	(C4)	SIGNED	2	MCCNCLIM	UIC LIMIT FOR NON-SWAPPABLES AND COMMON BEFORE ADJUSTING UPDATE INTERVAL
198	(C6)	SIGNED	2	MCCNCDEL	DELTA IN NON-SWAPPABLES AND COMMON UICS FOR INCREASING UPDATE INTERVAL
200	(C8)	SIGNED	2	MCCNCMAX	MAXIMUM INTERVAL BETWEEN UPDATES FOR NON-SWAPPABLES AND COMMON
202	(CA)	SIGNED	2	MCCSWLIM	UIC LIMIT FOR SWAPPABLES BEFORE ADJUSTING UPDATE INTERVAL
204	(CC)	SIGNED	2	MCCSWDEL	DELTA IN SWAPPABLES UICS FOR INCREASING UPDATE INTERVAL

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
206	(CE)	SIGNED	2	MCCSWMAX	MAXIMUN INTERVAL BETWEEN UPDATES FOR SWAPPABLES
208	(D0)	SIGNED	4	MCVSMXCT	SWAPPABLE MAXIMUM COUNT
212	(D4)	SIGNED	4	MCVINC	PR1 INTERVAL COUNT FOR SWAPPABLES
216	(D8)	SIGNED	4	MCVCURCT	PR1 INTERVAL COUNT FOR COMMON AND NON-SWAPPABLES
220	(DC)	SIGNED	4	MCVMAXCT	NON-SWAPPABLES AND COMMON MAXIMUM COUNT
224	(E0)	SIGNED	2	MCCSIWDL	% OF WORKING SET SIZE TWSS IS TO BE LOWERED BY
226	(E2)	SIGNED	2	MCCSIWDI	% OF WORKING SET SIZE TWSS IS TO BE INCREASED BY
228	(E4)	SIGNED	4	MCCSIETH	EXEC TIME THRESHOLD FOR PAGING RATE CALCULATE
232	(E8)	SIGNED	4	MCVMIGB	BASE MIGRATION COUNT
236	(EC)	SIGNED	4	MCCR19	reserved
240	(F0)	SIGNED	4	MCCR20	reserved
				Comment	
				Field MCCASCB was only used in COMPAT Mode and hence is made to a reserved field now	
				End of Comment	
244	(F4)	ADDRESS	4	*	Reserved
248	(F8)	ADDRESS	4	MCCMS104	SQA NO LONGER EXPANDED MESSAGE ADDRESS
				Comment	
				THE FOLLOWING TWO FIELDS ARE USED TO INITIALIZE THE RCE THRESHOLDS THAT CONTROL PAGE REPLACEMENT. THEY ARE ALSO USED TO CONTROL SWAP IN FAIL PROCESSING	
				End of Comment	
252	(FC)	UNSIGNED	4	MCCAVQTH	AVAIL FRAME LOW THRESHOLD
252	(FC)	SIGNED	2	MCCAFCLO	AVAIL FRAME QUEUE LOW THRESHOLD
254	(FE)	SIGNED	2	MCCAFCOK	AVAIL FRAME QUEUE OK THRESHOLD
256	(100)	SIGNED	4	MCCUICTH	MIN TIME BEFORE UIC UPDATING
260	(104)	SIGNED	4	MCCFXTM1	FIXED FRAME SHORTAGE TIME THRESHOLD
264	(108)	SIGNED	4	MCCFXTM2	FIXED FRAME SHORTAGE TIME THRESHOLD
268	(10C)	SIGNED	4	MCVCSACV	PREV GDACSACV VALUE
272	(110)	SIGNED	2	MCCDEFFX	DEFER FIX THRESHOLD (not used in z/Arch mode)
				Comment	
				EXTENDED REAL CONSTANTS	
				End of Comment	
274	(112)	SIGNED	2	MCCFXTPR	% All of real storage threshold Note: In ESAME mode, it is also used as % Between 16M and 2G lines storage threshold @64BITSRM
276	(114)	SIGNED	2	MCCFXEPR	% Below 16M line storage threshold
278	(116)	SIGNED	2	MCTAFCINCREASE	AFC increase to drive steal
280	(118)	SIGNED	2	MCCMEDUP	MEDIAN FIXED FRAME COUNT ADJUSTMENT UP
282	(11A)	SIGNED	2	MCCMEDDN	MEDIAN FIXED FRAME COUNT ADJUSTMENT DOWN
284	(11C)	SIGNED	4	MCTUPPERFXSTHR	Upper pageable storage threshold at which SRM stops to use the percentage values (default = 64G)
288	(120)	SIGNED	4	MCCMAXFX	All of real shortage threshold count
292	(124)	SIGNED	4	MCCRELCR	All of real critical shortage threshold count
296	(128)	SIGNED	4	MCCB16CR	Below the line critical shortage threshold count
300	(12C)	SIGNED	4	MCCRELOK	All of real OK threshold
304	(130)	SIGNED	4	MCCB16OK	Below the line OK threshold
				Comment	
				EXTENDED REAL VARIABLES	
				End of Comment	
308	(134)	SIGNED	4	MCVSBFXC	FIX CNT ACCUMULATOR
312	(138)	SIGNED	2	MCVSBFXA	AVE FIX % BELOW 16MEG
314	(13A)	SIGNED	2	MCVSBLTF	LONG TERM FIX %
316	(13C)	SIGNED	2	MCVMEDFC	MEDIAN FIX FRAME COUNT - READY USERS
318	(13E)	SIGNED	2	MCVASMCT	Samples taken for verification of ASM counts in RM1
320	(140)	SIGNED	4	MCVCAPWS	CAP WORKAREA - Pages reserved for swap in of primary working set and DREF frames from aux
324	(144)	SIGNED	2	MCCMS6L	MS6 INTERVAL LOWER LIMIT
326	(146)	SIGNED	2	MCVSWUPD	SWAP COUNTER UPDATE CTR

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
328	(148)	SIGNED	4	MCVOLDEL	Value of RCEAECLO, with amount by which RCEAECLO has been raised for swap-in subtracted out. Maintained in RM2 so that dynamic adjustment for expanded storage thresholds can be performed correctly.
				Comment	
				EXTENDED STORAGE VARIABLES	
				End of Comment	
332	(14C)	SIGNED	4	MCVMGAGE	EXTENDED STORE MIGRATION AGE
336	(150)	SIGNED	4	MCVPR9TG	PR9 TARGET FOR TRIM AND BUILDING SECONDARY WORKING SETS
340	(154)	ADDRESS	4	MCVPR5OU	OUCBPTR FOR PR5 WHEN REAL THRESHOLDS RAISED
344	(158)	UNSIGNED	4	MCVMGTME	WAITING-FOR-MIGRATOR TIME STAMP
348	(15C)	UNSIGNED	4	MCVWRAPS	SAVED VALUE OF RCEWRAPS
352	(160)	SIGNED	4	MCVMGCNT	SRM MIGRATE TIME COUNTER
356	(164)	SIGNED	4	MCVSECWS	Pages reserved for swap in of secondary working set and DREF frames from aux
360	(168)	BITSTRING	8	MCTSTMACREATETIME	Time when the STMA was created (RmctxToc format)
368	(170)	SIGNED	4	MCTRSV03	reserved was MCVDEFSE
372	(174)	SIGNED	4	MCCDEFAM	MULTIPLIER OF AFCOK THRESHOLD TO DETERMINE TARGET NUMBER OF FRAMES TO KEEP AVAILABLE
376	(178)	SIGNED	4	MCCMGTEX	MIGRATOR TIME EXCEEDED THRESHOLD
380	(17C)	SIGNED	4	MCCSWPET	EFRAMES RESERVED FOR PAGEOUTS BY SWAPS
384	(180)	SIGNED	2	MCCETGHT	EXTENDED STORE LOW THRESHOLD MULTIPLIER TO DETERMINE IF EXTENDED STORE IS TIGHT
386	(182)	SIGNED	2	MCCAECLO	AVAIL EXTENDED FRAME QUEUE LOW THRESHOLD
388	(184)	SIGNED	2	MCCAECOK	AVAIL EXTENDED FRAME QUEUE OK THRESHOLD
390	(186)	SIGNED	2	MCCESBMP	SWAP TO EXTENDED WORKING SET ADJUSTMENT
392	(188)	SIGNED	2	MCCPPSBF	PAGABLE PAGE/SEGMENT BUFF
394	(18A)	UNSIGNED	1	MCTRSV04	reserved - MCCMINTR
395	(18B)	UNSIGNED	1	MCCSIGTR	MIN AMOUNT OF FRAMES AN A.S. CAN HOLD AND BE SIGNIFICANT ENOUGH TO ATTEMPT A TRIM
396	(18C)	SIGNED	2	MCCSWUPT	SWAP CTR UPDATE THRESHOLD
398	(18E)	UNSIGNED	1	MCCB2GHI	High threshold for % of time running out of Central below 2G (Note: field actually is in tenth of a percent!!)
399	(18F)	UNSIGNED	1	MCCFSIDI	% OF WORKING SET SIZE TWSS IS INCREASED FOR FWA USERS
400	(190)	UNSIGNED	1	MCCES0LO	Low threshold for % of time running out of EXTENDED or CENTRAL (Note: field actually is in tenth of a percent!!)
401	(191)	UNSIGNED	1	MCCES0HI	High threshold for % of time running out of Central. (Note: field actually is in tenth of a percent!!)
402	(192)	SIGNED	2	MCVES0CT	MCTESTO0 SAMPLE COUNTER
404	(194)	SIGNED	2	MCVESSCT	EXTENDED STORE THRESHOLDS SAMPLE COUNTER
406	(196)	SIGNED	2	MCCESSTH	EXTENDED STORE THRESHOLDS SAMPLE THRESHOLD
408	(198)	SIGNED	4	MCVAECMN	MINIMUM RCEAEC SAMPLED
412	(19C)	BITSTRING	1	MCTEFLGS	EXTENDED STORE BITS
		1...		MCTMIGCN	MIGCNSTR SYSEVENT ISSUED
		.1..		MCTOVRMX	AT LEAST ONE STOR ISOL ADDR SP OVER MAX WSS EXISTS
		..1.		MCTOVRSI	OVERRIDE STOR ISOL IN MIGRATION
		...1		MCTESNA	EXTENDED STORE NOT AVAILABLE
		.... 1...		MCTESTO0	THE AMOUNT OF AVAILABLE EXTENDED STORAGE WENT TO 0
		.... .1..		MCTRSVB7	reserved
		.... ..1.		MCTEUICS	PR1 HAS BEEN SCHEDULED TO BUILD THE EXPANDED UIC BUCKETS
		1		MCTMIGTU	MIGCNSTR tried to unprotect storage but the migrator still could not meet its goal
413	(19D)	BITSTRING	1	MCTEFLG1	FLAGS MODIFIED UNDER SRM LOCK
		1...		MCTREDRIVEREQUEST	Redrive a REALSWAP, TRANSWAP or RSTORFL request
		.1..		MCTRSVB1	reserved
		..1.		MCTPEND	REALSWAP or TRANSWAP pending or fixed storage shortage
		...1		MCTRSVB2	reserved
		.... 1...		MCTSTORAGEMONITORING	Storage monitoring flag
		.... .1..		MCTSTORAGEMONITORINGAREAALLOCATED	Storage monitoring area allocated
		.... ..1.		*	RESERVED
		1		MCTDUMPACTIVE	MCVRELDL value lowered, because SVC Dump in progress
414	(19E)	SIGNED	2	MCCMXRIS	Maximum amount by which real storage thresholds should be raised on behalf of the DREF pages of an address space that is being considered for swap-in.
416	(1A0)	ADDRESS	4	MCCMS700	VECTOR WAIT MESSAGE ADDR
420	(1A4)	SIGNED	4	MCTSDUMPINIT	SDump reserved space, which gets added to the available frame queue.
424	(1A8)	SIGNED	4	MCVRELDL	SDump reserved space, which is currently added to the available frame queue targets

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
428	(1AC)	SIGNED	4	MCVMSGPT	Pointer to message stack
432	(1B0)	SIGNED	4	MCVDOMQP	Pointer to the DOM queue
436	(1B4)	SIGNED	4	MCCUICMX	UIC value to determine if stealing should be attempted instead of trimming
440	(1B8)	SIGNED	4	MCVFPT	Fixed and DREF shortage threshold count
444	(1BC)	SIGNED	4	MCCDRFCR	Fixed and DREF critical shortage threshold count
448	(1C0)	SIGNED	4	MCCDRFOK	Fixed and DREF OK threshold
452	(1C4)	SIGNED	4	MCVTWSS	TARGET WSS FOR COMMON
456	(1C8)	SIGNED	4	MCVSIWL	CMN LOW WSS SPECIFICATION
460	(1CC)	SIGNED	4	MCVSIWH	CMN HI WSS SPECIFICATION
464	(1D0)	SIGNED	4	MCVFMCT	CMN EFFECTIVE FRAME COUNT
468	(1D4)	SIGNED	4	MCCR23	
472	(1D8)	SIGNED	4	*	reserved
476	(1DC)	SIGNED	4	MCCMAXSW	Target maximum number of frames to be swapped for an address space
480	(1E0)	SIGNED	4	MCVAVQLT	TIME (RRPATOD) OF LAST LEVEL 1 AVQLOW SYSEVENT
484	(1E4)	SIGNED	4	MCCTOSEC	TWO SECOND VALUE TO FORCE STEAL OVERRIDE
488	(1E8)	SIGNED	2	MCCSTGT	STGTEST TIME INTERVAL (1 SEC)
490	(1EA)	SIGNED	2	MCCFXBCO	CUT-OFF AMOUNT OF FIXED BELOW FRAMES WHICH ARE STEALABLE
492	(1EC)	SIGNED	4	MCVBYTCT	PREVIOUS BYTE COUNT OF RESERVE STORAGE
496	(1F0)	SIGNED	4	MCVBLKCT	PREVIOUS BLOCK COUNT OF RESERVE STORAGE
500	(1F4)	ADDRESS	4	MCVSWPAS	Address of user picked for MS2 in QSCECMP and REALSWAP
504	(1F8)	SIGNED	4	MCVOLDCL	The "base" low central storage threshold component
508	(1FC)	SIGNED	4	MCVOLDCO	The "base" OK central storage threshold component
512	(200)	SIGNED	4	MCVPTLMT	Processor threshold raising limit
516	(204)	SIGNED	4	MCVLSD	Number of logical swap discretionary frames in the system compat mode only
520	(208)	UNSIGNED	4	MCVMINDF	This is the minimum error between ASM system pageouts in progress received count and ASM pageouts in progress completed count
524	(20C)	SIGNED	4	MCVPGINS	Pages reserved for swap-ins from aux, not already included in primary or secondary working sets
528	(210)	SIGNED	4	MCVAVAIL	The number of Central and Expanded available frames for input to IRARMTSI
532	(214)	SIGNED	4	MCVUIC1	total number of UIC bucket 1 frames in the system
536	(218)	SIGNED	4	MCVUIC2	total number of UIC bucket 2 frames in the system
540	(21C)	SIGNED	4	MCVUIC3	total number of UIC bucket 3 frames in the system
544	(220)	SIGNED	4	MCVUIC4	total number of UIC bucket 4 frames in the system
548	(224)	SIGNED	4	MCVUIC1S	total number of UIC bucket 1 frames in the system adjusted by storage isolation
552	(228)	SIGNED	4	MCVUIC2S	total number of UIC bucket 2 frames in the system adjusted by storage isolation
556	(22C)	SIGNED	4	MCVUIC3S	total number of UIC bucket 3 frames in the system adjusted by storage isolation
560	(230)	SIGNED	4	MCVUIC4S	total number of UIC bucket 4 frames in the system adjusted by storage isolation
564	(234)	SIGNED	4	MCVUIC1C	number of UIC bucket 1 frames in the common area adjusted by storage isolation
568	(238)	SIGNED	4	MCVUIC2C	number of UIC bucket 2 frames in the common area adjusted by storage isolation
572	(23C)	SIGNED	4	MCVUIC3C	number of UIC bucket 3 frames in the common area adjusted by storage isolation
576	(240)	SIGNED	4	MCVUIC4C	number of UIC bucket 4 frames in the common area adjusted by storage isolation
580	(244)	SIGNED	2	MCTRSTORFLCANCELTIME	Max. time that a RstorFl request is allowed to be redriven in case of failure
582	(246)	SIGNED	2	MCCCBBT	Central/Fixed Below 16 meg Balance Threshold
584	(248)	SIGNED	4	MCVEUIC1	expanded storage uic bucket 1
588	(24C)	SIGNED	4	MCVEUIC2	expanded storage uic bucket 2
592	(250)	SIGNED	4	MCVEUIC3	expanded storage uic bucket 3
596	(254)	SIGNED	4	MCVEUIC4	expanded storage uic bucket 4
600	(258)	SIGNED	4	MCVEXWSD	Count of discretionary expanded storage frames
604	(25C)	SIGNED	4	MCVEUICC	Count of invocations of pr1 used to determine when to build expanded uic buckets
608	(260)	SIGNED	4	MCVMXEUC	Number of pr1 invocation before expanded uic buckets are built
612	(264)	UNSIGNED	4	MCVCMPIB	Base value for common pages paged in from aux (base for RCECOMPI)
616	(268)	UNSIGNED	2	MCVCUBD1	UIC bucket delimiter. Used to calculate the UIC buckets.
618	(26A)	UNSIGNED	2	MCVCUBD2	UIC bucket delimiter. Used to calculate the UIC buckets.
620	(26C)	UNSIGNED	2	MCVCUBD3	UIC bucket delimiter. Used to calculate the UIC buckets.
622	(26E)	SIGNED	2	MCVCS0CT	Number of times available frame queue went to 0
624	(270)	SIGNED	2	MCVCSSCT	Central storage threshhold sample counter
626	(272)	SIGNED	2	MCVSHUIC	High UIC value for shared frames
628	(274)	SIGNED	4	MCVAFCMN	MINIMUM RCEAFC SAMPLED
632	(278)	SIGNED	4	MCVSTWSS	Protective processor storage target for shared area
636	(27C)	SIGNED	4	MCVSAUXB	Base for RceSgAux, count of shared area aux slots
640	(280)	SIGNED	4	MCVSUIC1	Shared area central UIC bucket 1
644	(284)	SIGNED	4	MCVSUIC2	Shared area central UIC bucket 2
648	(288)	SIGNED	4	MCVSUIC3	Shared area central UIC bucket 3
652	(28C)	SIGNED	4	MCVSUIC4	Shared area central UIC bucket 4
656	(290)	SIGNED	4	MCVSEUC1	Shared area expanded UIC bucket 1
660	(294)	SIGNED	4	MCVSEUC2	Shared area expanded UIC bucket 2
664	(298)	SIGNED	4	MCVSEUC3	Shared area expanded UIC bucket 3
668	(29C)	SIGNED	4	MCVSEUC4	Shared area expanded UIC bucket 4

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
672	(2A0)	UNSIGNED	4	*	reserved
676	(2A4)	UNSIGNED	4	*	reserved
				Comment	
				ESAME EXTENSIONS @64BITSRM	
				End of Comment	
680	(2A8)	SIGNED	2	MCCUICUP	Interval (in seconds) at which UIC update processing is taking place. In ESA mode the value will be 1, in ESAME mode the value is 10. Note that in ESAME mode not necessarily all address space UICs are updated in one run. SRM may release the SRM lock in between and continue UIC update processing later @64BITSRM
682	(2AA)	SIGNED	2	MCCCONBT	UIC value, below which frames maybe stolen from CASTOUT(NO) hiperspaces (ESAME mode only) @64BITSRM
684	(2AC)	SIGNED	4	MCCEMDIT	Maximum disabled time for STEAL and UIC Update processing before opening an enabled window in ESAME mode (srm_time units: 01x=1.024 milliseconds). The value is set in IEAVNP10 dependant on the mode, but the value will be used in ESAME mode only @64BITSRM
688	(2B0)	SIGNED	4	MCCMAXBT	Between 16M and 2G pageable storage shortage threshold count (ESAME mode only) @64BITSRM
692	(2B4)	SIGNED	4	MCCB2GCR	Between 16M and 2G pageable storage critical shortage threshold count (ESAME mode only) @64BITSRM
696	(2B8)	SIGNED	4	MCCB2GOK	Between 16M and 2G pageable storage OK threshold count (ESAME mode only) @64BITSRM
700	(2BC)	SIGNED	2	MCCQDRSV	Percentage of quad frame groups to be kept free. SRM invokes RSM quad frame steal processing if less than 5% (MCCQDRSV) of the number of allocated quad frame groups is available. (ESAME mode only) @64BITSRM
702	(2BE)	SIGNED	2	*	reserved
704	(2C0)	SIGNED	4	MCVOLDQF	Number of quad frame groups on the last change of RCEAFCLO/OK in RM2. Used by RM2 to adjust RCEAFCLO/OK and MCVOLDCL/CO
				Comment	
				High Virtual Shared Area Monitoring and Large Frame flags	
				End of Comment	
708	(2C4)	BITSTRING	1	MCTSHMF	Monitoring flags
		1...		MCTSHM1	First level msg issued
		.1..		MCTSHM2	Second level msg issued@WLMP64V
		..1.		MCTCOM1	First level msg issued
		...1		MCTCOM2	Second level msg issued@LCOM64A
		.... 1...		MCTLRG1	First level msg issued
		.... .1..		MCTLRG2	Second level msg issued@LLAPAGC
		.... ..1.		MCTSCM1	First level msg issued
		1		MCTSCM2	Second level msg issued not yet used
709	(2C5)	BITSTRING	1	MCTTARGETINCREASED	
		1...		MCTAFQINCREASED	The AFQ targets where just increased
		.1..		MCTBELOWAFQINCREASED	The AFQ below targets where just increased
		..1.		MCTABOVEAFQINCREASED	The AFQ above targets where just increased
		...1		MCTPRESTEALINCREASED	The PreSteal targets where just increased
		.... 111.		*	reserved
		1		MCTDRIVESTORAGETARGETADJUSTMENT	Force target recalculation
710	(2C6)	SIGNED	2	MCTTRIMUIC	UIC where trimming starts
				Comment	
				these counters are needed to manage the PreSteal targets	
				End of Comment	
712	(2C8)	SIGNED	2	MCTPRESTEALSAMPLESLOW	PreSteal samples where RCEAFC was below RCEDEFFX
714	(2CA)	SIGNED	2	MCTPRESTEALSAMPLESTOTAL	All PreSteal samples taken
716	(2CC)	SIGNED	4	MCTPRESTOLENMN	Minimum RCENumberOfPreStolenFrm average
720	(2D0)	UNSIGNED	4	MCTPRESTOLENLOW	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
					Low target for PreSteal
724	(2D4)	UNSIGNED	4	MCTPRESTOLENOK	
					OK target for PreSteal
728	(2D8)	UNSIGNED	2	MCTPRESTEALSAMPLETIME	
					Count for RM2 intervals
730	(2DA)	UNSIGNED	2	MCTPRESTEALSAMPLETARGET	
					in 10th of a percent
				Comment	
				UIC distribution percentage fields	
				End of Comment	
732	(2DC)	SIGNED	4	MCTUICDISTPERC1	
					Contains the frame distribution percentage for bucket 1
736	(2E0)	SIGNED	4	MCTUICDISTPERC2	
					Contains the frame distribution percentage for bucket 2
740	(2E4)	SIGNED	4	MCTUICDISTPERC3	
					Contains the frame distribution percentage for bucket 3
744	(2E8)	SIGNED	4	MCTUICDISTPERC4	
					Contains the frame distribution percentage for bucket 4
				Comment	
				Auxiliary Storage Management fields	
				End of Comment	
748	(2EC)	SIGNED	4	*	Reserved
752	(2F0)	UNSIGNED	2	MCTIRA205ITARGET	
					Percentage at which target the message gets issued
				Comment	
				System UIC fields	
				End of Comment	
754	(2F2)	UNSIGNED	2	MCTMINSYSTEMUIC	
					Minimum System UIC
756	(2F4)	UNSIGNED	2	MCTCURSYSTEMUIC	
					Current System UIC
758	(2F6)	UNSIGNED	2	MCTMAXSYSTEMUIC	
					Maximum System UIC
760	(2F8)	SIGNED	4	MCTSWAPSIZE	Maximum working set size SRM will swap
764	(2FC)	SIGNED	2	MCTREALSWAPCANCELTIME	
					Max. time that a REALSWAP or TRANSWAP request is allowed to be redriven in case of failure
766	(2FE)	UNSIGNED	1	MCTFIXEDSTORAGESHORTAGETYPE	
					Contains the shortage type, which is also issued in message IRA400E/IRA401E
767	(2FF)	UNSIGNED	1	MCTSTORAGETARGETADJUSTMENT	
					RM2 cycle count, used for storage target adjustments
768	(300)	SIGNED	4	MCTBELOWAFQMN	
					Minimum RCEPBAFC plus RCENBAFCC average
772	(304)	SIGNED	4	MCTABOVEAFQMN	
					Minimum RCEPAAFC plus RCENAAFCC average
				Comment	
				Fields for WTOR IRA420D processing	
				End of Comment	
776	(308)	SIGNED	4	MCTWTOR1ECB	ECB for WTOR 1
780	(30C)	SIGNED	4	MCTWTOR2ECB	ECB for WTOR 2
784	(310)	ADDRESS	4	MCTFSAA	Pointer to FSAA
788	(314)	ADDRESS	4	MCTASAA	Pointer to ASAA
792	(318)	BITSTRING	4	MCTWTORFLAGS	WTOR processing flags
792	(318)	BITSTRING	1	MCTWTOR1FLAGS	
					WTOR 1 processing flags
		1...		MCTWTOR1MASTERACTIVE	
					WTOR active indicator
		.1..		MCTWTOR1START	
					Start Request
		..1.		MCTWTOR1STOP	Stop Request
		...1 111.		*	reserved

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1		MCTWTOR1DELAY	
					Delay the next WTOR
793	(319)	BITSTRING	1	MCTWTOR2FLAGS	
					WTOR 2 processing flags
		1...		MCTWTOR2MASTERACTIVE	
					WTOR active indicator
		.1..		MCTWTOR2START	
					Start Request
		..1.		MCTWTOR2STOP	Stop Request
		...1 111.		*	reserved
		1		MCTWTOR2DELAY	
					Delay the next WTOR
794	(31A)	BITSTRING	2	MCTWTOR3FLAGS	
					reserved
				Comment	
				Global Storage Management percentages	
				End of Comment	
796	(31C)	UNSIGNED	1	MCTRELIEVEPERC	
					relieve percentage
797	(31D)	UNSIGNED	1	MCTAPPLPERC	Aapplication percentage
798	(31E)	UNSIGNED	2	*	reserved
				Comment	
				Warning level percentage ...	
				End of Comment	
800	(320)	UNSIGNED	1	MCTWARNINGB16PERC	
					below 16M
801	(321)	UNSIGNED	1	MCTWARNINGB2GPERC	
					between 16M and 2G
802	(322)	UNSIGNED	1	MCTWARNINGTOTPERC	
					all real
803	(323)	UNSIGNED	1	MCTWARNINGDRFPERC	
					all real and DREF
				Comment	
				Warning level in frames ...	
				End of Comment	
804	(324)	SIGNED	4	MCTWARNINGB16	
					below 16M
808	(328)	SIGNED	4	MCTWARNINGB2G	
					between 16M and 2G
812	(32C)	SIGNED	4	MCTWARNINGTOT	
					all real
816	(330)	SIGNED	4	MCTWARNINGDRF	
					all real and DREF
820	(334)	UNSIGNED	2	*	reserved
822	(336)	UNSIGNED	1	MCTFIXEDSHORTAGELEVEL	
					Fixed storage shortage level
823	(337)	BITSTRING	1	MCTFIXEDFL	Fixed Storage Flags
		1...		MCTFIXEDCULPRITPROCESSINGNEEDED	
					Fixed storage culprit processing needed
		.111 1111		*	Reserved
824	(338)	SIGNED	4	MCTAVERAGEFIXEDINCREASE	
					Average virtual storage increase
828	(33C)	SIGNED	4	MCTFRAMESFIXED	
					Number of frames fixed at the last STFXS invocation
				Comment	
				Fixed storage management timestamps @LHIAUXA	
				End of Comment	
832	(340)	BITSTRING	8	MCTIRA405IISSUETIME	
					When the last IRA405I message got issued (RmctxToc format)
840	(348)	BITSTRING	8	MCTFXSAPPLWARNINGTIME	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					When we entered the Appl warning processing the last time (RmctxToc format)@LHIAUXA
848	(350)	BITSTRING	8	MCTFXSSHORTAGETIME	When we entered the normal shortage processing the last time (RmctxToc format)@LHISTOA
856	(358)	BITSTRING	8	MCTCRITFXSSHORTAGETIME	When we entered the crit. shortage processing the last time (RmctxToc format)@LHISTOA
					Comment
					Auxiliary storage management timestamps
					End of Comment
864	(360)	BITSTRING	8	MCTIRA205IISSUETIME	When the last IRA205I message got issued (RmctxToc format)
872	(368)	BITSTRING	8	MCTAUXAPPLWARNINGTIME	When we entered the Appl warning processing the last time (RmctxToc format)@LHIAUXA
880	(370)	BITSTRING	8	MCTAUXSHORTAGETIME	When we entered the normal shortage processing the last time (RmctxToc format)@LHIAUXA
888	(378)	BITSTRING	8	MCTCRITAUXSHORTAGETIME	When we entered the crit. shortage processing the last time (RmctxToc format)@LHIAUXA
					Comment
					Available Frame Queue Shortage timestamp
					End of Comment
896	(380)	BITSTRING	8	MCTAFQSHORTAGETIME	When we saw the last time a AFQ shortage (RmctxToc format)
					Comment
					Application Warning level in frames ...
					End of Comment
904	(388)	SIGNED	4	MCTAPPLWARNB16	below 16M
908	(38C)	SIGNED	4	MCTAPPLWARNB2G	between 16M and 2G
912	(390)	SIGNED	4	MCTAPPLWARNTOT	all real
916	(394)	SIGNED	4	MCTAPPLWARNDRF	all real and DREF
					Comment
					Application Warning relieve level in frames ...
					End of Comment
920	(398)	SIGNED	4	MCTAPPLWARNRELB16	below 16M
924	(39C)	SIGNED	4	MCTAPPLWARNRELB2G	between 16M and 2G
928	(3A0)	SIGNED	4	MCTAPPLWARNRELTOT	all real
932	(3A4)	SIGNED	4	MCTAPPLWARNRELDRF	all real and DREF
936	(3A8)	CHARACTER	0	MCTEND	END OF MCT End of this block

MCT Cross Reference

Name	Hex Offset	Hex Value
MCCABOVEPERCENTAGE	95	
MCCABOVESHORTAGETHRESHOLD	9E	
MCCAECLO	182	
MCCAECOK	184	
MCCAFCLO	FC	
MCCAFCOK	FE	
MCCAPCPT	46	
MCCASMT1	68	
MCCASMT2	6A	
MCCASPCT	6E	
MCCAVQTH	FC	
MCCBELOWPERCENTAGE	94	
MCCBELOWSHORTAGETHRESHOLD	9C	
MCCB16CR	128	
MCCB16OK	130	
MCCB2GCR	2B4	
MCCB2GHI	18E	
MCCB2GOK	2B8	
MCCCBBT	246	
MCCCONBT	2AA	
MCCDEFAM	174	
MCCDEFFX	110	
MCCDFREC	42	
MCCDFRPC	40	
MCCDRFCR	1BC	
MCCDRFOK	1C0	
MCCDUMP	48	
MCCEMDIT	2AC	
MCCEND	6C	
MCCESBMP	186	
MCCESSTH	196	
MCCES0HI	191	
MCCES0LO	190	
MCCETGHT	180	
MCCE39PL	50	
MCCFSIDI	18F	
MCCFXBCO	1EA	
MCCFXBPR	58	
MCCFXEPR	114	
MCCFXTM1	104	
MCCFXTM2	108	
MCCFXTPR	112	
MCCFXUIC	B4	
MCCICSPL	4C	
MCCLIPT	6D	
MCCLSWUP	BC	
MCCMAXBT	2B0	
MCCMAXFX	120	
MCCMAXSW	1DC	
MCCMEDDN	11A	
MCCMEDUP	118	
MCCMGTEX	178	
MCCMS100	4	
MCCMS101	8	
MCCMS102	C	
MCCMS103	AC	
MCCMS104	F8	
MCCMS500	30	
MCCMS501	34	
MCCMS6L	144	
MCCMS700	1A0	
MCCMXRIS	19E	
MCCNCDEL	C6	
MCCNCLIM	C4	
MCCNCMAX	C8	
MCCOUCBL	54	
MCCPLUS	38	
MCCPPSBF	188	
MCCQDRSV	2BC	
MCCRELCR	124	
MCCRELOK	12C	
MCCRQSVL	4E	
MCCRSDL	52	
MCCRSPCT	6F	
MCCR19	EC	
MCCR20	F0	
MCCR23	1D4	
MCCSIETH	E4	
MCCSIGTR	18B	
MCCSIPRT	3E	
MCCSIWDI	E2	
MCCSIWDL	E0	
MCCSPCPT	44	
MCCSTGT	1E8	
MCCSWDEL	CC	
MCCSWLIM	CA	
MCCSWMAX	CE	
MCCSWPET	17C	
MCCSWUPT	18C	
MCCTOSEC	1E4	
MCCUICMX	1B4	
MCCUICTH	100	
MCCUICUP	2A8	
MCT	0	
MCTABOVEAFQINCREASED	2C5	20
MCTABOVEAFQMN	304	
MCTAFCINCREASE	116	
MCTAFQCOUNT	97	
MCTAFQENF55ISSUED	A8	08
MCTAFQINCREASED	2C5	80
MCTAFQSHORTAGETIME	380	
MCTAMS2	A9	10
MCTAPPLPERC	31D	
MCTAPPLWARNB16	388	
MCTAPPLWARNB2G	38C	
MCTAPPLWARNDRF	394	
MCTAPPLWARNRELB16	398	
MCTAPPLWARNRELB2G	39C	
MCTAPPLWARNRELDRF	3A4	
MCTAPPLWARNRELTOT	3A0	
MCTAPPLWARNTOT	390	
MCTASAA	314	
MCTASMA	A9	20
MCTASMFL	5F	
MCTASMW	A8	10
MCTASM1	A9	80
MCTASM2	A9	40
MCTAUXAPPLWARNINGTIME	368	
MCTAUXSHORTAGETIME	370	
MCTAVERAGEFIXEDINCREASE	338	
MCTAVERAGEVIRTINCREASE	60	
MCTAVQ1	A8	20

Name	Hex Offset	Hex Value
MCTBELOWAFQINCREASED	2C5	40
MCTBELOWAFQMN	300	
MCTB16SH	AB	20
MCTB2GSH	AB	01
MCTCFLGS	AB	
MCTCOM1	2C4	20
MCTCOM2	2C4	10
MCTCRITAUXSHORTAGETIME	378	
MCTCRITFXSSHORTAGETIME	358	
MCTCURSYSTEMUIC	2F4	
MCTDASDW	5F	40
MCTDASD1	5F	20
MCTDASD2	5F	10
MCTDIRECTPOFACTOR	56	
MCTDRIVESTORAGETARGETADJUSTMENT	2C5	01
MCTDRSHT	AB	10
MCTDUMPACTIVE	19D	01
MCTEFLGS	19C	
MCTEFLG1	19D	
MCTEND	3A8	
MCTESNA	19C	10
MCTESTO0	19C	08
MCTEUICS	19C	02
MCTFIXEDCULPRITPROCESSINGNEEDED	337	80
MCTFIXEDFL	337	
MCTFIXEDSHORTAGELEVEL	336	
MCTFIXEDSTORAGESHORTAGETYPE	2FE	
MCTFRAMESFIXED	33C	
MCTFSAA	310	
MCTFXA	AA	80
MCTFXMPL	AA	08
MCTFXSAPPLWARNINGTIME	348	
MCTFXSSHORTAGETIME	350	
MCTFX1	A9	02
MCTFX2	A9	01
MCTHIGHPAGINGRATE	A8	01
MCTIRA205IISSUETIME	360	
MCTIRA205ITARGET	2F0	
MCTIRA265IISSUETIME	18	
MCTIRA405IISSUETIME	340	
MCTLGAVQ	AA	02
MCTLRG1	2C4	08
MCTLRG2	2C4	04
MCTMAXSYSTEMUIC	2F6	
MCTMCT	0	
MCTMIGCN	19C	80
MCTMIGTU	19C	01
MCTMINSYSTEMUIC	2F2	
MCTNDISP	AA	40
MCTOFLGS	A9	
MCTOFLG1	AA	
MCTOVRMX	19C	40
MCTOVRSI	19C	20
MCTPEND	19D	20
MCTPREFENF55ISSUED	20	80
MCTPREFFLAGS	20	
MCTPREFSHORTAGE	20	01
MCTPREFSHORTAGECOUNT	22	
MCTPREFSHORTAGETIME	28	
MCTPREFSHORTAGETYPE	21	
MCTPRESTEALINCREASED	2C5	10
MCTPRESTEALSAMPLESLOW	2C8	
MCTPRESTEALSAMPLESTOTAL	2CA	
MCTPRESTEALSAMPLETARGET	2DA	
MCTPRESTEALSAMPLETIME	2D8	
MCTPRESTOLENLOW	2D0	
MCTPRESTOLENMN	2CC	
MCTPRESTOLENOK	2D4	
MCTPVTI	AB	08
MCTREALSWAPCANCELTIME	2FC	
MCTREDRIVEREQUEST	19D	80
MCTRELIEVEPERC	31C	
MCTRLSHT	AB	40
MCTRSTORFLCANCELTIME	244	
MCTRSVB1	19D	40
MCTRSVB2	19D	10
MCTRSVB5	AB	04
MCTRSVB6	AB	02
MCTRSVB7	19C	04
MCTRSV03	170	
MCTRSV04	18A	
MCTSCBT	AA	01
MCTSCM1	2C4	02
MCTSCM2	2C4	01
MCTSDUMPINIT	1A4	
MCTSFLGS	A8	
MCTSHMF	2C4	
MCTSHM1	2C4	80
MCTSHM2	2C4	40
MCTSHORT	AB	80
MCTSLOTSALLOCATED	10	
MCTSMS1	A9	08
MCTSMS2	A9	04
MCTSQAE	AA	10
MCTSQA1	A8	80
MCTSQA2	A8	40
MCTSTMACREATETIME	168	
MCTSTORAGEMONITORING	19D	08
MCTSTORAGEMONITORINGAREAALLOCATED	19D	04
MCTSTORAGETARGETADJUSTMENT	2FF	
MCTSWAPSIZE	2F8	
MCTTARGETINCREASED	2C5	
MCTTRIMUIC	2C6	
MCTUICDISTPERC1		

Name	Hex Offset	Hex Value
	2DC	
MCTUICDISTPERC2		
	2E0	
MCTUICDISTPERC3		
	2E4	
MCTUICDISTPERC4		
	2E8	
MCTUPPERFXSTHR		
	11C	
MCTVIRTCULPRITPROCESSINGNEEDED		
	5F	80
MCTVIRTSHORTAGELEVEL		
	5E	
MCTWARNINGB16		
	324	
MCTWARNINGB16PERC		
	320	
MCTWARNINGB2G		
	328	
MCTWARNINGB2GPERC		
	321	
MCTWARNINGDRF		
	330	
MCTWARNINGDRFPERC		
	323	
MCTWARNINGTOT		
	32C	
MCTWARNINGTOTPERC		
	322	
MCTWTOR	AA	20
MCTWTORAUTO	AA	04
MCTWTORFLAGS	318	
MCTWTOR1DELAY		
	318	01
MCTWTOR1ECB	308	
MCTWTOR1FLAGS		
	318	
MCTWTOR1MASTERACTIVE		
	318	80
MCTWTOR1START		
	318	40
MCTWTOR1STOP	318	20
MCTWTOR2DELAY		
	319	01
MCTWTOR2ECB	30C	
MCTWTOR2FLAGS		
	319	
MCTWTOR2MASTERACTIVE		
	319	80
MCTWTOR2START		
	319	40
MCTWTOR2STOP	319	20
MCTWTOR3FLAGS		
	31A	
MCVABOVESHORTAGESAMPLES		
	A4	
MCVABOVESHORTAGESAMPLESLM		
	A6	
MCVAECMN	198	
MCVAFCMN	274	
MCVASMCT	13E	
MCVAVAIL	210	
MCVAVQC	88	
MCVAVQLT	1E0	
MCVBELOWSHORTAGESAMPLES		
	A0	
MCVBELOWSHORTAGESAMPLESLM		
	A2	
MCVBLKCT	1F0	
MCVBYTCT	1EC	
MCVCAPWS	140	
MCVCHUIC	B6	
MCVCMPIB	264	
MCVCSACV	10C	
MCVCSSCT	270	
MCVCS0CT	26E	
MCVCUBD1	268	
MCVCUBD2	26A	
MCVCUBD3	26C	
MCVCURCT	D8	
MCVDFPGC	BE	
MCVDOMQP	1B0	
MCVESSCT	194	
MCVESWB	98	
MCVES0CT	192	
MCVEUICC	25C	
MCVEUIC1	248	
MCVEUIC2	24C	
MCVEUIC3	250	
MCVEUIC4	254	
MCVEXWSD	258	
MCVFMCT	1D0	
MCVFPT	1B8	
MCVFRCNT	86	
MCVHUICE	BA	
MCVINC	D4	
MCVLSD	204	
MCVMAXCT	DC	
MCVMEDFC	13C	
MCVMGAGE	14C	
MCVMGCNT	160	
MCVMGTME	158	
MCVMIGB	E8	
MCVMINDF	208	
MCVMSGPT	1AC	
MCVMXEUC	260	
MCVNWSMB	8C	
MCVOLDCL	1F8	
MCVOLDCO	1FC	
MCVOLDEL	148	
MCVOLDEO	C0	
MCVOLDQF	2C0	
MCVPGINS	20C	
MCVPR5OU	154	
MCVPR9TG	150	
MCVPTLMT	200	
MCVPVTRI	B8	
MCVRELDL	1A8	
MCVSAUXB	27C	
MCVSBFXA	138	
MCVSBFXC	134	
MCVSBLTF	13A	
MCVSECWS	164	
MCVSEUC1	290	
MCVSEUC2	294	
MCVSEUC3	298	
MCVSEUC4	29C	
MCVSHUIC	272	
MCVSIBP	78	
MCVSIBT	7C	
MCVSICM	6C	40
MCVSIFLG	6C	
MCVSIPG	6C	80
MCVSIPH	76	
MCVSIPI	6C	10
MCVSIPL	74	
MCVSIPR	80	
MCVSIWH	1CC	
MCVSIWL	1C8	
MCVSIWS	6C	20
MCVSMXCT	D0	
MCVSTCRI	84	
MCVSTGPT	70	
MCVSTWSS	278	
MCVSUIC1	280	
MCVSUIC2	284	
MCVSUIC3	288	
MCVSUIC4	28C	

Name	Hex Offset	Hex Value
MCVSWPAS	1F4	
MCVSWUPD	146	
MCVTMINQ	B0	
MCVTWSS	1C4	
MCVUIC1	214	
MCVUIC1C	234	
MCVUIC1S	224	
MCVUIC2	218	
MCVUIC2C	238	
MCVUIC2S	228	
MCVUIC3	21C	
MCVUIC3C	23C	
MCVUIC3S	22C	
MCVUIC4	220	
MCVUIC4C	240	
MCVUIC4S	230	
MCVWRAPS	15C	
MCVWSMB	90	

MDB Information

MDB Programming Interface information

Programming Interface information

MDB

End of Programming Interface information

MDB Heading Information

Common Name: Message Data Block (MDB)
Macro ID: IEAVM105
DSECT Name: MDB, MDBG, MDBSCP, or MDBT
Owning Component: Consoles (SC1CK)
Eye-Catcher ID: MDB
Offset: 4
Length: 4
Storage Attributes: Subpool: N/A
Key: N/A
Residency: Message data space for the address space which owns the Extended MCS Console.
Size: 160 bytes
Created by: Various users
Pointed to by: N/A
Serialization: N/A
Function: This is an architected structure consisting of a header and a combination of substructures known as objects (i.e. general, control program and text objects). It is used for message text (WTO/R) and DOMs.

MDB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MDB	START OF MDB HEADER
0	(0)	SIGNED	2	MDBLEN	MDB length
2	(2)	CHARACTER	2	MDBTYPE	MDB type
		1		MDBTYP1	"X'0001'" Type for MDB Type 1
4	(4)	CHARACTER	4	MDBMID	Acronym 'MDB '
8	(8)	BITSTRING	4	MDBVER	Revision code
		1		MDBVER1	"X'00000001'" Revision code 1
8	(8)	X'1'	0	MDBVID	"MDBVER1" Current revision code
8	(8)	X'C'	0	MDBHLEN	"*-MDB" Length of MDB Header section

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MDBG	General Object Section
0	(0)	SIGNED	2	MDBGLEN	General object length
2	(2)	CHARACTER	2	MDBGTYPE	General object type
		1		MDBGOBJ	"X'0001'" Type for general object
4	(4)	BITSTRING	4	MDBGMID (0)	Message ID
4	(4)	CHARACTER	1	MDBGSYID	System ID
5	(5)	BITSTRING	3	MDBGSEQ	Sequence Number
8	(8)	CHARACTER	8	MDBGTIMH	Time stamp HH.MM.SS format
16	(10)	CHARACTER	3	MDBGTIMT	Time stamp .TH format
19	(13)	CHARACTER	1	MDBGRSV1	Reserved
20	(14)	CHARACTER	7	MDBGDSTP	Date stamp
27	(1B)	CHARACTER	1	MDBGRSV2	Reserved
28	(1C)	BITSTRING	2	MDBGMFLG	Message flags
28	(1C)	BITSTRING	0	MDBGDOM	"X'8000'" DOM bit. If this bit is on it indicates that this MDB is for a DOM. The DOM information can be found in the DOM flags in the control program object (MDBDOMFL)
28	(1C)	BITSTRING	0	MDBGALRM	"X'4000'" Sound warning alarm (processor controller only)
28	(1C)	BITSTRING	0	MDBGHOLD	"X'2000'" Hold bit, Hold message until DOMed or deleted via other external means
30	(1E)	CHARACTER	2	MDBGRSV3	Reserved

Comment

The constants for the following fields can be found in section
PRESENTATION ATTRIBUTES EQUATES

End of Comment

32	(20)	BITSTRING	4	MDBGFGPA (0)	Foreground presentation attributes
32	(20)	BITSTRING	1	MDBGFCON	Foreground control field
33	(21)	BITSTRING	1	MDBGFCOL	Foreground color field
34	(22)	BITSTRING	1	MDBGFHIL	Foreground highlighting
35	(23)	BITSTRING	1	MDBGFINT	Foreground intensity
36	(24)	BITSTRING	4	MDBGBGPA (0)	Background presentation attributes
36	(24)	BITSTRING	1	MDBGBCON	Background control field
37	(25)	BITSTRING	1	MDBGBCOL	Background color field

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
38	(26)	BITSTRING	1	MDBGBHIL	Background highlighting
39	(27)	BITSTRING	1	MDBGBINT	Background intensity
40	(28)	CHARACTER	8	MDBGOSNM	Originating system name
48	(30)	CHARACTER	8	MDBGJBNM	Job name

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MDBSCP	Control Program Section
0	(0)	SIGNED	2	MDBCLEN	Control program object length
2	(2)	CHARACTER	2	MDBCTYPE	Object type
		.... ..1.		MDBCOBJ	"X'0002'" Type for control prog object
4	(4)	CHARACTER	16	MDBCPROD (0)	Originating system identifier
4	(4)	BITSTRING	4	MDBCVER	MVS CP object version level
8	(8)	CHARACTER	4	MDBCPNAM	Control Program name ("MVS")
12	(C)	CHARACTER	8	MDBCFMID	FMID of originating system
		1		MDBCVER1	"X'00000001'" MVS CP object version 1
		.... ..1.		MDBCVER2	"X'00000002'" JBB4422 object version 2
		11		MDBCVER3	"X'00000003'" OY65627 object version 3
		.... .1..		MDBCVER4	"X'00000004'" HBB5510 object version 4
		.... .1.1		MDBCVER5	"X'00000005'" HBB5520 object version 5
		...1		MDBCV10	"X'00000010'" Structurally equivalent ot HBB5520 with OW20064 (430)
		..1.		MDBCV20	"X'00000020'" Structurally equivalent ot HBB5520 with OW20064 (510)
		..11		MDBCV30	"X'00000030'" HBB5520 object with OW20064
		.1.1		MDBCV50	"X'00000050'" HBB7750 level
		.111		MDBCV70	"X'00000070'" HBB7770 level
12	(C)	X'70'	0	MDBCVID	"MDBCV70" Current MVS CP object version
12	(C)	X'E5E240'	0	MDBCMVS	"C'MVS '" Control Program name
20	(14)	CHARACTER	16	MDBCERC	Routing codes 1st bit = Route Code 1 2nd bit = Route Code 2 . . . 128th bit = Route Code 128
36	(24)	CHARACTER	2	MDBCDESC (0)	Descriptor codes
36	(24)	CHARACTER	1	MDBDESC1	Descriptor codes byte 1
		1...		MDBDESCA	"X'80'" System failure
		.1..		MDBDESCB	"X'40'" Immediate action required
		..1.		MDBDESCC	"X'20'" Eventual action required
		...1		MDBDESCD	"X'10'" System status
		.... 1...		MDBDESCE	"X'08'" Immediate command response
		.... .1..		MDBDESCF	"X'04'" Job status
		.... ..1.		MDBDESCG	"X'02'" Application program/processor
		1		MDBDESCH	"X'01'" Out-of-line
37	(25)	CHARACTER	1	MDBDESC2	Descriptor codes byte 2
		1...		MDBDESCI	"X'80'" Operator's request
		.1..		MDBDESCJ	"X'40'" Reserved
		..1.		MDBDESCK	"X'20'" Critical eventual action
		...1		MDBDESCL	"X'10'" Important Information
		.... 1...		MDBDESCM	"X'08'" Previously automated
		.... .1..		MDBDESCN	"X'04'" Reserved
		.... ..1.		MDBDESCO	"X'02'" Reserved
		1		MDBDESCP	"X'01'" Reserved
38	(26)	CHARACTER	2	MDBCMLVL (0)	Message level
38	(26)	CHARACTER	1	MDBMLVL1	Message level byte 1
		1...		MDBMLR	"X'80'" WTOR
		.1..		MDBMLIA	"X'40'" Immediate action
		..1.		MDBMLCE	"X'20'" Critical eventual action
		...1		MDBMLE	"X'10'" Eventual action
		.... 1...		MDBMLI	"X'08'" Informational
		.... .1..		MDBMLBC	"X'04'" Broadcast
		.... ..1.		MDBMLRSG	"X'02'" Reserved
		1		MDBMLRSH	"X'01'" Reserved
39	(27)	CHARACTER	1	MDBMLVL2	Message level byte 2
		1...		MDBMLRSI	"X'80'" Reserved
		.1..		MDBMLRSJ	"X'40'" Reserved
		..1.		MDBMLRSK	"X'20'" Reserved
		...1		MDBMLRSL	"X'10'" Reserved
		.... 1...		MDBMLRSM	"X'08'" Reserved
		.... .1..		MDBMLRSN	"X'04'" Reserved
		.... ..1.		MDBMLRSO	"X'02'" Reserved
		1		MDBMLRSP	"X'01'" Reserved
40	(28)	CHARACTER	2	MDBCATTR (0)	Message Attribute flags
40	(28)	CHARACTER	1	MDBCATT1	First byte of attributes
		1...		MDBCSUPP	"X'80'" Message is suppressed
		.1..		MDBCMCSC	"X'40'" Message is command response
		..1.		MDBCAUTH	"X'20'" Message issued by authorized program

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		MDBCRETN	"X'10'" Message is retained by AMRF
		.... 1...		MDBCSPVD	"X'08'" WQE Backlog Message
		.... .1..		MDBCQNLY	"X'04'" Console only
41	(29)	CHARACTER	1	MDBCATT2	Second byte of attributes
42	(2A)	SIGNED	2	MDBCRSV7	Reserved
44	(2C)	SIGNED	2	MDBCRSV5	Reserved
46	(2E)	SIGNED	2	MDBCASID	ASID of issuer
48	(30)	ADDRESS	4	MDBCTCB	Job Step TCB for issuer
52	(34)	BITSTRING	4	MDBCTOKN	Token (for DOM)
56	(38)	BITSTRING	1	MDBCSYID	System ID (for DOM)
57	(39)	BITSTRING	1	MDBDOMFL	DOM flags
		1...		MDBDMSGI	"X'80'" DOM by message id (can be found in MDBGMID)
		.1..		MDBDSYSI	"X'40'" DOM by system ID
		..1.		MDBDASID	"X'20'" DOM by ASID
		...1		MDBDJTCB	"X'10'" DOM by job step TCB
		.... 1...		MDBDTOKN	"X'08'" DOM by token
		.... .1..		MDBDNORM	"X'04'" This is a Normal DOM
58	(3A)	BITSTRING	1	MDBCMISC	Miscellaneous Routing Info
		1...		MDBCRSV2	"X'80'" Reserved. Was MDBCUD
		.1..		MDBCRSV3	"X'40'" Reserved. Was MDBCFUDO
		..1.		MDBRSV18	"X'20'" Reserved - was MDBCFIDO (Queue by ID only)
		...1		MDBCAUT	"X'10'" Queue by automation
		.... 1...		MDBCHC	"X'08'" Queue by hardcopy
		.... .1..		MDBCINTC	"X'04'" Receiving INTIDS (Console ID zero)
		.... ..1.		MDBCUNKC	"X'02'" Receiving UNKNIDS (Unknown Console IDs)
59	(3B)	BITSTRING	1	MDBCMSC2	Miscellaneous OPERLOG info
		1...		MDBCOCMD	"X'80'" Echo operator command
		.1..		MDBCICMD	"X'40'" Echo internal command
		..1.		MDBCWTL	"X'20'" Result of WTL macro
		...1		MDBCOPON	"X'10'" MDB has been sent from USS
60	(3C)	CHARACTER	8	MDBCOJID	Originating Job ID
68	(44)	CHARACTER	8	MDBCKEY	Retrieval key (Source: WTO)
76	(4C)	CHARACTER	8	MDBCAUTO	Automation token
84	(54)	CHARACTER	8	MDBCCART	Command and Response Token (Source: WTO)
92	(5C)	CHARACTER	4	MDBCCNID	4-Byte Console ID Note: This console id may not have a console name associated with it. The console id itself may not correspond to a real console. Console ids 00FFFFFFx and 000000FFx are examples of this.
96	(60)	CHARACTER	2	MDBCMSGT (0)	Message type
96	(60)	BITSTRING	1	MDBCMGT1	First byte of message type flags
		1...		MDBMSGTA	"X'80'" Display jobnames
		.1..		MDBMSGTB	"X'40'" Display status
		..1.		MDBMSGTC	"X'20'" Monitor active
		...1		MDBMSGTD	"X'10'" Indicates existence of QID field in WPL (AOS/1)
		.... 1...		MDBRSV13	"X'08'" Reserved
		.... .1..		MDBMSGTF	"X'04'" Monitor SESS
		.... ..1.		MDBRSV14	"X'02'" Reserved
		1		MDBRSV15	"X'01'" Reserved
97	(61)	BITSTRING	1	MDBCMGT2	Second byte of message type flags
98	(62)	SIGNED	2	MDBCRPYL	Reply ID Length
100	(64)	CHARACTER	8	MDBCRPYI	Reply ID (EBCDIC representation)
108	(6C)	CHARACTER	2	MDBCTOFF2	Like MDBCTOFF but allows for non-auth, non-action message
110	(6E)	CHARACTER	2	MDBCTOFF	Offset in the message text field of the beginning of the msg
112	(70)	BITSTRING	4	MDBCRPYB	Reply ID (Binary representation)
116	(74)	CHARACTER	1	MDBCAREA	Area ID
117	(75)	SIGNED	1	MDB_AUTOR_REPLY_LEN	Reply length for auto-reply
118	(76)	BITSTRING	4	MDBCLCNT	Number of lines in message
122	(7A)	CHARACTER	8	MDBCOJBN	Originating job name
130	(82)	CHARACTER	8	MDBCSPLX	Sysplex name
138	(8A)	CHARACTER	4	MDBCXMOD (0)	Copy of request flags (CTXTRFLG) from the WTO user exit interface
138	(8A)	CHARACTER	3	MDBCRFLG (0)	Comm Task user exit requests
138	(8A)	BITSTRING	1	MDBCRFB1	Request flags byte one
		1...		MDBCRCMT	"X'80'" Message text was changed
		.1..		MDBCRCRC	"X'40'" Routing code(s) were changed
		..1.		MDBCRCDC	"X'20'" Descriptor code(s) were changed
		...1		MDBCRQPC	"X'10'" Queued to a particular active console
		.... 1...		MDBRSV17	"X'08'" Reserved - was MDBCRQUN (Queue to a particular console unconditionally)
		.... .1..		MDBCRQRC	"X'04'" Queued by routing codes only
		.... ..1.		MDBRSV16	"X'02'" Reserved - was MDBCRCCN (1-byte console ID was changed)
		1		MDBCRPML	"X'01'" Minor lines were processed
139	(8B)	BITSTRING	1	MDBCRFB2	Request flags byte two
		1...		MDBCRDTM	"X'80'" Message was deleted
		.1..		MDBCROMS	"X'40'" MPF suppression Overrided

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		..1.		MDBCRFHC	"X'20'" Hardcopy forced
		...1		MDBCRNHC	"X'10'" No hardcopy forced
		.... 1...		MDBCRHCO	"X'08'" Only hardcopy forced
		.... .1..		MDBCRBCA	"X'04'" Broadcasted message to active consoles
		.... ..1.		MDBCRBCN	"X'02'" Did not broadcast message to active consoles
		1		MDBCRNRT	"X'01'" AMRF is not to retain this message
140	(8C)	BITSTRING	1	MDBCRFB3	Request flags byte three
		1...		MDBCRRET	"X'80'" AMRF is to retain this message
		.1..		MDBCRCKY	"X'40'" Changed the retrieval key
		..1.		MDBCRCFC	"X'20'" Changed the 4-byte console id
		...1		MDBCRCMF	"X'10'" Changed the message type flags
		.... 1...		MDBCRANO	"X'08'" Automation was not required
		.... .1..		MDBCRAYS	"X'04'" Automation was required and/or automation token updated
		.... ..1.		MDBCQHCO	"X'02'" Message issued hardcopy only
		1		MDBCRSV8	"X'01'" Reserved. Was MDBCHUD
141	(8D)	BITSTRING	1	MDBCSUPB	Suppression byte
		1...		MDBCSNSV	"X'80'" Not serviced by any WTO user exit routine
		.1..		MDBCSEER	"X'40'" A WTO user exit ABENDed while processing this message
		..1.		MDBCSNSI	"X'20'" Not serviced because of an incompatible request
		...1		MDBCSAUT	"X'10'" Indicate automation specified
		.... 1...		MDBC_PROCESSED_BY_MFA	
					"X'08'" Message Flood Automation processed this message
		.... .1..		MDBCSSSI	"X'04'" Suppressed by a subsystem
		.... ..1.		MDBCSWTO	"X'02'" Suppressed by a WTO user exit routine
		1		MDBCSMPF	"X'01'" Suppressed by MPF or Message Flood Automation
142	(8E)	CHARACTER	8	MDBCCNNM	Console name
150	(96)	CHARACTER	2	MDBCMCSF (0)	MCS flags from WPL
150	(96)	BITSTRING	1	MDBMCSF1	First byte of MCS flags
		1...		MDBMCSA	"X'80'" Route/Descriptor code fields present
		.1..		MDBMCSB	"X'40'" Message queued to console id in MDBCCNID
		..1.		MDBMCSC	"X'20'" MCSFLAG=RESP was specified
		...1		MDBMCSD	"X'10'" Message type field exists
		.... 1...		MDBMCSE	"X'08'" MCSFLAG=REPLY was specified
		.... .1..		MDBMCSF	"X'04'" MCSFLAG=BRDCST was specified
		.... ..1.		MDBMCSG	"X'02'" MCSFLAG=HRDCPY was specified
		1		MDBMCSHX	"X'01'" Reserved - meant MCSFLAG=QREG0 was specified
151	(97)	BITSTRING	1	MDBMCSF2	Second byte of MCS flags
		1...		MDBMCSI	"X'80'" MCSFLAG=NOTIME was specified
		.1..		MDBMCSJ	"X'40'" MLWTO indicator
		..1.		MDBMCSK	"X'20'" Primary subsystem use
		...1		MDBMCSL	"X'10'" Extended WPL used
		.... 1...		MDBMCSM	"X'08'" MCSFLAG= CMD was specified
		.... .1..		MDBMCSN	"X'04'" MCSFLAG=NOCPY was specified
		.... ..1.		MDBMCSO	"X'02'" WQEBLK used
152	(98)	SIGNED	2	MDB_AUTOR_DELAY	
					Auto-reply delay time
154	(9A)	CHARACTER	16	MDBCETOD	Time stamp of when message was issued. In STCKE format
170	(AA)	BITSTRING	1	MDB_MISC_FLAGS	
					Misc. Flags
		1...		MDB_AUTOR_DATA_VALID	
					"X'80'" MDB contains valid auto-reply data
		.1..		MDB_AUTOR_DELAY_IN_SEC	
					"X'40'" Auto-reply delay time is in seconds
		..1.		MDB_NO_SYSLOG	
					"X'20'" Copy of WQENSYL (for JES3).
		...1		MDB_WQEJ3B1	"X'10'" Copy of WQEJ3B1 (for JES3).
171	(AB)	CHARACTER	64	MDB_AUTOR_REPLY	
					Auto-reply reply
235	(EB)	CHARACTER	23		Reserved

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MDBT	Start of text object
0	(0)	SIGNED	2	MDBTLEN	Text object length
2	(2)	CHARACTER	2	MDBTTYPE	Text object type
		.... .1..		MDBTOBJ	"X'0004'" Type for message text object
4	(4)	CHARACTER	2	MDBTLNTY (0)	Line type flags - 2 bytes
4	(4)	CHARACTER	1	MDBTLNT1	Line type flags byte 1
		1...		MDBTCONT	"X'80'" Control text
		.1..		MDBTLABT	"X'40'" Label text
		..1.		MDBTDATT	"X'20'" Data text
		...1		MDBTENDT	"X'10'" End text

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.... 1...		MDBTPROT	"X'08'" Prompt text
		.... .1..		MDBTOPTT	"X'04'" Reserved for IBM use
		.... ..1.		MDBTRSV2	"X'02'" Reserved
5	(5)	CHARACTER	1	MDBTLNT2	Line type flags byte 2
		1		MDBTFPAF	"X'01'" Text object presentation attribute field overrides general object presentation attribute field
6	(6)	BITSTRING	4	MDBTMTPA (0)	Presentation attribute
6	(6)	BITSTRING	1	MDBTPCON	Presentation control field
7	(7)	BITSTRING	1	MDBTPCOL	Presentation color field
8	(8)	BITSTRING	1	MDBTPHIL	Presentation highlighting
9	(9)	BITSTRING	1	MDBTPINT	Presentation intensity
9	(9)	X'A'	0	MDBTMSGT	"*" Message text field
9	(9)	X'A'	0	MDBTMBOB	"MDBTMSGT-MDBT" Length of the message text object minus the text field. This can be used to compute the text field length (i.e.MDBTLEN-MDBTMBOB)
				Comment	
				Presentation Attributes Equates CONTROL ATTRIBUTES	
				End of Comment	
		1...		MDBSNALM	"X'80'" Sound note alarm (presentation device's 'beep' alarm)
				Comment	
				COLOR ATTRIBUTES	
				End of Comment	
		1111		MDBBLACK	"X'F0'" Presentation background-black on display, white on printer
		1111 ...1		MDBBLUE	"X'F1'" Color is blue
		1111 ..1.		MDBRED	"X'F2'" Color is red
		1111 ..11		MDBPINK	"X'F3'" Color is pink (magenta)
		1111 .1..		MDBGREEN	"X'F4'" Color is green
		1111 .1.1		MDBTURQ	"X'F5'" Color is turquoise (cyan)
		1111 .11.		MDBYELOW	"X'F6'" Color is yellow
		1111 .111		MDBWHITE	"X'F7'" Presentation neutral-white on display, black on a printer
				Comment	
				HIGHLIGHTING ATTRIBUTES	
				End of Comment	
		....		MDBHNONE	"X'00'" No highlighting is in effect
		1111 ...1		MDBBLINK	"X'F1'" Blinking highlight
		1111 ..1.		MDBRVIDO	"X'F2'" Reverse video highlight
		1111 .1..		MDBUNDER	"X'F4'" Underscore highlight
				Comment	
				INTENSITY ATTRIBUTES	
				End of Comment	
		111. .1..		MDBINORM	"X'E4'" Normal intensity
		111. 1...		MDBIHIGH	"X'E8'" High (bright) intensity

MDB Cross Reference

Name	Hex Offset	Hex Value
MDB	0	
MDB_AUTOR_DATA_VALID		
	AA	80
MDB_AUTOR_DELAY		
	98	
MDB_AUTOR_DELAY_IN_SEC		
	AA	40
MDB_AUTOR_REPLY		
	AB	
MDB_AUTOR_REPLY_LEN		
	75	
MDB_MISC_FLAGS		
	AA	
MDB_NO_SYSLOG		
	AA	20
MDB_WQEJ3B1	AA	10
MDBBLACK	9	F0
MDBBLINK	9	F1
MDBBLUE	9	F1
MDBC_PROCESSED_BY_MFA		
	8D	8
MDBCAREA	74	
MDBCASID	2E	
MDBCATTR	28	
MDBCATT1	28	
MDBCATT2	29	
MDBCAUT	3A	10
MDBCAUTH	28	20
MDBCAUTO	4C	
MDBCCART	54	
MDBCCNID	5C	
MDBCCNNM	8E	
MDBCDESC	24	
MDBCERC	14	
MDBCETOD	9A	
MDBCFMID	C	
MDBCHC	3A	8
MDBCICMD	3B	40
MDBCINTC	3A	4
MDBCKEY	44	
MDBCLCNT	76	
MDBCLEN	0	
MDBCMCSC	28	40
MDBCMCSF	96	
MDBCMGT1	60	
MDBCMGT2	61	
MDBCMISC	3A	
MDBCMLVL	26	
MDBCMSC2	3B	
MDBCMSGT	60	
MDBCMVS	C	E5E240
MDBCOBJ	2	2
MDBCOCMD	3B	80
MDBCOJBN	7A	
MDBCOJID	3C	
MDBCOPON	3B	10
MDBCPNAM	8	
MDBCPROD	4	
MDBCQHCO	8C	2
MDBCQNLY	28	4
MDBCRANO	8C	8
MDBCRAYS	8C	4
MDBCRBCA	8B	4
MDBCRBCN	8B	2
MDBCRCDC	8A	20
MDBCRCFC	8C	20
MDBCRCKY	8C	40
MDBCRCMF	8C	10
MDBCRCMT	8A	80
MDBCRCRC	8A	40
MDBCRDTM	8B	80
MDBCRETN	28	10
MDBCRFB1	8A	
MDBCRFB2	8B	
MDBCRFB3	8C	
MDBCRFHC	8B	20
MDBCRFLG	8A	
MDBCRHCO	8B	8
MDBCRNHC	8B	10
MDBCRNRT	8B	1
MDBCROMS	8B	40
MDBCRPML	8A	1
MDBCRPYB	70	
MDBCRPYI	64	
MDBCRPYL	62	
MDBCRQPC	8A	10
MDBCRQRC	8A	4
MDBCRRET	8C	80
MDBCRSV2	3A	80
MDBCRSV3	3A	40
MDBCRSV5	2C	
MDBCRSV7	2A	
MDBCRSV8	8C	1
MDBCSAUT	8D	10
MDBCSEER	8D	40
MDBCSMPF	8D	1
MDBCSNSI	8D	20
MDBCSNSV	8D	80
MDBCSPLX	82	
MDBCSPVD	28	8
MDBCSSSI	8D	4
MDBCSUPB	8D	
MDBCSUPP	28	80
MDBCSWTO	8D	2
MDBCSYID	38	
MDBCTCB	30	
MDBCTOFF	6E	
MDBCTOFF2	6C	
MDBCTOKN	34	
MDBCTYPE	2	
MDBCUNKC	3A	2
MDBCVER	4	
MDBCVER1	C	1
MDBCVER2	C	2
MDBCVER3	C	3
MDBCVER4	C	4
MDBCVER5	C	5
MDBCVID	C	70
MDBCV10	C	10
MDBCV20	C	20
MDBCV30	C	30
MDBCV50	C	50
MDBCV70	C	70
MDBCWTL	3B	20
MDBCXMOD	8A	
MDBDASID	39	20
MDBDESCA	24	80
MDBDESCB	24	40
MDBDESCC	24	20
MDBDESCD	24	10
MDBDESCE	24	8
MDBDESCF	24	4
MDBDESCG	24	2
MDBDESCH	24	1
MDBDESCI	25	80
MDBDESCJ	25	40
MDBDESCK	25	20
MDBDESCL	25	10
MDBDESCM	25	8
MDBDESCN	25	4
MDBDESCO	25	2
MDBDESCP	25	1
MDBDESC1	24	
MDBDESC2	25	

MDB Cross Reference

Name	Hex Offset	Hex Value
MDBDJTCB	39	10
MDBDMSGI	39	80
MDBDNORM	39	4
MDBDOMFL	39	
MDBDSYSI	39	40
MDBDTOKN	39	8
MDBG	0	
MDBGALRM	1C	4000
MDBGBCOL	25	
MDBGBCON	24	
MDBGBGPA	24	
MDBGBHIL	26	
MDBGBINT	27	
MDBGDOM	1C	8000
MDBGDSTP	14	
MDBGFCOL	21	
MDBGFCON	20	
MDBGFGPA	20	
MDBGFHIL	22	
MDBGFINT	23	
MDBGHOLD	1C	2000
MDBGJBNM	30	
MDBGLEN	0	
MDBGMFLG	1C	
MDBGMID	4	
MDBGOBJ	2	1
MDBGOSNM	28	
MDBGREEN	9	F4
MDBGRSV1	13	
MDBGRSV2	1B	
MDBGRSV3	1E	
MDBGSEQ	5	
MDBGSYID	4	
MDBGTIMH	8	
MDBGTIMT	10	
MDBGTYPE	2	
MDBHLEN	8	C
MDBHNONE	9	0
MDBIHIGH	9	E8
MDBINORM	9	E4
MDBLEN	0	
MDBMCSA	96	80
MDBMCSB	96	40
MDBMCSC	96	20
MDBMCSD	96	10
MDBMCSE	96	8
MDBMCSF	96	4
MDBMCSF1	96	
MDBMCSF2	97	
MDBMCSG	96	2
MDBMCSHX	96	1
MDBMCSI	97	80
MDBMCSJ	97	40
MDBMCSK	97	20
MDBMCSL	97	10
MDBMCSM	97	8
MDBMCSN	97	4
MDBMCSO	97	2
MDBMID	4	
MDBMLBC	26	4
MDBMLCE	26	20
MDBMLE	26	10
MDBMLI	26	8
MDBMLIA	26	40
MDBMLR	26	80
MDBMLRSG	26	2
MDBMLRSH	26	1
MDBMLRSI	27	80
MDBMLRSJ	27	40
MDBMLRSK	27	20
MDBMLRSL	27	10
MDBMLRSM	27	8
MDBMLRSN	27	4
MDBMLRSO	27	2
MDBMLRSP	27	1
MDBMLVL1	26	
MDBMLVL2	27	
MDBMSGTA	60	80
MDBMSGTB	60	40
MDBMSGTC	60	20
MDBMSGTD	60	10
MDBMSGTF	60	4
MDBPINK	9	F3
MDBRED	9	F2
MDBRSV13	60	8
MDBRSV14	60	2
MDBRSV15	60	1
MDBRSV16	8A	2
MDBRSV17	8A	8
MDBRSV18	3A	20
MDBRVIDO	9	F2
MDBSCP	0	
MDBSNALM	9	80
MDBT	0	
MDBTCONT	4	80
MDBTDATT	4	20
MDBTENDT	4	10
MDBTFPAF	5	1
MDBTLABT	4	40
MDBTLEN	0	
MDBTLNTY	4	
MDBTLNT1	4	
MDBTLNT2	5	
MDBTMBOB	9	A
MDBTMSGT	9	A
MDBTMTPA	6	
MDBTOBJ	2	4
MDBTOPTT	4	4
MDBTPCOL	7	
MDBTPCON	6	
MDBTPHIL	8	
MDBTPINT	9	
MDBTPROT	4	8
MDBTRSV2	4	2
MDBTTYPE	2	
MDBTURQ	9	F5
MDBTYPE	2	
MDBTYP1	2	1
MDBUNDER	9	F4
MDBVER	8	
MDBVER1	8	1
MDBVID	8	1
MDBWHITE	9	F7
MDBYELOW	9	F6

MDBP Information

MDBP Programming Interface information

Programming Interface information

MDBP

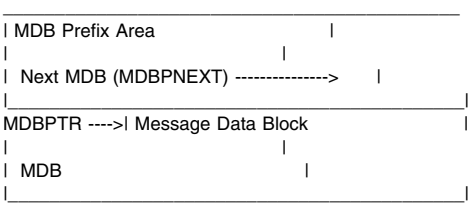
The following field is **NOT** programming interface information:

- MDBPOEXT

End of Programming Interface information

MDBP Heading Information

Common Name: Prefix area for Message Data Block
Macro ID: IEAVG132
DSECT Name: MDBPRFX
Owning Component: Communications Task (SC1CK)
Eye-Catcher ID: MDBP
Offset: 0
Length: 4
Storage Attributes: Subpool: N/A
Key: N/A
Residency: Message data space for the address space which owns the Extended MCS Console.
Size: 28 bytes
Created by: CNZM1ECI
Pointed to by: MDBPTR-LENGTH(MDBPRFX)
This mapping will always immediately
preceed the MDB (IEAVM105)
Serialization: N/A
Function: This prefix area is used to chain
all the MDBs for
a given message together. All MDBs in the message
data space will be prefixed by this control block.



MDBP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MDBPRFX	Prefix area for MDB
0	(0)	SIGNED	4	(0)	
0	(0)	CHARACTER	4	MDBPID	Acronym MDBP
4	(4)	CHARACTER	1	MDBPVER	Version level
4	(4)	X'1'	0	MDBPS410	"1" Version level for SP410
4	(4)	X'1'	0	MDBPVID	"MDBPS410" Current version level
5	(5)	CHARACTER	3		Reserved
8	(8)	ADDRESS	4	MDBPNEXT	Pointer to next MDB for this msg
12	(C)	CHARACTER	12		Reserved
24	(18)	ADDRESS	4	MDBPOEXT	Pointer to reserved extension
24	(18)	X'1C'	0	MDBPLNNO	"*-MDBPRFX" Length of MDBPRFX

MDBP Cross Reference

Name	**Hex Offset**	**Hex Value**
MDBPID	0	
MDBPLNNO	18	1C
MDBPNEXT	8	
MDBPOEXT	18	
MDBPRFX	0	
MDBPS410	4	1
MDBPVER	4	
MDBPVID	4	1

MGCRE Information

MGCRE Heading Information

Common Name: MGCRE parameter list
Macro ID: IEZMGCRE
DSECT Name: MGCEPL
Owning Component: MASTER SCHEDULER (SC1B8)
Eye-Catcher ID: MGCRE
Offset: 4
Length: 5
Storage Attributes: Subpool: ANY
Key: ANY
Residency: ANY
Size: 60 bytes for V1 MGCRE, 96 bytes for V2 MGCRE
128 bytes for MGCETEXT plus storage for optional UTOKEN and/or PTOKEN fields
Created by: Issuers of MGCRE macro
Pointed to by: Register 1 (Set up by the MGCRE macro)
Serialization: None
Function: Serves as a parameter list for the MGCRE macro for SVC 34 command processing.

MGCRE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	96	MGCEPL	MGCRE parameter list
0	(0)	UNSIGNED	1	MGCEFL1	Flag field '00'
1	(1)	UNSIGNED	1	MGCELGH	Flag byte-Control pgm use only@05C
		1...		MGCERTDM	Command is routed by ROUTE *ALL command
		.1..		MGCEOCSP	Original console id specified
		..1.		MGCERUNSYNCH	Run the command synchronously
		...1		MGCEDONOXSYS	Do no xsysmcs calls
		.... 1...		MGCEDOXSYSGETFREE	Do the xsysmcs get and free
		.... .111		*	Reserved

Comment

The MGCELFL field must be exactly mapped by the XACMFLGA field in the XSA.

End of Comment

2	(2)	UNSIGNED	2	MGCELFL	Flag field
2	(2)	UNSIGNED	1	MGCELF1	First byte of flag field
		1...		MGCEEXT	Extended form (MGCRE) parameter list is being used
		.1..		MGCESSI	Subsystem issued the command
		..1.		MGCECMD	Module IEAVC700 issued the command
		...1		MGCEHPY	Suppress hardcopy
		.... 1...		MGCETOK	TOKEN keyword specified
		.... .1..		MGCEIDSP	CONSID keyword specified
		.... ..1.		MGCENMSP	CONSNAME keyword specified
		1		MGCEAUSP	Command authority specified
3	(3)	UNSIGNED	1	MGCELF2	Second byte of flag field
		1...		MGCEFAST	Bypass SSI ,command exits and CMDAUTH
		.1..		MGCENPFX	No prefix processing
		..1.		MGCECTSP	CART keyword specified
		...1		MGCEPASS	RESERVED FOR SP313 COMPATIBILITY
		.... 1...		MGCEUTOK	RESERVED FOR SP313 COMPATIBILITY
		.... .1..		MGCERTD	Command was routed
		.... ..1.		MGCEENBY	RESERVED for APAR rework compatibility
		1		MGCEDFER	Deferred command execution
4	(4)	CHARACTER	5	MGCEACM	Control block acronym 'MGCRE'
9	(9)	UNSIGNED	1	MGCEVRSN	Version level
10	(A)	UNSIGNED	1	MGCELF3	Third byte of flags
		1...		MGCECOAC	Compressed ACEE is present
		.1..		MGCENOBY	Do not bypass RACROUTE for requeued commands
		..1.		MGCETSO	Command issued by TSO user
		...1		MGCEENVR	ENVRIN specified
		 1111		*	Reserved
11	(B)	CHARACTER	1	MGCERES1	Reserved
12	(C)	ADDRESS	4	MGCETXTP	Address of the command text

MGCRE Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
16	(10)	UNSIGNED	4	MGCETOKN	Token
20	(14)	CHARACTER	8	MGCECNNM	Console name
28	(1C)	UNSIGNED	4	MGCECNID	Console id

Comment

The MGCEDISP field must be exactly mapped by the XADISP field in the XSA (IEEXSA).

End of Comment

32	(20)	BITSTRING	1	MGCEDISP	Command disposition
		1...		MGCEDSPA	Command has MASTER authority. It is suggested that MGCEDSPM be used instead
		.1..		MGCEDSPM	Command has Master authority
		..11		*	Reserved for IBM use
		.... 1...		MGCEDSPE	Command issued by ARM
		.... .111		*	Reserved for IBM use

Comment

The MGCEAUTH field must be exactly mapped by the XAAUTH field in the XSA (IEEXSA).

End of Comment

33	(21)	BITSTRING	2	MGCEAUTH	Command authority level
33	(21)	BITSTRING	1	MGCEATHA	Byte one
		1...		MGCEATH1	Command has SYS authority
		.1..		MGCEATH2	Command has I/O authority
		..1.		MGCEATH3	Command has CONS authority
		...1 1111		*	Reserved
34	(22)	BITSTRING	1	MGCEATHB	Reserved
35	(23)	BITSTRING	1	MGCERES2	Reserved
36	(24)	CHARACTER	8	MGCECART	CART
44	(2C)	CHARACTER	8	MGCESYSN	Originating system name
52	(34)	ADDRESS	4	MGCEUTP	Utoken address
56	(38)	UNSIGNED	4	MGCEOCID	Originating console id (use for authority checking
60	(3C)	CHARACTER	0	MGCESZE1	Size of the MGCRE plist up to version 2 without the command text@06C
60	(3C)	ADDRESS	4	MGCEENVP	ENVRIN address
64	(40)	CHARACTER	32	MGCERESZ	Reserved
96	(60)	CHARACTER	0	MGCESZE3	Size of the MGCRE plist up to version 3 without the command text@07C

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	128	MGCETEXT	
0	(0)	SIGNED	2	MGCELEN	Command text length
2	(2)	CHARACTER	126	MGCETXT	Command text
128	(80)	CHARACTER	0	MGCESZE2	Size of command text and length of the command text

MGCRE Constants

Len	Type	Value		Name	Description
5	CHARACTER	MGCRE		MGCENAME	'MGCRE' acronym
1	DECIMAL		1	MGCESP41	Version level
1	DECIMAL		2	MGCEROAL	Version level with OW15497
1	DECIMAL		3	MGCE35671	Version level with OA35671
1	DECIMAL		3	MGCEVRID	Version level

MGCRE Cross Reference

Name	Hex Offset	Hex Value
MGCEACM	4	
MGCEATHA	21	
MGCEATHB	22	
MGCEATH1	21	80
MGCEATH2	21	40
MGCEATH3	21	20
MGCEAUSP	2	01
MGCEAUTH	21	
MGCECART	24	
MGCECMD	2	20
MGCECNID	1C	
MGCECNNM	14	
MGCECOAC	A	80
MGCECTSP	3	20
MGCEDFER	3	01
MGCEDISP	20	
MGCEDONOXSYS	1	10
MGCEDOXSYSGETFREE	1	08
MGCEDSPA	20	80
MGCEDSPE	20	08
MGCEDSPM	20	40
MGCEENBY	3	02
MGCEENVP	3C	
MGCEENVR	A	10
MGCEEXT	2	80
MGCEFAST	3	80
MGCEFL1	0	
MGCEHPY	2	10
MGCEIDSP	2	04
MGCELEN	0	
MGCELFL	2	
MGCELF1	2	
MGCELF2	3	
MGCELF3	A	
MGCELGH	1	
MGCENMSP	2	02
MGCENOBY	A	40
MGCENPFX	3	40
MGCEOCID	38	
MGCEOCSP	1	40
MGCEPASS	3	10
MGCEPL	0	
MGCERESZ	40	
MGCERES1	B	
MGCERES2	23	
MGCERTD	3	04
MGCERTDM	1	80
MGCERUNSYNCH	1	20
MGCESSI	2	40
MGCESYSN	2C	
MGCESZE1	3C	
MGCESZE2	80	
MGCESZE3	60	
MGCETEXT	0	
MGCETOK	2	08
MGCETOKN	10	
MGCETSO	A	20
MGCETXT	2	
MGCETXTP	C	
MGCEUTOK	3	08
MGCEUTP	34	
MGCEVRSN	9	

MGCRPL Information

MGCRPL Programming Interface information

Programming Interface information

MGCRPL

The following fields are **NOT** programming interface information:

- MGCRAUSP
- MGCRCMD
- MGCRCTSP
- MGCRDFER
- MGCRENBY
- MGCRFAST
- MGCRHCPY
- MGCRIDSP
- MGCRNMSP
- MGCRNPFX
- MGCRPASS
- MGCRRTD
- MGCRSSI

End of Programming Interface information

MGCRPL Heading Information

Common Name: MGCR PARAMETER LIST DEFINITION
Macro ID: IEZMGCR
DSECT Name: MGCRPL MGCRPTOK MGCRSTOK
Owning Component: MASTER SCHEDULER (SC1B8)
Eye-Catcher ID: NONE
Storage Attributes: Subpool: ANY
Key: ANY
Residency: ANY
Size: VARIABLE, 5 TO 214 BYTES, DEPENDING ON THE LENGTH OF THE TEXT, PLUS THE PRESENCE OF THE OPTIONAL UTOKEN AND/OR PTOKEN AREAS.
Created by: ISSUERS OF THE MGCR MACRO
Pointed to by: REGISTER 1 (SET UP BY MGCR MACRO)
Serialization: NONE
Function: PARAMETER LIST FOR THE MGCR MACRO.

MGCRPL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MGCRPL	
0	(0)	BITSTRING	1	MGCRFLG1	FLAGS FIELD
		1...		MGCRFI	"BIT0" MGCRFLG2 IS VALID
1	(1)	BITSTRING	1	MGCRLGTH	LENGTH OF BUFFER EXCLUDING TOKENS
2	(2)	BITSTRING	2	MGCRFLG2 (0)	FLAGS FIELD
2	(2)	BITSTRING	1	MGCRFLGA	
		1...		MGCRSV00	"BIT0" RESERVED
		.1..		MGCRSSI	"BIT1" SUBSYSTEM ISSUED COMMAND, FOR CONTROL PROGRAM USAGE ONLY
		..1.		MGCRCMD	"BIT2" IEAVC700 ISSUED COMMAND, FOR CONTROL PROGRAM USAGE ONLY
		...1		MGCRHCPY	"BIT3" SUPPRESS HARDCOPY, FOR CONTROL PROGRAM USAGE ONLY
		.... 1...		MGCRTOK	"BIT4" INDICATES MGCRPTKN CONTAINS A PROGRAM TOKEN
		.... .1..		MGCRIDSP	"BIT5" RESERVED FOR COMPATIBILITY W/ MGCRE
		.... ..1.		MGCRNMSP	"BIT6" RESERVED FOR COMPATIBILITY W/ MGCRE
		1		MGCRAUSP	"BIT7" RESERVED FOR COMPATILIBITY W/ MGCRE
3	(3)	BITSTRING	1	MGCRFLGB	FLAGS FIELD
		1...		MGCRFAST	"BIT0" RESERVED FOR COMPATIBILITY W/ MGCRE
		.1..		MGCRNPFX	"BIT1" RESERVED FOR COMPATIBILITY W/ MGCRE
		..1.		MGCRCTSP	"BIT2" RESERVED FOR COMPATIBILITY W/ MGCRE
		...1		MGCRPASS	"BIT3" COMMAND QUEUED FROM CONSOLXX
		.... 1...		MGCRUTOK	"BIT4" MGCRUTKN CONTAINS A UTOKEN
		.... .1..		MGCRRTD	"BIT5" RESERVED FOR COMPATIBILITY W/ MGCRE
		.... ..1.		MGCRENBY	"BIT6" RESERVED FOR APAR REWORK
		1		MGCRDFER	"BIT7" DEFERRED COMMAND EXECUTION DURING NIP
4	(4)	CHARACTER	126	MGCRTEXT	MAXIMUM SIZE OF COMMAND TEXT
4	(4)	X'82'	0	MGCRLTH	"*-MGCRPL" LENGTH OF COMMAND BUFFER EXCLUDING TOKENS
4	(4)	X'82'	0	MGCREND	"*" END OF COMMAND BUFFER BEFORE TOKENS
4	(4)	X'0'	0	MGCRTOKN	"MGCRPL,4,C'C'" COMPATABILITY WITH PRE-SP3.1.3 ***

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MGCRPTOK	TO MAP THE PROGRAM TOKEN
0	(0)	CHARACTER	4	MGCRPTKN	PROGRAM TOKEN

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MGCRSTOK	TO MAP THE SECURITY TOKEN
0	(0)	CHARACTER	80	MGCRUTKN	SECURITY TOKEN (UTOKEN)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MGCRPL	
0	(0)	X'86'	0	MGCRSIZ	"MGCRLTH+L'MGCRPTKN" MAXIMUM LENGTH OF COMMAND BUFFER EXCLUDING SECURITY TOKEN (UTOKEN)
0	(0)	X'D2'	0	MGCRSIZA	"MGCRLTH+L'MGCRUTKN" MAXIMUM LENGTH OF COMMAND BUFFER EXCLUDING PROGRAM TOKEN

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	X'D6'	0	MGCRSIZB	"MGCRLTH+L'MGCRPTKN+L'MGCRUTKN" MAXIMUM LENGTH OF COMMAND BUFFER INCLUDING BOTH TOKENS

MGCRPL Cross Reference

Name	Hex Offset	Hex Value
MGCRAUSP	2	1
MGCRCMD	2	20
MGCRCTSP	3	20
MGCRDFER	3	1
MGCRENBY	3	2
MGCREND	4	82
MGCRFAST	3	80
MGCRFI	0	80
MGCRFLGA	2	
MGCRFLGB	3	
MGCRFLG1	0	
MGCRFLG2	2	
MGCRHCPY	2	10
MGCRIDSP	2	4
MGCRLGTH	1	
MGCRLTH	4	82
MGCRNMSP	2	2
MGCRNPFX	3	40
MGCRPASS	3	10
MGCRPL	0	
MGCRPL	0	
MGCRPTKN	0	
MGCRPTOK	0	
MGCRRTD	3	4
MGCRSIZ	0	86
MGCRSIZA	0	D2
MGCRSIZB	0	D6
MGCRSSI	2	40
MGCRSTOK	0	
MGCRSV00	2	80
MGCRTEXT	4	
MGCRTOK	2	8
MGCRTOKN	4	0
MGCRUTKN	0	
MGCRUTOK	3	8

MIO Information

MIO Programming Interface Information

Programming Interface Information

MIO

End of Programming Interface Information

MIO Heading Information

Common Name: Message Input/Output Block Mapping Macro
Macro ID: CNLMMIO
DSECT Name: MIO MIOMSG
Owning Component: MVS Message Service (SCMMS)
Eye-Catcher ID: MIO
Offset: 0
Length: 4
Storage Attributes: Subpool: of caller
Key: of caller
Residency: of caller
Size: Variable
Created by: Callers of Translate message (using TRANMSG macro)
Pointed to by: MIO_PTR
Serialization: None required.
Function: Used to map the Message Input/Output Block used for input and output by the Message Translate user function.
Variable length portion consists of one message entry for each line of message text to be processed.

MIO Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MIO	
0	(0)	CHARACTER	4	MIOACRN	ACRONYM "MIO"
4	(4)	BITSTRING	1	MIOVRSN	MIO VERSION NUMBER
5	(5)	CHARACTER	3	MIOLANG	TRANSLATION LANGUAGE
8	(8)	SIGNED	4	MIOSIZE	SIZE OF THIS MIO
12	(C)	ADDRESS	4	MIOBFPTR	ADDRESS OF OUTPUT BUFFER
16	(10)	SIGNED	4	MIOBFSIZ	SIZE OF OUTPUT BUFFER
20	(14)	SIGNED	4	MIOBFUSD	SPACE USED IN OUTPUT BUFFER
24	(18)	SIGNED	4	MIOTRUNC	NUMBER OF MESSAGE TRUNCATED
28	(1C)	SIGNED	4	MIOXLATE	NUMBER OF 1ST MESSAGE TO TRANSLATE
32	(20)	BITSTRING	1	MIOFLAGS	MIO FLAGS
		1...		MIOUXLAT	"X'80'" TRANSLATE INVOCATION
		.1..		MIOUPRMZ	"X'40'" PARAMETERIZE INVOCATION
33	(21)	CHARACTER	3		RESERVED
36	(24)	SIGNED	4	MIOMSGNO	NUMBER OF MESSAGE ENTRIES
40	(28)	SIGNED	4	MIOOFFST	OFFSET TO 1ST MESSAGE ENTRY
44	(2C)	SIGNED	2	MIOMIDL	LENGTH OF MESSAGE IDENTIFIER
46	(2E)	CHARACTER	22	MIOMID	MESSAGE IDENTIFIER
68	(44)	CHARACTER	8		RESERVED
76	(4C)	SIGNED	4	MIOVDATL	LENGTH OF MESSAGE ENTRY AREA
80	(50)	SIGNED	2	MIOVDAT (0)	MIO VARIABLE SECTION

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MIOMSG	MIO MESSAGE ENTRY MAPPING
0	(0)	ADDRESS	4	MIOINPTP	POINTER TO INPUT MTB/MPB/TEXT
4	(4)	ADDRESS	4	MIOBUFFP	POINTER TO OUTPUT MTB/MPB
8	(8)	SIGNED	4	MIOFREAS (0)	FULLWORD REASON CODE
8	(8)	SIGNED	2	MIOMODID	MODULE ID
10	(A)	SIGNED	2	MIOREASN	REASON CODE
12	(C)	BITSTRING	1	MIOINFL	FLAGS FOR INPUT DATA
		1...		MIOXLATF	"X'80'" TRANSLATE THIS INPUT DATA
		.1..		MIOCONT	"X'40'" CONTINUATION OF PREVIOUS MSG
13	(D)	BITSTRING	1	MIOOUTFL	FLAGS FOR OUTPUT DATA
		1...		MIOXLERR	"X'80'" TRANSLATION ERROR
14	(E)	BITSTRING	1	MIOINTFL	INTERNAL FLAGS
		1...		MIOPRMZ	"X'80'" MESSAGE PARAMETERIZED
		.1..		MIOEMBED	"X'40'" EMBEDDED MESSAGE
15	(F)	CHARACTER	1		RESERVED
15	(F)	X'10'	0	MIOMSGL	"*-MIOMSG" LENGTH OF MIOMSG

MIO Cross Reference

Name	Hex Offset	Hex Value
MIO	0	
MIOACRN	0	
MIOBFPTR	C	
MIOBFSIZ	10	
MIOBFUSD	14	
MIOBUFFP	4	
MIOCONT	C	40
MIOEMBED	E	40
MIOFLAGS	20	
MIOFREAS	8	
MIOINFL	C	
MIOINPTP	0	
MIOINTFL	E	
MIOLANG	5	
MIOMID	2E	
MIOMIDL	2C	
MIOMODID	8	
MIOMSG	0	
MIOMSGL	F	10
MIOMSGNO	24	
MIOOFFST	28	
MIOOUTFL	D	
MIOPRMZ	E	80
MIOREASN	A	
MIOSIZE	8	
MIOTRUNC	18	
MIOUPRMZ	20	40
MIOUXLAT	20	80
MIOVDAT	50	
MIOVDATL	4C	
MIOVRSN	4	
MIOXLATE	1C	
MIOXLATF	C	80
MIOXLERR	D	80

MIR Information

MIR Heading Information

Common Name: MIR - Missing Interrupt Logrec Records
Macro ID: IOSDMIR
DSECT Name: MIR
Owning Component: IOS (SC1C3)
Eye-Catcher ID: None
Storage Attributes: Subpool: 245
Key: 0
Residency: Above 16M line
Size: 164 bytes
Created by: IOS Missing Interrupt Handler (IOSRMIHL)
Pointed to by: N/A
Serialization: None
Function: Maps the missing interrupt logrec record.

MIR Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	164	MIR	
0	(0)	CHARACTER	24	MIRHEADR	LOGREC header - See IHAHDR mapping macro for field descriptions. The MIH record type is X'71'.
24	(18)	CHARACTER	140	MIRDATA	MIH record dependent area---------
24	(18)	CHARACTER	8	MIRJOBNM	JOBNAME from ASID initiating I/O request, or blank
32	(20)	CHARACTER	52	MIRSCHIB	Subchannel Information Block, (SCHIB), obtained from the Store Subchannel issued in IOSRMIHP.
84	(54)	CHARACTER	8	MIRINTVL	MIH detection interval (EBCDIC)
				Comment	

				End of Comment	
92	(5C)	CHARACTER	1	MIRTYPE	Type of missing interrupt
				Comment	
					---------- MIH condition being recorded X'80' --- 1... .... - Missing CSCH interrupt X'40' --- .1.. .... - Missing HSCH interrupt X'20' --- ..1. .... - Idle device with work queued X'10' --- ...1 .... - Start pending in Subchannel X'08' --- .... 1... - I/O timeout condition X'04' --- .... .1.. - Mount pending X'02' --- .... ..1. - Missing primary status (channel and device end) X'01' --- ....1 - Missing secondary status (device end) ----------
				End of Comment	
93	(5D)	BITSTRING	1	MIRACTND	Default actions - as set by IOSRMIHP (MIH detection).
94	(5E)	BITSTRING	1	MIRACTNA	Attempted actions - passed to IOSRMIHR from IOSRMIHP (adjustments made by MIH exit).
95	(5F)	BITSTRING	1	MIRACTNS	Actually tried actions performed by IOSRMIHR.

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
				Comment	

					MIH action bytes - following bit mapping apply to all three action fields.

					X'80' --- 1... .... - Halt or Clear Subchannel
					X'40' --- .1.. .... - Simulate an I/O interrupt
					X'20' --- ..1. .... - Redrive the device
					X'10' --- ...1 .... - Requeue the I/O reqeust
					X'08' --- .... 1... - Issue message
					X'04' --- .... .1.. - LOG the condition (always ON)
					X'02' --- .... ..1. - (Reserved for future use)
					X'01' --- ....1 - (Reserved for future use)

					Selected fields from the device UCB prefix area

				End of Comment	
96	(60)	CHARACTER	24	MIRUCBPX	UCB Prefix selected fields
96	(60)	SIGNED	4	MIRPSID	UCBSID
100	(64)	BITSTRING	2	MIRPPMCW	UCBPMCW1
102	(66)	BITSTRING	1	MIRPLPM	UCBLPM
103	(67)	BITSTRING	1	MIRPLPUM	UCBLPUM
104	(68)	BITSTRING	1	MIRPPIM	UCBPIM
105	(69)	UNSIGNED	1	MIRPCHPS	UCBCHPID
				(4294967304:562116696)	
113	(71)	UNSIGNED	1	MIRPLEVL	UCBLEVEL
114	(72)	BITSTRING	1	MIRPIOSF	UCBIOSF1
115	(73)	BITSTRING	4	MIRPLVMS	UCBLVMSK
119	(77)	BITSTRING	1	MIRPMIHT	UCBMIHTI
				Comment	

					Selected fields from the device UCB common area

				End of Comment	
120	(78)	CHARACTER	10	MIRUCBCS	UCB Common area selected fields
120	(78)	BITSTRING	1	*	
		1...		MIRUALTC	UCBALTCU
121	(79)	BITSTRING	1	MIRUFLC	UCBFLC
122	(7A)	CHARACTER	2	MIRUCHAN	UCBCHAN (Note: This is the alias device number if device is a parallel access volume)
124	(7C)	CHARACTER	2	MIRUSFLS	UCBSFLS
126	(7E)	CHARACTER	4	MIRUTYPE	UCBTYP
				Comment	

					Selected fields from the device UCB device dependent area.
					Provided for DASD and TAPE only.

				End of Comment	
130	(82)	CHARACTER	8	MIRUCBDS	Device dependent UCB segment
130	(82)	CHARACTER	6	MIRDVOLI	UCBVOLI
136	(88)	BITSTRING	1	*	
		1...		MIRDMOUN	UCBMOUNT
137	(89)	BITSTRING	1	MIRDFL4	UCBFL4 (DASD only)
				Comment	

					MIH record flag bytes

				End of Comment	
138	(8A)	BITSTRING	1	MIRFLAG1	MIH record flags
		1...		MIRADDL1	MIH record additional data flag bit 1.
		.1..		MIRPAV	Device is parallel access volume

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		..1.		MIRTOHS	MIH timeout initiated by Hyperswap
		...1 1111		*	MIH record reserved flags.
139	(8B)	BITSTRING	1	MIRFLAG2	MIH record reserved flags
		Comment			

		MIH condition reason code associated with the MIH condition field MIRTYPE.			

		End of Comment			
140	(8C)	BITSTRING	1	MIRRSNC	MIH condition reason code
141	(8D)	CHARACTER	2	MIRBDEVN	UCBCHAN from base UCB for a parallel access volume (valid if MIRPAV is set)
143	(8F)	CHARACTER	1	MIRCCWCMD	CCW command code copied from the IOSVST field of the IOSB. Note: This may not be the actual command that received the MIH condition
		Comment			

		IOS services return codes for the Store Subchannel, Halt Subchannel and Clear Subchannel requests issued by MIH. With field X'FF', the IOS service function not issued.			

		End of Comment			
144	(90)	CHARACTER	1	MIRHLTRC	Halt request return code from IOSVHSCH.
145	(91)	CHARACTER	1	MIRCLRRC	Clear request return code from IOSVHSCH.
146	(92)	CHARACTER	1	MIRSTRC1	Store Subchannel request return code from IOSVSTSQ, issued in IOSRMIHP.
147	(93)	CHARACTER	1	MIRSTRC2	Store Subchannel request return code from IOSVSSCQ, issued in IOSRMIHR for Start Pending.
		Comment			

		The first word of the IRB from the CSCH interruption, which includes the subchannel control fields (which includes the clear pending bit).			

		End of Comment			
148	(94)	CHARACTER	4	MIRCIRB1	CSCH IRB word 1.
		Comment			

		The first word of the IRB in the SCHIB as a result of the Store Subchannel in IOSRMIHR for Start Pending MIH condition.			

		End of Comment			
152	(98)	CHARACTER	4	MIRSIRB1	STSCH SCHIB IRB word 1.
		Comment			

		Additional selected field from the device UCB prefix area			

		End of Comment			
156	(9C)	UNSIGNED	1	MIRUSSID	UCBSSID (Note: This is the alias subchannel set id if device is a parallel access volume)
		Comment			

		Driver id from the IOSB.			

		End of Comment			
157	(9D)	CHARACTER	1	MIRDRID	Driver id

MIR Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
Comment					

MIRVERSN = 1 data follows.					

End of Comment					
158	(9E)	UNSIGNED	1	MIRVERSN	Version mumber
159	(9F)	CHARACTER	1	*	Reserved
160	(A0)	UNSIGNED	4	MIRLEN	Total length of MIR

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	6	MIR_KEY_COUNT_LENGTH	Key-count-length-data
0	(0)	UNSIGNED	2	MIR_KEY	Key
2	(2)	UNSIGNED	2	MIR_COUNT	Count
4	(4)	UNSIGNED	2	MIR_LENGTH	Length

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	MIR_INTERROGATE_DATA	Interrogate data
0	(0)	BITSTRING	1	MIR_INTERROGATE_FLAGS	Flags
		1...		MIR_INTERROGATE_STATUSTEXT_VALID	The interrogate status text field is valid
		.111 1111		*	Reserved
1	(1)	CHARACTER	3	*	Reserved
4	(4)	CHARACTER	48	MIR_INTERROGATE_STATUSTEXT	Text describing the status of the I/O request
52	(34)	CHARACTER	8	*	Reserved
60	(3C)	UNSIGNED	2	MIR_INTERROGATE_INFO_LENGTH	Length of the variable interrogate information that follows
62	(3E)	CHARACTER	*	MIR_INTERROGATE_INFO	Variable interrogate information

MIR Constants

Len	**Type**	**Value**	**Name**	**Description**
Comment				

Constants				

End of Comment				
1	DECIMAL	1	MIRVERS1	MIRVERSN version 1
2	NUMB HEX	0001	MIRKEY1	Interrogate information key
2	NUMB HEX	0002	MIRKEY2	I/O device NED key
2	NUMB HEX	FFFF	MIRKEYEND	End of variable data key

MIR Cross Reference

Name	Hex Offset	Hex Value
MIR	0	
MIR_COUNT	2	
MIR_INTERROGATE_DATA	0	
MIR_INTERROGATE_FLAGS	0	
MIR_INTERROGATE_INFO	3E	
MIR_INTERROGATE_INFO_LENGTH	3C	
MIR_INTERROGATE_STATUSTEXT	4	
MIR_INTERROGATE_STATUSTEXT_VALID	0	80
MIR_KEY	0	
MIR_KEY_COUNT_LENGTH	0	
MIR_LENGTH	4	
MIRACTNA	5E	
MIRACTND	5D	
MIRACTNS	5F	
MIRADDL1	8A	80
MIRBDEVN	8D	
MIRCCWCMD	8F	
MIRCIRB1	94	
MIRCLRRC	91	
MIRDATA	18	
MIRDFL4	89	
MIRDMOUN	88	80
MIRDRID	9D	
MIRDVOLI	82	
MIRFLAG1	8A	
MIRFLAG2	8B	
MIRHEADR	0	
MIRHLTRC	90	
MIRINTVL	54	
MIRJOBNM	18	
MIRLEN	A0	
MIRPAV	8A	40
MIRPCHPS	69	
MIRPIOSF	72	
MIRPLEVL	71	
MIRPLPM	66	
MIRPLPUM	67	
MIRPLVMS	73	
MIRPMIHT	77	
MIRPPIM	68	
MIRPPMCW	64	
MIRPSID	60	
MIRRSNC	8C	
MIRSCHIB	20	
MIRSIRB1	98	
MIRSTRC1	92	
MIRSTRC2	93	
MIRTOHS	8A	20
MIRTYPE	5C	
MIRUALTC	78	80
MIRUCBCS	78	
MIRUCBDS	82	
MIRUCBPX	60	
MIRUCHAN	7A	
MIRUFLC	79	
MIRUSFLS	7C	
MIRUSSID	9C	
MIRUTYPE	7E	
MIRVERSN	9E	

MMB Information

MMB Heading Information

Common Name: MONITOR MESSAGE BLOCK
Macro ID: IEAMMB
DSECT Name: MMB or Cnz_tMMB
Owning Component: Console (SC1CK)
Eye-Catcher ID: MMB
Offset: 0
Length: 4
Storage Attributes: Subpool: 250
Key: 0
Size: 144 BYTES
Created by: IEAVMWSV
Pointed to by: UCMMBPTR FIELD OF THE UCM DATA AREA (FIRST MMB)
UCMMBEND FIELD OF THE UCM DATA AREA (LAST MMB)
MMBLINK FIELD OF THE MMB DATA AREA (NEXT MMB)
Serialization: NONE
Function: A MONITOR MESSAGE BLOCK IS CREATED FOR EACH WQE QUEUED FOR TPUT TO MONITORING TERMINALS

MMB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MMB	
0	(0)	CHARACTER	4	MMBNAME	BLOCK ID - MMB IN EBCDIC LEFT JUSTIFIED
4	(4)	ADDRESS	4	MMBLINK	POINTER TO NEXT MMB OR ZERO
8	(8)	ADDRESS	4	MMBBKPTR	POINTER TO PREVIOUS MMB OR ZERO
12	(C)	SIGNED	2	MMBTXLN	LENGTH OF TEXT
14	(E)	SIGNED	2	MMBTYPE (0)	MONITOR TYPE FLAGS
14	(E)	BITSTRING	1	MMBTYPE1	- FIRST BYTE OF MONITOR TYPE FLAGS
		1...		MMBJBNM	"BIT0" - MONITOR JOBNAMES
		.1..		MMBSTAT	"BIT1" - MONITOR STATUS
		..1.		MMBRSV01	"BIT2" - RESERVED
		...1		MMBRSV02	"BIT3" - RESERVED
		.... 1...		MMBRSV03	"BIT4" - RESERVED
		.... .1..		MMBSESS	"BIT5" - MONITOR SESSIONS
		.... ..1.		MMBRSV04	"BIT6" - RESERVED
		1		MMBRSV05	"BIT7" - RESERVED
15	(F)	BITSTRING	1	MMBTYPE2	- SECOND BYTE OF MONITOR TYPE FLAGS
16	(10)	CHARACTER	128	MMBTEXT	- MESSAGE TEXT
16	(10)	X'90'	0	MMBSIZE	"*-MMB" LENGTH OF MMB

MMB Cross Reference

Name	Hex Offset	Hex Value
MMB	0	
MMBBKPTR	8	
MMBJBNM	E	80
MMBLINK	4	
MMBNAME	0	
MMBRSV01	E	20
MMBRSV02	E	10
MMBRSV03	E	8
MMBRSV04	E	2
MMBRSV05	E	1
MMBSESS	E	4
MMBSIZE	10	90
MMBSTAT	E	40
MMBTEXT	10	
MMBTXLN	C	
MMBTYPE	E	
MMBTYPE1	E	
MMBTYPE2	F	

MPB Information

MPB Programming Interface information

Programming Interface information

MPB

End of Programming Interface information

MPB Heading Information

Common Name: Message Parameter Block Mapping Macro
Macro ID: CNLMMPB
DSECT Name: MPB MPBMSG MPBSB
Owning Component: MVS MESSAGE SERVICE (SCMMS)
Eye-Catcher ID: 'MPB '
Offset: 0
Length: 4
Storage Attributes: Subpool: of caller
Key: of caller
Residency: of caller
Size: Variable based on size of message being processed.
The size of this entry is located in MPBSIZE.
Created by: Callers of Message Translate (TRANMSG)
Callers of MPB build macros (BLDMPB,UPDTMPB)
Pointed to by: MIOINPTP field of the MIO area (input)
MIOBUFFP field of the MIO area (output)
MPB_PTR
Serialization: None required.
Function: Used to map the Message Parameter Block which contains a parameterized message. The variable length portion contains a message header followed by all substitution tokens for the message. It is used as a parameter list for the Message Translate Service (TRANMSG).

MPB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MPB	MESSAGE PARAMETER BLOCK
0	(0)	CHARACTER	4	MPBACRN	ACRONYM "MPB"
4	(4)	BITSTRING	1	MPBVRSN	MPB VERSION NUMBER
5	(5)	CHARACTER	3		RESEVERED
8	(8)	SIGNED	4	MPBSIZE	SIZE OF THIS MPB
12	(C)	SIGNED	4	MPBOFFST	OFFSET TO MESSAGE HEADER BLOCK
16	(10)	CHARACTER	8		RESERVED
24	(18)	SIGNED	4	MPBVDATL	LENGTH OF THE VARIABLE DATA AREA
28	(1C)	CHARACTER	1	MPBVDAT (0)	SPACE USED IN OUTPUT BUFFER

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MPBMSG	MPB HEADER BLOCK
0	(0)	CHARACTER	3	MPBFMTNM	MESSAGE FORMAT NUMBER
3	(3)	CHARACTER	2	MPBLNNM	MESSAGE LINE NUMBER
5	(5)	CHARACTER	1	MPBXFUNC	Extended function: When " 1", find the MsgID within the next line
6	(6)	CHARACTER	2		RESERVED
8	(8)	SIGNED	4	MPBSBCNT	COUNT OF SUBSTITUTION BLOCKS
12	(C)	SIGNED	4	MPBMIDL	LENGTH OF MESSAGE IDENTIFIER
16	(10)	CHARACTER	1	MPBMID (0)	MESSAGE IDENTIFIER

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MPBSB	MESSAGE SUBSTITUTION BLOCK
0	(0)	CHARACTER	1	MPBSBTYP	TYPE OF SUBSTITUTION BLOCK
1	(1)	CHARACTER	3		RESERVED
4	(4)	SIGNED	4	MPBTOKNL	LENGTH OF THE TOKEN NAME
8	(8)	CHARACTER	16	MPBTOKN	TOKEN NAME
24	(18)	SIGNED	4	MPBSUBL	LENGTH OF SUBSTITUTION DATA
28	(1C)	CHARACTER	1	MPBSUB (0)	SUBSTITUTION DATA

MPB Cross Reference

Name	Hex Offset	Hex Value
MPB	0	
MPBACRN	0	
MPBFMTNM	0	
MPBLNNM	3	
MPBMID	10	
MPBMIDL	C	
MPBMSG	0	
MPBOFFST	C	
MPBSB	0	
MPBSBCNT	8	
MPBSBTYP	0	
MPBSIZE	8	
MPBSUB	1C	
MPBSUBL	18	
MPBTOKN	8	
MPBTOKNL	4	
MPBVDAT	1C	
MPBVDATL	18	
MPBVRSN	4	
MPBXFUNC	5	

MPFT Information

MPFT Heading Information

Common Name: MESSAGE PROCESSING FACILITY TABLE (MPFT) MAPPING MACRO
Macro ID: IEEZB809
DSECT Name: MPFT, MPFTENTY, MPFMENTY
Owning Component: SYSTEM COMMAND (SC1B8)
Eye-Catcher ID: MPFT
Offset: 0
Length: 4
Storage Attributes: Main Storage: YES
Virtual Storage: NO
Subpool: 241 (CSA)
Key: 0
Data Space: NO
Residency: ANY
Size: MPFT -- X'0040' bytes
MPFTENTY -- X'0028' bytes
MPFMENTY -- X'0028' bytes
Created by: IEECB805
Pointed to by: UCMFMPFP field of the IEECUCM data area
UCMPOMPF field of the IEECUCM data area
Serialization: The MPF table is serialized on via an ENQ on the SYSZMCS.MPFTABLE resource. An exclusive ENQ is required to change or delete the table.
Function: Contains a sorted list of message ID's and/or prefixes that are eligible for processing by MPF.

MPFT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	64	MPFT	MPF TABLE
0	(0)	CHARACTER	4	MPFTACRN	CHARACTERS 'MPFT'
4	(4)	UNSIGNED	1	MPFTVRSN	VERSION LEVEL
5	(5)	BITSTRING	1	MPFTFLG	MPF FLAGS
		1...		MPFTDFLT	MPF .NOENTRY MESSAGE ID FOUND
		.1..		MPFTDSUP	MPF .NOENTRY SUP(YES)
		..1.		MPFTDRET	MPF .NOENTRY RETENTION INDICATOR FOR DISPLAY MPF OUTPUT
		...1		MPFTDAUT	MPF .NOENTRY AUTO(YES)
		.... 1...		MPFTDRYS	MPF .NOENTRY RETAIN(YES)
		.... .1..		MPFTDRI	MPF .NOENTRY RETAIN(I)
		.... ..1.		MPFTDRE	MPF .NOENTRY RETAIN(E)
		1		MPFTDRCE	MPF .NOENTRY RETAIN(CE)
6	(6)	BITSTRING	1	MPFTFLG2	MPF FLAG BYTE TWO
		1...		MPFTDSPA	MPF .NOENTRY SUP(ALL)
7	(7)	CHARACTER	1	MPFTRSV4	RESERVED
8	(8)	UNSIGNED	1	MPFTSPN	SUBPOOL NUMBER
9	(9)	UNSIGNED	3	MPFTSIZE	SIZE OF MPF TABLE TOTAL
12	(C)	UNSIGNED	2	MPFTNENG	NUMBER OF ENTRIES IN GENERIC TABLE
14	(E)	UNSIGNED	2	MPFTNENS	NUMBER OF ENTRIES IN SPECIFIC TABLE
16	(10)	UNSIGNED	2	MPFTENLN	LENGTH OF EACH ENTRY
18	(12)	UNSIGNED	2	MPFTMELN	MPF Module entry length
20	(14)	ADDRESS	4	MPFTGENP	POINTER TO THE FIRST ENTRY IN GENERIC TABLE
24	(18)	ADDRESS	4	MPFTSENP	POINTER TO THE FIRST ENTRY IN SPECIFIC TABLE
28	(1C)	ADDRESS	4	MPFTMENP	POINTER TO THE FIRST ENTRY IN EXIT TABLE
32	(20)	SIGNED	4	MPFTCECB	SET MPF COMMAND ECB
36	(24)	ADDRESS	4	MPFTASCB	ASCB ADDRESS OF IEECB805 TASK FOR CNZS1MPS POST
40	(28)	CHARACTER	8	MPFTDATK	MPF .NOENTRY TOKEN
48	(30)	UNSIGNED	2	MPFTNENM	NUMBER OF ENTRIES IN EXIT TABLE
50	(32)	CHARACTER	14	MPFTRSV	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	40	MPFTENTY	MPF TABLE ENTRY MAPPING
0	(0)	CHARACTER	10	MPFMSGID	MESSAGE ID
10	(A)	UNSIGNED	1	MPFTIDLN	LENGTH OF MESSAGE ID
11	(B)	BITSTRING	1	MPFTEFLG	ENTRY FLAGS
		1...		MPFTPREF	PREFIX ENTRY

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.1..		MPFSUPMS	SUPPRESS THE MESSAGE
		..1.		MPFABEND	USER EXIT ROUTINE ABENDED
		...1		MPFNTFND	USER EXIT ROUTINE NOT FOUND
		.... 1...		MPFXACTV	EXIT IS ACTIVE
		.... .1..		MPFRETAN	RETENTION INDICATOR FOR DISPLAY MPF OUTPUT
		.... ..1.		MPFAUTO	AUTOMATION SPECIFIED FOR THIS MSG
		1		MPFSUPA	SUPPRESS ALL MESSAGES INCLUDING COMMAND RESPONSES
12	(C)	CHARACTER	8	MPFEXNME	USER EXIT ROUTINE MODULE NAME
20	(14)	ADDRESS	4	MPFEXENT	ADDRESS OF ENTRY POINT
24	(18)	CHARACTER	8	MPFAUTOT	AUTOMATION TOKEN VALUE
32	(20)	ADDRESS	4	MPFTIWKP	POINTER TO 8-BYTE DATA AREA FOR AN INDIVIDUAL EXIT
36	(24)	BITSTRING	1	MPFRFLGS	INDIVIDUAL RETENTION FLAGS
		1...		MPFRETYS	RETAIN ALL ACTION MSGS
		.1..		MPFRETI	RETAIN IF IMMEDIATE ACTION MSG
		..1.		MPFRETE	RETAIN IF EVENTUAL ACTION MSG
		...1		MPFRETCE	RETAIN IF CRITICAL EVENTUAL ACTION MSG
		 1111		*	RESERVED
37	(25)	CHARACTER	2	MPFSUFFX	INDIVIDUAL SUFFIX
39	(27)	CHARACTER	1	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	40	MPFMENTY	MPF Module entry mapping
0	(0)	CHARACTER	8	MPFMNAME	Entry point name
8	(8)	ADDRESS	4	MPFMADDR	Starting address of module
12	(C)	SIGNED	4	MPFMLEN	Length of module
16	(10)	ADDRESS	4	MPFMEPA	Entry point of module
20	(14)	CHARACTER	8	MPFWKA	Workarea for exit
28	(1C)	CHARACTER	12	MPFMRSV	Reserved

MPFT Constants

Len	Type	Value		Name	Description
		Comment			
		THE ACRONYM AND VERSION NUMBER TO BE PLACED IN THE MPF TABLE.			
		End of Comment			
4	CHARACTER	MPFT		MPFTNAME	ACRONYM
1	DECIMAL		1	MPFTS212	LEVEL OS/VS2 JBB2125
1	DECIMAL		2	MPFTS410	LEVEL OS/VS2 HBB4410
1	DECIMAL		3	MPFTS422	LEVEL OS/VS2 JBB4422
1	DECIMAL		4	MPFTS727	LEVEL JBB7727
1	DECIMAL		4	MPFTVERN	CURRENT VERSION

MPFT Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
MPFABEND	B	20	MPFSUPA	B	01
MPFAUTO	B	02	MPFSUPMS	B	40
MPFAUTOT	18		MPFT	0	
MPFEXENT	14		MPFTACRN	0	
MPFEXNME	C		MPFTASCB	24	
MPFMADDR	8		MPFTCECB	20	
MPFMENTY	0		MPFTDATK	28	
MPFMEPA	10		MPFTDAUT	5	10
MPFMLEN	C		MPFTDFLT	5	80
MPFMNAME	0		MPFTDRCE	5	01
MPFMRSV	1C		MPFTDRE	5	02
MPFMSGID	0		MPFTDRET	5	20
MPFNTFND	B	10	MPFTDRI	5	04
MPFRETAN	B	04	MPFTDRYS	5	08
MPFRETCE	24	10	MPFTDSPA	6	80
MPFRETE	24	20	MPFTDSUP	5	40
MPFRETI	24	40	MPFTEFLG	B	
MPFRETYS	24	80	MPFTENLN	10	
MPFRFLGS	24		MPFTENTY	0	
MPFSUFFX	25		MPFTFLG	5	

Name	Hex Offset	Hex Value
MPFTFLG2	6	
MPFTGENP	14	
MPFTIDLN	A	
MPFTIWKP	20	
MPFTMELN	12	
MPFTMENP	1C	
MPFTNENG	C	
MPFTNENM	30	
MPFTNENS	E	
MPFTPREF	B	80
MPFTRSV	32	
MPFTRSV4	7	
MPFTSENP	18	
MPFTSIZE	9	
MPFTSPN	8	
MPFTVRSN	4	
MPFWKA	14	
MPFXACTV	B	08

MQE Information

MQE Heading Information

Common Name: IPL Message Queue Element (MQE).
Macro ID: IHAMQE
DSECT Name: MQE
Owning Component: InitiaL Program Load (SC1C9)
Eye-Catcher ID: NONE
Storage Attributes: Main Storage: YES
Virtual Storage: NO
Auxiliary Storage: NO
Subpool: 245
Key: 0
Data Space: NO
Residency: MQEs are created in the IPL workspace. IEAIPL99 copies the MQEs to SQA (SP 245, below the 16 MB line) before the IPL workspace is deleted.
Size: 4 + length of WPL + length of WPLFLGS (see IEZWPL)
Created by: IEAIPL35 creates one MQE for each message it is requested to issue.
Pointed to by: MQH1ST - Points to the first MQE on the IPL message queue.
MQHNTH - Points to the last MQE on the IPL message queue.
Serialization: NONE
Function: During IPL a console is not available. Messages issued during IPL are therefore saved in MQEs, which are queued on to the IPL Message Queue. Messages contained in MQEs are issued when the WTO becomes available during NIP.

MQE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	144	MQE	
0	(0)	ADDRESS	4	MQENEXT	Pointer to the next MQE on the queue.
4	(4)	CHARACTER	138	MQEDATA	The WTO parameter list.
142	(8E)	CHARACTER	2	*	Pad for doubleword alignment
144	(90)	CHARACTER	0	*	For double word boundary.

MQH Information

MQH Heading Information

Common Name: IPL Message Queue Header (MQH)
Macro ID: IHAMQH
Owning Component: Initial Program Load (SC1C9)
Eye-Catcher ID: None
Subpool and Key: Created in the IPL work space, copied to subpool 245.
Size: 12 bytes
Created by: IEAIPL30 creates one MQH.
Pointed to by: IVTMQHP during IPL
NVTMQHP during NIP
Serialization: None
Function: The MQH is the header for the IPL message queue. During IPL a console is not available. Messages issued during IPL are therefore saved in Message Queue Elements (MQEs), which are queued on to the IPL Message Queue. Messages contained in MQEs are issued when the NIP console is initialized.

MQH Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	MQH	
0	(0)	ADDRESS	4	MQH1ST	Pointer to the first (oldest) MQE.
4	(4)	ADDRESS	4	MQHCOUNT	Number of MQE's on the queue.
8	(8)	ADDRESS	4	MQHNTH	Pointer to the Nth (youngest) MQE.
12	(C)	ADDRESS	4	*	For DWORD boundary.

MSGS Information

MSGS Heading Information

Common Name: Generalized Message Service Parameter List (MSGS)
Macro ID: IEAVM101
DSECT Name: MSGS
Owning Component: Console (SC1CK)
Eye-Catcher ID: MSGS
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: Caller's Storage AUXILARY STORAGE: Caller's Storage
Subpool: Caller's Storage
Key: Caller's Storage
Data Space: Caller's Storage
Residency: Caller's Storage
Size: 44 BYTES
Created by: Caller
Pointed to by: Register 1 points to a word which points to the MSGS.
Serialization: None
Function: Provides the interface between the modules that need to issue messages and the Generalized Message Service Module (IEAVM200). This module contains a table of message Ids that the Message Service Module uses to build the requested message.

MSGS Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	44	MSGS	Generalized Message Service Parameter List
0	(0)	CHARACTER	4	MSGSACRN	Acronym 'MSGS'
4	(4)	UNSIGNED	1	MSGSVRSN	Version Level
5	(5)	BITSTRING	1	MSGSRFLG	Request flags
		1...		MSGSBAIM	Build and issue message
		.1..		MSGSBAQM	Build and queue message
		..1.		MSGSIAMQ	Issue all messages on queue
		...1		MSGSHDCY	Write to hardcopy only
		 1111		*	Reserved
6	(6)	CHARACTER	2	*	Reserved
8	(8)	SIGNED	4	MSGSLNUM	CONSOLxx statement line number for sorting the message queue
12	(C)	UNSIGNED	4	MSGSMSGI	Message Id indicator
16	(10)	CHARACTER	2	*	Reserved
18	(12)	UNSIGNED	2	MSGSNMBI	Total number of inserts (includes sub-inserts)
20	(14)	ADDRESS	4	MSGSIPTR	Pointer to inserts
24	(18)	ADDRESS	4	MSGSMQPB	Address of a word containing the pointer to beginning of message queue. Serialization of the queue is up to the caller of this service.
28	(1C)	ADDRESS	4	MSGSMQPE	Address of a word containing the pointer to end of message queue
32	(20)	UNSIGNED	4	MSGSDOMID	DOM Id of msg returned to caller
36	(24)	CHARACTER	8	*	Reserved

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	12	MSGSISRT	Message insert
0	(0)	UNSIGNED	2	MSGSILNT	Length of insert text
2	(2)	BITSTRING	1	MSGSIFLG	Insert flags
		1...		MSGSICHH	Convert insert to printable hex characters
		.1..		MSGSICHD	Convert insert to printable decimal characters
		..1.		MSGSISUB	Insert contains sub-insert
		...1		MSGSIZRO	Suppress leading zeros
		 1111		*	Reserved
3	(3)	BITSTRING	1	*	Reserved
4	(4)	ADDRESS	4	MSGSITXT	Pointer to text of insert
8	(8)	UNSIGNED	1	MSGSISUP	Number of leading zeros to suppress
9	(9)	CHARACTER	1	MSGSIDEL	Delimiter character
10	(A)	CHARACTER	2	*	Reserved

MSGS Constants

Len	Type	Value	Name	Description
		Comment		
		The acronym and the version number to be placed in the MSGS Control Block.		
		End of Comment		
4	CHARACTER	MSGS	MSGSNAME	Acronym
1	DECIMAL	1	MSGSS220	Level JBB2220
1	DECIMAL	2	MSGSS410	Level HBB4410
1	DECIMAL	2	MSGSVERN	Current version level
		Comment		
		Table of message Ids:		
		End of Comment		
4	DECIMAL	0	MSPREBLT	Id for pre-built message
4	DECIMAL	18901	MS189I01	Msg IEA189I version 1
4	DECIMAL	18902	MS189I02	Msg IEA189I version 2
4	DECIMAL	19400	MS194I00	Msg IEA194I
4	DECIMAL	19501	MS195I01	Msg IEA195I version 1
4	DECIMAL	19502	MS195I02	Msg IEA195I version 2
4	DECIMAL	19503	MS195I03	Msg IEA195I version 3
4	DECIMAL	19504	MS195I04	Msg IEA195I version 4
4	DECIMAL	19505	MS195I05	Msg IEA195I version 5
4	DECIMAL	19506	MS195I06	Msg IEA195I version 6
4	DECIMAL	19507	MS195I07	Msg IEA195I version 7
4	DECIMAL	19508	MS195I08	Msg IEA195I version 8
4	DECIMAL	19509	MS195I09	Msg IEA195I version 9
4	DECIMAL	19510	MS195I10	Msg IEA195I version 10
4	DECIMAL	19511	MS195I11	Msg IEA195I version 11
4	DECIMAL	19512	MS195I12	Msg IEA195I version 12
4	DECIMAL	19513	MS195I13	Msg IEA195I version 13
4	DECIMAL	19514	MS195I14	Msg IEA195I vers 14
4	DECIMAL	19601	MS196I01	Msg IEA196I version 1
4	DECIMAL	19602	MS196I02	Msg IEA196I version 2
4	DECIMAL	19603	MS196I03	Msg IEA196I version 3
4	DECIMAL	19604	MS196I04	Msg IEA196I version 4
4	DECIMAL	19605	MS196I05	Msg IEA196I version 5
4	DECIMAL	19606	MS196I06	Msg IEA196I version 6
4	DECIMAL	19608	MS196I08	Msg IEA196I version 8
4	DECIMAL	19609	MS196I09	Msg IEA196I version 9
4	DECIMAL	19610	MS196I10	Msg IEA196I version 10
4	DECIMAL	19611	MS196I11	Msg IEA196I version 11
4	DECIMAL	19612	MS196I12	Msg IEA196I version 12
4	DECIMAL	19613	MS196I13	Msg IEA196I version 13
4	DECIMAL	19614	MS196I14	Msg IEA196I version 14
4	DECIMAL	19615	MS196I15	Msg IEA196I version 15
4	DECIMAL	19616	MS196I16	Msg IEA196I version 16
4	DECIMAL	19617	MS196I17	Msg IEA196I version 17
4	DECIMAL	19619	MS196I19	Msg IEA196I version 19
4	DECIMAL	19620	MS196I20	Msg IEA196I version 20
4	DECIMAL	50400	MS504I00	Msg IEA504I
4	DECIMAL	18000	ME180I00	Msg IEE180I
4	DECIMAL	18100	ME181I00	Msg IEE181I
4	DECIMAL	18200	ME182I00	Msg IEE182I
4	DECIMAL	25400	MS254I00	Msg IEA254I
4	DECIMAL	26001	ME260I01	Msg IEE260I version 1
4	DECIMAL	26002	ME260I02	Msg IEE260I version 2
4	DECIMAL	26003	ME260I03	Msg IEE260I version 3
4	DECIMAL	26004	ME260I04	Msg IEE260I version 4
4	DECIMAL	26005	ME260I05	Msg IEE260I version 5
		Comment		
		SMCS Console Messages		
		End of Comment		
4	DECIMAL	4901	SN049I01	Msg IEE049I vers 1
4	DECIMAL	4902	SN049I02	Msg IEE049I vers 2
4	DECIMAL	5000	SN050I00	Msg IEE050I
4	DECIMAL	5100	SN051I00	Msg IEE051I

Len	Type	Value	Name	Description
4	DECIMAL	5201	SN052I01	Msg IEE052I vers 1
4	DECIMAL	5202	SN052I02	Msg IEE052I vers 2
4	DECIMAL	5203	SN052I03	Msg IEE052I vers 3
4	DECIMAL	5301	SN053I01	Msg IEE053I vers 1
4	DECIMAL	5302	SN053I02	Msg IEE053I vers 2
4	DECIMAL	5303	SN053I03	Msg IEE053I vers 3
4	DECIMAL	5401	SN054I01	Msg IEE054I vers 1
4	DECIMAL	5402	SN054I02	Msg IEE054I vers 2
4	DECIMAL	5501	SN055I01	Msg IEE055I vers 1
4	DECIMAL	5502	SN055I02	Msg IEE055I vers 2
4	DECIMAL	5700	SN057I00	Msg IEE057I
4	DECIMAL	5800	SN058I00	Msg IEE058I
4	DECIMAL	6600	SN066I00	Msg IEE066I
4	DECIMAL	6700	SN067I00	Msg IEE067I
4	DECIMAL	81900	SN819E00	Msg IEE819E
4	DECIMAL	82300	SN823E00	Msg IEE823E
4	DECIMAL	82900	SN829E00	Msg IEE829E

MSGS Cross Reference

Name	Hex Offset	Hex Value
MSGS	0	
MSGSACRN	0	
MSGSBAIM	5	80
MSGSBAQM	5	40
MSGSDOMID	20	
MSGSHDCY	5	10
MSGSIAMQ	5	20
MSGSICHD	2	40
MSGSICHH	2	80
MSGSIDEL	9	
MSGSIFLG	2	
MSGSILNT	0	
MSGSIPTR	14	
MSGSISRT	0	
MSGSISUB	2	20
MSGSISUP	8	
MSGSITXT	4	
MSGSIZRO	2	10
MSGSLNUM	8	
MSGSMQPB	18	
MSGSMQPE	1C	
MSGSMSGI	C	
MSGSNMBI	12	
MSGSRFLG	5	
MSGSVRSN	4	

MSRASDCA Information

MSRASDCA Heading Information

Common Name: MASTER SCHEDULER COMMAND RAS DATA COMMUNICATIONS AREA (MSRASDCA)
Macro ID: IEEZB808
DSECT Name: MSRASDCA
Owning Component: SYSTEM COMMAND (SC1B8)
Eye-Catcher ID: MRAS
Offset: 0
Length: 4
Storage Attributes: Subpool: 230
Key: 0
Size: 116 BYTES
Created by: IEECB860 AND IEE0003D
Pointed to by: REGISTER 3, ON RETURN FROM IEECB860
FIELD XAMSRAS IN THE XSA
Serialization: NONE
Function: MAPS THE DATA AREA USED TO COMMUNICATE SDWA DATA BETWEEN:
.
MAINLINE SYSTEM COMMAND PROCESSORS AND THE MASTER SCHEDULER COMMAND PROCESSOR ESTAE RECOVERY MODULE - IEECB860
.
MODULES IN THE SVC 34 LOAD(IGC0003D) AND THE SVC 34 ESTAE MODULE - IEE5103D

MSRASDCA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MSRASDCA	- MASTER SCHEDULER RAS DATA COMMUNICATIONS AREA
0	(0)	CHARACTER	4	MSRCBID	CONTROL BLOCK ID - MRAS
4	(4)	CHARACTER	1	MSRVERSN	VERSION LEVEL
4	(4)	X'1'	0	MSRSP21	"1" VERSION LEVEL OS/VS2 HBB2102
4	(4)	X'2'	0	MSRSP212	"2" VERSION LEVEL OS/VS2 JBB2125
4	(4)	X'2'	0	MSRVERID	"MSRSP212" VERSION LEVEL - UPDATED FOR SIZE OR INCOMPATIBLE CHANGE
5	(5)	BITSTRING	1	MSRFLGS1	FLAGS BYTE
		1...		MSRNOMSG	"X'80'" DO NOT ISSUE MESSAGE
6	(6)	CHARACTER	2	MSRES1	RESERVED
8	(8)	CHARACTER	8	MSRLNAME	FAILING LOAD MODULE NAME
16	(10)	CHARACTER	8	MSRCNAME	FAILING CSECT NAME
21	(15)	CHARACTER	1	MSREXITF	IF SET TO 'X' CSECT IS NOT IN CONTROL
21	(15)	X'E7'	0	MSREXITI	"C'X'" USED TO INDICATE CSECT NOT IN CONTROL
24	(18)	CHARACTER	5	MSRCMPID	COMPONENT ID OF FAILING MODULE
29	(1D)	CHARACTER	23	MSRCMND	FAILING COMMAND
52	(34)	CHARACTER	16	MSRMODLV	LEVEL OF FAILING MODULE
68	(44)	CHARACTER	4	MSREASNC	REASON CODE OR RETURN CODE FOR ABEND
72	(48)	BITSTRING	2	MSRDSIZE	LENGTH OF VARIABLE DATA AREA
74	(4A)	BITSTRING	2	MSRDLEN	LENGTH OF VARIABLE DATA
76	(4C)	SIGNED	2	MSRDPVA (0)	FLAGS DESCRIBING MSRVRA, TO BE MOVED TO SDWAVRA
76	(4C)	BITSTRING	1	MSRDPVA1	BYTE ONE OF SDWADPVA
		1...		MSRHEX	"X'80'" MSRVRA DATA TO BE PRINTED BY EREP IN HEX
		.1..		MSREBC	"X'40'" MSRVRA DATA TO BE PRINTED BY EREP IN EBCDIC
		..1.		MSRVRAM	"X'20'" MSRVRA DATA IS IN THE FORMAT MAPPED BY THE IHAVRA MACRO
77	(4D)	BITSTRING	1	MSRDPVA2	RESERVED
78	(4E)	SIGNED	2	MSRES2	RESERVED
80	(50)	SIGNED	4	MSRES3	RESERVED
84	(54)	ADDRESS	4	MSRVRAD	ADDRESS OF MSRVRA
88	(58)	ADDRESS	4	MSRRTYAD	ADDRESS OF RETRY ROUTINE WHERE AN SDWA IS AVAILABLE
92	(5C)	ADDRESS	4	MSRRTYNS	ADDRESS OF RETRY ROUTINE IN THE EVENT OF NO SDWA
96	(60)	ADDRESS	4	MSRCLPAD	ADDRESS OF CLEANUP ROUTINE
100	(64)	ADDRESS	4	MSRRUBAD	ADDRESS OF REGISTER UPDATE BLOCK - MUST BE PROVIDED IF RETRY SPECIFIED
104	(68)	ADDRESS	4	MSRDMPEX	ADDRESS OF DUMP EXIT
108	(6C)	ADDRESS	4	MSRPARMP	POINTER TO IEECB860'S PARM AREA
112	(70)	BITSTRING	2	MSRPARML	LENGTH OF IEECB860'S PARM AREA
114	(72)	CHARACTER	2	MSRES4	RESERVED

MSRASDCA Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MSRVRADS	VARIABLE DATA AREA DSECT
0	(0)	CHARACTER	255	MSRVRA	VARIABLE DATA TO BE MOVED TO SDWAVRA

MSRASDCA Cross Reference

Name	Hex Offset	Hex Value
MSRASDCA	0	
MSRCBID	0	
MSRCLPAD	60	
MSRCMND	1D	
MSRCMPID	18	
MSRCNAME	10	
MSRDLEN	4A	
MSRDMPEX	68	
MSRDPVA	4C	
MSRDPVA1	4C	
MSRDPVA2	4D	
MSRDSIZE	48	
MSREASNC	44	
MSREBC	4C	40
MSRES1	6	
MSRES2	4E	
MSRES3	50	
MSRES4	72	
MSREXITF	15	
MSREXITI	15	E7
MSRFLGS1	5	
MSRHEX	4C	80
MSRLNAME	8	
MSRMODLV	34	
MSRNOMSG	5	80
MSRPARML	70	
MSRPARMP	6C	
MSRRTYAD	58	
MSRRTYNS	5C	
MSRRUBAD	64	
MSRSP21	4	1
MSRSP212	4	2
MSRVERID	4	2
MSRVERSN	4	
MSRVRA	0	
MSRVRAD	54	
MSRVRADS	0	
MSRVRAM	4C	20

MTB Information

MTB Programming Interface information

Programming Interface information

MTB

End of Programming Interface information

MTB Heading Information

Common Name: Message Text Block Mapping Macro
Macro ID: CNLMMTB
DSECT Name: MTB MTBMSG
Owning Component: MVS MESSAGE SERVICE (SCMMS)
Eye-Catcher ID: 'MTB '
Offset: 0
Length: 4
Storage Attributes: Subpool: of caller
Key: of caller
Residency: of caller
Size: Variable based on size of parameterized form of the message being processed. Size of this MTB is in field MTBSIZE.
Created by: Callers of Message Parameterize
Callers of Message Translate
Pointed to by: MIOINPTP field of the MIO area
MIOBUFFP field of the MIO area
Serialization: None required.
Function: Used to map the Message Text Block. When input to the MVS Message Service it contains USA English message. Output MTB may contain multiple translated messages.

MTB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MTB	Message text block
0	(0)	CHARACTER	4	MTBACRN	Acronym "MTB "
4	(4)	BITSTRING	1	MTBVRSN	MTB version
5	(5)	CHARACTER	3		Reserved
8	(8)	SIGNED	4	MTBSIZE	Size of MTB
12	(C)	CHARACTER	3	MTBLNGCD	Output language code
15	(F)	BITSTRING	1	MTBFLAGS	Message flags
		1...		MTBDBCS	"X'80'" DBCS indicator
16	(10)	CHARACTER	4		Reserved
20	(14)	SIGNED	4	MTBCOUNT	Count of message records
24	(18)	SIGNED	4	MTBOFFST	Offset to first entry in MTBVDAT
28	(1C)	CHARACTER	8		Reserved
36	(24)	SIGNED	4	MTBVDATL	Length of variable data area
40	(28)	CHARACTER	1	MTBVDAT (0)	Variable data area

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	MTBMSG	Message record block
0	(0)	SIGNED	2	MTBTEXTL	Length of message entry
2	(2)	CHARACTER	1	MTBTEXT (0)	Message text

MTB Cross Reference

Name	Hex Offset	Hex Value
MTB	0	
MTBACRN	0	
MTBCOUNT	14	
MTBDBCS	F	80
MTBFLAGS	F	
MTBLNGCD	C	
MTBMSG	0	
MTBOFFST	18	
MTBSIZE	8	
MTBTEXT	2	
MTBTEXTL	0	
MTBVDAT	28	
MTBVDATL	24	
MTBVRSN	4	

MTT Information

MTT Heading Information

Common Name: Master Trace Table Mapping Macro
Macro ID: IEEZB806
DSECT Name: MTTABLE, MTENTRY
Owning Component: Master Scheduler (SC1B8)
Eye-Catcher ID: 'MTT '
Offset: 0
Length: 4
Storage Attributes: Main Storage: Master's Address Space
Virtual Storage: n/a
Auxiliary Storage: n/a
Subpool: 229
Key: 0
Data Space: n/a
Residency: Above 31-bit line
Size: Table Header: 128 bytes
Entry Header: 10 bytes
Entry Data : user defined
Total size varies between 16K and 999K
Created by: CNZM1TRC
Pointed to by: BAMTTBL field of the BASEA data area
Serialization: CONSOLE address space local lock
Function: Maps the Master Trace Table and an entry in the table.

MTT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	MTTABLE	ALIGN MASTER TRACE TABLE MAPPING TO A DOUBLE WORD BOUNDARY
0	(0)	CHARACTER	128	MTTHDRA	HEADER AREA OF TABLE
0	(0)	CHARACTER	4	MTTID	MASTER TRACE TABLE IDENT.
4	(4)	ADDRESS	4	MTTCURPT	ADDR OF CURRENT ENTRY
8	(8)	ADDRESS	4	MTTENTPT	ADDR OF STORAGE AREA FOR TABLE ENTRIES
12	(C)	ADDRESS	4	MTTENDPT	ADDR OF FIRST BYTE BEYOND END OF TABLE
16	(10)	UNSIGNED	4	MTTSIZE	SUBPOOL AND LENGTH FOR FREEMAIN
16	(10)	UNSIGNED	1	MTTSP	SUBPOOL OF TABLE
17	(11)	UNSIGNED	3	MTTLEN	LENGTH OF TABLE
20	(14)	CHARACTER	12	MTTWRPTM	TIME TABLE INITIALIZED OR TIME LAST WRAPPED IN FORM IT/WTHH:MM:SS.S, PRODUCED VIA THE USE OF THE CONTIME MACRO
32	(20)	ADDRESS	4	MTTWRPPT	ADDR OF LAST ENTRY STORED BEFORE TABLE WRAP
36	(24)	SIGNED	4	*	Reserved (Was MTTPFLAG)
40	(28)	SIGNED	4	MTTDAREA	DATA AREA LENGTH
44	(2C)	CHARACTER	84	*	Reserved (Was MTTWK808)
128	(80)	CHARACTER	*	MTTENTA	STORAGE AREA FOR TABLE ENTRIES

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	MTENTRY	ALIGNS TO BYTE BNDRY
0	(0)	CHARACTER	10	MTENTHDR	TABLE ENTRY HEADER
0	(0)	BITSTRING	2	MTENTFLG	FLAGS SET BY CALLER
2	(2)	BITSTRING	2	MTENTTAG	IDENTIFIES CALLER
4	(4)	BITSTRING	4	MTENTIMM	CALLERS IMMEDIATE DATA
8	(8)	BITSTRING	2	MTENTLEN	LENGTH OF CALLER'S DATA
10	(A)	CHARACTER	*	MTENTDAT	DATA PASSED BY CALLER

MTT Cross Reference

Name	Hex Offset	Hex Value
MTENTDAT	A	
MTENTFLG	0	
MTENTHDR	0	
MTENTIMM	4	
MTENTLEN	8	
MTENTRY	0	
MTENTTAG	2	
MTTABLE	0	
MTTCURPT	4	
MTTDAREA	28	
MTTENDPT	C	
MTTENTA	80	
MTTENTPT	8	
MTTHDRA	0	
MTTID	0	
MTTLEN	11	
MTTSIZE	10	
MTTSP	10	
MTTWRPPT	20	
MTTWRPTM	14	

NEL Information

NEL Programming Interface information

Programming Interface information

NEL

ONLY the following field is part of the programming interface information:

- NELXA2

End of Programming Interface information

NEL Heading Information

Common Name: Interpreter Entrance List
Macro ID: IEFNEL
DSECT Name: NEL (defined by invoker), NELEXITS and NELEXENT (when exits are included)
Owning Component: Converter / Interpreter (SC1B9)
Eye-Catcher ID: None
Storage Attributes: Subpool: 10, 252, 253
Key: Determined by caller
Residency: Below
Size: 40 (decimal) for Interpreter,
66 (decimal) for Converter,
when exits are included then
additional 8 + 8*number of exits
(plus 6 for double-word alignment
for Converter)
Created by: Invoker of Converter or Interpreter
Pointed to by: - Register 1 on entry to the Converter
- Register 1 on entry to the Interpreter
- NELEXLST points to NELEXITS when exits are included
Serialization: None
Function: This macro provides a symbolic mapping of the parameter lists required when invoking the Converter or Interpreter subroutines. Each list and its exit sublist must be constructed in dynamically allocated storage prior to calling the Converter or Interpreter.

NEL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0		
0	(0)	DBL WORD	8	NELLIST (0)	
0	(0)	ADDRESS	4	NELQMPA	PTR TO QMPA PROVIDING ACCESS TO CALLER'S SWA
4	(4)	ADDRESS	4	NELEXLST	PTR TO C/I'S LIST OF SPECIAL EXITS
8	(8)	ADDRESS	4	NELCOMID	PTR TO CONSOLE IDENTIFIER
12	(C)	ADDRESS	4	NELTXTCB	PTR TO OPEN ACB FOR INTERNAL TEXT DATA SET
16	(10)	ADDRESS	4	NELMSGCB	PTR TO OPEN ACB FOR MESSAGE DATA SET
20	(14)	ADDRESS	4	NELJMR	PTR TO JOB MANAGEMENT RECORD
		Comment			
		NEL OPTION SWITCHES COMMON TO CONVERTER AND INTERPRETER			
		End of Comment			
24	(18)	BITSTRING	1	NELOPSWT	OPTION SWITCHES
		1...		NELSMF	"X'80'" IF ZERO, INDICATES A STARTED TASK
		.1..		NELTSOP	"X'40'" TERM=TS HAS BEEN SPECIFIED AND OVERRIDES ALL OTHER PARAMETERS ON THE DD STATEMENT
		..1.		NELRECVY	"X'20'" PROCESSING IS IN RECOVERY MODE AND MESSAGES ARE TO BE SURPRESSED
		...1		NELCNDGM	"X'10'" USE CONDITIONAL GETMAINS
		.... 1...		NELNEW	"X'08'" NEW FORMAT PARAMETER LIST
		.... .1..		NELTERM	"X'04'" TERMINATE CONVERTER ENVIRONMENT
25	(19)	ADDRESS	3	NELSYSNP	POINTER TO NAME OF THE SUBSYSTEM THAT SELECTED THIS JOB
		Comment			
		CONVERTER POINTERS			
		End of Comment			
28	(1C)	ADDRESS	4	NELJCLCB	PTR TO OPEN ACB FOR SPOOLED JCL DATA SET
32	(20)	ADDRESS	4	NELPROCB	PTR TO OPEN DCB FOR PROCEDURE LIBRARY
36	(24)	ADDRESS	4	NELSTMCB	PTR TO OPEN ACB FOR STATEMENT IMAGE DATA SET
		Comment			
		CONVERTER PARM FIELD MAPPING			
		End of Comment			
40	(28)	CHARACTER	1	NELPARMO	PARAMETER OPTIONS
		1		NELPGMN	"X'01'" PROGRAMMER NAME REQUIRED
		.... ..1.		NELACCT	"X'02'" ACCOUNT NUMBER REQUIRED
		.... .1..		NELXA2	"X'04'" USER SWA ABOVE INDICATOR
41	(29)		2	NELJPRTY	DEFAULT JOB PRIORITY
43	(2B)		6	NELTIME	DEFAULT FOR JOB TIME LIMIT

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
49	(31)		3	NELREG	DEFAULT REGION SIZE
52	(34)		1	NELCOMDS	COMMAND DISPOSITION 0 - EXECUTE COMMAND 1 - DISPLAY AND EXECUTE COMMAND 2 - DISPLAY AND REQUEST DISPOSITION 3 - IGNORE COMMAND
53	(35)		1	NELLABEL	LABEL PROCESSING 0 - BLP WILL BE TREATED AS NL 1 - BLP WILL BE TREATED AS BYPASS LABEL
54	(36)	CHARACTER	4	NELAUTH	MCS COMMAND AUTHORITY
58	(3A)	CHARACTER	2	NELMSGL (0)	MESSAGE LEVEL DEFAULTS
58	(3A)	CHARACTER	1	NELMSGL1	JCL MSGLEVEL DEFAULT
59	(3B)	CHARACTER	1	NELMSGL2	ALLOCATION MSGLEVEL DEFAULT
60	(3C)	CHARACTER	1	NELMSGCL	DEFAULT SYSTEM OUTPUT CLASS(MSGCLASS)
61	(3D)	CHARACTER	1		RESERVED
62	(3E)	BITSTRING	4	NELENVIR	ADDRESS OF EXISTING CONVERTER ENVIRONMENT
62	(3E)	X'42'	0	NELCSIZE	"*" USED TO DETERMINE SIZE OF CONVERTER DATA
					Comment
					INTERPRETER POINTERS
					End of Comment
28	(1C)	ADDRESS	4	NELJCT	PTR TO JCT IN SWA
					Comment
					INTERPRETER OPTION SWITCH
					End of Comment
32	(20)	BITSTRING	1	NELOPSW2	OPTION SWITCHES - BYTE 2
		1...		NELADSPC	"X'80'" FAIL JOB IF ADDRSPC=REAL CODED BY UNAUTHORIZED USER(E.G. LOGON PROC)
		.1..		NELSWBSP	"X'40'" SWB SUPPORT IS TO BE PROVIDED
		..1.		NELXA1	"X'20'" CALLER SWA ABOVE INDICATOR
		...1		NELSISO	"X'10'" SYSIN/SYSOUT SWA BELOW INDICATOR
		.... 1...		NELWTOSP	"X'08'" SUPPRESS WTO MESSAGES
		.... .1..		NELSMSBY	"X'04'" BYPASS DFSMS IDAX PROCESSING
33	(21)	CHARACTER	1	NELDSENQSHR	DSENQSHR JOBCLASS attribute NOTE: This byte must be mapped identically in IEFSSJS. This byte must only be used for the DSENQSHR JOBCLASS attribute, and only updated when an additional DSENQSHR value is to be added.
		1...		NELDSENQSHR_AUTO	"X'80'" DSENQSHR value AUTO
		.1..		NELDSENQSHR_ALLOW	"X'40'" DSENQSHR value ALLOW
		....		NELDSENQSHR_DISALLOW	"X'00'" DSENQSHR value DISALLOW NOTE: ALLOW is the default for V2R1 installations. If this is a downlevel installation, it will assume the value of DISALLOW (which is 0). Therefore, the function will always be disabled on V1R13 and below level installations
34	(22)	CHARACTER	2		RESERVED
36	(24)	ADDRESS	4	NELJICA	PTR TO JES/INTERPRETER COMMUNICATION AREA
36	(24)	X'28'	0	NELISIZE	"*" USED TO DETERMINE SIZE OF INTERPRETER DATA
					Comment
					NEL EXIT LIST MAPPING
					End of Comment
66	(42)	X'48'	0	NELEXTLN	"72" - SYMBOLIC LENGTH OF EXIT LIST
72	(48)	DBL WORD	8	NELEXITS (0)	
72	(48)	CHARACTER	8	NELEXHDR (0)	EXIT LIST HEADER RECORD
72	(48)	SIGNED	2	NELEXLEN	LIST LENGTH
74	(4A)	SIGNED	2	NELXRTCD	INTERPRETER RETURN CODE
76	(4C)	CHARACTER	4		
80	(50)	CHARACTER	8	IAMEXIT (0)	
80	(50)	BITSTRING	1	IAMEXLK	LINKAGE DEFINITION
81	(51)	BITSTRING	1	IAMEXID	EXIT IDENTIFICATION
82	(52)	CHARACTER	6	IAMEXEP	ENTRY POINT
88	(58)	CHARACTER	8	FAMEXIT (0)	
88	(58)	BITSTRING	1	FAMEXLK	LINKAGE DEFINITION
89	(59)	BITSTRING	1	FAMEXID	EXIT IDENTIFICATION
90	(5A)	CHARACTER	6	FAMEXEP	ENTRY POINT
96	(60)	CHARACTER	8	QEPEXIT (0)	
96	(60)	BITSTRING	1	QEPEXLK	LINKAGE DEFINITION

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
97	(61)	BITSTRING	1	QEPEXID	EXIT IDENTIFICATION
98	(62)	CHARACTER	6	QEPEXEP	ENTRY POINT
104	(68)	CHARACTER	8	SMFEXIT (0)	
104	(68)	BITSTRING	1	SMFEXLK	LINKAGE DEFINITION
105	(69)	BITSTRING	1	SMFEXID	EXIT IDENTIFICATION
106	(6A)	CHARACTER	6	SMFEXEP	ENTRY POINT
112	(70)	CHARACTER	8	TXTEXIT (0)	
112	(70)	BITSTRING	1	TXTEXLK	LINKAGE DEFINITION
113	(71)	BITSTRING	1	TXTEXID	EXIT IDENTIFICATION
114	(72)	CHARACTER	6	TXTEXEP	ENTRY POINT
120	(78)	CHARACTER	8	RTNEXIT (0)	
120	(78)	BITSTRING	1	RTNEXLK	LINKAGE DEFINITION
121	(79)	BITSTRING	1	RTNEXID	EXIT IDENTIFICATION
122	(7A)	CHARACTER	6	RTNEXEP	ENTRY POINT
128	(80)	CHARACTER	8	QLPEXIT (0)	
128	(80)	BITSTRING	1	QLPEXLK	LINKAGE DEFINITION
129	(81)	BITSTRING	1	QLPEXID	EXIT IDENTIFICATION
130	(82)	CHARACTER	6	QLPEXEP	ENTRY POINT
136	(88)	CHARACTER	8	JDVEXIT (0)	
136	(88)	BITSTRING	1	JDVEXLK	LINKAGE DEFINITION
137	(89)	BITSTRING	1	JDVEXID	EXIT IDENTIFICATION
138	(8A)	CHARACTER	6	JDVEXEP	ENTRY POINT
				Comment	
				GENERAL EXIT LIST ENTRY MAPPING	
				End of Comment	
144	(90)	CHARACTER	1	NELEXENT (0)	ORIGIN ZERO
144	(90)	CHARACTER	1	NELEXLK	LINKAGE IDENTIFICATION
145	(91)	CHARACTER	1	NELEXID	EXIT IDENTIFICATION
146	(92)	CHARACTER	6	NELEXEP	EXIT ENTRY POINT
146	(92)	X'94'	0	NELEXEPA	"NELEXEP+2" DISPL OF ADDR SPECIFIED
				Comment	
				CONSTANTS USED IN EXIT LIST GENERATION LINKAGE ID	
				End of Comment	
		..1.		NELEXAD4	"X'20'" ENTRY POINT SPECIFIED AS 4-BYTE ADDR
		.1..		NELEXADD	"X'40'" ENTRY POINT SPECIFIED AS 3-BYTE ADDRESS
		1...		NELEXNAM	"X'80'" ENTRY POINT SPECIFIED AS 6-BYTE MODULE NAME
		11..		NELEXVCN	"X'C0'" ENTRY POINT SPECIFIED AS V-CON AT EXIT POINT
		....		NELEXNOP	"X'00'" EXIT ENTRY IS TO BE IGNORED * *
				Comment	
				EXIT ID	
				End of Comment	
		.1..		NELIAMEX	"X'40'" SPECIAL INPUT ACCESS METHOD EXIT ID
		..11		NELJDVEX	"X'30'" SPECIAL DATA JDVT NAME POINTER ID
		..1.		NELRTNEX	"X'20'" SPECIAL RETURN EXIT ID
		...1		NELFAMEX	"X'10'" SPECIAL FIND ACCESS METHOD EXIT ID
		.... 1...		NELQEPEX	"X'08'" SPECIAL QUEUE MANAGER ENTRY POINT ID
		1...		NELTXTEX	"X'80'" POST SCAN TEXT EXIT ID
		.... .111		NELSMFEX	"X'07'" SYSTEM MANAGEMENT FACILITIES EXIT ID
		.... .1..		NELQLPEX	"X'04'" SPECIAL QUEUE MANAGER FOR LOCATE MODE ENTRY POINT ID
		.... 1..1		NELUJVEX	"X'09'" IEFUJV with Subsystem Environment Information ID

NEL Cross Reference

Name	Hex Offset	Hex Value
FAMEXEP	5A	
FAMEXID	59	
FAMEXIT	58	
FAMEXLK	58	
IAMEXEP	52	
IAMEXID	51	
IAMEXIT	50	
IAMEXLK	50	
JDVEXEP	8A	
JDVEXID	89	
JDVEXIT	88	
JDVEXLK	88	
NELACCT	28	2
NELADSPC	20	80
NELAUTH	36	
NELCNDGM	18	10
NELCOMDS	34	
NELCOMID	8	
NELCSIZE	3E	42
NELDSENQSHR	21	
NELDSENQSHR_ALLOW	21	40
NELDSENQSHR_AUTO	21	80
NELDSENQSHR_DISALLOW	21	0
NELENVIR	3E	
NELEXADD	92	40
NELEXAD4	92	20
NELEXENT	90	
NELEXEP	92	
NELEXEPA	92	94
NELEXHDR	48	
NELEXID	91	
NELEXITS	48	
NELEXLEN	48	
NELEXLK	90	
NELEXLST	4	
NELEXNAM	92	80
NELEXNOP	92	0
NELEXTLN	42	48
NELEXVCN	92	C0
NELFAMEX	92	10
NELIAMEX	92	40
NELISIZE	24	28
NELJCLCB	1C	
NELJCT	1C	
NELJDVEX	92	30
NELJICA	24	
NELJMR	14	
NELJPRTY	29	
NELLABEL	35	
NELLIST	0	
NELMSGCB	10	
NELMSGCL	3C	
NELMSGL	3A	
NELMSGL1	3A	
NELMSGL2	3B	
NELNEW	18	8
NELOPSWT	18	
NELOPSW2	20	
NELPARMO	28	
NELPGMN	28	1
NELPROCB	20	
NELQEPEX	92	8
NELQLPEX	92	4
NELQMPA	0	
NELRECVY	18	20
NELREG	31	
NELRTNEX	92	20
NELSISO	20	10
NELSMF	18	80
NELSMFEX	92	7
NELSMSBY	20	4
NELSTMCB	24	
NELSWBSP	20	40
NELSYSNP	19	
NELTERM	18	4
NELTIME	2B	
NELTSOP	18	40
NELTXTCB	C	
NELTXTEX	92	80
NELUJVEX	92	9
NELWTOSP	20	8
NELXA1	20	20
NELXA2	28	4
NELXRTCD	4A	
QEPEXEP	62	
QEPEXID	61	
QEPEXIT	60	
QEPEXLK	60	
QLPEXEP	82	
QLPEXID	81	
QLPEXIT	80	
QLPEXLK	80	
RTNEXEP	7A	
RTNEXID	79	
RTNEXIT	78	
RTNEXLK	78	
SMFEXEP	6A	
SMFEXID	69	
SMFEXIT	68	
SMFEXLK	68	
TXTEXEP	72	
TXTEXID	71	
TXTEXIT	70	
TXTEXLK	70	

NLLE Information

NLLE Heading Information

Common Name: Nucleus Load List Element (NLLE)
Macro ID: IEANLLE
DSECT Name: None
Owning Component: IPL (SC1C9)
Eye-Catcher ID: NLLE
Offset: 0
Length: 4
Storage Attributes: Subpool: IPL workspace
Key: 0
Residency: Above 16M
Size: See XREF
Created by: IEAIPL40
IEAIPL42
IPXI50PS
Pointed to by: IVTNLLEF
IVTNLLEL
NLLNEXT
Serialization: None
Function: An NLLE is built for each module that is loaded into the DAT-on nucleus.

NLLE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	80	NLLE	Nucleus load list element
0	(0)	CHARACTER	4	NLLID	NLLE identifier ('NLLE')
4	(4)	ADDRESS	4	NLLNEXT	Pointer to next NLLE
8	(8)	CHARACTER	8	NLLNAME	SYS1.NUCLEUS member name of module
16	(10)	ADDRESS	4	NLLPDS	Pointer to PDS directory entry
20	(14)	ADDRESS	4	NLLCESDP	Pointer to CESD list
24	(18)	SIGNED	4	NLLCESDL	Length of CESD list
28	(1C)	ADDRESS	4	NLLRLOCP	Pointer to relocation tables
32	(20)	SIGNED	4	NLLRLOCL	Length of relocation tables
36	(24)	SIGNED	2	NLLDIMCE	Dimension of CESD list
38	(26)	SIGNED	2	NLLCSECT	Number of CSECTs in module
40	(28)	SIGNED	2	NLLESDID	ESDID of control section to which first block of text belongs
42	(2A)	BITSTRING	1	NLLFLAGS	Flag byte
		1...		NLLFNOWS	If 1, a wait state is NOT to be loaded if this module is not found
		.1..		NLLRSTRT	If 1, if a wait state is loaded because this module is not found, the wait stat should be restartable.
		..11 1111		*	Reserved
43	(2B)	UNSIGNED	1	*	Reserved
44	(2C)	SIGNED	4	NLLEPTAB	Position in the nucleus entry point table where the current load module ends and the next load module begins.
48	(30)	CHARACTER	8	NLLSECTIONSQ (4294967300:562119400)	
48	(30)	SIGNED	4	NLLSECTION_FIRST	Index of the first section entry in this load module for this part of the Nucleus.
52	(34)	SIGNED	4	NLLSECTION_LAST	Index of the last section entry in this load module for this part of the Nucleus.

NLLE Constants

Len	Type	Value	Name	Description
			Comment	
			The following constant is used to place an identifier in each NLLE (NLLID field).	
			End of Comment	
4	CHARACTER	NLLE	NLLIDNM	NLLE identifier
			Comment	
			The following constants define the ordering of the Nucleus "sections" starting from low virtual addresses to high virtual addresses.	
			End of Comment	
4	DECIMAL	1	NLL_NS_RW_INDEX	Read Write Nucleus Section
4	DECIMAL	2	NLL_NS_RO_INDEX	Read Only Nucleus Section
4	DECIMAL	3	NLL_NS_ERO_INDEX	Extended Read Only Nucleus Section
4	DECIMAL	4	NLL_NS_ERW_INDEX	Extended Read Write Nucleus Section
			Comment	
			Warning: The following constant must be updated if the layout of Nucleus ever changes to include a different number of sections.	
			End of Comment	
4	DECIMAL	4	NLLNUCSECTIONSDIM	Dimension of the NLLSectionsQ array.

NLLE Cross Reference

Name	Hex Offset	Hex Value
NLLCESDL	18	
NLLCESDP	14	
NLLCSECT	26	
NLLDIMCE	24	
NLLE	0	
NLLEPTAB	2C	
NLLESDID	28	
NLLFLAGS	2A	
NLLFNOWS	2A	80
NLLID	0	
NLLNAME	8	
NLLNEXT	4	
NLLPDS	10	
NLLRLOCL	20	
NLLRLOCP	1C	
NLLRSTRT	2A	40
NLLSECTION_FIRST	30	
NLLSECTION_LAST	34	
NLLSECTIONSQ	30	

NSSA Information

NSSA Heading Information

Common Name: RTM Normal Stack Save Area
Macro ID: IHANSSA
DSECT Name: NSSA
Owning Component: Recovery Termination Manager (SCRTM)
Eye-Catcher ID: NSSA
Offset: 0
Length: 4
Storage Attributes: Subpool: 239
Key: 0
Residency: Above 16M
Size: Calculated
Created by: RTM
Pointed to by: N/A
Serialization: None
Function: The NSSA contains a saved copy of the normal FRR stack when an enabled unlocked task has established FRRs using the EUT=YES option of the SETFRR macro.

NSSA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	1288	NSSA	
0	(0)	CHARACTER	4	NSSAID	CONTROL BLOCK ID - NSSA
4	(4)	ADDRESS	4	NSSALINK	POINTER TO NEXT NSSA IN POOL
8	(8)	CHARACTER	1280	NSSAFRRS	AREA LARGE ENOUGH TO HOLD ENTIRE NORMAL STACK MINUS 12 BYTES
1288	(508)	CHARACTER	0	NSSAEND	

NUCMP Information

NUCMP Heading Information

Common Name: Nucleus Map Entry
Macro ID: IEANUCMP
DSECT Name: NUCMENT
Owning Component: Nucleus Initialization Program (SC1C8)
Eye-Catcher ID: None
Storage Attributes: Subpool: Nucleus
Key: 0
Residency: above 16M
Size: 16 bytes per entry
Created by: IEAIPL05
Pointed to by: CVTNUCMP
Serialization: None
Function: Describes the format of a nucleus map entry.

NUCMP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	NUCMENT	ENTRY IN THE NUCLEUS MAP
0	(0)	CHARACTER	8	NUCMNAME	CSECT OR ENTRY POINT NAME
8	(8)	ADDRESS	4	NUCMADDR	ADDRESS OF ENTRY POINT
12	(C)	CHARACTER	1	NUCMFLAG	VARIOUS ASSORTED FLAGS
		11..		*	UNUSED, MUST BE 0
		..1.		NUCMAM64	AMODE 64
		...1		NUCMSECT	1 IF CSECT
		 1111		NUCMRRAM	RSECT, RMODE, AMODE
		.... 1...		NUCMRSEC	RSECT FLAG
		.... .1..		NUCMRMOD	RMODE FLAG, (0 - 24 BIT), (1 - ANY)
		11		NUCMAMOD	AMODE FLAG WHEN NUCAM64 IS OFF: (00 - 24 BIT), (01 - 24 BIT), (10 - 31 BIT), (11 - ANY)
13	(D)	UNSIGNED	3	NUCMLEN	LENGTH TO END OF CSECT

NUCMP Cross Reference

Name	Hex Offset	Hex Value
NUCMADDR	8	
NUCMAMOD	C	03
NUCMAM64	C	20
NUCMENT	0	
NUCMFLAG	C	
NUCMLEN	D	
NUCMNAME	0	
NUCMRMOD	C	04
NUCMRRAM	C	0F
NUCMRSEC	C	08
NUCMSECT	C	10

NVT Information

NVT Heading Information

Common Name: NIP Vector Table
Macro ID: IHANVT
DSECT Name: NVT
Owning Component: Nucleus Initialization Program (SC1C8)
Eye-Catcher ID: NVT
Offset: 0
Length: 4
Storage Attributes: Subpool: 245
Key: 0
Residency: Nucleus, then moved to subpool 245
Size: 656 bytes
Created by: IEAVNIP0
IEAVNIPM
Pointed to by: CVTNVT0
Serialization: None
Function: The NVT is the basic control block used during NIP processing. It contains pointers to numerous NIP-associated control blocks and to various NIP service routines.

NVT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	656	NVT	Begin based NVT
0	(0)	CHARACTER	4	NVTID	CONTROL BLOCK ID
4	(4)	CHARACTER	8	NVTMODNM	NAME OF THE ACTIVE RIM
12	(C)	ADDRESS	4	NVTMODEP	ENTRY POINT ADDRESS OF THE ACTIVE RIM
16	(10)	CHARACTER	3	NVTR010	Reserved
19	(13)	UNSIGNED	1	NVT07BRC	Reason code for 07B wait state
20	(14)	ADDRESS	4	NVTSPTT	ADDRESS OF THE VSM SPTTINDX
24	(18)	CHARACTER	8	NVTPARMM	Copy of merged set of load parameters
32	(20)	ADDRESS	4	NVTVSP	ADDRESS OF VSM SUBPOOL TABLE
36	(24)	ADDRESS	4	NVTLPALP	ADDRESS OF THE LPA DEVICE SUPPORT MODULE LIST
40	(28)	SIGNED	4	NVTLPALL	LENGTH OF THE LPA DEVICE SUPPORT MODULE LIST
44	(2C)	ADDRESS	4	NVTMQHP	POINTER TO THE IPL MESSAGE QUEUE HEADER (MQH).
48	(30)	ADDRESS	4	NVTDIAGA	POINTER TO NIP DIAGNOSTIC AREA
52	(34)	UNSIGNED	2	NVTIODFD	Hex device number of IODF Dataset used during IPL
54	(36)	CHARACTER	1	NVTNPATR	Module attribute
		1111 111.		*	Reserved
		1		NVTCTLGV	NVTCTLGP is valid if 1
55	(37)	CHARACTER	1	NVTFLLB	SVCLIB LOGREC
		1...		NVTFLSLB	SVCLIB LOGREC
56	(38)	ADDRESS	4	NVTMSTCB	NIP TCB pointer
60	(3C)	ADDRESS	4	NVTCTLGP	Address of catalog info.
64	(40)	ADDRESS	4	NVTMASCB	Master ASCB address
68	(44)	ADDRESS	4	NVTUSERP	Pointer to list of user parmlib elements.
72	(48)	ADDRESS	4	NVTSYMH	Pointer to symbol element header.
76	(4C)	ADDRESS	4	NVTSVCTB	Address of SVC table
80	(50)	BITSTRING	4	NVTFLGS	Misc NVTFlgs.
80	(50)	CHARACTER	1	NVTFLGS1	First Byte of Flags.
		1...		NVTVM	MVS Guest under VM.
		.1..		NVTVMXA	MVS Guest under VM/XA. NOTE: NVTVM will be on also.
81	(51)	CHARACTER	1	NVTFLGS2	2nd Byte of Flags.
		1...		NVTNOLMG	No License Manager Msg
		.1..		NVTLMSPE	Do "special" processing for this LM call, since this is "our" call
		..1.		NVTWARNUND	WARNUND processing is in effect
84	(54)	ADDRESS	4	NVTIGCER	SVC error routine address
88	(58)	ADDRESS	4	NVTVVMDI	LPA hash value address
92	(5C)	ADDRESS	4	NVTMSLNK	LINK parmlist address
96	(60)	ADDRESS	4	NVTAVTP	Address of the Allocation Vector Table (AVT)
100	(64)	ADDRESS	4	NVTNCRP	Virtual address of NCR
104	(68)	ADDRESS	4	NVTLOAD	Virtual address of LOADxx
108	(6C)	ADDRESS	4	NVTNP0AD	Address of NIP0
112	(70)	SIGNED	4	NVTNP0NO	Number of pages in IEAVNIP0 and IEAVNIPH combined.
116	(74)	ADDRESS	4	NVTIGXER	ESR error routine
120	(78)	SIGNED	4	NVTR078	Reserved
124	(7C)	ADDRESS	4	NVTLSQAS	End of master SQA
128	(80)	SIGNED	2	NVTSQANO	Number of SQA pages
130	(82)	SIGNED	2	NVTLSQNO	Number of LSQA pages

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
132	(84)	SIGNED	2	NVTNCRC	Count of NCRs in buffer
134	(86)	SIGNED	2	NVTNCRL	Length of each NCR
136	(88)	SIGNED	2	NVTLOADL	Length of LOADxx record
138	(8A)	SIGNED	2	NVTNVSQA	No.of virtual seg of SQA
140	(8C)	CHARACTER	8	NVTABSAV	SVC table - SVC 13
140	(8C)	ADDRESS	4	NVTABFST	
144	(90)	CHARACTER	4	NVTABSEC	
148	(94)	SIGNED	4	NVTPCIES	PCIE size
152	(98)	CHARACTER	8	NVTPCIEA	PCIE address
160	(A0)	ADDRESS	4	NVTALSQA	Low address of M.S. LSQA
164	(A4)	ADDRESS	4	NVTASQA	Low address of SQA
168	(A8)	SIGNED	4	NVTESQAB	Total additional ESQA buffer for each subchannel installed.
172	(AC)	ADDRESS	4	NVTRTMSA	Addr. of RTM branch entry
176	(B0)	CHARACTER	8	NVTN0PSW	PSW points to NIP0
184	(B8)	ADDRESS	4	NVTDOMID	DOMed message IEA247I
188	(BC)	CHARACTER	3	NVTR0BC	Reserved
191	(BF)	CHARACTER	1	NVTMTLSH	MTLSHARE value
192	(C0)	ADDRESS	4	NVTPPS	Address of MLPA
196	(C4)	ADDRESS	4	NVTPPE	Ending address of MLPA
200	(C8)	ADDRESS	4	NVTEPPS	Address of EMLPA
204	(CC)	ADDRESS	4	NVTEPPE	Ending address of EMLPA
208	(D0)	CHARACTER	1	NVTARCLV	Architecture level
209	(D1)	CHARACTER	1	NVTNCXID	Nucleus extension ID
210	(D2)	CHARACTER	1	*	
		1...		NVTETR	ETR indicator
		.1..		NVTPCS	PCS indicator
		..1.		NVTETRSS	ETR secondary indicator
		...1		NVTCSP	CSP command indicator
		.... 1...		*	Reserved
		.... .1..		NVTASYNC	Asynchronous paging indicator
		11		*	Reserved
211	(D3)	CHARACTER	1	NVTFLCN	
		1...		NVTWTOIN	WTO initialized
		.1..		NVTCI	System console communications are supported.
		..11 111.		*	Reserved
		1		NVTCLKER	TOD clock was in error
212	(D4)	SIGNED	4	NVTTOD	TOD clock value
216	(D8)	CHARACTER	8	NVTMCPSW	Machine check PSW
224	(E0)	CHARACTER	8	NVTWTPSW	System wait state PSW
224	(E0)	CHARACTER	4	NVTWPSW1	First word of PSW
228	(E4)	SIGNED	4	NVTWPSW2	Second word of PSW
228	(E4)	CHARACTER	2	NVTIDPSW	NIP module name
230	(E6)	CHARACTER	2	NVTWSCD	Wait state code
230	(E6)	CHARACTER	1	NVTFLWS1	
231	(E7)	CHARACTER	1	NVTFLWSC	
231	(E7)	CHARACTER	1	NVTIX	End initial NVT

Comment

NVT pointers to IEAVNIPM routines

End of Comment

232	(E8)	ADDRESS	4	NVTR0E8	Reserved
236	(EC)	ADDRESS	4	NVTSENSE	SENSE routine address
240	(F0)	ADDRESS	4	NVTR0F0	Reserved.
244	(F4)	ADDRESS	4	NVTTIME	TIME routine address
248	(F8)	ADDRESS	4	NVTR0F8	Reserved.
252	(FC)	ADDRESS	4	NVTR0FC	Reserved.
				(4294967299:562114560)	
264	(108)	ADDRESS	4	NVTOPEN	NIPOPEN routine address
268	(10C)	ADDRESS	4	NVTMOUNT	NIPMOUNT routine address
272	(110)	ADDRESS	4	NVTPRMPT	NIPPRMPT routine address
276	(114)	ADDRESS	4	NVTR114	Reserved
				(4294967299:562114560)	
288	(120)	ADDRESS	4	NVTNSRVP	DFP NIP service vector address
292	(124)	ADDRESS	4	NVTNIPM	IEAVNIPM base reg save area
				(4294967298:562114560)	
300	(12C)	ADDRESS	4	NVTNPM4	NIP OPEN and MOUNT routine
304	(130)	ADDRESS	4	NVTNCTAD	NIP console table address
308	(134)	ADDRESS	4	NVTUCB	Active console UCB addr
312	(138)	SIGNED	4	NVTCODE	Active console device code
316	(13C)	ADDRESS	4	NVTR13C	Reserved
				(4294967298:562114560)	
324	(144)	ADDRESS	4	NVTDCBIC	Input console DCB address

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
328	(148)	ADDRESS	4	NVTDCBOC	Output console DCB address
332	(14C)	ADDRESS	4	NVTDCBSN	SYS1.NUCLEUS DCB address
336	(150)	ADDRESS	4	NVTMBUF	SQA message buffer address
340	(154)	ADDRESS	4	NVTMBEND	End of message buffer
344	(158)	ADDRESS	4	NVTSPE	NIPSPE queue origin
				(4294967298:562114560)	
352	(160)	SIGNED	4	NVTR160	Reserved
356	(164)	CHARACTER	2	NVTCPUAD	Address of CPU with lock
358	(166)	CHARACTER	2	NVTR166	Reserved
360	(168)	SIGNED	2	NVTR168	Reserved
362	(16A)	CHARACTER	2	NVTR16A	Reserved
364	(16C)	ADDRESS	4	NVTR16C	Reserved
368	(170)	ADDRESS	4	NVTPAREA	First parameter area pointer
372	(174)	ADDRESS	4	NVTPTAB	Origin of parameter table
376	(178)	ADDRESS	4	NVTQSBUF	Quick start buffer address
380	(17C)	ADDRESS	4	NVTSVCN	SVC table work area address
384	(180)	ADDRESS	4	NVTR180	Reserved
				(4294967300:562114560)	
400	(190)	ADDRESS	4	NVTVRBLD	LPA BLDL entry address
404	(194)	ADDRESS	4	NVTR194	Reserved
408	(198)	ADDRESS	4	NVTCSLIB	SYS1.LPALIB DCB address
412	(19C)	ADDRESS	4	NVTCSLNM	Current LPA name address
416	(1A0)	ADDRESS	4	NVTCSIOB	Address of IOB fail cold start
420	(1A4)	ADDRESS	4	NVTR1A4	Reserved
				(4294967298:562114560)	
428	(1AC)	CHARACTER	8	NVTXCTL	XCTL address
436	(1B4)	CHARACTER	8	NVTLOCAT	LOCATE SVC table entry
436	(1B4)	ADDRESS	4	NVTLFST	SVC routine address
440	(1B8)	CHARACTER	4	NVTLSEC	Flags and attribute
444	(1BC)	CHARACTER	8	NVTWTSAV	Save WTO SVC table entry
444	(1BC)	ADDRESS	4	NVTWTFST	SVC routine address
448	(1C0)	CHARACTER	4	NVTWTSEC	Flags and attributes
452	(1C4)	ADDRESS	4	NVTR1C4	Reserved
				(4294967305:562114560)	
488	(1E8)	BITSTRING	4	NVTRTYIN	Retry Info
		1...		NVTRETRY	Retry requested. Must be reset by the recovery routine
492	(1EC)	ADDRESS	4	NVTONUCS	NUCLEUS start address
496	(1F0)	ADDRESS	4	NVTONUCE	NUCLEUS ending address
500	(1F4)	ADDRESS	4	NVTPLDCB	PARMLIB DCB address
504	(1F8)	SIGNED	4	NVTPLBKL	PARMLIB block size
508	(1FC)	ADDRESS	4	NVTPLBFS	PARMLIB buffer address
512	(200)	ADDRESS	4	NVTPLBFE	PARMLIB buffer end address
516	(204)	ADDRESS	4	NVTPLRCD	PARMLIB buffer record processed
520	(208)	CHARACTER	8	NVTPLNAM	Name of last PARMLIB member
528	(210)	CHARACTER	1	NVTFLPO	Parameter options flags
		1...		NVTFLLST	Dispaly PARMLIB lists
		.1..		NVTSYSP	NP03 in prompt mode
		..11		*	Reserved.
		.... 1...		NVTFLQS	LPA is quick startable
		.... .1..		NVTFLWS	Warm start VAM datasets
		.... ..1.		NVTNPFL	NOPROT was specified for FLPA
		1		NVTNPML	NOPROT was specified for MLPA
529	(211)	CHARACTER	3	NVTR211	Reserved
532	(214)	CHARACTER	64	NVTDIAG	Diagnostic area
532	(214)	ADDRESS	4	NVTDIAGR	Register savearea
				(15:562114560)	
596	(254)	CHARACTER	60	NVTNCT	NIP console table
656	(290)	CHARACTER	0	*	End of NVT

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8	NVTPARMS	Mapping of merged set of load parameters used to IPL.
0	(0)	CHARACTER	4	NVTIODFU	IODF Dataset unit address in EBCDIC
4	(4)	CHARACTER	2	NVTLOADS	LOADxx Member Suffix
6	(6)	CHARACTER	1	NVTPROMT	Prompt Operator Flag.
7	(7)	CHARACTER	1	NVTNUCID	IEANUC0X suffix: Nucleus ID

NVT Constants

Len	Type	Value	Name	Description
1	HEX	30	NVTFLWAB	UNEXPECTED TASK ABEND
1	HEX	32	NVTFLWNN	NIP MODULE NOT FOUND BY BLDL
1	HEX	33	NVTFLWBN	I/O ERROR ON BLDL
1	HEX	37	NVTFLWNL	REQUIRED LIBRARY NOT FOUND
1	HEX	0A	NVTFLWLC	SYS1.LINKLIB NOT CATALOGED
1	HEX	39	NVTFLWPR	PERM RESIDENT MOUNT CONFLICT
1	HEX	3C	NVTFLWNM	INSUFFICIENT AUXILIARY STORAGE
1	HEX	3F	NVTFLWSE	NIP DIAGNOSED SYSTEM ERROR
1	HEX	40	NVTFLWAM	UNEXPECTED NIP TASK ABEND
1	HEX	07	NVTFLWNC	No NIP console.
1	HEX	46	NVTF0UPC	NIP0 PROG CHECK
1	HEX	7B	NVTFNXAX	RESULTS FROM AN OPERATION EXCEPTION ON INSTRUCTION SUPPORTED BY THE 370/XA EXTENSIONS ARCHITECTURE.
1	HEX	7B	NVTFNFNI	Facility not installed (of which LAE is one).
1	HEX	60	NVTWCN02	FINDPAGE FAILURE
1	HEX	61	NVTWCN03	STORE CLOCK ERROR
1	HEX	4A	NVTWCN05	TOD CLOCK ERROR
1	HEX	63	NVTWCN01	GETMAIN FAILED
1	HEX	5C	NVTWCN07	CANT RETRIEVE CATALOG POINTER FROM THE SYS1.NUCLEUS D.S.
1	HEX	5D	NVTWCN08	CANT READ DSCB FOR MASTER CATALOG
1	HEX	5E	NVTWCN09	I/O ERROR READING CATALOG SELF-DESCRIBING RECORDS
1	HEX	5F	NVTWCN10	CATALOG DAMAGE
1	HEX	64	NVTWCN11	RTM ENTERED AT NIP
1	HEX	65	NVTWCN12	INVALID SVC ISSUED
1	HEX	44	NVTWCN14	MACHINE CHECK
1	HEX	59	NVTWCN17	UNDEFINED BLDL RC
1	HEX	E8	NVTWS0E8	LOADED BY IGFRIM00
1	HEX	1F	NVTXMSIM	Request for cross memory simulation
1	HEX	53	NVTWCSQA	SQA HAS BEEN EXHAUSTED.

NVT Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
NVT	0		NVTFLPO	210	
NVTABFST	8C		NVTFLQS	210	08
NVTABSAV	8C		NVTFLSLB	37	80
NVTABSEC	90		NVTFLWS	210	04
NVTALSQA	A0		NVTFLWSC	E7	
NVTARCLV	D0		NVTFLWS1	E6	
NVTASQA	A4		NVTID	0	
NVTASYNC	D2	04	NVTIDPSW	E4	
NVTAVTP	60		NVTIGCER	54	
NVTCI	D3	40	NVTIGXER	74	
NVTCLKER	D3	01	NVTIODFD	34	
NVTCODE	138		NVTIODFU	0	
NVTCPUAD	164		NVTIX	E7	
NVTCSIOB	1A0		NVTLFST	1B4	
NVTCSLIB	198		NVTLMSPE	51	40
NVTCSLNM	19C		NVTLOAD	68	
NVTCSP	D2	10	NVTLOADL	88	
NVTCTLGP	3C		NVTLOADS	4	
NVTCTLGV	36	01	NVTLOCAT	1B4	
NVTDCBIC	144		NVTLPALL	28	
NVTDCBOC	148		NVTLPALP	24	
NVTDCBSN	14C		NVTLSEC	1B8	
NVTDIAG	214		NVTLSQAS	7C	
NVTDIAGA	30		NVTLSQNO	82	
NVTDIAGR	214		NVTMASCB	40	
NVTDOMID	B8		NVTMBEND	154	
NVTEPPE	CC		NVTMBUF	150	
NVTEPPS	C8		NVTMCPSW	D8	
NVTESQAB	A8		NVTMODEP	C	
NVTETR	D2	80	NVTMODNM	4	
NVTETRSS	D2	20	NVTMOUNT	10C	
NVTFLCN	D3		NVTMQHP	2C	
NVTFLGS	50		NVTMSLNK	5C	
NVTFLGS1	50		NVTMSTCB	38	
NVTFLGS2	51		NVTMTLSH	BF	
NVTFLLB	37		NVTNCRC	84	
NVTFLLST	210	80	NVTNCRL	86	

Name	Hex Offset	Hex Value
NVTNCRP	64	
NVTNCT	254	
NVTNCTAD	130	
NVTNCXID	D1	
NVTNIPM	124	
NVTNOLMG	51	80
NVTNPATR	36	
NVTNPFL	210	02
NVTNPML	210	01
NVTNPM4	12C	
NVTNP0AD	6C	
NVTNP0NO	70	
NVTNSRVP	120	
NVTNUCID	7	
NVTNVSQA	8A	
NVTN0PSW	B0	
NVTONUCE	1F0	
NVTONUCS	1EC	
NVTOPEN	108	
NVTPAREA	170	
NVTPARMM	18	
NVTPARMS	0	
NVTPCIEA	98	
NVTPCIES	94	
NVTPCS	D2	40
NVTPLBFE	200	
NVTPLBFS	1FC	
NVTPLBKL	1F8	
NVTPLDCB	1F4	
NVTPLNAM	208	
NVTPLRCD	204	
NVTPPE	C4	
NVTPPS	C0	
NVTPRMPT	110	
NVTPROMT	6	
NVTPTAB	174	
NVTQSBUF	178	
NVTRETRY	1E8	80
NVTRTMSA	AC	
NVTRTYIN	1E8	
NVTR0BC	BC	
NVTR0E8	E8	
NVTR0FC	FC	
NVTR0F0	F0	
NVTR0F8	F8	
NVTR010	10	
NVTR078	78	
NVTR1A4	1A4	
NVTR1C4	1C4	
NVTR114	114	
NVTR13C	13C	
NVTR16A	16A	
NVTR16C	16C	
NVTR160	160	
NVTR166	166	
NVTR168	168	
NVTR180	180	
NVTR194	194	
NVTR211	211	
NVTSENSE	EC	
NVTSPE	158	
NVTSPTT	14	
NVTSQANO	80	
NVTSVCN	17C	
NVTSVCTB	4C	
NVTSYMH	48	
NVTSYSP	210	40
NVTTIME	F4	
NVTTOD	D4	
NVTUCB	134	
NVTUSERP	44	
NVTVM	50	80
NVTVMXA	50	40
NVTVRBLD	190	
NVTVSP	20	
NVTVVMDI	58	
NVTWARNUND	51	20
NVTWPSW1	E0	
NVTWPSW2	E4	
NVTWSCD	E6	
NVTWTFST	1BC	
NVTWTOIN	D3	80
NVTWTPSW	E0	
NVTWTSAV	1BC	
NVTWTSEC	1C0	
NVTXCTL	1AC	
NVT07BRC	13	

OMDG Information

OMDG Heading Information

Common Name: Operations Measurement Data Gatherer Parameter List
Macro ID: IEZVG102
DSECT Name: OMDGLIST
Owning Component: Communications task (SC1CK)
Eye-Catcher ID: OMDG
Offset: 0
Length: 4
Storage Attributes: Residency: User's storage
Size: 52 bytes
Created by: Caller
Pointed to by: Reg 1 -> PTR -> Parmlist
Serialization: N/A
Function: Provides the interface between the caller and Commtask data collection routine to calculate the the number of WTO's, Commands and WTL's issued per millisecond. It also provides the maximum number of WQE's, ORE's and AMRQ's there are for a specified interval.

OMDG Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	OMDGLIST	OMD gatherer parameter list
0	(0)	CHARACTER	4	OMDGACR	Acronym 'OMDG'
4	(4)	CHARACTER	1	OMDGVER	Version level
5	(5)	CHARACTER	3		Reserved
8	(8)	BITSTRING	1	OMDGFNCD	Function codes
		1...		OMDGWTO	"X'80'" Code for WTO's
		.1..		OMDGCMD	"X'40'" Code for commands
		..1.		OMDGWTL	"X'20'" Code for WTL's
		...1		OMDGWQE	"X'10'" Code for max # WTO's on the queue
		.... 1...		OMDGORE	"X'08'" Code for max # WTOR's on the queue
		.... .1..		OMDGAMR	"X'04'" Code for max # AMRQ's on the queue
9	(9)	BITSTRING	1		Reserved
10	(A)	BITSTRING	1	OMDGRSCD	Reason codes
		1...		OMDGRWTO	"X'80'" No WTO data gathered
		.1..		OMDGRCMD	"X'40'" No commands data gathered
		..1.		OMDGRWTL	"X'20'" no WTL data gathered
11	(B)	BITSTRING	1		Reserved
12	(C)	SIGNED	4	OMDGWTOI	# of WTO's issued
16	(10)	SIGNED	4	OMDGCMDI	# of commands issued
20	(14)	SIGNED	4	OMDGWTLI	# of WTL's issued
24	(18)	SIGNED	4	OMDGWQEB	Max # of WTO's on the queue
28	(1C)	SIGNED	4	OMDGOREB	Max # of WTOR's on the queue
32	(20)	SIGNED	4	OMDGAMRE	Max # of entries on AMRQ queue
36	(24)	CHARACTER	16		Reserved
36	(24)	X'34'	0	OMDGLEN	"*-OMDGLIST" Length of macro
36	(24)	X'1'	0	OMDGSP41	"1" Version level MVS/XA HBB4410
36	(24)	X'1'	0	OMDGVRID	"OMDGSP41" Version level

OMDG Cross Reference

Name	Hex Offset	Hex Value
OMDGACR	0	
OMDGAMR	8	4
OMDGAMRE	20	
OMDGCMD	8	40
OMDGCMDI	10	
OMDGFNCD	8	
OMDGLEN	24	34
OMDGLIST	0	
OMDGORE	8	8
OMDGOREB	1C	
OMDGRCMD	A	40
OMDGRSCD	A	
OMDGRWTL	A	20
OMDGRWTO	A	80
OMDGSP41	24	1
OMDGVER	4	
OMDGVRID	24	1
OMDGWQE	8	10
OMDGWQEB	18	
OMDGWTL	8	20
OMDGWTLI	14	
OMDGWTO	8	80
OMDGWTOI	C	

OPSPL Information

OPSPL Heading Information

Common Name: ASM ILROPS00 Parameter List
Macro ID: ILROPSPL
DSECT Name: OPSPL
Owning Component: Auxiliary Storage Manager (SC1CW)
Eye-Catcher ID: None
Storage Attributes: Subpool: Caller defined
Key: Caller defined
Residency: Caller defined
Size: 68-bytes
Created by: Caller
Pointed to by: User defined variable, OPSPLPTR
Serialization: none
Function: Contains information necessary to interface with module ILROPS00. It serves as the parameter list for input and output for the module.

OPSPL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	136	OPSPL	ILROPS00 Parameter List
0	(0)	BITSTRING	1	OPSFLG1	Input flags
		1...		*	Reserved
		.1..		OPSPGAD	PAGEADD/PAGEDEL time indicator. 1 = processing a PAGEADD or PAGEDEL, after NIP time, 0 = processing during NIP
		..1.		OPSLOCV	1 = Locate the VIO journaling data set and return to caller with the return code. 0 = Do regular page data set OPEN processing.
		...1		OPSF1R1	Reserved
		.... 1...		OPSNOCB	Control block flag. 1 = do not get control blocks for the data set (used at NIP time to ensure that a data set is mounted and online without building control blocks for it), 0 = build data set control blocks.
		.... .1..		OPSPCD	PLPA/common flag. . 1 = data set is either the PLPA, or common page data set, 0 = data set is a local page data set
		.... ..1.		*	Reserved
		1		OPSF1R2	Reserved
1	(1)	UNSIGNED	1	OPSDUSE	Device usage code
2	(2)	UNSIGNED	1	OPSFUNC	ILROPS00 function code
3	(3)	CHARACTER	1	OPSRSV1	Reserved
4	(4)	CHARACTER	44	OPSDSN	Data set name (on input). This field is overlaid on output.
4	(4)	ADDRESS	4	OPSIORB	IORB pointer
8	(8)	ADDRESS	4	OPSUCB	UCB pointer
12	(C)	ADDRESS	4	OPSEDB	EDB pointer
16	(10)	UNSIGNED	4	OPSLTNUM	Number of slots on the data set
20	(14)	CHARACTER	2	OPSDVTYP	Device type
22	(16)	CHARACTER	6	OPSVOL	Volume serial number
28	(1C)	ADDRESS	4	OPSQAPTR	GETMAIN pointer in SQA
32	(20)	ADDRESS	4	OPSQALEN	GETMAIN length in SQA
36	(24)	CHARACTER	4	OPSCCHHB	Beginning CCHH for ECKD
40	(28)	CHARACTER	4	OPSCCHHE	Ending CCHH for ECKD
44	(2C)	BITSTRING	1	OPSFLG2	ILROPS00 output flags
		1...		OPSECKD	ECKD architecture flag. 1 = data set is on an ECKD device, 0 = non-ECKD device.
		.1..		OPSPAVOK	PAV capable device. 1 = data set is on a device defined to be PAV capable. 0 = data set is normal.
		..1.		OPSCACHEOK	1 = data set is on a device for which we should not bypass caching, 0 = caching should be bypassed
		...1		*	Reserved
		 1111		OPSF2R	Reserved
45	(2D)	UNSIGNED	1	*	Reserved
46	(2E)	UNSIGNED	2	OPSCCWS	Number of PCCWs actually initialized - may be affected by an excess CCW count
48	(30)	CHARACTER	20	OPSPART2	Other output section of the parameter list
48	(30)	ADDRESS	4	OPSDEIB	DEIB pointer
52	(34)	UNSIGNED	2	OPSDCCWS	Number of PCCWs for the device type, before considering any excess CCW count
54	(36)	CHARACTER	2	*	Reserved
56	(38)	BITSTRING	8	OPSDTIME	Dataset define timestamp
64	(40)	CHARACTER	4	OPSALOCD	Allocation reason code fields
64	(40)	CHARACTER	2	OPSERROR	Error reason code for allocation failure

OPSPL Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
66	(42)	CHARACTER	2	OPSINFO	Information reason code for allocation failure
				Comment	
				FREE CONTROL BLOCKS functional parameter list	
				End of Comment	
68	(44)	CHARACTER	16	OPSFREE_PL	Free control blocks parameter list
68	(44)	ADDRESS	4	OPSFREE_EDB	EDB address
72	(48)	ADDRESS	4	OPSFREE_DEIB	DEIB address (page only)
76	(4C)	ADDRESS	4	OPSFREE_IORB	IORB/IOSB/SRB/SRB address
80	(50)	ADDRESS	4	OPSFREE_PAT	PAT address
				Comment	
				UNALLOCATE DATA SET functional parameter list	
				End of Comment	
84	(54)	CHARACTER	12	OPSUNALC_PL	Unallocate data set parameter list
84	(54)	ADDRESS	4	OPSUNALC_UCB	UCB address
88	(58)	UNSIGNED	1	*	Reserved
89	(59)	BITSTRING	1	OPSUNALC_FLAGS	
					Flags
		1...		*	Reserved
90	(5A)	CHARACTER	2	*	Reserved
92	(5C)	ADDRESS	4	OPSUNALC_DSNPTR	
					Dataset name address
				Comment	
				BUILD CONTROL BLOCKS functional parameter list	
				End of Comment	
96	(60)	CHARACTER	40	OPSBLD_PL	Build control blocks parameter list
96	(60)	ADDRESS	4	OPSBLD_PCTQA	Address of the PCT queue header (e.g. PARTPCTQ)
100	(64)	ADDRESS	4	OPSBLD_XARTE	PARTE address
104	(68)	ADDRESS	4	OPSBLD_DVTAB	DVTAB address
108	(6C)	ADDRESS	4	OPSBLD_PCT	PCT address
112	(70)	ADDRESS	4	OPSBLD_PAT	PAT address
116	(74)	SIGNED	4	OPSBLD_LENPAT	
					PAT length
120	(78)	SIGNED	4	OPSBLD_SLTNUM	
					Number of slots
124	(7C)	CHARACTER	4	OPSBLD_CCHHB	Beginning CCHH
128	(80)	CHARACTER	4	OPSBLD_CCHHE	Ending CCHH
132	(84)	CHARACTER	2	OPSBLD_DVTYP	Device type
134	(86)	UNSIGNED	1	OPSBLD_DUSE	Device usage type
135	(87)	BITSTRING	1	*	Reserved

OPSPL Constants

Len	Type	Value	Name	Description
			Comment	
			ILROPS00 function codes.	
			End of Comment	
1	DECIMAL	0	OPSOPEN	OPEN DATA SET code
1	DECIMAL	1	OPSFREE	FREE DATA SET CONTROL BLOCKS code
1	DECIMAL	2	OPSUNALC	UNALLOCATE DATA SET code
1	DECIMAL	3	OPSBLD	BUILD PAT/PCT code
1	DECIMAL	4	OPSCU	NIP-time UCB processing code
1	DECIMAL	5	OPSPAV	Enable PAV processing
1	DECIMAL	6	OPSSER	Perform MSI-time ENQ processing
1	DECIMAL	7	OPSCACHE	Perform Post-MSI Cache initialization

OPSPL Cross Reference

Name	Hex Offset	Hex Value
OPSALOCD	40	
OPSBLD_CCHHB	7C	
OPSBLD_CCHHE	80	
OPSBLD_DUSE	86	
OPSBLD_DVTAB	68	
OPSBLD_DVTYP	84	
OPSBLD_LENPAT	74	
OPSBLD_PAT	70	
OPSBLD_PCT	6C	
OPSBLD_PCTQA	60	
OPSBLD_PL	60	
OPSBLD_SLTNUM	78	
OPSBLD_XARTE	64	
OPSCACHEOK	2C	20
OPSCCHHB	24	
OPSCCHHE	28	
OPSCCWS	2E	
OPSDCCWS	34	
OPSDEIB	30	
OPSDSN	4	
OPSDTIME	38	
OPSDUSE	1	
OPSDVTYP	14	
OPSECKD	2C	80
OPSEDB	C	
OPSERROR	40	
OPSFLG1	0	
OPSFLG2	2C	
OPSFREE_DEIB	48	
OPSFREE_EDB	44	
OPSFREE_IORB	4C	
OPSFREE_PAT	50	
OPSFREE_PL	44	
OPSFUNC	2	
OPSF1R1	0	10
OPSF1R2	0	01
OPSF2R	2C	0F
OPSINFO	42	
OPSIORB	4	
OPSLOCV	0	20
OPSLTNUM	10	
OPSNOCB	0	08
OPSPART2	30	
OPSPAVOK	2C	40
OPSPCD	0	04
OPSPGAD	0	40
OPSPL	0	
OPSQALEN	20	
OPSQAPTR	1C	
OPSRSV1	3	
OPSUCB	8	
OPSUNALC_DSNPTR	5C	
OPSUNALC_FLAGS	59	
OPSUNALC_PL	54	
OPSUNALC_UCB	54	
OPSVOL	16	

ORB Information

ORB Heading Information

Common Name: Operation Request Block
Macro ID: IHAORB
DSECT Name: IHAORB
Owning Component: I/O Supervisor (SC1C3)
Eye-Catcher ID: N/A
Offset: N/A
Length: N/A
Storage Attributes: Subpool: 245
Key: 0
Data Space: N/A
Residency: 31 Bit
Size: 12 or 32 bytes
Created by: Issuers of the SSCH instruction
Pointed to by: IOWORB field of the IOWA (IOSDIOWA)
Serialization: None
Function: Maps the hardware operation request block. The ORB is the operand of the start subchannel instruction and contains the interruption parameter, the address of the first CCW, and status information.

ORB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	32	ORB	
				Comment	
		ORB basic section. This section does not include the ORB extension.			
				End of Comment	
0	(0)	CHARACTER	12	ORBBASIC	ORB basic section
0	(0)	BITSTRING	4	ORBIP	Interrupt parameter
0	(0)	SIGNED	4	ORBIPA	Interrupt parameter
0	(0)	ADDRESS	4	ORBIPP	Interrupt parameter
4	(4)	BITSTRING	1	ORBFLG0	Flags
		1111		ORBKEY	- Key
		.... 1...		ORBS	- Channel program has suspend capability.
		.... .1..		ORBC	- Streaming Mode Control
		.... ..1.		ORBM	- Synchronize Control - PCI
		1		ORBY	- Synchronize Control
5	(5)	BITSTRING	1	ORBFLG1	Flags
		1...		ORBF	- Format of channel program. If 0, format 0 CCWs. If 1, format 1 CCWs.
		.1..		ORBP	- Prefetch of CCWs is allowed
		..1.		ORBI	- Initial status response requested.
		...1		ORBA	- Address limit check required
		.... 1...		ORBSSPI	- Suppress suspend interrupt
		.... .1..		ORBB	- Channel program type control. When 0, ORBCPA designates a CCW channel program. When 1, ORBCPA designates a TCW channel program.
		.... ..1.		ORBH	- 8-byte IDAWs
		1		ORBT	- 2K 8-byte IDAWs
6	(6)	BITSTRING	1	ORBLPM	Logical path mask (LPM) to be used for this request
7	(7)	BITSTRING	1	ORBFLG2	Flags
		1...		ORBL	- Incorrect length suppression mode
		.1..		ORBD	- Modified CCW indirect data addressing control (MIDAW). Allows MIDAWs to be used in the channel program.
		..1.		*	- Reserved
		...1		*	- Reserved
		.... 1...		*	- Reserved
		.... .1..		*	- Reserved
		.... ..1.		*	- Reserved
		1		ORBX	- ORB extension is present
8	(8)	ADDRESS	4	ORBCPA	Absolute CCW or TCW address

ORB Cross Reference

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					End of ORB basic section. If ORBX is off, this represents the end of the ORB.
					End of Comment
12	(C)	CHARACTER	0	ORBBEND	End of ORB basic section
					Comment
					Start of ORB extension. This section is present only when ORBX is on.
					End of Comment
12	(C)	CHARACTER	20	ORBEXT	ORB extension
12	(C)	CHARACTER	4	ORBWORD3	ORB word 3
12	(C)	UNSIGNED	1	ORBCSSPR	Channel-subsystem priority
13	(D)	UNSIGNED	1	*	Reserved
14	(E)	UNSIGNED	1	ORBCUNPR	Control-unit priority
15	(F)	UNSIGNED	1	*	Reserved
16	(10)	CHARACTER	4	ORBWORD4	ORB word 4
16	(10)	UNSIGNED	4	*	Reserved
20	(14)	CHARACTER	4	ORBWORD5	ORB word 5
20	(14)	UNSIGNED	4	*	Reserved
24	(18)	CHARACTER	4	ORBWORD6	ORB word 6
24	(18)	UNSIGNED	4	*	Reserved
28	(1C)	CHARACTER	4	ORBWORD7	ORB word 7
28	(1C)	UNSIGNED	4	*	Reserved
					Comment
					End of ORB with the ORB extension (ORBX is on)
					End of Comment
32	(20)	CHARACTER	0	ORBEND	End of ORB with the ORB extension

ORB Cross Reference

Name	Hex Offset	Hex Value
ORB	0	
ORBA	5	10
ORBB	5	04
ORBBASIC	0	
ORBBEND	C	
ORBC	4	04
ORBCPA	8	
ORBCSSPR	C	
ORBCUNPR	E	
ORBD	7	40
ORBEND	20	
ORBEXT	C	
ORBF	5	80
ORBFLG0	4	
ORBFLG1	5	
ORBFLG2	7	
ORBH	5	02
ORBI	5	20
ORBIP	0	
ORBIPA	0	
ORBIPP	0	
ORBKEY	4	F0
ORBL	7	80
ORBLPM	6	
ORBM	4	02
ORBP	5	40
ORBS	4	08
ORBSSPI	5	08
ORBT	5	01
ORBWORD3	C	
ORBWORD4	10	
ORBWORD5	14	
ORBWORD6	18	
ORBWORD7	1C	
ORBX	7	01
ORBY	4	01

ORE Information

ORE Programming Interface information

Programming Interface information

ORE

End of Programming Interface information

ORE Heading Information

Common Name: OPERATOR REPLY ELEMENT DEFINITION
Macro ID: IHAORE
DSECT Name: OREF
Owning Component: COMMUNICATIONS TASK (SC1CK)
Eye-Catcher ID: ORE
Offset: +20x
Length: 4
Storage Attributes: Subpool: 231
Key: 0
Residency: ABOVE 16 MB IN REAL STORAGE
Size: OREF -- X'0080' bytes
Created by: CNZS1WTO, CNZQ1MTC
Pointed to by: UCMRPYQ FIELD OF THE UCM DATA AREA
ORELKP FIELD OF THE ORE DATA AREA (NEXT ORE)
SSWTORE FIELD OF THE SSOB DATA AREA
Serialization: CMS AND LOCAL LOCKS
Function: THIS MACRO MAPS THE OPERATOR REPLY ELEMENT

ORE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	OREF	START OF ORE
0	(0)	ADDRESS	4	ORELKP	LINKAGE POINTER
4	(4)	CHARACTER	2	OREID	REPLY IDENTIFICATION X'0000' when reply id > 99
6	(6)	CHARACTER	1	OREXA	FLAGS
		Comment			
		Bit definitions:			
		End of Comment			
		1...		OREFORGN	"X'80'" WTOR WAS NOT ISSUED ON THIS SYSTEM
		.1..		OREKEY0	"X'40'" WTOR ISSUED BY KEY 0 USER (BYPASS VALIDITY CHECK)
		..1.		ORESWAP	"X'20'" TASK SWAPPED OUT
		...1		ORESUSP	"X'10'" PROCESSING TEMPORARILY SUSPENDED (OS/VS2) MDC001
		.... 1...		OREINUSE	"X'08'" WTOR NOT COMPLETED
		.... .1..		OREDMCMP	"X'04'" DOM HAS COMPLETED
		.... ..1.		OREDOMD	"X'02'" HAVE PROCESSED A DOMC FOR THIS WTOR
		1		ORERSV06	"X'01'" RESERVED
7	(7)	CHARACTER	1	OREXC	BUFFER STATUS FLAGS
		Comment			
		Bit definitions:			
		End of Comment			
		1...		OREBUFA	"X'80'" BUFFER IS AVAILABLE
		.1..		OREBUFB	"X'40'" BUFFER IN USE
		..1.		OREBUFC	"X'20'" ORE IS TO BE DELETED, DO NOT PROCESS REPLY (OS/VS2) MDC002
		...1		ORERSV01	"X'10'" Reserved - Was OREBUFD
		.... 1...		OREBUFE	"X'08'" BUFFER SERVICED
		.... .1..		ORESAVD	"X'04'" ORE/WQE SAVED IN RECOVERY, NO B23
		.... ..1.		OREFRID	"X'02'" Free reply id when freeing ORE
		1		OREWTORP	"X'01'" Indicates that the associated WTOR ECB has been POSTed. Set by IEAVVRP2.
8	(8)	ADDRESS	4	ORETCB	POINTER TO TCB
8	(8)	ADDRESS	4	ORETCBA	ADDRESS OF TCB
12	(C)	ADDRESS	4	OREWQE	ADDRESS OF ASSOCIATED DUMMY WQE - (USED BY THE SUBSYSTEM)
16	(10)	ADDRESS	4	ORERPY	POINTER TO REPLY BUFFER
16	(10)	ADDRESS	4	ORERPYA	ADDRESS OF REPLY BUFFER
20	(14)	ADDRESS	4	OREECB	POINTER TO REQUESTOR'S REPLY ECB
20	(14)	ADDRESS	4	OREECBA	ADDRESS OF REQUESTOR'S REPLY ECB
24	(18)	SIGNED	2	OREASID	ADDRESS SPACE IDENTIFIER (OS/VS2) MDC003
26	(1A)	SIGNED	2	ORERSV11	RESERVED (OS/VS2) MDC004
28	(1C)	ADDRESS	4	OREOPBUF	POINTER TO OPERATOR REPLY BUFFER (OS/VS2) MDC005
32	(20)	CHARACTER	4	ORECBID	CONTROL BLOCK ID 'ORE '
36	(24)	BITSTRING	1	OREVRSN	VERSION LEVEL
37	(25)	CHARACTER	2	ORERSV07	RESERVED - WAS 16 ROUTING CODES
39	(27)	BITSTRING	1	ORERSV08	RESERVED - WAS ORERCID
40	(28)	ADDRESS	4	ORERWQE	ADDRESS OF ASSOCIATED REAL WQE
44	(2C)	SIGNED	4	OREDOMID	DOM ID

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
44	(2C)	BITSTRING	1	ORESYSID	SYSTEM ID
45	(2D)	SIGNED	3	ORESEQN	24-BIT OREDOMID
48	(30)	ADDRESS	1	ORELNTH	MAXIMUM LENGTH OF REPLY
49	(31)	CHARACTER	3	ORERSV12	RESERVED BYTES
52	(34)	CHARACTER	16	ORERTCDE	16 BYTES OF ROUTING CODES
52	(34)	CHARACTER	1	ORERTA	FIRST BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT001	"X'80'" PRIMARY OPERATOR ACTION
		.1..		ORERT002	"X'40'" PRIMARY OPERATOR INFORMATION
		..1.		ORERT003	"X'20'" TAPE POOL
		...1		ORERT004	"X'10'" DIRECT ACCESS POOL
		.... 1...		ORERT005	"X'08'" TAPE LIBRARY
		.... .1..		ORERT006	"X'04'" DISK LIBRARY
		.... ..1.		ORERT007	"X'02'" UNIT RECORD POOL
		1		ORERT008	"X'01'" TELEPROCESSING CONTROL
53	(35)	BITSTRING	1	ORERTB	SECOND BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT009	"X'80'" SYSTEM SECURITY
		.1..		ORERT010	"X'40'" SYSTEM/ERROR MAINTENANCE
		..1.		ORERT011	"X'20'" PROGRAMMER INFORMATION
		...1		ORERT012	"X'10'" EMULATOR INFORMATION
		.... 1...		ORERT013	"X'08'" USER ROUTING CODE
		.... .1..		ORERT014	"X'04'" USER ROUTING CODE
		.... ..1.		ORERT015	"X'02'" USER ROUTING CODE
		1		ORERT016	"X'01'" USER ROUTING CODE
54	(36)	BITSTRING	1	ORERTC	THIRD BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT017	"X'80'" USER ROUTING CODE
		.1..		ORERT018	"X'40'" USER ROUTING CODE
		..1.		ORERT019	"X'20'" USER ROUTING CODE
		...1		ORERT020	"X'10'" USER ROUTING CODE
		.... 1...		ORERT021	"X'08'" RESERVED FOR JES USAGE
		.... .1..		ORERT022	"X'04'" RESERVED FOR JES USAGE
		.... ..1.		ORERT023	"X'02'" RESERVED FOR JES USAGE
		1		ORERT024	"X'01'" RESERVED FOR JES USAGE
55	(37)	BITSTRING	1	ORERTD	FOURTH BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT025	"X'80'" RESERVED FOR JES USAGE
		.1..		ORERT026	"X'40'" RESERVED FOR JES USAGE
		..1.		ORERT027	"X'20'" RESERVED FOR JES USAGE
		...1		ORERT028	"X'10'" RESERVED FOR JES USAGE
		.... 1...		ORERT029	"X'08'" DISASTER RECOVERY
		.... .1..		ORERT030	"X'04'" RESERVED
		.... ..1.		ORERT031	"X'02'" RESERVED
		1		ORERT032	"X'01'" RESERVED
56	(38)	BITSTRING	1	ORERTE	FIFTH BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT033	"X'80'" RESERVED

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.1..		ORERT034	"X'40'" RESERVED
		..1.		ORERT035	"X'20'" RESERVED
		...1		ORERT036	"X'10'" RESERVED
		.... 1...		ORERT037	"X'08'" RESERVED
		.... .1..		ORERT038	"X'04'" RESERVED
		.... ..1.		ORERT039	"X'02'" RESERVED
		1		ORERT040	"X'01'" RESERVED
57	(39)	BITSTRING	1	ORERTF	SIXTH BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT041	"X'80'" JOB STATUS MESSAGE
		.1..		ORERT042	"X'40'" GENERAL INFO. ABOUT JES2 OR JES3
		..1.		ORERT043	"X'20'" RESERVED FOR JES USAGE
		...1		ORERT044	"X'10'" RESERVED FOR JES USAGE
		.... 1...		ORERT045	"X'08'" RESERVED FOR JES USAGE
		.... .1..		ORERT046	"X'04'" RESERVED FOR JES USAGE
		.... ..1.		ORERT047	"X'02'" RESERVED FOR JES USAGE
		1		ORERT048	"X'01'" RESERVED FOR JES USAGE
58	(3A)	BITSTRING	1	ORERTG	SEVENTH BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT049	"X'80'" RESERVED FOR JES USAGE
		.1..		ORERT050	"X'40'" RESERVED FOR JES USAGE
		..1.		ORERT051	"X'20'" RESERVED FOR JES USAGE
		...1		ORERT052	"X'10'" RESERVED FOR JES USAGE
		.... 1...		ORERT053	"X'08'" RESERVED FOR JES USAGE
		.... .1..		ORERT054	"X'04'" RESERVED FOR JES USAGE
		.... ..1.		ORERT055	"X'02'" RESERVED FOR JES USAGE
		1		ORERT056	"X'01'" RESERVED FOR JES USAGE
59	(3B)	BITSTRING	1	ORERTH	EIGHTH BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT057	"X'80'" RESERVED FOR JES USAGE
		.1..		ORERT058	"X'40'" RESERVED FOR JES USAGE
		..1.		ORERT059	"X'20'" RESERVED FOR JES USAGE
		...1		ORERT060	"X'10'" RESERVED FOR JES USAGE
		.... 1...		ORERT061	"X'08'" RESERVED FOR JES USAGE
		.... .1..		ORERT062	"X'04'" RESERVED FOR JES USAGE
		.... ..1.		ORERT063	"X'02'" RESERVED FOR JES USAGE
		1		ORERT064	"X'01'" RESERVED FOR JES USAGE
60	(3C)	BITSTRING	1	ORERTI	NINTH BYTE OF ROUTING CODES
		Comment			
		Bit definitions:			
		End of Comment			
		1...		ORERT065	"X'80'" PROCESSOR RELATED MESSAGE
		.1..		ORERT066	"X'40'" PROCESSOR RELATED MESSAGE
		..1.		ORERT067	"X'20'" PROCESSOR RELATED MESSAGE
		...1		ORERT068	"X'10'" PROCESSOR RELATED MESSAGE
		.... 1...		ORERT069	"X'08'" PROCESSOR RELATED MESSAGE
		.... .1..		ORERT070	"X'04'" PROCESSOR RELATED MESSAGE
		.... ..1.		ORERT071	"X'02'" PROCESSOR RELATED MESSAGE
		1		ORERT072	"X'01'" PROCESSOR RELATED MESSAGE
61	(3D)	BITSTRING	1	ORERTJ	TENTH BYTE OF ROUTING CODES

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					Bit definitions:
					End of Comment
		1...		ORERT073	"X'80'" PROCESSOR RELATED MESSAGE
		.1..		ORERT074	"X'40'" PROCESSOR RELATED MESSAGE
		..1.		ORERT075	"X'20'" PROCESSOR RELATED MESSAGE
		...1		ORERT076	"X'10'" PROCESSOR RELATED MESSAGE
		.... 1...		ORERT077	"X'08'" PROCESSOR RELATED MESSAGE
		.... .1..		ORERT078	"X'04'" PROCESSOR RELATED MESSAGE
		.... ..1.		ORERT079	"X'02'" PROCESSOR RELATED MESSAGE
		1		ORERT080	"X'01'" PROCESSOR RELATED MESSAGE
62	(3E)	BITSTRING	1	ORERTK	ELEVENTH BYTE OF ROUTING CODES
					Comment
					Bit definitions:
					End of Comment
		1...		ORERT081	"X'80'" PROCESSOR RELATED MESSAGE
		.1..		ORERT082	"X'40'" PROCESSOR RELATED MESSAGE
		..1.		ORERT083	"X'20'" PROCESSOR RELATED MESSAGE
		...1		ORERT084	"X'10'" PROCESSOR RELATED MESSAGE
		.... 1...		ORERT085	"X'08'" PROCESSOR RELATED MESSAGE
		.... .1..		ORERT086	"X'04'" PROCESSOR RELATED MESSAGE
		.... ..1.		ORERT087	"X'02'" PROCESSOR RELATED MESSAGE
		1		ORERT088	"X'01'" PROCESSOR RELATED MESSAGE
63	(3F)	BITSTRING	1	ORERTL	TWELFTH BYTE OF ROUTING CODES
					Comment
					Bit definitions:
					End of Comment
		1...		ORERT089	"X'80'" PROCESSOR RELATED MESSAGE
		.1..		ORERT090	"X'40'" PROCESSOR RELATED MESSAGE
		..1.		ORERT091	"X'20'" PROCESSOR RELATED MESSAGE
		...1		ORERT092	"X'10'" PROCESSOR RELATED MESSAGE
		.... 1...		ORERT093	"X'08'" PROCESSOR RELATED MESSAGE
		.... .1..		ORERT094	"X'04'" PROCESSOR RELATED MESSAGE
		.... ..1.		ORERT095	"X'02'" PROCESSOR RELATED MESSAGE
		1		ORERT096	"X'01'" PROCESSOR RELATED MESSAGE
64	(40)	BITSTRING	1	ORERTM	THIRTEENTH BYTE OF ROUTING CODES
					Comment
					Bit definitions:
					End of Comment
		1...		ORERT097	"X'80'" DEVICE RELATED MESSAGE
		.1..		ORERT098	"X'40'" DEVICE RELATED MESSAGE
		..1.		ORERT099	"X'20'" DEVICE RELATED MESSAGE
		...1		ORERT100	"X'10'" DEVICE RELATED MESSAGE
		.... 1...		ORERT101	"X'08'" DEVICE RELATED MESSAGE
		.... .1..		ORERT102	"X'04'" DEVICE RELATED MESSAGE
		.... ..1.		ORERT103	"X'02'" DEVICE RELATED MESSAGE
		1		ORERT104	"X'01'" DEVICE RELATED MESSAGE
65	(41)	BITSTRING	1	ORERTN	FOURTEENTH BYTE OF ROUTING CODES
					Comment
					Bit definitions:
					End of Comment
		1...		ORERT105	"X'80'" DEVICE RELATED MESSAGE
		.1..		ORERT106	"X'40'" DEVICE RELATED MESSAGE
		..1.		ORERT107	"X'20'" DEVICE RELATED MESSAGE
		...1		ORERT108	"X'10'" DEVICE RELATED MESSAGE
		.... 1...		ORERT109	"X'08'" DEVICE RELATED MESSAGE
		.... .1..		ORERT110	"X'04'" DEVICE RELATED MESSAGE
		.... ..1.		ORERT111	"X'02'" DEVICE RELATED MESSAGE

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1		ORERT112	"X'01'" DEVICE RELATED MESSAGE
66	(42)	BITSTRING	1	ORERTO	FIFTEENTH BYTE OF ROUTING CODES
				Comment	
				Bit definitions:	
				End of Comment	
		1...		ORERT113	"X'80'" DEVICE RELATED MESSAGE
		.1..		ORERT114	"X'40'" DEVICE RELATED MESSAGE
		..1.		ORERT115	"X'20'" DEVICE RELATED MESSAGE
		...1		ORERT116	"X'10'" DEVICE RELATED MESSAGE
		.... 1...		ORERT117	"X'08'" DEVICE RELATED MESSAGE
		.... .1..		ORERT118	"X'04'" DEVICE RELATED MESSAGE
		.... ..1.		ORERT119	"X'02'" DEVICE RELATED MESSAGE
		1		ORERT120	"X'01'" DEVICE RELATED MESSAGE
67	(43)	BITSTRING	1	ORERTP	SIXTEENTH BYTE OF ROUTING CODES
				Comment	
				Bit definitions:	
				End of Comment	
		1...		ORERT121	"X'80'" DEVICE RELATED MESSAGE
		.1..		ORERT122	"X'40'" DEVICE RELATED MESSAGE
		..1.		ORERT123	"X'20'" DEVICE RELATED MESSAGE
		...1		ORERT124	"X'10'" DEVICE RELATED MESSAGE
		.... 1...		ORERT125	"X'08'" DEVICE RELATED MESSAGE
		.... .1..		ORERT126	"X'04'" DEVICE RELATED MESSAGE
		.... ..1.		ORERT127	"X'02'" DEVICE RELATED MESSAGE
		1		ORERT128	"X'01'" DEVICE RELATED MESSAGE
68	(44)	CHARACTER	8	OREWTORU	USERID OF WTOR ISSUER
76	(4C)	ADDRESS	4	ORECNRA	ADDRESS OF 12 BYTE FIELD FOR REPLYING CONSOLE NAME/ID
80	(50)	CHARACTER	12	ORECNDAT	REPLYING CONSOLE NAME AND ID
80	(50)	CHARACTER	8	ORECNNME	REPLYING CONSOLE NAME
88	(58)	SIGNED	4	ORECNID	REPLYING CONSOLE ID
92	(5C)	SIGNED	4	ORERPIDB	Reply id (binary representation
96	(60)	ADDRESS	4	ORE_JSTCB	Job Step TCB address of the WTOR issuer
100	(64)	SIGNED	4	ORE_TOKEN	Token associated with the WQE
104	(68)	ADDRESS	4	ORE_PREV	Previous ORE
108	(6C)	BITSTRING	1	ORE_FLAGS	Flags
				Comment	
				Bit definitions:	
				End of Comment	
		1...		ORE_AUTOR_MONITORED	"X'80'" Autor data provided for this WTOR
		.1..		ORE_PROCESSED_BY_AUTO_REPLY	"X'40'"
		..1.		ORE_AUTO_REPLY_IGNORED	"X'20'" Auto-reply processing told to be ignored for this WTOR
109	(6D)	CHARACTER	19	ORE_RESERVED	Reserved
				Comment	
				CONSTANTS FOR OREVRSN	
				End of Comment	
109	(6D)	X'1'	0	ORESP13	"1" ORE IS AT JBB1326 LEVEL
109	(6D)	X'2'	0	ORESP22	"2" ORE IS AT JBB2220 LEVEL
109	(6D)	X'3'	0	ORESP313	"3" ORE IS AT JBB3313 LEVEL
109	(6D)	X'4'	0	ORESP410	"4" ORE IS AT HBB4410 LEVEL
109	(6D)	X'A'	0	ORE_HBB7730	"10" ORE IS AT HBB7730 LEVEL
109	(6D)	X'A'	0	OREVRID	"10" VERSION LEVEL VALUE
109	(6D)	X'E7'	0	K_ORE_SUBPOOL	"231" Subpool
109	(6D)	X'D9C540'	0	K_ORE_ACRONYM	"C'ORE '" Eyecatcher
109	(6D)	X'80'	0	OREF_LEN	"*-OREF"

ORE Cross Reference

Name	Hex Offset	Hex Value
K_ORE_ACRONYM		
	6D	D9C540
K_ORE_SUBPOOL		
	6D	E7
ORE_AUTO_REPLY_IGNORED		
	6C	20
ORE_AUTOR_MONITORED		
	6C	80
ORE_FLAGS	6C	
ORE_HBB7730	6D	A
ORE_JSTCB	60	
ORE_PREV	68	
ORE_PROCESSED_BY_AUTO_REPLY		
	6C	40
ORE_RESERVED	6D	
ORE_TOKEN	64	
OREASID	18	
OREBUFA	7	80
OREBUFB	7	40
OREBUFC	7	20
OREBUFE	7	8
ORECBID	20	
ORECNDAT	50	
ORECNID	58	
ORECNNME	50	
ORECNRA	4C	
OREDMCMP	6	4
OREDOMD	6	2
OREDOMID	2C	
OREECB	14	
OREECBA	14	
OREF	0	
OREF_LEN	6D	80
OREFORGN	6	80
OREFRID	7	2
OREID	4	
OREINUSE	6	8
OREKEY0	6	40
ORELKP	0	
ORELNTH	30	
OREOPBUF	1C	
ORERPIDB	5C	
ORERPY	10	
ORERPYA	10	
ORERSV01	7	10
ORERSV06	6	1
ORERSV07	25	
ORERSV08	27	
ORERSV11	1A	
ORERSV12	31	
ORERTA	34	
ORERTB	35	
ORERTC	36	
ORERTCDE	34	
ORERTD	37	
ORERTE	38	
ORERTF	39	
ORERTG	3A	
ORERTH	3B	
ORERTI	3C	
ORERTJ	3D	
ORERTK	3E	
ORERTL	3F	
ORERTM	40	
ORERTN	41	
ORERTO	42	
ORERTP	43	
ORERT001	34	80
ORERT002	34	40
ORERT003	34	20
ORERT004	34	10
ORERT005	34	8
ORERT006	34	4
ORERT007	34	2
ORERT008	34	1
ORERT009	35	80
ORERT010	35	40
ORERT011	35	20
ORERT012	35	10
ORERT013	35	8
ORERT014	35	4
ORERT015	35	2
ORERT016	35	1
ORERT017	36	80
ORERT018	36	40
ORERT019	36	20
ORERT020	36	10
ORERT021	36	8
ORERT022	36	4
ORERT023	36	2
ORERT024	36	1
ORERT025	37	80
ORERT026	37	40
ORERT027	37	20
ORERT028	37	10
ORERT029	37	8
ORERT030	37	4
ORERT031	37	2
ORERT032	37	1
ORERT033	38	80
ORERT034	38	40
ORERT035	38	20
ORERT036	38	10
ORERT037	38	8
ORERT038	38	4
ORERT039	38	2
ORERT040	38	1
ORERT041	39	80
ORERT042	39	40
ORERT043	39	20
ORERT044	39	10
ORERT045	39	8
ORERT046	39	4
ORERT047	39	2
ORERT048	39	1
ORERT049	3A	80
ORERT050	3A	40
ORERT051	3A	20
ORERT052	3A	10
ORERT053	3A	8
ORERT054	3A	4
ORERT055	3A	2
ORERT056	3A	1
ORERT057	3B	80
ORERT058	3B	40
ORERT059	3B	20
ORERT060	3B	10
ORERT061	3B	8
ORERT062	3B	4
ORERT063	3B	2
ORERT064	3B	1
ORERT065	3C	80
ORERT066	3C	40
ORERT067	3C	20
ORERT068	3C	10
ORERT069	3C	8
ORERT070	3C	4
ORERT071	3C	2
ORERT072	3C	1
ORERT073	3D	80
ORERT074	3D	40
ORERT075	3D	20
ORERT076	3D	10
ORERT077	3D	8

ORE Cross Reference

Name	Hex Offset	Hex Value
ORERT078	3D	4
ORERT079	3D	2
ORERT080	3D	1
ORERT081	3E	80
ORERT082	3E	40
ORERT083	3E	20
ORERT084	3E	10
ORERT085	3E	8
ORERT086	3E	4
ORERT087	3E	2
ORERT088	3E	1
ORERT089	3F	80
ORERT090	3F	40
ORERT091	3F	20
ORERT092	3F	10
ORERT093	3F	8
ORERT094	3F	4
ORERT095	3F	2
ORERT096	3F	1
ORERT097	40	80
ORERT098	40	40
ORERT099	40	20
ORERT100	40	10
ORERT101	40	8
ORERT102	40	4
ORERT103	40	2
ORERT104	40	1
ORERT105	41	80
ORERT106	41	40
ORERT107	41	20
ORERT108	41	10
ORERT109	41	8
ORERT110	41	4
ORERT111	41	2
ORERT112	41	1
ORERT113	42	80
ORERT114	42	40
ORERT115	42	20
ORERT116	42	10
ORERT117	42	8
ORERT118	42	4
ORERT119	42	2
ORERT120	42	1
ORERT121	43	80
ORERT122	43	40
ORERT123	43	20
ORERT124	43	10
ORERT125	43	8
ORERT126	43	4
ORERT127	43	2
ORERT128	43	1
ORERWQE	28	
ORESAVD	7	4
ORESEQN	2D	
ORESP13	6D	1
ORESP22	6D	2
ORESP313	6D	3
ORESP410	6D	4
ORESUSP	6	10
ORESWAP	6	20
ORESYSID	2C	
ORETCB	8	
ORETCBA	8	
OREVRID	6D	A
OREVRSN	24	
OREWQE	C	
OREWTORP	7	1
OREWTORU	44	
OREXA	6	
OREXC	7	

OUCB Information

OUCB Programming Interface information

Programming Interface information

OUCB

The following fields are **NOT** programming interface information:

- OUCBACT
- OUCBACTP
- OUCBASCB
- OUCBPAGP
- OUCBSUBN
- OUCBWMG

End of Programming Interface information

OUCB Heading Information

Common Name: RESOURCES MANAGER USER CONTROL BLOCK
Macro ID: IRAOUCB
DSECT Name: OUCB
Owning Component: SYSTEMS RESOURCE MANAGER (SC1CX)
Eye-Catcher ID: OUCB
Offset: 0
Length: 4
Storage Attributes: Main Storage: ESQA
Subpool: 245
Key: 0
Residency: Above 16M line
Size: 2048 bytes
Created by: IRAEVMEM, IRARMERR
Pointed to by: ASCBOUCB field of the ASCB data area
OUCBFWD field of the OUCB data area
OUCBBCK field of the OUCB data area
OUCBACT field of the OUCB data area
RMQHFWD field of the RMQH data area
RMQHBCK field of the RMQH data area
RMCTAQHD, RMCTINQE, RMCTOTQE, RMCTWTQE,
RMCTDFQF, RMCTDFQL,
RMCTLSQE fields of the RMCT data area
Serialization: SRM lock, Compare and Swap (CS) instruction
Function: The OUCB describes the status of the associated memory (user) to the system resources manager. It contains resource usage information needed to decide when to swap-in the memory. The OUCB is positioned on transitional system resources manager chains to indicate actions to be taken for that memory.

OUCB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	OUCB	,
0	(0)	CHARACTER	256	OUCB1BLK (0)	- FIRST 256 BYTES OF OUCB
0	(0)	CHARACTER	128	OUCBCHE1 (0)	- 1st cache line of OUCB
0	(0)	CHARACTER	4	OUCBNAME	- BLOCK IDENTIFICATION
4	(4)	ADDRESS	4	OUCBFWD	- SWAP CHAIN FORWARD POINTER
8	(8)	ADDRESS	4	OUCBBCK	- SWAP CHAIN BCKWARD POINTER
12	(C)	SIGNED	4	OUCBTMA	- TIME OF LAST ANALYSYS
16	(10)	BITSTRING	1	OUCBQFL	- SWAPPABILITY TRANSITION FLAGS
		1...		OUCBGOO	"BIT0 - Address space is being swapped out or logically swapped
		.1..		OUCBGOI	"BIT1" - TRANSITIONING INTO CORE
		..1.		OUCBGOB	"BIT2" - TRANSITIONING BETWEEN STATES
		...1		OUCBQSFL	"BIT3" - QSCEFL RECURSION FLAG
		.... 1...		OUCBOFF	"BIT4" - REQUESTING ENTER WAIT STATE
		.... .1..		OUCBOUT	"BIT5" - REQUESTING ENTER OUT STATE
		.... ..1.		OUCBLSW	"BIT6" - LOGICALLY SWAPPED
		1		OUCBDLYB	"BIT7" - DELAYED BY RTO ON OUT QUEUE
17	(11)	BITSTRING	1	OUCBSFL	- SWAPOUT CONTINUATION FLAGS
		1...		OUCBNSW	"BIT0" - NON-SWAPPABLE STATUS
		.1..		OUCBCTI	"BIT1" - CTL INHIBITS QUIESCE
		..1.		OUCBBIB	"BIT2" - BRING IN FOR CANCEL
		...1		OUCBINV	"BIT3" - =1 IF OUCB IS INVALID
		.... 1...		OUCBNSWI	"BIT4" - PREVENT SWAP IN
		.... .1..		OUCBPVL	"BIT5" - PRIVILEGED PROGRAM RUNNING
		.... ..1.		OUCBENQ	"BIT6" - ENQ RESIDENT STATUS
		1		OUCBSCN	"BIT7" - SWAP CHAIN TERMINATION MARK
18	(12)	BITSTRING	1	OUCBYFL	- USER TYPE FLAGS
		1...		OUCBPSTE	"BIT0" - POST ERROR
		.1..		OUCBSTT	"BIT1" - START CREATED USER
		..1.		OUCBLOG	"BIT2" - LOGON CREATED USER
		...1		OUCBMNT	"BIT3" - MOUNT CREATED USER
		.... 1...		OUCBRSFL	"BIT4" - Restore Fail
		.... .1..		OUCBAXS	"BIT5" - AUX SHORTAGE FORCED SWAP
		.... ..1.		OUCBDTA	"BIT6" - DATA ACCUMULATION IMPACTED
		1		OUCBFXS	"BIT7" - FIXED STORAGE FORCED SWAP
19	(13)	BITSTRING	1	OUCBAFL	- ALGORITHM STATUS FLAGS
		1...		OUCBIRSW	"BIT0" - REALSWAP IN PENDING
		.1..		OUCBAPG	"BIT1" - APG ALGORITHM APPLICABLE
		..1.		OUCBREPT	"BIT2" - RPGNS ARE PRESENT

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		OUCBENQI	"BIT3" - OUCBENQ WAS ON AT SOME POINT DURING THE POLICY ADJUSTMENT INTERVAL (goal mode ONLY)
		.... 1...		OUCBJSR	"BIT4" - JOBSELECT RECEIVED
		Comment			
		EQU BIT5 - reserved was POTR			
		End of Comment			
		.... ..1.		OUCBNWT	"BIT6" - MSO DETECTED NONSWAPPABLE WAIT
		1		OUCBASW	"BIT7" - AUTHORIZED FOR DONTSWAP
20	(14)	BITSTRING	1	OUCBTFL	- TRANSACTION STATUS FLAGS
		1...		OUCBATR	"BIT0" - TRANSACTION IN EXISTENCE
		.1..		OUCBSTR	"BIT1" - TRANSACTION START PENDING
		..1.		OUCBNTR	"BIT2" - TRANSACTION STOP PENDING
		...1		OUCBRTR	"BIT3" - TRANSACTION RESUME PENDING
		.... 1...		OUCBPCH	"BIT4" - PERF GRP PERIOD CHANGE PENDING
		.... .1..		OUCBMAR	"BIT5" - ACTIVITY RECORDING SUBTRACT FLG
		.... ..1.		OUCBINP	"BIT6" - INITIATOR ATTACH PENDING
		1		OUCBINC	"BIT7" - INITIATOR ATTACH CURRENT
21	(15)	BITSTRING	1	OUCBEFL	- EVENT STATUS FALGS
		1...		OUCBLWT	"BIT0" - LONG WAIT STATUS
		Comment			
		EQU BIT1 - reserved - was terminal wait EQU BIT2 - reserved - was output wait EQU BIT3 - reserved - was composite input EQU BIT4 - reserved - was NQF EQU BIT5 - reserved - was QUEST			
		End of Comment			
		.... ..1.		OUCBQSC	"BIT6" - QSCECMP EVENT PROCESSED
		1		OUCBMWT	"BIT7" - MSO DETECTED WAIT STATUS
22	(16)	SIGNED	1	OUCBRSV3	- RESERVED
23	(17)	BITSTRING	1	OUCBUFL	- USER TYPE FLAGS
		1...		OUCBJSFS	"BIT0" - JOB SELECT DELAYED
		.1..		OUCBJSAS	"BIT1" - JOB SELECT DELAYED
		..1.		OUCBNSWDP	"BIT2" - NSW Address Space currently non dispachable
		...1		OUCBTSWP	"BIT3" - TRANSWAP IN PROGRESS
		.... 1...		OUCBTSWC	"BIT4" - TRANSWAP COMPLETE
		.... .1..		OUCBSI	"BIT5" - STORAGE ISOL CONTROL ACTIVE
		.... ..1.		OUCBENQR	"BIT6" - OucbEnq on during RA interval
		1		OUCBSIFX	"BIT7" - FIXED TARGET WORKING SET SIZE FOR GRS STORAGE ISOLATION 2
24	(18)	BITSTRING	1	OUCBLFL	- ALGORITHM STATUS FLAGS
		1...		OUCBEAS	"BIT0" - Early address space that has not been through InitAtt yet
		.1..		OUCBQSRV	"BIT1" - Space was managed as a server when it was quiesced
		..1.		OUCBRQSC	"BIT2" - QUIESCED BY RESET COMMAND
		...1		OUCBFWA	"BIT3" - FAST WORKLOAD ACCEPTANCE APPL
		.... 1...		OUCBTET	"BIT4" - TRANSWAP EXPIRATION TIMER
		.... .1..		OUCBREDP	"BIT5" - Reduced preemption required
		.... ..1.		OUCBPRF	"BIT6" - PREVIOUS RESOURCE FAILURE
		1		OUCBSTGI	"BIT7" - Address space has been recognized initializing storage
25	(19)	BITSTRING	1	OUCBRFL	- MORE USER FLAGS
		1...		OUCBCSFS	"BIT0" - SWAP IN FAIL DEFER BIT -USER ON WAIT QUEUE
		.1..		OUCBCSFM	"BIT1" - SWAP IN MESSAGE REQUIRED
		..1.		OUCBEASI	"BIT2" - EARLY ADDRESS SPACE
		...1		OUCBHIDP	"BIT3" - EARLY A.S. NEEDS HIGH PRTY
		.... 1...		OUCBBJOB	"BIT4" - Address space is running a batch job
		.... .1..		OUCBSTFX	"BIT5" - FIXED BELOW FRAMES ARE STELABLE INDICATOR
		.... ..1.		OUCBDFSW	"BIT6" - SWAP IN FAIL SPECIAL PROCESSING Real thresholds raised on behalf of the address space's primary working set.
		1		OUCBLLSW	"BIT7" - LAST SWAP WAS LOGICAL
26	(1A)	BITSTRING	1	OUCBNDP	- NEW ASCB DISPATCHING PRIORITY
27	(1B)	BITSTRING	1	OUCBTNDP	- NEW TS DISPATCHING PRIORITY
28	(1C)	BITSTRING	1	OUCBMFL	- MISCELLANEOUS FLAGS
		1...		OUCBSBT	"BIT0" - STOLE BELOW THRESHOLD
		.1..		OUCBAFAP	"BIT1" - Auxiliary swap-in frame allocation is pending for primary working set (MCVCAPWS was raised)
		..1.		OUCBDFS2	"BIT2" - THRES RAISED BY SEC WORKING SET SIZE
		...1		OUCBMGSW	"BIT3" - SELECTED FOR MIG SWAP
		.... 1...		OUCBDISC	"BIT4" - Frames are included in the logical swap discretionary count

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... .1..		OUCBASAP	"BIT5" - SEC WORKING SET ALLOC PENDING
		.... ..1.		OUCBMPUR	"BIT6" - SELECTED FOR MIG PURGE
		1		OUCBACNT	"BIT7" - ACCOUNT NUMBER SPECIFIED ON JOB
29	(1D)	SIGNED	1	OUCBIAC	- INITIATOR ATTACH COUNT
30	(1E)	SIGNED	1	OUCBRSV1	- reserved
31	(1F)	SIGNED	1	OUCBPGP	- CURRENT WPGP OFFSET IN WPGD goal mode: period number
32	(20)	SIGNED	4	OUCBWMG (0)	WPGD OFFSET IN WPGD TABLE
32	(20)	SIGNED	2	OUCBWSCI	goal mode: service class index
34	(22)	SIGNED	2	OUCBWRCI	goal mode: report class index
36	(24)	BITSTRING	1	OUCBMFL2	- More miscellaneous flags. These are serialized by the SRM lock.
		1...		OUCBVFMG	"BIT0" - MESSAGE ISSUED ON BEHALF OF A.S. SWAPPED OUT DUE TO VECTOR WAIT
		.1..		OUCBMGIN	"BIT1" - SRM has requested that RSM inhibit migration of DREF pages
		..1.		OUCBAFPD	"BIT2" - Auxiliary swap-in frame allocation is pending for swap-in of DREF pages (MCVCAPWS was raised)
		...1		OUCBDFDR	"BIT3" Real thresholds were raised on behalf of the DREF pages in the address space.
		.... 1...		OUCBDFDE	"BIT4" Expanded storage thresholds were raised on behalf of the DREF pages in the address space.
		.... .1..		OUCBMIGP	"BIT5" - MIGPURGE indicator ON- indicates this address space has been returned to RSM for this instance of MIGPURGE OFF- address space has not been returned to RSM added by @YA65372, moved by
		.... ..1.		OUCBRPT1	"BIT6" The first time of entry into RPT - Raise Processor Threshold
		1		OUCBERST	"BIT7" Eligible for restart flag
37	(25)	BITSTRING	1	OUCBMFL3	- More miscellaneous flags
		1...		OUCBAPPC	"BIT0" APPC transaction program
		.1..		OUCBPMON	"BIT1" Indicate that this address space is being monitored
		..1.		OUCBVALV	"BIT2" Working set management recommendation value is valid
		...1		OUCBOMVS	"BIT3" OpenMVS transaction program
		.... 1...		OUCBTBMN	"BIT4" address space is to be monitored when enough data has been gathered about it
		.... .1..		OUCBSTTA	"BIT5" When ON, OUCBWKTM for this address space has been added to the system transaction time in the workload activity reporting table for the pg period.
		.... ..1.		OUCBGWRK	"BIT6" Initiator started using GETWORK interface. Distinction is important for workload activity reporting.
		1		OUCBINIT	"BIT7" Started task is an initiator.
38	(26)	SIGNED	2	OUCBDMO	- OFFSET INTO DOMAIN TABLE, VALID ONLY IN compatibility mode
40	(28)	SIGNED	1	OUCBDMN	DOMAIN NUMBER
41	(29)	SIGNED	1	OUCBSRC	SWAP OUT REASON CODE
42	(2A)	SIGNED	2	OUCBSWC	- TRANSACTION SWAP COUNT
44	(2C)	ADDRESS	4	OUCBASCB	- ASCB ADDRESS
48	(30)	ADDRESS	4	OUCBPAGP	- Pointer to the APAG for this address space
52	(34)	SIGNED	4	OUCBTMW	- WLM INTERVAL START TIME
56	(38)	SIGNED	4	OUCBWMS	- INTERVAL SERVICE ACCUMULATOR
60	(3C)	SIGNED	4	OUCBCPU	- INTERVAL CPU SERVICE ACCUM
64	(40)	SIGNED	4	OUCBIOC	- INTERVAL I/O SERVICE ACCUM
68	(44)	SIGNED	4	OUCBMSO	- INTERVAL MSO SERVICE ACCUM
72	(48)	SIGNED	4	OUCBTMS	- TIME OF LAST SWAP ACTION
76	(4C)	SIGNED	4	OUCBTMO	- TRANSACTION START TIME
80	(50)	SIGNED	4	OUCBDRFR	Count of DREF pages in real storage. Updated from values returned from RSM IARXCNTF routine.
84	(54)	ADDRESS	4	OUCBACT	- ACTION QUEUE FORWARD POINTER
88	(58)	SIGNED	4	OUCBCSW (0)	- COMPARE AND SWAP FIELD NAME
88	(58)	BITSTRING	1	OUCBACN (2)	- DEFERRED ACTION FLAGS
90	(5A)	BITSTRING	1	OUCBCFL	- MULTIPROCESS CONDITION FLAGS
		1...		OUCBRDY	"BIT0" - USERRDY EVENT RECEIVED
		.1..		OUCBRSM	"BIT1" - RSM SERVICE OUTSTANDING
		..1.		OUCBESSS	"BIT2" - SUSPENDED FOR SWAPOUT TO EXT
		...1		OUCBESSW	"BIT3" - HAS BEEN OR WILL BE SWAPPED TO EXTENDED
		.... 1...		OUCBASSW	"BIT4" - Has been or will be swapped to Auxiliary
		.... .1..		OUCBCSMF	"BIT5" - On = SMF needs to be notified that the address space is swapped in
		.... ..1.		OUCBSHBN	"BIT6" Server history block needed
		1		OUCBHITR	"BIT7" HIT has run since UserRdy
91	(5B)	BITSTRING	1	OUCBCSBT	- Compare and swap bits
		1...		OUCBTRM	"BIT0" - TERMINAL WAIT STATUS
		.1..		OUCBOWT	"BIT1" - OUTPUT TERMINAL WAIT
		..1.		OUCBCIM	"BIT2" - COMPOSITE INPUT MESSAGE
		...1		OUCBPSTR	"BIT3" - IF POST ERROR, RECOVER
		.... 1...		OUCBSTA	"BIT4" Swap turn around
		.... .1..		OUCBQSS	"BIT5" - QSCEST EVENT PROCESSED
		.... ..1.		OUCBOIW	"BIT6" - If on, indicates that a detected wait should be treated as an OpenMVS input wait
		1		OUCBOOW	"BIT7" - If on, indicates that a OpenMVS output wait condition is present
92	(5C)	SIGNED	4	OUCBCMRV	- COMPOSITE RECOM VALUE

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
92	(5C)	X'5C'	0	OUCBWMR	"OUCBCMRV" WLM RECOMMENDATION VALUE
96	(60)	SIGNED	4	OUCBWMRL	Workload recommendation value saved at swap-out
100	(64)	SIGNED	2	OUCBVAL	- Working set management recommendation value
102	(66)	BITSTRING	1	OUCBPFL	- Processing flags
		1...		OUCBFTDN	"BIT0" - On = Trimming has been completed for this address space
		.1..		OUCBPSD	"BIT1" - On = This address space is a direct physical swap
		..1.		OUCBDPSW	"BIT2" - On = Delayed Physical Swap
		...1		OUCBSRP	"BIT3" - On = Steal recently referenced pages pages on the next steal attempt from the Address Space
		.... 1...		OUCBPTDN	"BIT4" - On = Preliminary trimming has been completed for this address spac
		.... .1..		OUCBFTDNNOSWAP	"BIT5" - On = Trimming has been completed for this address space no swapping will be done
103	(67)	SIGNED	1	OUCBACTL	Length of storage allocated to save accounting data. Note that this length can be larger than the account data actually saved.
104	(68)	SIGNED	4	OUCBIOCL (2)	- I/O service accumulator
104	(68)	X'68'	0	OUCBERS	"OUCBIOCL" - Deprecated
104	(68)	X'68'	0	OUCBERS1	"OUCBERS" - Deprecated
104	(68)	X'6C'	0	OUCBERS2	"OUCBERS+4" - Deprecated
112	(70)	BITSTRING	1	OUCBDSPC	- CURRENT DISPATCHING CONTROL
		1...		OUCBMTW	"BIT0" - CURRENT CONTROL IS MTW
		..1.		OUCBTS	"BIT2" - CURRENT CTL IS TIME SLICING
		...1		OUCBTSC3	"BIT3" - WORKAREA FOR TS
		.... 1...		OUCBTSC4	"BIT4" - WORKAREA FOR TS
		.... .1..		OUCBTSC5	"BIT5" - WORKAREA FOR TS
		.... ..1.		OUCBTSC6	"BIT6" - WORKAREA FOR TS
		1		OUCBTSC7	"BIT7" - WORKAREA FOR TS
113	(71)	BITSTRING	1	OUCBDSPN	- NEW DISPATCHING CONTROL
		1...		OUCBNMTW	"BIT0" - NEW CONTROL IS MTW
		..1.		OUCBNTS	"BIT2" - NEW CONTROL IS TIME SLICING
		...1		OUCBTSN3	"BIT3" - WORKAREA FOR TS
		.... 1...		OUCBTSN4	"BIT4" - WORKAREA FOR TS
		.... .1..		OUCBTSN5	"BIT5" - WORKAREA FOR TS
		.... ..1.		OUCBTSN6	"BIT6" - WORKAREA FOR TS
		1		OUCBTSN7	"BIT7" - WORKAREA FOR TS
114	(72)	SIGNED	2	OUCBNTSP	- NUM OF ADD'L TRANSWAPS PENDING
116	(74)	SIGNED	4	OUCBPSS (2)	- CPU PAGE SECONDS
116	(74)	X'74'	0	OUCBPS1	"OUCBPSS" - HIGH WORD PAGE SECONDS
116	(74)	X'78'	0	OUCBPS2	"OUCBPSS+4" - LOW WORD PAGE SECONDS
124	(7C)	SIGNED	4	OUCBPST	- TIME OF LAST WORKING SET CHANGE
128	(80)	CHARACTER	128	OUCBCHE2 (0)	2nd cache line of OUCB
128	(80)	SIGNED	4	OUCBRCT	Interval RCT service accum
132	(84)	SIGNED	4	OUCBIIT	Interval I/O Interrupt service accumulator
136	(88)	SIGNED	2	OUCBNDS	- NUM OUTSTANDING DONTSWAPS
138	(8A)	BITSTRING	1	OUCBNTSG	- NEW TIME SLICE GROUP NUMBER
139	(8B)	SIGNED	1	OUCBRSV2	- reserved
140	(8C)	SIGNED	4	OUCBTME	- LAST RESPONSE TIME
144	(90)	SIGNED	4	OUCBTML	- TIME OF LAST TERMWAIT
148	(94)	SIGNED	4	OUCBDWMS	- INTVL DMN SVCE ACCUM
152	(98)	SIGNED	4	OUCBSRB	- INTERVAL SRB SERVICE ACCUM
156	(9C)	SIGNED	4	OUCBTWSS	- TARGET WORKING SET SIZE
160	(A0)	SIGNED	4	OUCBTMP	- PERF GRP PERIOD START TIME
164	(A4)	SIGNED	4	OUCBDLYT	- RTO DELAY END TIME
168	(A8)	SIGNED	4	OUCBHST	Interval Hiperspace service accumulator
172	(AC)	SIGNED	4	OUCBCFS	Accumulated sample of RAXFMCT for determing avg central storage
176	(B0)	CHARACTER	4	OUCBSUBN	- Subsystem name used by SMF and for workload activity reporting
180	(B4)	SIGNED	2	OUCBRPG	- RESET PERFORMANCE GROUP NUMBER
182	(B6)	SIGNED	2	OUCBSPG	- SPECIFIED PERFORMANCE GROUP NUMBER
182	(B6)	X'B8'	0	OUCBFPGO	"*" FPG OUTPUT AREA
184	(B8)	SIGNED	2	OUCBNPG	- NEW PERFORMANCE GROUP NUMBER
186	(BA)	SIGNED	2	OUCBSRPG	- SUBSYSTEM RPGN
188	(BC)	SIGNED	2	OUCBNRPG	- TRXNAME RPGN
190	(BE)	SIGNED	2	OUCBURPG	- USERID RPGN
192	(C0)	SIGNED	2	OUCBCRPG	- TRXCLASS RPGN
194	(C2)	SIGNED	2	OUCBARPG	- ACCOUNT NUMBER RPGN
196	(C4)	SIGNED	4	OUCBDRFP	Count of DREF pages in processor storage. Updated from values returned from RSM IARXCNTF routine.
200	(C8)	CHARACTER	8	OUCBTRXN	- TRANSACTION NAME
208	(D0)	CHARACTER	8	OUCBUSRD	- USERID
216	(D8)	CHARACTER	8	OUCBCLS	- TRANSACTION CLASS NAME
224	(E0)	SIGNED	4	OUCBTRS	accumulated transaction service
228	(E4)	SIGNED	4	OUCBTRR	transaction residency time accumulator
232	(E8)	SIGNED	4	OUCBACTP	pointer to accounting info (mapped by IRAACNT). OUCBACNT bit should be interrogated before referencing the accounting data.

OUCB Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
236	(EC)	SIGNED	4	OUCBSWSS	- SEC WORKING SET SIZE
240	(F0)	SIGNED	4	OUCBPSUM	BASE VALUE FOR PAGEIN COUNT
244	(F4)	SIGNED	2	OUCBFIXB	CNT OF BELOW FRAMES NEEDED FOR FIXED/LSQA PAGES
246	(F6)	BITSTRING	1	OUCBAPLV	APPC optimization level
247	(F7)	BITSTRING	1	OUCBESAP	individual expanded storage access policy, goal mode only
248	(F8)	SIGNED	4	OUCBRST (2)	- PAGE RESIDENCY TIME IN 1024 MICROSECOND UNITS
248	(F8)	X'F8'	0	OUCBRST1	"OUCBRST" - HIGH WORD PG RES SEC
248	(F8)	X'FC'	0	OUCBRST2	"OUCBRST+4" - LOW WORD PG RES SEC
256	(100)	BITSTRING	128		OUCB line 3, mapped by IRAOUCBX
384	(180)	BITSTRING	128		OUCB line 4, mapped by IRAOUCBX
512	(200)	BITSTRING	128		OUCB line 5, mapped by IRAOUCBX
640	(280)	BITSTRING	128		OUCB line 6, mapped by IRAOUCBX
768	(300)	BITSTRING	128		OUCB line 7, mapped by IRAOUCBX
896	(380)	BITSTRING	128		OUCB line 8, mapped by IRAOUCBX
1024	(400)	BITSTRING	128		OUCB line 9, mapped by IRAOUCBX
1152	(480)	BITSTRING	128		OUCB line 10, mapped by IRAOUCBX
1280	(500)	BITSTRING	128		OUCB line 11, mapped by IRAOUCBX
1408	(580)	BITSTRING	128		OUCB line 12, mapped by IRAOUCBX
1536	(600)	BITSTRING	128		OUCB line 13, mapped by IRAOUCBX
1664	(680)	BITSTRING	128		OUCB line 14, mapped by IRAOUCBX
1792	(700)	BITSTRING	128		OUCB line 15, mapped by IRAOUCBX
1920	(780)	BITSTRING	128		OUCB line 16, mapped by IRAOUCBX
2048	(800)	BITSTRING	128		OUCB line 17, mapped by IRAOUCBX
2176	(880)	BITSTRING	128		OUCB line 18, mapped by IRAOUCBX
2304	(900)	BITSTRING	128		OUCB line 19, mapped by IRAOUCBX
2432	(980)	BITSTRING	128		OUCB line 20, mapped by IRAOUCBX
2560	(A00)	BITSTRING	128		OUCB line 21, mapped by IRAOUCBX
2688	(A80)	BITSTRING	128		OUCB line 22, mapped by IRAOUCBX
2816	(B00)	DBL WORD	8	OUCBEND (0)	- END OF OUCB
2816	(B00)	X'B00'	0	OUCBLEN	"OUCBEND-OUCB" - LENGTH OF OUCB
186	(BA)	SIGNED	2	OUCBRPGN	RPGN ARRAY

OUCB Cross Reference

Name	Hex Offset	Hex Value
OUCB	0	
OUCBACN	58	0
OUCBACNT	1C	1
OUCBACT	54	
OUCBACTL	67	0
OUCBACTP	E8	0
OUCBAFAP	1C	40
OUCBAFL	13	0
OUCBAFPD	24	20
OUCBAPG	13	40
OUCBAPLV	F6	0
OUCBAPPC	25	80
OUCBARPG	C2	0
OUCBASAP	1C	4
OUCBASCB	2C	
OUCBASSW	5A	8
OUCBASW	13	1
OUCBATR	14	80
OUCBAXS	12	4
OUCBBCK	8	
OUCBBIB	11	20
OUCBBJOB	19	8
OUCBCFL	5A	0
OUCBCFS	AC	0
OUCBCHE1	0	
OUCBCHE2	80	
OUCBCIM	5B	20
OUCBCLS	D8	40404040
OUCBCMRV	5C	0
OUCBCPU	3C	0
OUCBCRPG	C0	0
OUCBCSBT	5B	0
OUCBCSFM	19	40
OUCBCSFS	19	80
OUCBCSMF	5A	4
OUCBCSW	58	
OUCBCTI	11	40
OUCBDFDE	24	8
OUCBDFDR	24	10
OUCBDFSW	19	2
OUCBDFS2	1C	20
OUCBDISC	1C	8
OUCBDLYB	10	1
OUCBDLYT	A4	0
OUCBDMN	28	0
OUCBDMO	26	0
OUCBDPSW	66	20
OUCBDRFP	C4	0
OUCBDRFR	50	0
OUCBDSPC	70	0
OUCBDSPN	71	0
OUCBDTA	12	2
OUCBDWMS	94	0
OUCBEAS	18	80
OUCBEASI	19	20
OUCBEFL	15	0
OUCBEND	B00	
OUCBENQ	11	2
OUCBENQI	13	10
OUCBENQR	17	2
OUCBERS	68	68
OUCBERST	24	1
OUCBERS1	68	68
OUCBERS2	68	6C
OUCBESAP	F7	0
OUCBESSS	5A	20
OUCBESSW	5A	10
OUCBFIXB	F4	0
OUCBFPGO	B6	B8
OUCBFTDN	66	80
OUCBFTDNNOSWAP		
	66	4
OUCBFWA	18	10
OUCBFWD	4	

Name	Hex Offset	Hex Value
OUCBFXS	12	1
OUCBGOB	10	20
OUCBGOI	10	40
OUCBGOO	10	80
OUCBGWRK	25	2
OUCBHIDP	19	10
OUCBHITR	5A	1
OUCBHST	A8	0
OUCBIAC	1D	0
OUCBIIT	84	0
OUCBINC	14	1
OUCBINIT	25	1
OUCBINP	14	2
OUCBINV	11	10
OUCBIOC	40	0
OUCBIOCL	68	0
OUCBIRSW	13	80
OUCBJSAS	17	40
OUCBJSFS	17	80
OUCBJSR	13	8
OUCBLEN	B00	B00
OUCBLFL	18	0
OUCBLLSW	19	1
OUCBLOG	12	20
OUCBLSW	10	2
OUCBLWT	15	80
OUCBMAR	14	4
OUCBMFL	1C	0
OUCBMFL2	24	0
OUCBMFL3	25	0
OUCBMGIN	24	40
OUCBMGSW	1C	10
OUCBMIGP	24	4
OUCBMNT	12	10
OUCBMPUR	1C	2
OUCBMSO	44	0
OUCBMTW	70	80
OUCBMWT	15	1
OUCBNAME	0	D6E4C3C2
OUCBNDP	1A	FF
OUCBNDS	88	1
OUCBNMTW	71	80
OUCBNPG	B8	0
OUCBNRPG	BC	0
OUCBNSW	11	80
OUCBNSWDP	17	20
OUCBNSWI	11	8
OUCBNTR	14	20
OUCBNTS	71	20
OUCBNTSG	8A	FF
OUCBNTSP	72	0
OUCBNWT	13	2
OUCBOFF	10	8
OUCBOIW	5B	2
OUCBOMVS	25	10
OUCBOOW	5B	1
OUCBOUT	10	4
OUCBOWT	5B	40
OUCBPAGP	30	
OUCBPCH	14	8
OUCBPFL	66	0
OUCBPGP	1F	C
OUCBPMON	25	40
OUCBPRF	18	2
OUCBPSD	66	40
OUCBPSS	74	0
OUCBPST	7C	0
OUCBPSTE	12	80
OUCBPSTR	5B	10
OUCBPSUM	F0	0
OUCBPS1	74	74
OUCBPS2	74	78
OUCBPTDN	66	8
OUCBPVL	11	4
OUCBQFL	10	20
OUCBQSC	15	2
OUCBQSFL	10	10
OUCBQSRV	18	40
OUCBQSS	5B	4
OUCBRCT	80	0
OUCBRDY	5A	80
OUCBREDP	18	4
OUCBREPT	13	20
OUCBRFL	19	0
OUCBRPG	B4	0
OUCBRPGN	BA	
OUCBRPT1	24	2
OUCBRQSC	18	20
OUCBRSFL	12	8
OUCBRSM	5A	40
OUCBRST	F8	0
OUCBRST1	F8	F8
OUCBRST2	F8	FC
OUCBRSV1	1E	0
OUCBRSV2	8B	0
OUCBRSV3	16	0
OUCBRTR	14	10
OUCBSBT	1C	80
OUCBSCN	11	1
OUCBSFL	11	94
OUCBSHBN	5A	2
OUCBSI	17	4
OUCBSIFX	17	1
OUCBSPG	B6	0
OUCBSRB	98	0
OUCBSRC	29	0
OUCBSRP	66	10
OUCBSRPG	BA	0
OUCBSTA	5B	8
OUCBSTFX	19	4
OUCBSTGI	18	1
OUCBSTR	14	40
OUCBSTT	12	40
OUCBSTTA	25	4
OUCBSUBN	B0	40404040
OUCBSWC	2A	0
OUCBSWSS	EC	0
OUCBTBMN	25	8
OUCBTET	18	8
OUCBTFL	14	0
OUCBTMA	C	0
OUCBTME	8C	0
OUCBTML	90	0
OUCBTMO	4C	0
OUCBTMP	A0	0
OUCBTMS	48	0
OUCBTMW	34	0
OUCBTNDP	1B	FF
OUCBTRM	5B	80
OUCBTRR	E4	0
OUCBTRS	E0	0
OUCBTRXN	C8	40404040
OUCBTS	70	20
OUCBTSC3	70	10
OUCBTSC4	70	8
OUCBTSC5	70	4
OUCBTSC6	70	2
OUCBTSC7	70	1
OUCBTSN3	71	10
OUCBTSN4	71	8
OUCBTSN5	71	4
OUCBTSN6	71	2
OUCBTSN7	71	1
OUCBTSWC	17	8
OUCBTSWP	17	10
OUCBTWSS	9C	0
OUCBUFL	17	0
OUCBURPG	BE	0

OUCB Cross Reference

Name	Hex Offset	Hex Value
OUCBUSRD	D0	40404040
OUCBVAL	64	0
OUCBVALV	25	20
OUCBVFMG	24	80
OUCBWMG	20	
OUCBWMR	5C	5C
OUCBWMRL	60	0
OUCBWMS	38	0
OUCBWRCI	22	0
OUCBWSCI	20	0
OUCBYFL	12	0
OUCB1BLK	0	

OUSB Information

OUSB Heading Information

Common Name: RESOURCES MANAGER USER SWAPPABLE BLOCK
Macro ID: IHAOUSB
DSECT Name: OUSB
Owning Component: System Resources Manager (SC1CX)
Eye-Catcher ID: OUSB
Offset: 0
Length: 4
Storage Attributes: Main Storage: YES
Virtual Storage: Address space
Auxiliary Storage: NO
Subpool: 255
Key: 0
Data Space: No
Residency: Above 16M line
Size: 304
Created by: IEAVEMIN
Pointed to by: ASXBOUSB field of the ASXB data area
Serialization: SRM lock
Function: THE OUSB IS USED BY THE SYSTEM RESOURCES MANAGER TO SAVE INFORMATION FROM THE OUXB, SO THAT THE OUXB MAY BE FREED WHEN THE DESCRIBED ADDRESS SPACE IS SWAPPED OUT. THE OUSB RESIDES IN LSQA, AND IS SWAPPED OUT ALONG WITH THE ADDRESS SPACE. THE OUSB ALSO SERVES TO ACCUMULATE USER PAGING STATISTICS FOR THE SYSTEM RESOURCES MANAGER.

OUSB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	376	OUSB	
0	(0)	CHARACTER	4	OUSBNAME	BLOCK IDENTIFICATION - 'OUSB'
4	(4)	CHARACTER	56	OUSBPAGE	OUSB PAGING INFO
4	(4)	SIGNED	4	OUSBPIN	SESSION PAGE-IN ACCUMULATOR
8	(8)	SIGNED	4	OUSBPOUT	SESSION PAGE-OUT ACCUMULATOR
12	(C)	SIGNED	4	OUSBCRMS	CACHE READ MISS ACCUMULATOR
16	(10)	SIGNED	4	OUSBVAMI	SESS VAM PAGE-IN ACCUMULATOR
20	(14)	SIGNED	4	OUSBVAMO	SESS VAM PAGE-OUT ACCUMULATOR
24	(18)	SIGNED	4	OUSBVAMR	SESS VAM RECLAIM ACCUMULATOR
28	(1C)	CHARACTER	12	OUSBSWAP	SWAPPING INFO FOR SMF
28	(1C)	SIGNED	4	OUSBSPIN	SWAPPING PAGE-IN ACCUMULATOR
32	(20)	SIGNED	4	OUSBSPOT	SWAPPING PAGE-OUT ACCUMULATOR
36	(24)	SIGNED	4	OUSBSWCT	SESSION SWAP CNT ACCUMULATOR
40	(28)	SIGNED	4	OUSBCAPI	COMMON PAGE-IN ACCUM
44	(2C)	SIGNED	4	OUSBHSPI	HIPERSPACE PAGE-IN COUNT
48	(30)	SIGNED	4	OUSBSTCT	PAGES STOLEN ACCUM
52	(34)	SIGNED	4	OUSBLPAI	LPA PAGE IN
56	(38)	SIGNED	4	OUSBHSPO	HIPERSPACE PAGE-OUT COUNT
60	(3C)	CHARACTER	146	OUSBSAVE	OUXBFLDS SAVEAREA
206	(CE)	SIGNED	2	OUSBR80	RESERVED
208	(D0)	CHARACTER	48	OUSBPAG2	more paging info for reporting purposes
208	(D0)	SIGNED	4	OUSBBPIN	interval block page-in accumulator
212	(D4)	SIGNED	4	OUSBBPNE	interval block page-in from ES accumulator
216	(D8)	SIGNED	4	OUSBPINE	interval page-in from ES accumulator
220	(DC)	SIGNED	4	OUSBBPOT	interval block page-out accumulator
224	(E0)	SIGNED	4	OUSBBPTE	interval block page-out to ES accumulator
228	(E4)	SIGNED	4	OUSBPOTE	interval page-out to ES accumulator
232	(E8)	SIGNED	4	OUSBBKIA	interval blocks in aux accumulator
236	(EC)	SIGNED	4	OUSBBKIE	interval blocks in ES accumulator
240	(F0)	SIGNED	4	OUSBBKOA	interval blocks out aux accumulator
244	(F4)	SIGNED	4	OUSBBKOE	interval blocks out ES accumulator
248	(F8)	SIGNED	4	OUSBSPPI	interval shared page-ins from aux accumulator
252	(FC)	SIGNED	4	OUSBSPEI	interval shared page-ins from ES accumulator
256	(100)	CHARACTER	48	OUSBSAV2	more fields need to be saved across swaps
256	(100)	BITSTRING	8	OUSBRCT	Base RCT time
264	(108)	BITSTRING	8	OUSBPRSS	Base preemptable/client SRB time for WM1 service calculation
272	(110)	UNSIGNED	4	OUSBWAIT	Accumulated I/O wait time for the adress space from CMB. Includes pending time and control unit queue time. In 128 microsecond units

OUSB Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
276	(114)	UNSIGNED	4	OUSBCON	Accumulated I/O connect time for the adress space from CMB. In 128 microsecond units
280	(118)	UNSIGNED	4	OUSBIOSC	Count of samples included in OUSBWAIT, OUSBCON, OUSBDISC
284	(11C)	UNSIGNED	4	OUSBDISC	Accumulated I/O disconnect time for the adress space from CMB. In 128 microsecond units.
288	(120)	UNSIGNED	4	OUSBFCON	Accumulated I/O FICON connect time for the address space from CMB. In 128 microsecond units
292	(124)	UNSIGNED	4	OUSBFDIS	Accumulated I/O FICON disconnect time for the address space from the CMB. In 128 microsecond units
296	(128)	UNSIGNED	4	OUSBFMNO	FICON magic number - for every I/O interrupt from a device attached to a FICON native CHPID, IOS will add one millisecond to this field.
300	(12C)	UNSIGNED	4	OUSBFWAIT	Accumulated I/O FICON wait time for the adress space from CMB. Includes pending time and control unit queue time. In 128 microsecond units
304	(130)	CHARACTER	8	OUSBPAG3	LARGE PAGE INFO REPORTED BY SMF
304	(130)	SIGNED	4	OUSBLPIN	INTERVAL LARGE PAGE-IN ACCUMULATOR
308	(134)	SIGNED	4	OUSBLPOUT	INTERVAL LARGE PAGE-OUT ACCUMULATOR
312	(138)	UNSIGNED	4	OUSBTHRO	Induced throttle time
316	(13C)	UNSIGNED	4	OUSBCNTD	Contention Delta time
320	(140)	CHARACTER	56	OUSBRSVDA	Reserved for anybody
376	(178)	CHARACTER	0	OUSBEND	END OF OUSB End of this block

OUSB Cross Reference

Name	Hex Offset	Hex Value
OUSB	0	
OUSBBKIA	E8	
OUSBBKIE	EC	
OUSBBKOA	F0	
OUSBBKOE	F4	
OUSBBPIN	D0	
OUSBBPNE	D4	
OUSBBPOT	DC	
OUSBBPTE	E0	
OUSBCAPI	28	
OUSBCNTD	13C	
OUSBCON	114	
OUSBCRMS	C	
OUSBDISC	11C	
OUSBEND	178	
OUSBFCON	120	
OUSBFDIS	124	
OUSBFMNO	128	
OUSBFWAIT	12C	
OUSBHSPI	2C	
OUSBHSPO	38	
OUSBIOSC	118	
OUSBLPAI	34	
OUSBLPIN	130	
OUSBLPOUT	134	
OUSBNAME	0	
OUSBPAGE	4	
OUSBPAG2	D0	
OUSBPAG3	130	
OUSBPIN	4	
OUSBPINE	D8	
OUSBPOTE	E4	
OUSBPOUT	8	
OUSBPRSS	108	
OUSBRCT	100	
OUSBRSVDA	140	
OUSBR80	CE	
OUSBSAVE	3C	
OUSBSAV2	100	
OUSBSPEI	FC	
OUSBSPIN	1C	
OUSBSPOT	20	
OUSBSPPI	F8	
OUSBSTCT	30	
OUSBSWAP	1C	
OUSBSWCT	24	
OUSBTHRO	138	
OUSBVAMI	10	
OUSBVAMO	14	
OUSBVAMR	18	
OUSBWAIT	110	

OUXB Information

OUXB Programming Interface information

Programming Interface information

OUXB

End of Programming Interface information

OUXB Heading Information

Common Name: RESOURCES MANAGER USER EXTENSION BLOCK
Macro ID: IHAOUXB
DSECT Name: OUXB
Owning Component: System Resources Manager (SC1CX)
Eye-Catcher ID: OUXB
Offset: 0
Length: 4
Storage Attributes: Main Storage: YES
Virtual Storage: Common
Auxiliary Storage: No
Subpool: 245
Key: 0
Data Space: no
Residency: Above 16M line
Size: 640 bytes @LSMF30B
Created by: IRAEVMCR @WLMP128
MASTER OUXB is located in IRARMCNS @WLMP128
Pointed to by: ASCBOUXB field of the ASCB data area
Serialization: SRM lock
Function: THE OUXB CONTAINS SUCH SYSTEM RESOURCES MANAGER DATA ABOUT AN ADDRESS SPACE AS IS NOT REQUIRED BY THE SYSTEM RESOURCES MANAGER WHILE THAT ADDRESS SPACE IS SWAPPED OUT. THE STORAGE FOR THE OUXB IS FREED DURING THE SWAPPED-OUT PERIOD. THE OUXB RESIDES IN SQA, SO IT MAY BE REFERENCED WITHOUT HAVING ADDRESSABILITY TO THE DESCRIBED ADDRESS SPACE.

OUXB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	OUXB	
0	(0)	CHARACTER	256	OUXB1BLK (0)	- FIRST 256 BYTES OF OUXB
0	(0)	CHARACTER	4	OUXBNAME	- BLOCK IDENTIFICATION
4	(4)	SIGNED	4	OUXBPSET	- MS6 BASE PREEMPTABLE/CLIENT SRB EXECUTION TIME
8	(8)	SIGNED	4	OUXBMET	- MSO BASE CPU MEASUREMENT
12	(C)	SIGNED	4	OUXBRSV0	- reserved
12	(C)	X'10'	0	OUXBPAGE	"*" PAGING INFO REPORTED BY SMF
16	(10)	SIGNED	4	OUXBPIN	- INTERVAL PAGE-IN ACCUMULATOR
20	(14)	SIGNED	4	OUXBPOUT	- INTERVAL PAGE-OUT ACCUMULATOR
24	(18)	SIGNED	4	OUXBCRMS	- CACHE READ MISS ACCUMULATOR
28	(1C)	SIGNED	4	OUXBVAMI	- INTERVAL VAM PAGE-IN ACCUMULATR
32	(20)	SIGNED	4	OUXBVAMO	- INTERVAL VAM PAGE-OUT ACCUMULAT
36	(24)	SIGNED	4	OUXBVAMR	- INTERVAL VAM RECLAIM ACCUMULATR
36	(24)	X'28'	0	OUXBSWAP	"*" SWAP INFORMATION
40	(28)	SIGNED	4	OUXBSPIN	SWAP PAGE IN COUNT
44	(2C)	SIGNED	4	OUXBSPOT	SWAP PAGE OUT COUNT
48	(30)	SIGNED	4	OUXBSWCT	SWAP COUNT
52	(34)	SIGNED	4	OUXBCAPI	- INTERVAL COMMON AREA PAGINS
56	(38)	SIGNED	4	OUXBHSPI	- HIPERSPACE PAGE-IN COUNT
60	(3C)	SIGNED	4	OUXBSTCT	- PAGES STOLEN ACCUMULATOR
64	(40)	SIGNED	4	OUXBLPAI	- LPA PAGE IN
68	(44)	SIGNED	4	OUXBHSPO	- HIPERSPACE PAGE-OUT COUNT
72	(48)	SIGNED	2	OUXBCPDL	- Delayed sample count - count for reduced preemption of the number of times this address space was delayed during a cycle (20) of samples
74	(4A)	SIGNED	2	OUXBSTC	- INTERVAL STEAL CALL COUNT
76	(4C)	SIGNED	4	OUXBEJST	- BASE EXEC TIME FOR 101%
80	(50)	ADDRESS	4	OUXBTSW	- TRANSWAP ECB ADDRESS
80	(50)	X'54'	0	OUXBFLDS	"*" OUXB information saved in OUSB at QSCEMP (Queisce Complete)
84	(54)	SIGNED	4	OUXBRSV2	- reserved
84	(54)	X'58'	0	OUXBACNT	"*" ACCOUNTING INFORMATION
88	(58)	SIGNED	4	OUXBTRC	- SESSION TRANSACTION COUNT
92	(5C)	SIGNED	4	OUXBJBS	- SESSION SERVICE ACCUMULATOR
96	(60)	SIGNED	4	OUXBJBT	- SESSION TIME ACCUMULATOR
100	(64)	SIGNED	4	OUXBRSVD	- reserved
104	(68)	SIGNED	4	OUXBTRT	- TRANSACTION TIME ACCUMULATOR
108	(6C)	SIGNED	4	OUXBJBR	- SESSION RESIDENCY ACCUMULATOR
112	(70)	SIGNED	4	OUXBRSVE	- reserved
116	(74)	SIGNED	4	OUXBJCPU	- SESSION CPU SERVICE ACCUM
120	(78)	SIGNED	4	OUXBTCPU	- TRANSACTION CPU SERVICE ACCUM
124	(7C)	SIGNED	4	OUXBJIOC	- SESSION I/O SERVICE ACCUM
128	(80)	SIGNED	4	OUXBTIOC	- TRANSACTION I/O SERVICE ACCUM
132	(84)	SIGNED	4	OUXBJMSO	- SESSION STORAGE SERVICE ACCUM

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
136	(88)	SIGNED	4	OUXBTMSO	- TRANSACTION STORAGE SERVICE ACC
140	(8C)	SIGNED	4	OUXBJSRB	- SESSION SRB SERVICE ACCUM
144	(90)	SIGNED	4	OUXBTSRB	- TRANSACTION SRB SERVICE ACCUM
148	(94)	SIGNED	4	OUXBCSET	- Base preemptable and client SRB time, utilized in AP1.
152	(98)	SIGNED	4	OUXBIOSM	- SMF BASE EXCP COUNT
156	(9C)	SIGNED	4	OUXBDCTI	- DEVIVE CONN TIME BASE
160	(A0)	DBL WORD	8	OUXBCPS	- WLM CPU MEASRMT - 64BIT NMB
168	(A8)	DBL WORD	8	OUXBMSS	- WLM MSO BASE SERVICE VALUE
176	(B0)	DBL WORD	8	OUXBSBS	WLM SRB BASE SERVICE VALUE
184	(B8)	SIGNED	4	OUXBCUQT	Accumulated I/O control unit queue time for the address space from CMB. In 128 microsecond units.
188	(BC)	SIGNED	4	OUXBSTD	- AUX BASE START TIME
192	(C0)	SIGNED	4	OUXBPRS	- PERF GRP PERIOD STARTING SERVIC
196	(C4)	SIGNED	2	OUXBWCT	- APG BASE SHORT WAIT COUNT
198	(C6)	SIGNED	1	OUXBRSV1	- RESERVED
199	(C7)	BITSTRING	1	OUXBFLGS	- FLAG BYTE
		1...		OUXBWMO	"BIT0" - TSO COMMAND ENDED
		.1..		OUXBCLST	"BIT1" - TSO CLIST MODE
		..1.		OUXBPRM2	"BIT2" - Address Space formerly had full preemption
		...1		OUXBISWI	"BIT3" - Ignore paging data because address space was just swapped in
		.... 1...		OUXBGFRR	"BIT4" - Getmained by IRARMERR
200	(C8)	SIGNED	4	OUXBVSC	- AUX BASE VAM SLOT COUNT
204	(CC)	SIGNED	4	OUXBNVC	- AUX BASE NONVAM SLOT COUNT
208	(D0)	SIGNED	2	OUXBFIXC	- BASE USER FIXED FRAME COUNT
210	(D2)	SIGNED	2	OUXBUIC	- HIGHEST UNREF FRAME COUNT
212	(D4)	SIGNED	4	OUXBSIBP	- BASE PAGE IN COUNT
216	(D8)	SIGNED	4	OUXBSIBR	- BASE RESIDENT TIME
220	(DC)	SIGNED	4	OUXBSIBE	- BASE EXECUTION TIME
224	(E0)	SIGNED	2	OUXBSIPR	- RECENT PAGE IN RATE
226	(E2)	ADDRESS	2	(2)	- RESERVED
230	(E6)	SIGNED	2	OUXBBSWC	- Base short wait count
232	(E8)	DBL WORD	8	OUXBAET	- APG BASE CPU MEASUREMENT
240	(F0)	SIGNED	4	OUXBUICT	- TIME UIC UPDT LAST DONE
244	(F4)	SIGNED	4	OUXBTSIO	- TRANSACTION RESIDENT INTERVAL I/O SERVICE
248	(F8)	SIGNED	2	OUXBCPWS	- Swapped in sample count - count for reduced preemption of the number of samples during a sample cycle (20) that this address space was swapped in
250	(FA)	SIGNED	2	OUXBDSCN	- Dispatchable count: the number of times that this address space has been found in subroutine CPUTLCK to be dispatchable yet no CPU time has accumulated for it.
252	(FC)	SIGNED	4	OUXBEJT2	- LOWER HALF OF ASCBEJST AT SWAP IN
256	(100)	CHARACTER	256	OUXB2BLK (0)	- SECOND PART OF OUXB
256	(100)	DBL WORD	8	OUXBEWST	- ASCBEWST AT SWAP IN
264	(108)	SIGNED	4	OUXBFMCT	- Effective Frame Count
268	(10C)	SIGNED	4	OUXBTRIM	- Count of frames above 512 or the Target Working Set
272	(110)	CHARACTER	48	OUXBPAG2 (0)	More paging info for reporting purposes
272	(110)	SIGNED	4	OUXBBPIN	- Interval block page-in accumulator
276	(114)	SIGNED	4	OUXBBPNE	- Interval block page-in from ES accumulator
280	(118)	SIGNED	4	OUXBPINE	- Interval page-in from ES accumulator
284	(11C)	SIGNED	4	OUXBBPOT	- Interval block page-out accumulator
288	(120)	SIGNED	4	OUXBBPTE	- Interval block page-out to ES accumulator
292	(124)	SIGNED	4	OUXBPOTE	- Interval page-out to ES accumulator
296	(128)	SIGNED	4	OUXBBKIA	- Interval blocks in aux accumulator
300	(12C)	SIGNED	4	OUXBBKIE	- Interval blocks in ES accumulator
304	(130)	SIGNED	4	OUXBBKOA	- Interval blocks out aux accumulator
308	(134)	SIGNED	4	OUXBBKOE	- Interval blocks out ES accumulator
312	(138)	SIGNED	4	OUXBSPPI	- Interval shared page-ins from aux
316	(13C)	SIGNED	4	OUXBSPEI	- Interval shared page-ins from ES
320	(140)	DBL WORD	8	OUXBIIT	- Base I/O Interrupt time
328	(148)	DBL WORD	8	OUXBHST	- Base Hiperspace time
336	(150)	SIGNED	4	OUXBTRA	- TCB ready accumulator
340	(154)	SIGNED	4	OUXBMTA	- Multi-tasking accumulator
344	(158)	SIGNED	4	OUXBPSTO	- Processor storage base (RAXFMCT + RAXESCT)
348	(15C)	SIGNED	4	OUXBAPIN	- Auxiliary page-in base (OUXBPIN + OUXBBPIN)
352	(160)	CHARACTER	48	OUXBSAV2 (0)	More fields need to be saved across swaps
352	(160)	DBL WORD	8	OUXBRCT	- Base RCT time
360	(168)	DBL WORD	8	OUXBPRSS	- Base preemptable and client SRB time for service calculation
368	(170)	SIGNED	4	OUXBWAIT	Accumulated I/O wait time for the address space from CMB. Includes pending time and control unit queue time. In 128 microsecond units
372	(174)	SIGNED	4	OUXBCON	Accumulated I/O connect time for the address space from CMB. In 128 microsecond units
376	(178)	SIGNED	4	OUXBIOSC	Count of samples included in OUXBWAIT, OUXBCON, OUXBDISC
380	(17C)	SIGNED	4	OUXBDISC	Accumulated I/O disconnect time for address space from CMB. In 128 microsecond units

OUXB Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
384	(180)	SIGNED	4	OUXBFCON	Accumulated I/O FICON connect time for the address space from CMB. In 128 microsecond units
388	(184)	SIGNED	4	OUXBFDIS	Accumulated I/O FICON disconnect time for address space from CMB. In 128 microsecond units
392	(188)	SIGNED	4	OUXBFMNO	FICON magic number - for every I/O interrupt from a device attached to a FICON native CHPID, IOS will add one millisecond to this field
396	(18C)	SIGNED	4	OUXBFWAIT	Accumulated I/O FICON wait time for for address space from CMB. Includes pending time and control unit queue time. In 128 microsecond units
400	(190)	SIGNED	4	OUXBSPVB	Base for RaxSpVlc (shared page validation count) used by aux shortage processing
404	(194)	ADDRESS	4	OUXBIRSP	- REALSWAP ECB address or, if high order bit is on, address of an ECB list. This is the pending ECB / list T-P1
408	(198)	ADDRESS	4	OUXBIRS	- REALSWAP ECB address or, if high order bit is on, address of an ECB list. This is the processing ECB / list R-P2
412	(19C)	SIGNED	4	OUXBTLRS	SRM Timestamp when normal TCB/SRB Last Received Service.
416	(1A0)	SIGNED	4	OUXBTLRT	SRM Timestamp when normal Address Space Last Received Trickles.
420	(1A4)	SIGNED	4	OUXBTLR#	Number of WEBs that received a Trickle.
424	(1A8)	DBL WORD	8	OUXBIOCA	I/O count accumulator
432	(1B0)	DBL WORD	8	OUXBIOCB	Base for calculating I/O count deltas
440	(1B8)	DBL WORD	8	OUXBTMSL	tran.storage serv. acc. lon
448	(1C0)	DBL WORD	8	OUXBJMSL	session storage serv.acc.lon
456	(1C8)	DBL WORD	8	OUXBJBSL	session servic accum. long
464	(1D0)	DBL WORD	8	OUXBRSV3	reserved
472	(1D8)	DBL WORD	8	OUXBPRSL	pg period starting serv. lon
480	(1E0)	DBL WORD	8	OUXBJCPL	session cpu service acc. lon
488	(1E8)	DBL WORD	8	OUXBTCPL	trans. cpu serv. accum. lon
496	(1F0)	DBL WORD	8	OUXBJSRL	session srb service acc. lon
504	(1F8)	DBL WORD	8	OUXBTSRL	transaction srb serv.acc.lon
512	(200)	CHARACTER	128	OUXB3BLK (0)	- third part of OUXB
512	(200)	DBL WORD	8	OUXBJIOL	session i/o service acc. lon
520	(208)	DBL WORD	8	OUXBTIOL	transaction i/o serv.acc.lon
520	(208)	X'210'	0	OUXBPAG3	"*" LARGE PAGE INFO REPORTED BY SMF
528	(210)	SIGNED	4	OUXBLPIN	- INTERVAL LARGE PAGE-IN ACCUMULATOR
532	(214)	SIGNED	4	OUXBLPOUT	- INTERVAL LARGE PAGE-OUT ACCUMULATOR
536	(218)	SIGNED	4	OUXBTHRO	Accumulated I/O induced throttle time for the address space from CMB. In 128 microsecond units.
540	(21C)	SIGNED	4	OUXBCNTD	Accumulated I/O contention time for the address space from CMB. In 128 microsecond units.
544	(220)	SIGNED	4	OUXBRESV (24)	reserved
640	(280)	DBL WORD	8	OUXBEND (0)	- END OF OUXB
640	(280)	X'280'	0	OUXBLEN	"OUXBEND-OUXB" - LENGTH OF OUXB

OUXB Cross Reference

Name	Hex Offset	Hex Value
OUXB	0	
OUXBACNT	54	58
OUXBAET	E8	0
OUXBAPIN	15C	0
OUXBBKIA	128	0
OUXBBKIE	12C	0
OUXBBKOA	130	0
OUXBBKOE	134	0
OUXBBPIN	110	0
OUXBBPNE	114	0
OUXBBPOT	11C	0
OUXBBPTE	120	0
OUXBBSWC	E6	0
OUXBCAPI	34	0
OUXBCLST	C7	40
OUXBCNTD	21C	0
OUXBCON	174	0
OUXBCPDL	48	0
OUXBCPS	A0	0
OUXBCPWS	F8	0
OUXBCRMS	18	0
OUXBCSET	94	0
OUXBCUQT	B8	0
OUXBDCTI	9C	0
OUXBDISC	17C	0
OUXBDSCN	FA	0
OUXBEJST	4C	0
OUXBEJT2	FC	0
OUXBEND	280	
OUXBEWST	100	0
OUXBFCON	180	0
OUXBFDIS	184	0
OUXBFIXC	D0	0
OUXBFLDS	50	54
OUXBFLGS	C7	0
OUXBFMCT	108	0
OUXBFMNO	188	0
OUXBFWAIT	18C	0
OUXBGFRR	C7	8
OUXBHSPI	38	0
OUXBHSPO	44	0
OUXBHST	148	0
OUXBIIT	140	0
OUXBIOCA	1A8	0
OUXBIOCB	1B0	0
OUXBIOSC	178	0
OUXBIOSM	98	0
OUXBIRS	198	
OUXBIRSP	194	
OUXBISWI	C7	10
OUXBJBR	6C	0
OUXBJBS	5C	0
OUXBJBSL	1C8	
OUXBJBT	60	0

Name	Hex Offset	Hex Value
OUXBJCPL	1E0	
OUXBJCPU	74	0
OUXBJIOC	7C	0
OUXBJIOL	200	0
OUXBJMSL	1C0	0
OUXBJMSO	84	0
OUXBJSRB	8C	0
OUXBJSRL	1F0	0
OUXBLEN	280	280
OUXBLPAI	40	0
OUXBLPIN	210	0
OUXBLPOUT	214	0
OUXBMET	8	0
OUXBMSS	A8	0
OUXBMTA	154	0
OUXBNAME	0	D6E4E7C2
OUXBNVC	CC	0
OUXBPAGE	C	10
OUXBPAG2	110	
OUXBPAG3	208	210
OUXBPIN	10	0
OUXBPINE	118	0
OUXBPOTE	124	0
OUXBPOUT	14	0
OUXBPRM2	C7	20
OUXBPRS	C0	0
OUXBPRSL	1D8	
OUXBPRSS	168	0
OUXBPSET	4	0
OUXBPSTO	158	0
OUXBRCT	160	0
OUXBRESV	220	
OUXBRSVD	64	0
OUXBRSVE	70	0
OUXBRSV0	C	0
OUXBRSV1	C6	0
OUXBRSV2	54	0
OUXBRSV3	1D0	
OUXBSAV2	160	
OUXBSBS	B0	0
OUXBSIBE	DC	0
OUXBSIBP	D4	0
OUXBSIBR	D8	0
OUXBSIPR	E0	0
OUXBSPEI	13C	0
OUXBSPIN	28	0
OUXBSPOT	2C	0
OUXBSPPI	138	0
OUXBSPVB	190	0
OUXBSTC	4A	0
OUXBSTCT	3C	0
OUXBSTD	BC	0
OUXBSWAP	24	28
OUXBSWCT	30	0
OUXBTCPL	1E8	
OUXBTCPU	78	0
OUXBTHRO	218	0
OUXBTIOC	80	0
OUXBTIOL	208	0
OUXBTLR#	1A4	0
OUXBTLRS	19C	0
OUXBTLRT	1A0	0
OUXBTMSL	1B8	
OUXBTMSO	88	0
OUXBTRA	150	0
OUXBTRC	58	0
OUXBTRIM	10C	0
OUXBTRT	68	0
OUXBTSIO	F4	0
OUXBTSRB	90	0
OUXBTSRL	1F8	0
OUXBTSW	50	
OUXBUIC	D2	0
OUXBUICT	F0	0

Name	Hex Offset	Hex Value
OUXBVAMI	1C	0
OUXBVAMO	20	0
OUXBVAMR	24	0
OUXBVSC	C8	0
OUXBWAIT	170	0
OUXBWCT	C4	0
OUXBWMO	C7	80
OUXB1BLK	0	
OUXB2BLK	100	
OUXB3BLK	200	

PARM4CB Information

PARM4CB Heading Information

Common Name: Input for IEFAB4CB
Macro ID: IEFZB4CB
DSECT Name: None
Owning Component: Allocation (SC1B4)
Eye-Catcher ID: None
Storage Attributes: Key: 1
Residency: Any
Size: 12 bytes * number of devices to be processed
Created by: Issuers of IEFPEND (Currently only Consoles)
Pointed to by: DEVLIST parameter of IEFPEND
Serialization: None
Function: Maps the device list which is used by the IEFPEND macro, Consoles and IEFAB4CB.
For Online requests originating from Consoles, the larger CB_devices_Online structure is used for enhanced communication between Allocation and Consoles.

PARM4CB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	12	CB_DEVICES (*)	List of pending devices
0	(0)	CHARACTER	12	CB_DEVICES_PENDING	Offline/Unload section
0	(0)	CHARACTER	4	CB_DEVNUM	Device number in EBCDIC
4	(4)	ADDRESS	4	CB_DVUCBPTR	Corresponding UCB pointer
8	(8)	CHARACTER	1	CB_DEVFLAGS	Flags
		1...		CB_ACTION_COMPLETE	Requested function completed successfully
		.1..		CB_ACTION_PENDING	Requested function still pending due to the device state
		..1.		CB_JES3_VARY_NEEDED	Device must be varied online to JES3.
		...1		CB_DEVICE_INVALID	No UCB could be found for the input device number
		 1111		*	Reserved
9	(9)	CHARACTER	3	*	Reserved

PARM4CB Cross Reference

Name	Hex Offset	Hex Value
CB_ACTION_COMPLETE	8	80
CB_ACTION_PENDING	8	40
CB_DEVFLAGS	8	
CB_DEVICE_INVALID	8	10
CB_DEVICES	0	
CB_DEVICES_PENDING	0	
CB_DEVNUM	0	
CB_DVUCBPTR	4	
CB_JES3_VARY_NEEDED	8	20

PART Information

PART Heading Information

Common Name: Paging Activity Reference Table
Macro ID: ILRPART
DSECT Name: PART
Owning Component: Auxiliary Storage Manager (SC1CW)
Eye-Catcher ID: PART
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: YES
Subpool: 245
Key: 0
Data Space: NO
Residency: Above 16 Megabytes virtual
Size: Header is 80 bytes. Each entry (PARTE) is 96 bytes.
There can be up to 256 PARTEs.
Created by: ILRASRM1
Pointed to by: ASMPART field of the ASMVT data area.
IORPARTE field of the IORB points to a PART entry (PARTE)
PAREPARE field of the PARTE points to the next PARTE in use
PATPART field of the PAT points to the PARTE associated with that PAT.
Serialization: ASMGL lock
Function: PART is the map relating the collection of logical slots of auxiliary storage to identifiable page data sets.

PART Map

Offsets Dec	Offsets Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	24656	PART	Paging Activity Reference Table
0	(0)	CHARACTER	80	PARTHDR	PART header. Contains general information about the page data sets.
0	(0)	CHARACTER	4	PARTIDEN	'PART' identifier.
4	(4)	SIGNED	4	PARTSIZE	Total number of entries in the PART, used or unused.
8	(8)	SIGNED	2	PARTEUSE	Total number of PART entries currently in use.
10	(A)	SIGNED	2	PARTLAST	Index number of the last PARTE which is in use (zero-based).
12	(C)	ADDRESS	4	PARTCIR0	Circular queue header for PAV data sets.
16	(10)	ADDRESS	4	PARTSCME	Pointer to SCM PARTE
20	(14)	ADDRESS	4	PARTCIR2	Circular queue header for movable-head data sets.
24	(18)	ADDRESS	4	PARTDSNL	Address of the page data set section of the ASM data set name list, in ECSA. This address replaces the TPARTBLE pointer when the DSNLIST is built by ILRTMI00.
24	(18)	ADDRESS	4	PARTTPAR	Address of TPARTBLE for use by ILRTMI00.
28	(1C)	ADDRESS	4	PARTPCTQ	Address of first in chain of one or more PCTs that have been built for the device types containing open page data sets.
32	(20)	SIGNED	2	PARTLCNT	Count of active local page data sets
34	(22)	BITSTRING	1	PARTFLG1	PART flags
		1...		PARTNVIO	VIO-accepting data set flag. 1 = no VIO-accepting data sets are in use, 0 = at least one VIO-accepting data set is in use.
		.111 1111		*	Reserved
35	(23)	CHARACTER	1	*	Reserved
36	(24)	CHARACTER	8	PARTNPCW	Queue of AIAs to be redriven because there were no PCCWs available.
36	(24)	ADDRESS	4	PARTNPCF	First AIA on no-PCCW queue
40	(28)	ADDRESS	4	PARTNPCL	Last AIA on no-PCCW queue
44	(2C)	UNSIGNED	4	PARTTIME	Sum of total service times for all local page data sets
48	(30)	ADDRESS	4	PARTPLPA	Address of the PARTE for the PLPA data set
52	(34)	ADDRESS	4	PARTLOCA	Address of the PARTE for the first local page data set
56	(38)	ADDRESS	4	PARTLSTA	Address of the PARTE for the last in-use local page data set.
60	(3C)	UNSIGNED	4	PARTLORQ	Lowest single request service time (PARERQTM) amongst all local paging data sets.
64	(40)	CHARACTER	15	PARTRSV2	Reserved
79	(4F)	BITSTRING	1	PARTLVL	PART level ID
80	(50)	CHARACTER	96	PARTENTS (255:562134688)	The PART entries. One PARTE represents one page data set. A PARTE is built for each page data set opened at IPL time and for each potential data set that can be added later up to a maximum of 256 total entries.

Offsets Dec	Offsets Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	96	PARTENT	PART Entry

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	ADDRESS	4	PAREPARE	Pointer to next PARTE in use.
4	(4)	UNSIGNED	1	PAREBRST	Burst size for this data set
5	(5)	UNSIGNED	1	PAREIORN	Number of IORBs built for this data set
6	(6)	CHARACTER	2	PARERSV4	Reserved
8	(8)	CHARACTER	1	PARETYPE	Page data set type flags
		1...		PAREPLPA	PLPA data set flag.
		.1..		PARECOMM	Common data set flag.
		..1.		PARESCM	SCM PARTE
		...1		PARELOCL	Local data set flag.
		.... 1...		*	Reserved
		.... .1..		*	Reserved
		.... ..1.		PAREPD	PAGEDEL-in-process flag. 1 = PAGEDEL is in process for this data set, 0 = PAGEDEL not active.
		1		PAREDRN	Draining flag. 1 = data set is draining, 0 = data set not draining.
9	(9)	CHARACTER	1	PAREFLG1	PARTE flags
		1...		PARENUSE	PARTE not in use flag. 1 = PARTE not in use, 0 = PARTE in use.
		.1..		PAREDSBD	Data set bad flag. 1 = ASM has marked this page data set bad. It is no longer being used for write requests, and is effectively read-only, 0 = data set in normal read/write use.
		..1.		*	Reserved.
		...1		PARENVIO	NONVIO flag. 1 = data set is a NONVIO data set, 0 = data set is not NONVIO.
		.... 1...		PAREPAVOK	PAV capable device. 1 = data set is on a device defined to be PAV capable. 0 = data set is normal.
		.... .1..		PAREPAVACTIVE	PAV support is active 1 = Device is a HyperPAV or an alias is defined for this data set (Traditional PAV). 0 = data set is normal.
		.... ..1.		PARECACHEOK	1 = data set is on a device for which we should not bypass caching, 0 = caching should be bypassed
		1		PARECKD	ECKD architecture flag. 1 = data set is on an ECKD device, 0 = data set is not on an ECKD device.
10	(A)	SIGNED	2	PARENN	PART number for this PARTE.
12	(C)	ADDRESS	4	PAREDEIB	Pointer to the DEIB for this data set
16	(10)	SIGNED	4	PARESZSL	Total defined size of the data set, in slots.
20	(14)	SIGNED	4	PARESLTA	Number of currently available slots on the data set.
24	(18)	SIGNED	4	PARERRCT	Number of permanent I/O errors suffered by this page data set.
28	(1C)	ADDRESS	4	PAREIORB	Pointer to first IORB for this page data set.

Comment

Multiply defined area.
- For paging data set PARTEs: PAT ptr and PCT ptr
- For SCM PARTE: Non-block write AIA defer queue

End of Comment

32	(20)	CHARACTER	8	*	
32	(20)	CHARACTER	8	*	Paging data set PARTEs
32	(20)	ADDRESS	4	PAREPATP	Pointer to PAT for this page data set.
36	(24)	ADDRESS	4	PAREPCTP	Pointer to PCT for the type of device on which this data set resides.
32	(20)	CHARACTER	8	*	SCM PARTE
32	(20)	ADDRESS	4	PARESCMAIAQF	Ptr to 1st AIA on SCM defer queue.
36	(24)	ADDRESS	4	PARESCMAIAQL	Ptr to last AIA on SCM defer queue.
40	(28)	ADDRESS	4	PAREEDBP	Pointer to EDB for page data set.
44	(2C)	ADDRESS	4	PAREUCBP	Pointer to UCB for page data set.
48	(30)	ADDRESS	4	*	Reserved
52	(34)	UNSIGNED	4	PARETIME	Total service time for this data set (used for locals only)
56	(38)	UNSIGNED	4	PARERQTM	Latest calculation of single-request service time for this data set (used for locals only)
60	(3C)	CHARACTER	2	PARERSV3	Reserved
62	(3E)	SIGNED	2	PAREREQS	Number of outstanding I/O requests on the data set (used for all page data sets)
64	(40)	BITSTRING	1	PAREFLG2	Flag byte
		1...		PAREPAVQ	PAV queue flag. 1 = data set is on the PAV circular queue, 0 = data set not on the PAV queue.
		.1..		*	Reserved
		..1.		PAREMOVQ	Moveable-head queue flag. 1 = data set is on the moveable-head circular queue, 0 = data set is not on the moveable-head queue.
		...1		PAREPKER	Pack error flag. 1 = data set bad due to pack error, 0 = data set not bad due to pack error.
		.... 1...		PAREFRSB	ILRFRSRB scheduled flag. 1 = ILRFRSRB scheduled, 0 = ILRFRSRB not scheduled
		.... .1..		PARESLT0	Slot 0 error flag. 1 = slot 0 is bad due to an I/O error, 0 = slot 0 is usable.
		.... ..1.		PARECATE	Catalog access error flag. 1 = catalog access failed due to an uncorrectable error. 0 = catalog is usable.
		1		*	Reserved
65	(41)	UNSIGNED	3	PARELSLT	Write cursor. Contains the slot number of the last slot written-to on the data set

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
Comment					
Multiply defined area. - For paging data set PARTEs: CTB address, migration count, data set definition time. - For SCM PARTE: Input defer queue, block output defer queue					
End of Comment					
68	(44)	CHARACTER	16	*	
68	(44)	CHARACTER	16	*	Paging data set PARTEs
68	(44)	ADDRESS	4	PAREOCTB	Address of old CTB, if any.
72	(48)	SIGNED	4	PAREMIGA	Migrated slot count
76	(4C)	BITSTRING	8	PAREDTIM	Data set definition timestamp
68	(44)	CHARACTER	16	*	SCM PARTE
68	(44)	ADDRESS	4	PARESCMAIAINPUTQF	1st AIA on input defer queue
72	(48)	ADDRESS	4	PARESCMAIAINPUTQL	Last AIA on input defer queue
76	(4C)	ADDRESS	4	PARESCMAIABLOCKOUTQF	1st AIA on block output defer queue
80	(50)	ADDRESS	4	PARESCMAIABLOCKOUTQL	Last AIA on block output defer queue 4@L6D
84	(54)	CHARACTER	12	PARERSV1	Reserved

PART Constants

Len	Type	Value		Name	Description
1	DECIMAL		0	PARTPLPN	PART number of PLPA data set
1	DECIMAL		1	PARTCOMN	PART number of common data set
1	DECIMAL		2	PARTRSVD	Reserved constant. Was the PART number of the DUPLEX data set.
1	DECIMAL		3	PARTLOCN	PART number of first local data set
1	HEX	02		PARTLEVL	PART level ID

PART Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
PAREBRST	4		PARERQTM	38	
PARECACHEOK	9	02	PARERRCT	18	
PARECATE	40	02	PARERSV1	54	
PARECKD	9	01	PARERSV3	3C	
PARECOMM	8	40	PARERSV4	6	
PAREDEIB	C		PARESCM	8	20
PAREDRN	8	01	PARESCMAIABLOCKOUTQF		
PAREDSBD	9	40		4C	
PAREDTIM	4C		PARESCMAIABLOCKOUTQL		
PAREEDBP	28			50	
PAREFLG1	9		PARESCMAIAINPUTQF		
PAREFLG2	40			44	
PAREFRSB	40	08	PARESCMAIAINPUTQL		
PAREIORB	1C			48	
PAREIORN	5		PARESCMAIAQF	20	
PARELOCL	8	10	PARESCMAIAQL	24	
PARELSLT	41		PARESLTA	14	
PAREMIGA	48		PARESLT0	40	04
PAREMOVQ	40	20	PARESZSL	10	
PARENN	A		PARETIME	34	
PARENUSE	9	80	PARETYPE	8	
PARENVIO	9	10	PAREUCBP	2C	
PAREOCTB	44		PART	0	
PAREPARE	0		PARTCIR0	C	
PAREPATP	20		PARTCIR2	14	
PAREPAVACTIVE			PARTDSNL	18	
	9	04	PARTENT	0	
PAREPAVOK	9	08	PARTENTS	50	
PAREPAVQ	40	80	PARTEUSE	8	
PAREPCTP	24		PARTFLG1	22	
PAREPD	8	02	PARTHDR	0	
PAREPKER	40	10	PARTIDEN	0	
PAREPLPA	8	80	PARTLAST	A	
PAREREQS	3E		PARTLCNT	20	

PART Cross Reference

Name	Hex Offset	Hex Value
PARTLOCA	34	
PARTLORQ	3C	
PARTLSTA	38	
PARTLVL	4F	
PARTNPCF	24	
PARTNPCL	28	
PARTNPCW	24	
PARTNVIO	22	80
PARTPCTQ	1C	
PARTPLPA	30	
PARTRSV2	40	
PARTSCME	10	
PARTSIZE	4	
PARTTIME	2C	
PARTTPAR	18	

PAT Information

PAT Heading Information

Common Name: Page Allocation Table
Macro ID: ILRPAT
DSECT Name: PAT
Owning Component: Auxiliary Storage Manager (SC1CW)
Eye-Catcher ID: PAT
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: YES
Subpool: 245
Key: 0
Data Space: NO
Residency: Above 16 Megabytes virtual
Size: 24 bytes + 1 bit per slot in the paging space
Created by: ILRASRIM, ILRPGEXP
Pointed to by: PAREPATP field of the PARTE data area
Serialization: The PATMAPs are serialized by the ASMGL lock.
Function: The PAT is an exact representation of allocated slots within a paging space.

PAT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	PAT	Page Allocation Table
0	(0)	CHARACTER	24	PATHDR	PAT header
0	(0)	CHARACTER	4	PATIDENT	'PAT ' identifier
4	(4)	ADDRESS	4	PATPART	Pointer to the PART entry
8	(8)	UNSIGNED	2	PATCYLNO	Number of cylinder maps in this PAT
10	(A)	SIGNED	2	PATCYLSZ	Number of slots per cylinder
12	(C)	SIGNED	2	PATCYLMW	Number of words required to map one cylinder
14	(E)	CHARACTER	2	PATRSV1	Reserved
16	(10)	CHARACTER	4	PATCCHHB	CCHH of the beginning of the data set
20	(14)	CHARACTER	4	PATCCHHE	CCHH of the end of the data set
24	(18)	CHARACTER	*	PATMAP	Slot allocation bit map.
24	(18)	CHARACTER	4	PATCYLS (*)	Cylinder map words

PAT Cross Reference

Name	Hex Offset	Hex Value
PAT	0	
PATCCHHB	10	
PATCCHHE	14	
PATCYLMW	C	
PATCYLNO	8	
PATCYLS	18	
PATCYLSZ	A	
PATHDR	0	
PATIDENT	0	
PATMAP	18	
PATPART	4	
PATRSV1	E	

PCB Information

PCB Heading Information

Common Name: PAGE CONTROL BLOCK
Macro ID: IARPCB
DSECT Name: PCB
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: None
Storage Attributes: Virtual Storage: Yes
Subpool: 245
Key: 0
Residency: Anywhere
Size: 144 Bytes
Created by: IARUGRPB
Pointed to by: PCBFQPTR field of the PCB Data Area
PCBBQPTR field of the PCB Data Area
RABLDPQF field of the RAB Data Area
RABLDPQL field of the RAB Data Area
RABNPQF field of the RAB Data Area
RABNPQL field of the RAB Data Area
RABCPQF field of the RAB Data Area
RABCPQL field of the RAB Data Area
Serialization: Varies
Function: Represents a paging operation to RSM

PCB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	152	PCB	
0	(0)	ADDRESS	4	PCBFQPTR	FORWARD PCB QUEUE POINTER
4	(4)	ADDRESS	4	PCBBQPTR	BACKWARD PCB QUEUE POINTER
8	(8)	BITSTRING	1	PCBSTATE	STATE OF THE RPB
		1...		PCBONFRQ	PCB IS ON THE FREE RPB QUE
		.1..		PCBPDINT	PCB IS INTERCEPTED FOR RPB POOL DELETION
		..11 1111		*	RESERVED FOR RPB STATES
9	(9)	UNSIGNED	1	PCBTYPE	RPB CONTROL BLOCK TYPE. FOR A PCB THIS FIELD IS KPCBRPB
10	(A)	CHARACTER	1	PCBQID	QUEUE ID FOR CURRENT QUEUE---- PLEASE REFER TO THE RESTRICTION IN THE PROLOG ABOVE WHEN DEFINING ADDITIONAL QIDs (THIS MUST BE UNIQUE TO FCBs) 00=>UNQUEUED-PCB 10=>LOCAL-DEFERRED-PCB-QUEUE 11=>DEFERRED-PCB-QUEUE 12=>NOTIFICATION-PCB-QUEUE 13=>ADDRESS-SPACE-CREATE-QUEUE 14=>COMMIT-PCB-QUEUE 15=>Satisfied defer queue FD=>FLAWED-PCB
11	(B)	BITSTRING	1	PCBFLGS1	FLAG BYTE 1
		1...		PCBFCBA	PCB IS ASSOCIATED WITH AN FCB
		.1..		PCBFAIL	REQUEST HAS FAILED
		..1.		PCBIOERR	FAILURE DUE TO AN I/O ERROR
		...1		PCBXMERR	FAILURE DUE TO XMEM ACCESS ERR
		.... 1...		PCBASBO	ASSOCIATE FAILURE
		.... .1..		PCBSPAGE	PCB IS FOR A SHARED PAGE
		.... ..1.		PCBMEGAP	PCB IS FOR A megarooed page
		1		PCBDFRIO	PCB is for Defer I/O
12	(C)	BITSTRING	1	PCBFLGS2	FLAG BYTE 2
		1...		PCBFIXHI	THE FIX COUNT IN THE PGT FOR THIS PAGE HAS BEEN ADJUSTED ONE HIGHER THAN NORMAL SPECIFICALLY FOR THE REQUEST REPRESENTED BY THIS PCB
		.1..		PCBOUT	PCB IS FOR OUTPUT I/O
		..1.		PCBNOITV	WHEN PCB IS ON THE DPQ, THERE IS NO INTENT TO VALIDATE THE PAGE
		...1		PCBINCWS	PAGE IS TO BE INCLUDED IN THE WORKING SET BY SWAP OUT WHEN THE I/O COMPLETES
		.... 1...		PCBFIX	PCB IS FOR AN ACTIVE ADDRESS SPACE PAGE FIX OR FOR AN ACTIVE DATA SPACE PAGE IOON REQUEST
		.... .1..		PCBBELOW	NEED REAL STORAGE BELOW 16M
		.... ..1.		PCBPREF	NEED PREFERRED AREA REAL STG
		1		PCBBAVQL	GENERAL DEFER SHOULD BYPASS THIS PCB DURING AVQLOW
13	(D)	BITSTRING	1	PCBFLGS3	FLAG BYTE 3
		1...		PCBVDISC	PCB DISCONNECTED FROM VIRTUAL
		.1..		PCBRDISC	PCB DISCONNECTED FROM REAL
		..1.		PCBFRAUX	FREE AUX STG WHEN I/O COMPLETES
		...1		PCBFREAL	FREE FRAME WHEN I/O COMPLETES
		.... 1...		*	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... 1...		PCBXPTNA	XPTLPID FIELD SHOULD NOT BE ACCESSED WHEN I/O COMPLETES - Used when in 390 Mode
		.... 1...		PCBPTEXNA	PTEXLPIDP FIELD SHOULD NOT BE ACCESSED WHEN I/O COMPLETES - Used when in ESAME Mode
		.... .1..		PCBNOTRS	I/O COMPLETION SHOULD NOT TRAS
		.... ..1.		PCBNODFR	PCB SHOULD BE SENT TO THE I/O CANCEL ROUTINE BY GENERAL DEFER
		1		PCBTOP	WHEN PCBFREAL=1, THE PFTE ASSOCIATED WITH THIS PCB SHOULD BE SENT TO THE TOP OF THE AFQ AFTER ZEROING OUT THE PFTASID
14	(E)	BITSTRING	1	PCBFLGS4	FLAG BYTE 4
		1...		PCBCHGON	THE CHANGE BIT FOR THIS PAGE SHOULD BE SET ON WHEN THE PAGE IS VALIDATED. (INPUT ONLY)
		.1..		PCBVDIA	THIS PCB HAS A VDI
		..1.		PCBCOM	PCB IS FOR A COMMIT
		...1		PCBDIS	PCB IS FOR A DISASSOCIATE
		.... 1...		PCBPRM	PCB IS FOR PRIMING FUNC.
		.... .1..		PCBNOHLK	HOME ADDRESS SPACE SERIALIZATION IS NOT NEEDED ON I/O COMP.
		.... ..1.		PCBINNVP	DO NOT VALIDATE PAGE WHEN INPUT I/O COMPLETES.
		1		PCBNOVAL	DO NOT VALIDATE PAGE IF THERE IS AN OUTPUT I/O ERROR.
15	(F)	BITSTRING	1	PCBFLGS5	FLAG BYTE 5
		1...		PCBADISC	PCB disconnected from aux
		.1..		PCBHVSPAGE	PCB IS FOR A HIGH VIRTUAL SHARED PAGE
		..1.		PCBINCR	PCB is a VDI and has incremented
		...1		PCBHVCOMMON	PCB IS FOR A HIGH VIRTUAL Common PAGE
		.... 1...		PCBUPDATETOTPOTOTPI	Indicates that paging counts must be updated
		.... .1..		PCBLARGEPAGE	PCB is for a Large Page Deferral Request.
		11		*	RESERVED
16	(10)	CHARACTER	1	PCBFID	EXTERNAL FUNCTION ID
17	(11)	BITSTRING	1	PCBFLGSA	FUNCTION FLAG BYTE A - MEANINGS DEPEND ON FUNCTION - SEE BELOW
18	(12)	UNSIGNED	2	PCBEPID	ENTRY POINT ID OF ENTRY POINT INITIALIZING THIS PCB
20	(14)	ADDRESS	4	PCBRPCBQ	ADDRESS OF RELATED PCB OR ZERO
24	(18)	ADDRESS	4	PCBPRAB	ADDR OF PAGE RAB
28	(1C)	ADDRESS	4	PCBHRAB	ADDR OF HOME RAB REQUESTING I/O
32	(20)	UNSIGNED	4	PCBTCB	ADDR OF TCB REQUESTING I/O
32	(20)	UNSIGNED	4	PCBSSRB	ADDR OF SSRB REQUESTING I/O
32	(20)	ADDRESS	4	PCBVVSA	INITIAL VSA IF A VDAC PCB
36	(24)	ADDRESS	4	PCBRB	ADDR OF RB REQUESTING I/O OR 0
36	(24)	ADDRESS	4	PCBFCB	ADDR OF FCB IF PCBFCBA=1
36	(24)	ADDRESS	4	PCBSFTE	ADDRESS OF THIS PAGE'S SFTE IF SWAP-IN OR SWAP-OUT PCB
40	(28)	CHARACTER	8	PCBVSA64	
				Comment	
				VIRTUAL ADDRESS OF PAGE. Valid if PCBSPAGE=0	
				End of Comment	
40	(28)	UNSIGNED	4	*	Not used in ESA mode
44	(2C)	ADDRESS	4	PCBVSA	VIRTUAL ADDRESS OF PAGE. Valid if PCBSPAGE=0
44	(2C)	ADDRESS	4	PCBSDH	Address of SDH if PCBSPAGE=1 and (PCBOUT=1 and PCBPFTE->PFTIOMC=0)
44	(2C)	ADDRESS	4	PCBSPE	Address of SPE if PCBSPAGE=1 and (PCBOUT=0 or PCBPFTE->PFTIOMC=1)
48	(30)	CHARACTER	16	*	
48	(30)	CHARACTER	16	*	390 Mode Mapping - These fields will remain at the same offsets so that external 390 mode users will not need to modify their programs
48	(30)	ADDRESS	4	PCBPFTE	ADDRESS OF PFTE BACKING VIRTUAL
52	(34)	ADDRESS	4	PCBPGTE	ADDRESS OF PGTE FOR PAGE
56	(38)	ADDRESS	4	PCBXPTE	ADDRESS OF XPTE FOR PAGE
60	(3C)	ADDRESS	4	*	Reserved
48	(30)	CHARACTER	16	*	ESAME Mode Mapping
48	(30)	ADDRESS	8	PCBPFTE64	ADDRESS OF PFTE BACKING VIRTUAL - ESAME
56	(38)	ADDRESS	8	PCBPTE	Virtual Address of PTE for Page. Not applicable to pages above 2G.
64	(40)	CHARACTER	4	PCBFUNAR	FUNCTION AREA - MAPPED AS REQUIRED BY EACH FUNCTION
68	(44)	CHARACTER	4	PCBPROG	PROGRAMMING WORD
		1111		PCBSTYPE	SPACE TYPE (8=DATA SPACE).
		.... 1...		*	RESERVED
68	(44)	BITSTRING	2	PCBUDSX	USER DATA SPACE INDEX
71	(47)	BITSTRING	1	PCBRVTEX	RVTE INDEX
72	(48)	UNSIGNED	4	PCBEXITS	PCB EXIT INDEXES
72	(48)	ADDRESS	1	PCBDEFRX	DEFER EXIT ROUTINE INDEX

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
73	(49)	ADDRESS	1	PCBIOCMX	I/O COMPLETION EXIT RTN INDEX. IF THIS INDEX IS FOR THE SWAP PURGE I/O COMPLETION EXIT AND THE ORIGINAL I/O COMPLETION ROUTINE MUST ALSO RUN, THEN THE ORIGINAL INDEX WILL BE FOUND IN THE PCBSWAPX FIELD.
74	(4A)	ADDRESS	1	PCBTERMX	TERMINATION EXIT ROUTINE INDEX
75	(4B)	ADDRESS	1	PCBSWAPX	SWAP-OUT EXIT ROUTINE INDEX IF SWAP PURGE HAS NOT RUN. IF SWAP PURGE HAS RUN, THEN THIS FIELD WILL CONTAIN 0 OR, IF THE ORIGINAL I/O COMPLETION EXIT MUST ALSO RUN, THE ORIGINAL I/O COMPLETION INDEX.
76	(4C)	ADDRESS	4	PCBRVR	ADDRESS OF THE RVR WHEN THIS PCB REPRESENTS HOME I/O
80	(50)	ADDRESS	4	PCBIWB	IWB ADDRESS - ZERO IF NONE
84	(54)	UNSIGNED	4	PCBNUMFRAMES	Number of above the bar frames required by this request
88	(58)	CHARACTER	4	*	Reserved
92	(5C)	CHARACTER	60	PCBAIA	AIA AREA
92	(5C)	CHARACTER	60	PCBVDI	VDI AREA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
17	(11)	STRUCTURE	1	PCBSFFLA	SEGMENT FAULT FLAGS
		1...		PCBSFINT	INTERNAL RSM CALLER STOPPED
		.111 1111		*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
17	(11)	STRUCTURE	1	PCBMGFLA	MIGRATION FLAGS
		1...		PCBMGMPA	THERE IS AN MPE ASSOCIATED WITH THIS PCB.
		.111 1111		*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
17	(11)	STRUCTURE	1	PCBDSFLA	DISASSOC. FLAGS
		1...		PCBDSVDS	I/O IS EXPECTED TO BE VIRTUALLY DISCONNECTED.
		.111 1111		*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
17	(11)	STRUCTURE	1	PCBCMFLA	COMMIT FLAGS
		1...		PCBCMALL	THIS PAGE IS PART OF A COMMIT ALL REQUEST.
		.1..		PCBCMPFR	THE FRAME BACKING THIS PAGE WAS ASSIGNED BY COMMIT.
		..1.		PCBCMFG	THE PAGE IS IN A FRESHLY OBTAINED STATE.
		...1 1111		*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
17	(11)	STRUCTURE	1	PCBPVFLA	DATA SPACE PAGE VALIDATION
		1...		PCBPVFLH	CALLER WAS RUNNING UNDER THE PFLIH
		.111 1111		*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
17	(11)	STRUCTURE	1	PCBGDFLA	General defer
		1...		PCBABOVE	Requestor requires a frame that resides below the bar
		.1..		PCBABOVEBAR	Requestor requires a frame that resides above the bar
		..11 1111		*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
64	(40)	STRUCTURE	4	PCBMGFUN	MIGRATION FUNCTION AREA
64	(40)	ADDRESS	4	PCBMGMPE	MPE POINTER

PCB Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
64	(40)	STRUCTURE	4	PCBCMFUN	COMMIT FUNCTION AREA
64	(40)	ADDRESS	4	PCBCMRVR	ADDRESS OF THE RVR ASSOCIATED WITH THIS PAGE.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
64	(40)	STRUCTURE	4	PCBPVFUN	DATA SP PAGE VALIDATION FUNCTION AREA
64	(40)	BITSTRING	1	PCBPVTYP	PAGE TYPE FROM DSPFIND
65	(41)	BITSTRING	3	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
64	(40)	STRUCTURE	4	PCBSWFUN	SWAP FUNCTION AREA
64	(40)	CHARACTER	2	*	RESERVED
66	(42)	SIGNED	2	PCBSWDCT	DREF COUNT FOR MIGRATED DATA SPACE DREF PAGES DURING SWAP-IN INPUT I/O.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
64	(40)	STRUCTURE	4	PCBIOFUN	DEFINE function area
64	(40)	BITSTRING	1	*	Reserved (PCBPVTYP)
65	(41)	UNSIGNED	3	PCBIORNG#	Range index of page

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
64	(40)	STRUCTURE	4	PCBUNCHANGED2SCM	
64	(40)	CHARACTER	4	PCBUNCHANGED2SCMLSID	LSID of the aux slot which previously backed the page

PCB Constants

Len	Type	Value	Name	Description
		Comment		
		PCB QUEUE IDS CAUTION: DO TO AN RSMDFATA (IPCS) REQUIREMENT, THE QUEUE IDS OF CF AND CE CANNOT BE USED FOR A PCB QUEUE ID. IN ADDITION, PCB QUEUE IDS CANNOT BE USED FOR FCB QUEUE IDS (SEE NOTE IN PROLOG).		
		End of Comment		
1	HEX	00	PCBUNQDN	UNQUEUED
1	HEX	10	PCBLDPQN	LOCAL DEFERRED PCB QUEUE
1	HEX	11	PCBDPQN	DEFERRED PCB QUEUE
1	HEX	12	PCBNPQN	NOTIFICATION PCB QUEUE
1	HEX	13	PCBASPQN	ADDR SPACE CREATE PCB QUEUE
1	HEX	14	PCBCPQN	COMMIT PCB QUEUE
1	HEX	15	PCBSATDPQN	Satisfied defer queue
1	HEX	FD	PCBFLAWN	PCB WAS FOUND FLAWED DURING RECOVERY PROCESSING
		Comment		
		PCB CHAIN AND INTERNAL QUEUE IDS USED BY IPCS (THESE MUST BE UNIQUE FROM THE MAINLINE QUEUE IDS ABOVE) CAUTION: DUE TO AN RSMDFATA (IPCS) REQUIREMENT, THE QUEUE IDS OF CF AND CE CANNOT BE USED FOR A PCB QUEUE ID.		
		End of Comment		
1	HEX	C0	PCBSIPQN	ID FOR THE SWAP INTERNAL PCB QUEUE - FCB BASED
1	HEX	C1	PCBSIDQN	ID FOR THE SWAP INTERNAL DREF PCB QUEUE - FCB BASED
1	HEX	C2	PCBRDPQN	ID FOR THE RELATED PCB QUEUE - DPQ BASED
1	HEX	C3	PCBRLDQN	ID FOR THE RELATED PCB QUEUE - LDPQ BASED
1	HEX	C4	PCBRPFQN	ID FOR THE RELATED PCB QUEUE - PFQ PFTE BASED
1	HEX	C5	PCBRPDQN	ID FOR THE RELATED PCB QUEUE - PDFQ PFTE BASED

Len	Type	Value	Name	Description
1	HEX	C6	PCBRPRQN	ID FOR THE RELATED PCB QUEUE - PRFQ PFTE BASED
1	HEX	C7	PCBSPPQN	ID FOR THE SWOUT INTERNAL PCB POOL QUEUE - FCB BASED
1	HEX	C8	PCBSSPQN	ID FOR THE SWOUT INTERNAL SWS PCB QUEUE - FCB BASED

Comment

RPB CONTROL BLOCK TYPE FOR PCB

End of Comment

1	HEX	00	KPCBRPB	PCB TYPE CONSTANT

PCB Cross Reference

Name	Hex Offset	Hex Value
PCB	0	
PCBABOVE	11	80
PCBABOVEBAR	11	40
PCBADISC	F	80
PCBAIA	5C	
PCBASBO	B	08
PCBBAVQL	C	01
PCBBELOW	C	04
PCBBQPTR	4	
PCBCHGON	E	80
PCBCMALL	11	80
PCBCMFG	11	20
PCBCMFLA	11	
PCBCMFUN	40	
PCBCMPFR	11	40
PCBCMRVR	40	
PCBCOM	E	20
PCBDEFRX	48	
PCBDFRIO	B	01
PCBDIS	E	10
PCBDSFLA	11	
PCBDSVDS	11	80
PCBEPID	12	
PCBEXITS	48	
PCBFAIL	B	40
PCBFCB	24	
PCBFCBA	B	80
PCBFID	10	
PCBFIX	C	08
PCBFIXHI	C	80
PCBFLGSA	11	
PCBFLGS1	B	
PCBFLGS2	C	
PCBFLGS3	D	
PCBFLGS4	E	
PCBFLGS5	F	
PCBFQPTR	0	
PCBFRAUX	D	20
PCBFREAL	D	10
PCBFUNAR	40	
PCBGDFLA	11	
PCBHRAB	1C	
PCBHVCOMMON	F	10
PCBHVSPAGE	F	40
PCBINCR	F	20
PCBINCWS	C	10
PCBINNVP	E	02
PCBIOCMX	49	
PCBIOERR	B	20
PCBIOFUN	40	
PCBIORNG#	41	
PCBIWB	50	
PCBLARGEPAGE	F	04
PCBMEGAP	B	02
PCBMGFLA	11	
PCBMGFUN	40	
PCBMGMPA	11	80
PCBMGMPE	40	
PCBNODFR	D	02
PCBNOHLK	E	04
PCBNOITV	C	20
PCBNOTRS	D	04
PCBNOVAL	E	01
PCBNUMFRAMES	54	
PCBONFRQ	8	80
PCBOUT	C	40
PCBPDINT	8	40
PCBPFTE	30	
PCBPFTE64	30	
PCBPGTE	34	
PCBPRAB	18	
PCBPREF	C	02
PCBPRM	E	08
PCBPROG	44	
PCBPTE	38	
PCBPTEXNA	D	08
PCBPVFLA	11	
PCBPVFLH	11	80
PCBPVFUN	40	
PCBPVTYP	40	
PCBQID	A	
PCBRB	24	
PCBRDISC	D	40
PCBRPCBQ	14	
PCBRVR	4C	
PCBRVTEX	47	
PCBSDH	2C	
PCBSFFLA	11	
PCBSFINT	11	80
PCBSFTE	24	
PCBSPAGE	B	04
PCBSPE	2C	
PCBSSRB	20	
PCBSTATE	8	
PCBSTYPE	44	F0
PCBSWAPX	4B	
PCBSWDCT	42	
PCBSWFUN	40	
PCBTCB	20	
PCBTERMX	4A	
PCBTOP	D	01
PCBTYPE	9	
PCBUDSX	44	
PCBUNCHANGED2SCM	40	
PCBUNCHANGED2SCMLSID	40	
PCBUPDATETOTPOTOTPI	F	08
PCBVDI	5C	
PCBVDIA	E	40
PCBVDISC	D	80
PCBVSA	2C	
PCBVSA64	28	
PCBVVSA	20	
PCBXMERR	B	10
PCBXPTE	38	
PCBXPTNA	D	08

PCCA Information

PCCA Programming Interface information

Programming Interface information

PCCA

ONLY the following fields are part of the programming interface information:

- PCCACAFM
- PCCACPID
- PCCACPUA
- PCCASLIH
- PCCASTPI
- TOKEN

End of Programming Interface information

PCCA Heading Information

Common Name: PHYSICAL CONFIGURATION COMMUNICATION AREA
Macro ID: IHAPCCA
DSECT Name: PCCA
Owning Component: RECONFIGURATION (SC1CZ)
Eye-Catcher ID: PCCA
Offset: 0
Length: 4
Storage Attributes: Subpool: 245
Key: 0
Size: 584 BYTES
Created by: IEAVNIP0
IEEVCPR
Pointed to by: PCCAV... field of the PCCAVT data area
PSAPCCAV field of the PSA data area
PSAPCCAR field of the PSA data area
PCCAEMSA field of the PCCA data area (receiving routine)
Serialization: DISABLEMENT
Function: CONTAINS INFORMATION ABOUT THE PHUSICAL FACILITIES ASSOCIATED WITH EACH PROCESSOR IN THE SYSTEM

PCCA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PCCA	
0	(0)	CHARACTER	4	PCCAPCCA	- CONTROL BLOCK ACRONYM IN EBCDIC
4	(4)	BITSTRING	12	PCCACPID	- CPU ID (CONTAINS SERIAL NUMBER)
16	(10)	SIGNED	2	PCCACPUA	- PHYSICAL CPU ADDRESS
18	(12)	SIGNED	2	PCCACAFM	- BIT MASK CORRESPONDING TO PHYSICAL CPU ADDRESS. This mask covers only CPUs 0-15 and can be used only for CPU affinity checking.
20	(14)	ADDRESS	4	PCCATQEP	- TQE POINTER
24	(18)	ADDRESS	4	PCCAPSAV	- VIRTUAL ADDRESS OF PSA
28	(1C)	ADDRESS	4	PCCAPSAR	- ABSOLUTE ADDRESS OF PSA
32	(20)	BITSTRING	1	PCCAISCE	- INTERRUPT SUB-CLASSES TO ENABLE
33	(21)	BITSTRING	3	PCCAMCHF (0)	- MACHINE CHECK FLAGS
33	(21)	BITSTRING	1		
		1...		PCCASMCH	"X'80'" - A SOFTWARE-SIMULATED MACHINE CHECK OCCURRED
36	(24)	SIGNED	4	PCCACRG6 (0)	- CONTROL REGISTER 6
36	(24)	BITSTRING	1	PCCAISCM	- INTERRUPTION SUBCLASS MASK
37	(25)	ADDRESS	3	PCCACR6L	- LOW-ORDER THREE BYTES OF CR 6
40	(28)	SIGNED	4	PCCASLIH	- NUMBER OF ENTRIES TO THE I/O SLIH
44	(2C)	SIGNED	4	PCCASTPI	- NUMBER OF TPI WITH CC=1
48	(30)	SIGNED	4	PCCAXSLF	- EXCESSIVE SPIN LENGTH FACTOR.
52	(34)	SIGNED	4	PCCARSPR	- RELATIVE SPEED (X4096) OF THIS PROCESSOR.
56	(38)	SIGNED	4	PCCATRW1 (0)	- TRAP WORD 1. FLAG AND DATA, SET BY SCIXL.
56	(38)	SIGNED	2	PCCATRDA	- TRAP DATA
58	(3A)	CHARACTER	1	PCCATRFL	- TRAP FLAG
59	(3B)	CHARACTER	1		- RESERVED
60	(3C)	ADDRESS	4	PCCARV88	- RESERVED
64	(40)	DBL WORD	8	PCCA_PARTIALCPUMASK	64-BIT partial CPU BIT MASK, USE WITH PCCA_PartialCpuMaskOffset TO OBTAIN A COMPLETE MASK
64	(40)	DBL WORD	8	PCCA_CPU_ADDRESS_MASK	64-BIT CPU BIT MASK, USE WITH PCCA_CPU_ADDRESS_MASK_OFFSET TO OBTAIN A COMPLETE MASK
64	(40)	X'40'	0	PCCA_CPU_ADDRESS_MASK32	"PCCA_CPU_ADDRESS_MASK,4,C'X'" 32-bit mask for CPUs 0-31
64	(40)	X'40'	0	PCCA_CPU_AFFINITY_MASK	"PCCA_CPU_ADDRESS_MASK,2,C'X'" 16-bit mask for CPUs 0-15 for affinity checking
72	(48)	ADDRESS	4	PCCARV91	- RESERVED
76	(4C)	ADDRESS	4	PCCARV92	- RESERVED
80	(50)	ADDRESS	4	PCCARV93	- RESERVED
84	(54)	ADDRESS	4	PCCARV94	- RESERVED
88	(58)	ADDRESS	4	PCCARV95	- RESERVED
92	(5C)	ADDRESS	4	PCCARV96	- RESERVED
96	(60)	ADDRESS	4	PCCARV97	- RESERVED
100	(64)	ADDRESS	4	PCCARV98	- RESERVED
104	(68)	ADDRESS	4	PCCARV99	- RESERVED
108	(6C)	ADDRESS	4	PCCARV9A	- RESERVED
112	(70)	ADDRESS	4	PCCARV9B	- RESERVED
116	(74)	ADDRESS	4	PCCARV9C	- RESERVED
120	(78)	ADDRESS	4	PCCARV9D	- RESERVED

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
124	(7C)	ADDRESS	4	PCCARV9E	- RESERVED
128	(80)	BITSTRING	4	PCCATMST (0)	- TIMER STATUS BYTES
128	(80)	BITSTRING	1	PCCATMFL	- FIRST BYTE OF PCCATMST
		1...		PCCAINIT	"X'80'" - ENTRY HAS BEEN INITIALIZED
		.1..		PCCASYNC	"X'40'" - CLOCK OUT OF SYNCHRONIZATION
		..1.		PCCAVKIL	"X'20'" - CONFIG CPU SHOULD BE CANCELLED
		...1		PCCAMCC	"X'10'" - PROCESSING FOR PERMANENTLY DAMAGED CLOCK COMPARATOR MUST BE DONE
		.... 1...		PCCAMINT	"X'08'" - PROCESSING FOR CPU TIMER MUST BE DONE
		.... .1..		PCCARV02	"X'04',,C'X'" - RESERVED
		.... ..1.		PCCARV03	"X'02',,C'X'" - RESERVED
		1		PCCARV04	"X'01',,C'X'" - RESERVED
129	(81)	BITSTRING	1	PCCATODE	- TOD CLOCK ERROR FLAGS
		1...		PCCANUTD	"X'80'" - CLOCK CANNOT BE USED
		.1..		PCCANFTD	"X'40'" - CLOCK SHOULD NOT BE RESET
		..11 1111		PCCACTTD	"X'3F'" - ERROR COUNT (6 BITS)
130	(82)	BITSTRING	1	PCCACCE	- FLAGS FOR CLOCK COMPARATOR
		1...		PCCANUCC	"X'80'" - CLOCK COMPARATOR CANNOT BE USED
		.1..		PCCANFCC	"X'40'" - CLOCK COMPARATOR SHOULD NOT BE RESET
		..11 1111		PCCACTCC	"X'3F'" - ERROR COUNT (6 BITS)
131	(83)	BITSTRING	1	PCCAINTE	- FLAGS FOR CPU TIMER
		1...		PCCANUIN	"X'80'" - CPU TIMER CANNOT BE USED
		.1..		PCCANFIN	"X'40'" - CPU TIMER SHOULD NOT BE RESET
		..11 1111		PCCACTIN	"X'3F'" - ERROR COUNT (6 BITS)
132	(84)	SIGNED	4	PCCARPB	- EXTERNAL CALL SIGP BUFFER
		1...		PCCASWTH	"X'80'" SWITCH REQUEST
		.1..		PCCAIOQC	"X'40'" IOQ compresseion request
		..1.		PCCARQCK	"X'20'" RQCHECK REQUEST
		...1		PCCAGTFR	"X'10'" GTF REQUEST
		.... 1...		PCCAIOSE	"X'08'" I/O enable/disable request
		.... .1..		PCCAMODE	"X'04'" MODE REQUEST
		.... ..1.		PCCASTCP	"X'02'" STOPCP REQUEST
		1		PCCAMEMS	"X'01'" MEMSWT REQUEST
		1...		PCCAPPSA	"X'80'" PrimePSA request
136	(88)	CHARACTER	16	PCCAEMSB (0)	- EMERGENCY SIGNAL SIGP BUFFER
136	(88)	BITSTRING	4	PCCAEMSI (0)	- FIRST WORD OF EMS BUFFER
136	(88)	BITSTRING	1	PCCARISP	- CONTAINS PARALLEL/SERIAL REQUEST INDICATOR FOR REMOTE IMMEDIATE SIGNAL
		1...		PCCAPARL	"X'80'" - PARALLEL REQUEST
		.1..		PCCASERL	"X'40'" - SERIAL REQUEST
		..1.		PCCABCST	"X'20'" - BROADCAST REQUEST
		...1		PCCARV07	"X'10',,C'X'" - RESERVED
		.... 1...		PCCARV08	"X'08',,C'X'" - RESERVED
		.... .1..		PCCARV09	"X'04',,C'X'" - RESERVED
		.... ..1.		PCCARV10	"X'02',,C'X'" - RESERVED
		1		PCCARV11	"X'01',,C'X'" - RESERVED
137	(89)	BITSTRING	1	PCCAEMS2	- SECOND BYTE OF PCCAEMSI
		1...		PCCASERP	"X'80'" - SERIAL PENDING INDICATOR
		.1..		PCCARV13	"X'40',,C'X'" - RESERVED
		..1.		PCCARV14	"X'20',,C'X'" - RESERVED
		...1		PCCARV15	"X'10',,C'X'" - RESERVED
		.... 1...		PCCARV16	"X'08',,C'X'" - RESERVED
		.... .1..		PCCARV17	"X'04',,C'X'" - RESERVED
		.... ..1.		PCCARV18	"X'02',,C'X'" - RESERVED
		1		PCCARV19	"X'01',,C'X'" - RESERVED
138	(8A)	BITSTRING	1	PCCAEMS3	- THIRD BYTE OF PCCAEMSI
		1...		PCCASERF	"X'80'" - SERIAL REQUEST FAILED
		.1..		PCCARV21	"X'40',,C'X'" - RESERVED
		..1.		PCCARV22	"X'20',,C'X'" - RESERVED
		...1		PCCARV23	"X'10',,C'X'" - RESERVED
		.... 1...		PCCARV24	"X'08',,C'X'" - RESERVED
		.... .1..		PCCARV25	"X'04',,C'X'" - RESERVED
		.... ..1.		PCCARV26	"X'02',,C'X'" - RESERVED
		1		PCCARV27	"X'01',,C'X'" - RESERVED
139	(8B)	BITSTRING	1	PCCARMSB	- CONTAINS RMS INDICATOR
		1...		PCCARV28	"X'80',,C'X'" - RESERVED
		.1..		PCCARV29	"X'40',,C'X'" - RESERVED
		..1.		PCCARV30	"X'20',,C'X'" - RESERVED
		...1		PCCARV31	"X'10',,C'X'" - RESERVED
		.... 1...		PCCARV32	"X'08',,C'X'" - RESERVED
		.... .1..		PCCARV33	"X'04',,C'X'" - RESERVED
		.... ..1.		PCCARV34	"X'02',,C'X'" - RESERVED
		1		PCCARMS	"X'01'" - SIGP WAS ISSUED VIA RMS
140	(8C)	ADDRESS	4	PCCAEMSP	- REMOTE IMMEDIATE SIGNAL PARAMETER ADDRESS

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
144	(90)	ADDRESS	4	PCCAEMSE	- REMOTE IMMEDIATE SIGNAL RECEIVING ROUTINE ENTRY POINT ADDRESS
148	(94)	ADDRESS	4	PCCAEMSA	- PCCA ADDRESS OF THE RECEIVING ROUTINE
152	(98)	ADDRESS	4	PCCAPWAV	- VIRTUAL ADDRESS OF MCH PROCESSOR WORK AREA
156	(9C)	ADDRESS	4	PCCAPWAR	- REAL ADDRESS OF MCH PROCESSOR WORK AREA
160	(A0)	ADDRESS	4	PCCALRBV	- VIRTUAL ADDRESS OF MCH LOGREC BUFFER
164	(A4)	ADDRESS	4	PCCALRBR	- REAL ADDRESS OF MCH LOGREC BUFFER
168	(A8)	BITSTRING	1	PCCARIOS (208)	- RESERVED FOR IOS USE
376	(178)	BITSTRING	1	PCCAATTR	- PROCESSOR ATTRIBUTES
		1...		PCCACPUM	"X'80'" - INDICATOR THAT DEAD CPU HAD A MALFUNCTION
		.1..		PCCAIO	"X'40'" - PROCESSOR HAS I/O CAPABILITY
		..1.		PCCANPFA	"X'20'" - WHEN SET, PAGE FAULT ASSIST SHOULD NOT BE USED
		...1		PCCAR101	"X'10',,C'X'" - RESERVED
		.... 1...		PCCAR102	"X'08',,C'X'" - RESERVED
		.... .1..		PCCAZIIP	"X'04'" - zIIP
		.... .1..		PCCA_BYLPAR_ZIIP	"X'04'" - zIIP
		.... .1..		PCCASUP	"X'04'" - zIIP
		.... .1..		PCCA_BYLPAR_SUP	"X'04'" - zIIP
		.... ..1.		PCCADSCR	"X'02'" - Discretionary Processor
		1		PCCAIFA	"X'01'" - Special Processor
		1		PCCA_BYLPAR_ZAAP	"X'01'" -
		1		PCCA_BYLPAR_IFA	"X'01'" -
377	(179)	BITSTRING	1	PCCAMFA	- MALFUNCTION ALERT FLAGS
		1...		PCCASMFA	"X'80'" - SIMULATED MALFUNCTION ALERT
378	(17A)	BITSTRING	1	PCCAACRN	- CAUSE OF ACR
		....		PCCAKUKN	"X'00'" UNKNOWN ERROR
		1		PCCAKMFA	"X'01'" MALFUNCTION ALERT, CPU CHECKSTOPPED
		.... ..1.		PCCAKIPT	"X'02'" INSTRUCTION PROCESSING DAMAGE THRESHOLD
		11		PCCAKSDT	"X'03'" SYSTEM DAMAGE THRESHOLD
		.... .1..		PCCAKIVT	"X'04'" INVALID REGISTER OR PSW THRESHOLD
		.... .1.1		PCCAKTCT	"X'05'" TIME OF DAY CLOCK DAMAGE THRESHOLD
		.... .11.		PCCAKPTT	"X'06'" PROCESSOR TIMER DAMAGE THRESHOLD
		.... .111		PCCAKCCT	"X'07'" CLOCK COMPARATOR DAMAGE THRESHOLD
		.... 1...		PCCAKPST	"X'08'" PRIMARY SYNC DAMAGE THRESHOLD
		.... 1..1		PCCAKADT	"X'09'" ETR ATTACHMENT DAMAGE THRESHOLD
		.... 1.1.		PCCAKSLT	"X'0A'" SWITCH TO LOCAL THRESHOLD
		.... 1.11		PCCAKESL	"X'0B'" EXCESSIVE SPIN LOOP
		.... 11..		PCCAKTCF	"X'0C'" TOD CLOCK SYNCHRONIZATION FAILURE
		.... 11.1		PCCAKDAT	"X'0D'" MALFUNCTION OF DAT HARDWARE
		.... 111.		PCCAKSCF	"X'0E'" TOD CLOCK COULD NOT BE SYNCHRONIZED TO ETR
		 1111		PCCAKUME	"X'0F'" UNRECOVERABLE MACHINE ERROR
		...1		PCCAKFHS	"X'10'" The CPU failed to handle the STP synch-check machine check
		...1 ...1		PCCAKMCF	"X'11'" The master CPU processing an STP synch-check machine check failed
		...1 ..1.		PCCAKPIR	"X'12'" Recursive program checks
		...1 ..11		PCCAKPCM	"X'13'" Program check during MCH processing
		...1 .1..		PCCAKMRM	"X'14'" Multiple restarts during MCH processing
		...1 .1.1		PCCAKRMC	"X'15'" Recursive machine checks
379	(17B)	BITSTRING	1	PCCARCFF	Reconfig flags. Serialized by reconfig ENQ
		1...		PCCACWLM	"X'80'" CPU on/offline initiated by WLM
380	(17C)	BITSTRING	1	PCCA_PHYSICAL_CPUID	Valid only when not LPAR or when dedicated processors. IBM recommends using PCCACPUA.
381	(17D)	BITSTRING	1		RESERVED
382	(17E)	BITSTRING	2	PCCAPROCCLASS	The class of processor for this CPU. Possible values are defined by equates in IHAPSA beginning with PsaProcClass_
382	(17E)	BITSTRING	2	PCCA_BYLPAR_PROCCLASS	The class of processor for this CPU.
382	(17E)	BITSTRING	1	PCCAPROCCLASS_BYTE0	
383	(17F)	BITSTRING	1	PCCAPROCCLASS_BYTE1	
382	(17E)	BITSTRING	1	PCCA_BYLPAR_PROCCLASS_BYTE0	
383	(17F)	BITSTRING	1	PCCA_BYLPAR_PROCCLASS_BYTE1	
384	(180)	BITSTRING	2	PCCAR180	Reserved
386	(182)	SIGNED	2	PCCA_PARTIALCPUMASKOFFSET	THE BYTE OFFSET INTO A FULL CPU MASK THIS 8 BYTE MASK BLOCK (PCCA_CPU_ADDRESS_MASK) IS IN. WILL BE A MULTIPLE OF 8, WITH A MACIMUM VALUE (ECVTMaxMPNumBytesInMask-8)
386	(182)	SIGNED	2	PCCA_CPU_ADDRESS_MASK_OFFSET	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
					THE BYTE OFFSET INTO A FULL CPU MASK THIS 8 BYTE MASK BLOCK (PCCA_CPU_ADDRESS_MASK) IS IN. WILL BE A MULTIPLE OF 8, WITH A MACIMUM VALUE (ECVTMaxMPNumBytesInMask-8)
388	(184)	BITSTRING	196		- RESERVED
388	(184)	X'248'	0	PCCAEND	"*" End of PCCA

PCCA Cross Reference

Name	Hex Offset	Hex Value
PCCA	0	
PCCA_BYLPAR_IFA		
	178	1
PCCA_BYLPAR_PROCCLASS		
	17E	0
PCCA_BYLPAR_PROCCLASS_BYTE0		
	17E	
PCCA_BYLPAR_PROCCLASS_BYTE1		
	17F	
PCCA_BYLPAR_SUP		
	178	4
PCCA_BYLPAR_ZAAP		
	178	1
PCCA_BYLPAR_ZIIP		
	178	4
PCCA_CPU_ADDRESS_MASK		
	40	0
PCCA_CPU_ADDRESS_MASK_OFFSET		
	182	0
PCCA_CPU_ADDRESS_MASK32		
	40	40
PCCA_CPU_AFFINITY_MASK		
	40	40
PCCA_PARTIALCPUMASK		
	40	0
PCCA_PARTIALCPUMASKOFFSET		
	182	0
PCCA_PHYSICAL_CPUID		
	17C	0
PCCAACRN	17A	0
PCCAATTR	178	0
PCCABCST	88	20
PCCACAFM	12	0
PCCACCE	82	0
PCCACPID	4	0
PCCACPUA	10	0
PCCACPUM	178	80
PCCACRG6	24	
PCCACR6L	25	
PCCACTCC	82	3F
PCCACTIN	83	3F
PCCACTTD	81	3F
PCCACWLM	17B	80
PCCADSCR	178	2
PCCAEMSA	94	
PCCAEMSB	88	
PCCAEMSE	90	
PCCAEMSI	88	
PCCAEMSP	8C	
PCCAEMS2	89	0
PCCAEMS3	8A	0
PCCAEND	184	248
PCCAGTFR	84	10
PCCAIFA	178	1
PCCAINIT	80	80
PCCAINTE	83	0
PCCAIO	178	40
PCCAIOQC	84	40
PCCAIOSE	84	8
PCCAISCE	20	0
PCCAISCM	24	0
PCCAKADT	17A	9
PCCAKCCT	17A	7
PCCAKDAT	17A	D
PCCAKESL	17A	B
PCCAKFHS	17A	10
PCCAKIPT	17A	2
PCCAKIVT	17A	4
PCCAKMCF	17A	11
PCCAKMFA	17A	1
PCCAKMRM	17A	14
PCCAKPCM	17A	13
PCCAKPIR	17A	12
PCCAKPST	17A	8
PCCAKPTT	17A	6
PCCAKRMC	17A	15
PCCAKSCF	17A	E
PCCAKSDT	17A	3
PCCAKSLT	17A	A
PCCAKTCF	17A	C
PCCAKTCT	17A	5
PCCAKUKN	17A	0
PCCAKUME	17A	F
PCCALRBR	A4	
PCCALRBV	A0	
PCCAMCC	80	10
PCCAMCHF	21	
PCCAMEMS	84	1
PCCAMFA	179	0
PCCAMINT	80	8
PCCAMODE	84	4
PCCANFCC	82	40
PCCANFIN	83	40
PCCANFTD	81	40
PCCANPFA	178	20
PCCANUCC	82	80
PCCANUIN	83	80
PCCANUTD	81	80
PCCAPARL	88	80
PCCAPCCA	0	D7C3C3C1
PCCAPPSA	84	80
PCCAPROCCLASS		
	17E	0
PCCAPROCCLASS_BYTE0		
	17E	
PCCAPROCCLASS_BYTE1		
	17F	
PCCAPSAR	1C	
PCCAPSAV	18	
PCCAPWAR	9C	
PCCAPWAV	98	
PCCARCFF	17B	0
PCCARIOS	A8	
PCCARISP	88	0
PCCARMS	8B	1
PCCARMSB	8B	0
PCCARPB	84	0
PCCARQCK	84	20
PCCARSPR	34	0
PCCARV02	80	4
PCCARV03	80	2
PCCARV04	80	1
PCCARV07	88	10
PCCARV08	88	8
PCCARV09	88	4
PCCARV10	88	2

PCCA Cross Reference

Name	Hex Offset	Hex Value
PCCARV11	88	1
PCCARV13	89	40
PCCARV14	89	20
PCCARV15	89	10
PCCARV16	89	8
PCCARV17	89	4
PCCARV18	89	2
PCCARV19	89	1
PCCARV21	8A	40
PCCARV22	8A	20
PCCARV23	8A	10
PCCARV24	8A	8
PCCARV25	8A	4
PCCARV26	8A	2
PCCARV27	8A	1
PCCARV28	8B	80
PCCARV29	8B	40
PCCARV30	8B	20
PCCARV31	8B	10
PCCARV32	8B	8
PCCARV33	8B	4
PCCARV34	8B	2
PCCARV88	3C	
PCCARV9A	6C	
PCCARV9B	70	
PCCARV9C	74	
PCCARV9D	78	
PCCARV9E	7C	
PCCARV91	48	
PCCARV92	4C	
PCCARV93	50	
PCCARV94	54	
PCCARV95	58	
PCCARV96	5C	
PCCARV97	60	
PCCARV98	64	
PCCARV99	68	
PCCAR101	178	10
PCCAR102	178	8
PCCAR180	180	0
PCCASERF	8A	80
PCCASERL	88	40
PCCASERP	89	80
PCCASLIH	28	0
PCCASMCH	21	80
PCCASMFA	179	80
PCCASTCP	84	2
PCCASTPI	2C	0
PCCASUP	178	4
PCCASWTH	84	80
PCCASYNC	80	40
PCCATMFL	80	0
PCCATMST	80	
PCCATODE	81	0
PCCATQEP	14	
PCCATRDA	38	
PCCATRFL	3A	
PCCATRW1	38	
PCCAVKIL	80	20
PCCAXSLF	30	0
PCCAZIIP	178	4

PCCAVT Information

PCCAVT Programming Interface information

Programming Interface information

PCCAVT

End of Programming Interface information

PCCAVT Heading Information

Common Name: Physical Configuration Communication Area Vector Table
Macro ID: IHAPCCAT
DSECT Name: PCCAVT
Owning Component: MP reconfiguration (SC1CZ)
Eye-Catcher ID: PCCAVT
Offset: ????????
Length: ????????
Storage Attributes: Subpool: 245
Key: 0
Size: CVTMAXMP+1 PCCAT00P Entries
Created by: IEAVNIP0
Pointed to by: CVTPCCAT field of the CVT data area.
Serialization: Disablement for external interrupts
Function: Contains the address of a PCCA for each CPU.

PCCAVT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PCCAVT	
0	(0)	ADDRESS	4	PCCAT00P	- ADDRESS OF PCCA FOR CPU 0. There are CVTMAXMP+1 entries. Do not reference entries beyond CVTMAXMP+1.
4	(4)	ADDRESS	4	PCCAT01P	- ADDRESS OF PCCA FOR CPU 1
8	(8)	ADDRESS	4	PCCAT02P	- ADDRESS OF PCCA FOR CPU 2
12	(C)	ADDRESS	4	PCCAT03P	- ADDRESS OF PCCA FOR CPU 3
16	(10)	ADDRESS	4	PCCAT04P	- ADDRESS OF PCCA FOR CPU 4
20	(14)	ADDRESS	4	PCCAT05P	- ADDRESS OF PCCA FOR CPU 5
24	(18)	ADDRESS	4	PCCAT06P	- ADDRESS OF PCCA FOR CPU 6
28	(1C)	ADDRESS	4	PCCAT07P	- ADDRESS OF PCCA FOR CPU 7
32	(20)	ADDRESS	4	PCCAT08P	- ADDRESS OF PCCA FOR CPU 8
36	(24)	ADDRESS	4	PCCAT09P	- ADDRESS OF PCCA FOR CPU 9
40	(28)	ADDRESS	4	PCCAT10P	- ADDRESS OF PCCA FOR CPU 10
44	(2C)	ADDRESS	4	PCCAT11P	- ADDRESS OF PCCA FOR CPU 11
48	(30)	ADDRESS	4	PCCAT12P	- ADDRESS OF PCCA FOR CPU 12
52	(34)	ADDRESS	4	PCCAT13P	- ADDRESS OF PCCA FOR CPU 13
56	(38)	ADDRESS	4	PCCAT14P	- ADDRESS OF PCCA FOR CPU 14
60	(3C)	ADDRESS	4	PCCAT15P	- ADDRESS OF PCCA FOR CPU 15
64	(40)	ADDRESS	4	PCCAT16_31P (16)	- ADDRESS OF PCCAs for CPUs 16-31
128	(80)	ADDRESS	4	PCCAT32_63P (32)	- ADDRESS OF PCCAs for CPUs 32-63
256	(100)	ADDRESS	4	PCCAT64_127P (64)	- ADDRESS OF PCCAs for CPUS 64-127
256	(100)	X'200'	0	PCCATEND	"*" END OF PCCAT. There are CVTMAXMP+1 entries. Do not reference entries beyond CVTMAXMP+1

PCCAVT Cross Reference

Name	Hex Offset	Hex Value
PCCATEND	100	200
PCCAT00P	0	
PCCAT01P	4	
PCCAT02P	8	
PCCAT03P	C	
PCCAT04P	10	
PCCAT05P	14	
PCCAT06P	18	
PCCAT07P	1C	
PCCAT08P	20	
PCCAT09P	24	
PCCAT10P	28	
PCCAT11P	2C	
PCCAT12P	30	
PCCAT13P	34	
PCCAT14P	38	
PCCAT15P	3C	
PCCAT16_31P	40	
PCCAT32_63P	80	
PCCAT64_127P	100	
PCCAVT	0	

PCCW Information

PCCW Heading Information

Common Name: ASM Paging Channel Command Work Area
Macro ID: ILRPCCW
DSECT Name: PCCW
Owning Component: Auxiliary Storage Manager (SC1CW)
Eye-Catcher ID: PCCW
Offset: 0
Length: 4
Storage Attributes: Subpool: 245
Key: 0
Residency: Above 16M
Size: 128 bytes
Created by: ILROPS00
Pointed to by: IORPCCW field of the IORB data area
PCCWPCCW field of the PCCW data area
ASMPCCWQ field of the ASMVT data area
Serialization: The PCCW is serialized by the PCCW available queue. The PCCW is kept on an available queue and removed when needed.
Function: PCCW describes the string of channel command words which are passed by the I/O supervisor to the channel for I/O processing of a page.

PCCW Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	128	PCCW	Paging Channel Command Workarea
0	(0)	CHARACTER	4	PCCWID	PCCW identifier 'PCCW'
4	(4)	UNSIGNED	1	PCCWSECT	Sector for Set Sector CCW
5	(5)	CHARACTER	1	PCCWFLGS	Internal flags
		1...		PCCWFERR	X'80' = I/O error
		.1..		PCCWINIO	X'40' = This PCCW reserved for input I/O
		..11 1111		*	Reserved
6	(6)	CHARACTER	2	PCCWRSV1	Reserved
8	(8)	ADDRESS	4	PCCWPCCW	Next PCCW address
12	(C)	ADDRESS	4	PCCWAIA	Associated AIA address
16	(10)	ADDRESS	4	PCCWIORB	IORB address
20	(14)	ADDRESS	4	PCCWREAL	Real address of this PCCW
24	(18)	CHARACTER	8	PCCWIDAW	Extended IDAW for 64-bit I/O. Only used when running in ESAME mode.
32	(20)	CHARACTER	24	PCCWRSV3	Reserved - used by extended CKD format, not by this format
56	(38)	CHARACTER	8	PCCWCHHR	Full seek address - MBBCCHHR
56	(38)	CHARACTER	1	PCCWM	Extent number
57	(39)	CHARACTER	2	PCCWBB	Bin number
59	(3B)	CHARACTER	2	PCCWCC	Cylinder number
61	(3D)	CHARACTER	2	PCCWHH	Track (head) number
63	(3F)	CHARACTER	1	PCCWR	Record number
64	(40)	CHARACTER	8	PCCWSEEK	Seek CCW
64	(40)	CHARACTER	1	PCCWSK	Seek opcode
65	(41)	CHARACTER	1	PCCWSKFG	Seek flags
66	(42)	CHARACTER	2	PCCWSKCT	Seek count
68	(44)	ADDRESS	4	PCCWSKAD	Seek CCW address
72	(48)	CHARACTER	8	PCCWSSEC	Set Sector CCW
72	(48)	CHARACTER	1	PCCWSS	Set Sector opcode
73	(49)	CHARACTER	1	PCCWSSFG	Set Sector flags
74	(4A)	CHARACTER	2	PCCWSSCT	Set Sector count
76	(4C)	ADDRESS	4	PCCWSSAD	Set Sector CCW address
80	(50)	CHARACTER	8	PCCWSRCH	Search ID Equal CCW
80	(50)	CHARACTER	1	PCCWSIDE	Search ID Equal opcode
81	(51)	CHARACTER	1	PCCWSIFG	Search ID Equal flags
82	(52)	CHARACTER	2	PCCWSICT	Search ID Equal count
84	(54)	ADDRESS	4	PCCWSIAD	Search ID Equal CCW address
88	(58)	CHARACTER	8	PCCWTIC	TIC CCW
88	(58)	CHARACTER	1	PCCWT	TIC opcode
89	(59)	CHARACTER	1	PCCWTFG	TIC flags
90	(5A)	CHARACTER	2	PCCWTCT	TIC count
92	(5C)	ADDRESS	4	PCCWTAD	TIC CCW address
96	(60)	CHARACTER	8	PCCWRW	Read/write CCW
96	(60)	CHARACTER	1	PCCWRDWT	Read/Write opcode
97	(61)	CHARACTER	1	PCCWRWFG	Read/Write flags

PCCW Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
98	(62)	CHARACTER	2	PCCWCNT	Read/Write count
100	(64)	ADDRESS	4	PCCWADDR	Read/Write CCW address
104	(68)	CHARACTER	8	PCCWNOP	NOP (or TIC) CCW
104	(68)	CHARACTER	1	PCCWN	NOP opcode
105	(69)	CHARACTER	1	PCCWNFG	NOP flags
106	(6A)	CHARACTER	2	PCCWNCT	NOP count
108	(6C)	ADDRESS	4	PCCWNAD	NOP CCW address
112	(70)	CHARACTER	10	PCCWSPPD	Set Paging Parameters data
112	(70)	CHARACTER	1	PCCWSPFL	Set Paging Parameters flag byte
		1...		PCCWSPSQ	Sequential flag
		.1..		PCCWSPR1	Read once flag
113	(71)	CHARACTER	1	PCCWSPBC	Set Paging Parameters block count. Used when sequential flag is set, otherwise is zero.
114	(72)	CHARACTER	2	PCCWSPCA	Set Paging Parameters base cylinder address (always zero)
116	(74)	CHARACTER	2	PCCWRSV4	Reserved
118	(76)	CHARACTER	4	PCCWSPSK	Set Paging Parameters seek address
122	(7A)	CHARACTER	6	PCCWRSVD	Reserved
128	(80)	CHARACTER	0	*	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
32	(20)	STRUCTURE	64	PCCWECKD	PCCW for extended architecture
32	(20)	CHARACTER	32	PCCWDEFD	Define Extent data
32	(20)	CHARACTER	1	PCCWDMSK	Define Extent mask byte
33	(21)	CHARACTER	1	PCCWDATR	Define Extent attribute byte
34	(22)	UNSIGNED	2	PCCWDSZ	Define Extent record size
36	(24)	CHARACTER	3	PCCWDRSV	Reserved
39	(27)	CHARACTER	1	PCCWGAEX	Global attributes extended byte
		1111 11..		*	Unused
		.... ..1.		PCCWEP	Extended parameter
		1		*	Reserved
40	(28)	CHARACTER	4	PCCWCCHB	Beginning CCHH of Define Extent
44	(2C)	CHARACTER	4	PCCWCCHE	Ending CCHH of Define Extent
48	(30)	CHARACTER	9	*	Unused portion of DX
57	(39)	CHARACTER	1	PCCWIOP	I/O priority
58	(3A)	CHARACTER	6	*	Unused portion of DX
64	(40)	CHARACTER	16	PCCWLOCD	Locate Record data
64	(40)	CHARACTER	1	PCCWLOPB	Locate Record operation byte
65	(41)	CHARACTER	1	PCCWLAUX	Locate Record auxiliary byte
66	(42)	UNSIGNED	2	PCCWLREC	Number of records
68	(44)	CHARACTER	4	PCCWLSEK	Seek address
72	(48)	CHARACTER	5	PCCWLSRC	Search argument
77	(4D)	CHARACTER	1	PCCWLSEC	Sector number
78	(4E)	UNSIGNED	2	PCCWLTRN	Transfer length factor
80	(50)	CHARACTER	8	PCCWDEFE	Define Extent CCW
80	(50)	CHARACTER	1	PCCWDEOP	Define Extent opcode
81	(51)	CHARACTER	1	PCCWDEFG	Define Extent flag
82	(52)	UNSIGNED	2	PCCWDECT	Define Extent count
84	(54)	ADDRESS	4	PCCWDEAD	Define Extent data address
88	(58)	CHARACTER	8	PCCWLOCR	Locate Record CCW
88	(58)	CHARACTER	1	PCCWLROP	Locate Record opcode
89	(59)	CHARACTER	1	PCCWLRFG	Locate Record flag
90	(5A)	CHARACTER	2	PCCWLRCT	Locate Record count
92	(5C)	ADDRESS	4	PCCWLRAD	Locate Record data address

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
64	(40)	STRUCTURE	8	PCCWSETP	Set Paging Parameters CCW
64	(40)	CHARACTER	1	PCCWSPOP	Set Paging Parameters opcode
65	(41)	CHARACTER	1	PCCWSPFG	Set Paging Parameters flag
66	(42)	CHARACTER	2	PCCWSPCT	Set Paging Parameters count
68	(44)	ADDRESS	4	PCCWSPAD	Set Paging Parameters address

PCCW Cross Reference

Name	Hex Offset	Hex Value
PCCW	0	
PCCWADDR	64	
PCCWAIA	C	
PCCWBB	39	
PCCWCC	3B	
PCCWCCHB	28	
PCCWCCHE	2C	
PCCWCHHR	38	
PCCWCNT	62	
PCCWDATR	21	
PCCWDEAD	54	
PCCWDECT	52	
PCCWDEFD	20	
PCCWDEFE	50	
PCCWDEFG	51	
PCCWDEOP	50	
PCCWDMSK	20	
PCCWDRSV	24	
PCCWDSZ	22	
PCCWECKD	20	
PCCWEP	27	02
PCCWFERR	5	80
PCCWFLGS	5	
PCCWGAEX	27	
PCCWHH	3D	
PCCWID	0	
PCCWIDAW	18	
PCCWINIO	5	40
PCCWIOP	39	
PCCWIORB	10	
PCCWLAUX	41	
PCCWLOCD	40	
PCCWLOCR	58	
PCCWLOPB	40	
PCCWLRAD	5C	
PCCWLRCT	5A	
PCCWLREC	42	
PCCWLRFG	59	
PCCWLROP	58	
PCCWLSEC	4D	
PCCWLSEK	44	
PCCWLSRC	48	
PCCWLTRN	4E	
PCCWM	38	
PCCWN	68	
PCCWNAD	6C	
PCCWNCT	6A	
PCCWNFG	69	
PCCWNOP	68	
PCCWPCCW	8	
PCCWR	3F	
PCCWRDWT	60	
PCCWREAL	14	
PCCWRSVD	7A	
PCCWRSV1	6	
PCCWRSV3	20	
PCCWRSV4	74	
PCCWRW	60	
PCCWRWFG	61	
PCCWSECT	4	
PCCWSEEK	40	
PCCWSETP	40	
PCCWSIAD	54	
PCCWSICT	52	
PCCWSIDE	50	
PCCWSIFG	51	
PCCWSK	40	
PCCWSKAD	44	
PCCWSKCT	42	
PCCWSKFG	41	
PCCWSPAD	44	
PCCWSPBC	71	
PCCWSPCA	72	
PCCWSPCT	42	
PCCWSPFG	41	
PCCWSPFL	70	
PCCWSPOP	40	
PCCWSPPD	70	
PCCWSPR1	70	40
PCCWSPSK	76	
PCCWSPSQ	70	80
PCCWSRCH	50	
PCCWSS	48	
PCCWSSAD	4C	
PCCWSSCT	4A	
PCCWSSEC	48	
PCCWSSFG	49	
PCCWT	58	
PCCWTAD	5C	
PCCWTCT	5A	
PCCWTFG	59	
PCCWTIC	58	

PCDPARMS Information

PCDPARMS Heading Information

Common Name: PCDALT Parameter list
Macro ID: IEFZB459
DSECT Name: PCDPARMS
Owning Component: Allocation (SC1B4)
Eye-Catcher ID: None
Storage Attributes: Subpool: 230
Key: 1
Size: 72 bytes
Created by: Allocation
Pointed to by: PCDPARMP (Automatic Storage)
Serialization: None.
Function: Provides a symbolic mapping for the parameter list and the automatic data area to be passed to IEFHB410 via the PCDALT macro.

PCDPARMS Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	72	PCDPARMS	PCDALT PARMLIST
0	(0)	CHARACTER	24	PCDINPUT	INPUT TO DALT MANAGER
0	(0)	UNSIGNED	2	PCDUNIT1	Unit address, swap-from unit when PCDDSWAP set
2	(2)	UNSIGNED	2	PCDUNIT2	Swap-to unit for DDR swap
4	(4)	UNSIGNED	2	PCDASID	CURRENT ASID
6	(6)	SIGNED	2	PCDCOUNT	UPDATE VALUE
8	(8)	UNSIGNED	2	PCDFCODE	Function code
		1...		PCDUDALT	UPDATE DALTUSE COUNT
		.1..		PCDCNT	GET TOTAL DALTUSE FOR UNIT
		..1.		PCDCNTA	GET DALTUSE FOR THIS UNIT/ASID
		...1		PCDCDALT	CLEAR DALT
		.... 1...		PCDDSWAP	Swap DALTs for DDR swap
8	(8)	BITSTRING	1	PCDRSV00	Reserved
10	(A)	CHARACTER	2	PCDRSV01	RESERVED
12	(C)	UNSIGNED	4	PCDRCNT	TOTAL DALTUSE FOR UNIT
16	(10)	UNSIGNED	4	PCDRCNTA	TOTAL DALTUSE FOR UNIT/ASID
20	(14)	CHARACTER	4	PCDRSV02	RESERVED
24	(18)	CHARACTER	48	PCDAUTO	AUTOMATIC DATA AREA

PCDPARMS Cross Reference

Name	Hex Offset	Hex Value
PCDASID	4	
PCDAUTO	18	
PCDCDALT	8	10
PCDCNT	8	40
PCDCNTA	8	20
PCDCOUNT	6	
PCDDSWAP	8	08
PCDFCODE	8	
PCDINPUT	0	
PCDPARMS	0	
PCDRCNT	C	
PCDRCNTA	10	
PCDRSV00	8	
PCDRSV01	A	
PCDRSV02	14	
PCDUDALT	8	80
PCDUNIT1	0	
PCDUNIT2	2	

PCRA Information

PCRA Heading Information

Common Name: Program Call Recovery Area
Macro ID: IHAPCRA
DSECT Name: PCRA
Owning Component: PC/AUTH (SCXMS)
Eye-Catcher ID: None
Storage Attributes: Subpool: None
Key: 0
Size: 24 bytes
Created by: PC/Auth service routines issuing SETFRR
Pointed to by: PCRAPTR in each PC/Auth service routine; PCRAMAIN
Serialization: Serialized (input) by the PC/Auth local lock.
Accessable only when the PC/Auth recovery environment exists.
Function: Describes the FRR parameter area returned by the SETFRR macro (as used by the Program Call/Authorization services).

PCRA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	24	PCRA	MAPS THE 6 WORD FRR PARAMETER AREA RETURNED BY SETFRR. USED BY PC/AUTH SERVICES AND THEIR FRR.
0	(0)	UNSIGNED	2	PCRAEERC	ENVIRONMENTAL ERROR REASON CODE
0	(0)	UNSIGNED	1	PCRASERV	ONE BYTE IDENTIFIER OF THE SERVICE ROUTINE IN CONTROL. SEE CONSTANTS THAT FOLLOW.

Comment

```
. | 1 - LXRES LINKAGE INDEX RESERVE
        | 2 - LXFRE LINKAGE INDEX FREE
        | 3 - ETCRE ENTRY TABLE CREATE
        | 4 - ETDES ENTRY TABLE DESTROY
        | 5 - ETCON ENTRY TABLE CONNECT
        | 6 - ETDIS ENTRY TABLE DISCONNECT
        | 7 - AXRES AUTHORIZATION INDEX RESERVE
        | 8 - AXFRE AUTHORIZATION INDEX FREE
        | 9 - AXEXT AUTHORIZATION INDEX EXTRACT
        | A - AXSET AUTHORIZATION INDEX SET
        | B - ATSET AUTHORIZATION TABLE SET
        | C - PCARM PC/AUTH RESOURCE MANAGER
        | D - XPCR PC/AUTH FRR FINDS PCRASERV INVALID
        | E-10 - AVAILABLE FOR FUTURE USAGE
        | 11-13 - USED BY PCLINK - (UNAVAILABLE)
        | 14 - USED BY IEAVXMAS (UNAVAILABLE)
```

End of Comment

1	(1)	UNSIGNED	1	PCRAREAS	ABEND REASON CODE. CODES COMMON TO ALL SERVICES FOLLOW.

Comment

```
. | 00 - UNEXPECTED ERROR.
        | 01 - GETMAIN FOR DYNAMIC WORKAREA
        | (XMDDASP SUBPOOL) FAILED.
        | 02 - GETMAIN FOR SQA (S.P 245) FAILED.
        | 03 - GETMAIN FOR PC/AUTH LSQA (S.P. 255) FAILED.
        | 05 - GETMAIN FOR PC/AUTH PAGEABLE STORAGE
        | (XMDPSP SUBPOOL) FAILED.
        | 06 - FREEMAIN FOR SQA (S.P 245) FAILED.
        | 07 - FREEMAIN FOR PC/AUTH LSQA (S.P. 255) FAILED.
        | 09 - FREEMAIN FOR PC/AUTH PAGEABLE STORAGE
        | (XMDPSP SUBPOOL) FAILED.
        | 0A - FREEMAIN FOR DYNAMIC WORKAREA
        | (XMDDASP SUBPOOL) FAILED.
        | 97 - UNEXPECTED ERROR.
        | 98 - PC/AUTH SERVCIES ARE INOPERABLE
        | (SVTXMSOP HAS BEEN TURNED OFF).
        | 99 - PC/AUTH CONTROL BLOCK DAMAGE DETECTED.
```

End of Comment

PCRA Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
2	(2)	BITSTRING	1	*	FIRST FLAG BYTE
		1...		PCRARSB1	RESERVED
		.1..		PCRACML	PC/AUTH LOCAL LOCK HELD
		..1.		PCRACMS	CMS LOCK HELD
		...1		PCRAKCML	CALLER HELD PC/AUTH LOCAL LOCK (THEREFORE, DONT RELEASE IT)
		.... 1...		PCRACLUP	SERVICE ROUTINE'S FRR CLEANUP EXIT INVOCATION IS REQUESTED
		.... .1..		PCRARCUR	RETRY RECURSION INDICATOR
		.... ..1.		PCRAFRRE	FRR WAS ENTERED AS AN FRR
		1		PCRARMGR	FRR ENTERED AS RESOURCE MGR
3	(3)	BITSTRING	1	*	SECOND FLAG BYTE
		1...		PCRA1ST	THIS PCRA IS FOR 1ST LEVEL FRR
		.1..		PCRA2ND	THIS PCRA IS FOR 2ND LEVEL FRR (THIS IS THE MAIN PCRA)
		..1.		PCRANTH	THIS PCRA IS FOR NTH LEVEL FRR
		...1		PCRAPERC	PERCOLATE TO CALLER FLAG
		.... 1...		PCRAREC2	IEAVXPCR RECURSION FLAG
		.... .1..		PCRAFRRG	FRR GETMAIN IN PROGRESS
		.... ..1.		PCRADUMP	AN SDUMP HAS BEEN REQUESTED
		1		PCRARSB2	RESERVED
4	(4)	ADDRESS	4	PCRASTTK	PCLINK STACK TOKEN
8	(8)	ADDRESS	4	PCRARSV1	RESERVED WORD (3RD WORD)
12	(C)	UNSIGNED	1	PCRAFOOT	PRIMARY FRR FOOTPRINT
13	(D)	BITSTRING	1	PCRARSV2	RESERVED
14	(E)	SIGNED	2	PCRARSV3	RESERVED
16	(10)	ADDRESS	4	PCRARRDA	FRR DYNAMIC DATA AREA ADDRESS
20	(14)	ADDRESS	4	PCRASRRA	ADDRESS OF SERVICE ROUTINE RECOVERY AREA (VALID ONLY FOR THE MAIN PCRA ASSOCIATED WITH THE 2ND LEVEL FRR).
20	(14)	ADDRESS	4	PCRAMAIN	ADDRESS OF MAIN PCRA (VALID FOR A PCRA ASSOCIATED WITH THE 1ST OR AN NTH LEVEL FRR).

PCRA Constants

Len	Type	Value	Name	Description
		Comment		
		THE FOLLOWING CONSTANTS ARE SET IN PCRASERV TO ENABLE THE FRR TO DETERMINE WHICH SERVICE ROUTINE IS IN CONTROL.		
		End of Comment		
1	DECIMAL	1	LXRES	LINKAGE INDEX RESERVE
1	DECIMAL	2	LXFRE	LINKAGE INDEX FREE
1	DECIMAL	3	ETCRE	ENTRY TABLE CREATE
1	DECIMAL	4	ETDES	ENTRY TABLE DESTROY
1	DECIMAL	5	ETCON	ENTRY TABLE CONNECT
1	DECIMAL	6	ETDIS	ENTRY TABLE DISCONNECT
1	DECIMAL	7	AXRES	AUTHORIZATION INDEX RESERVE
1	DECIMAL	8	AXFRE	AUTHORIZATION INDEX FREE
1	DECIMAL	9	AXEXT	AUTHORIZATION INDEX EXTRACT
1	DECIMAL	10	AXSET	AUTHORIZATION INDEX SET
1	DECIMAL	11	ATSET	AUTHORIZATION TABLE SET
1	DECIMAL	12	PCARM	PC/AUTH RESOURCE MANAGER
1	DECIMAL	13	XPCR	PC/AUTH FRR (USED WHEN FRR FINDS PCRASERV INVALID)
		Comment		
		THE FOLLOWING CONSTANTS DEFINE THE 053 ABEND REASON CODES WHICH ARE COMMON TO ALL PC/AUTH SERVICES.		
		End of Comment		
1	DECIMAL	1	PCRAGM01	GETMAIN FOR DYNAMIC WORKAREA (XMDDASP SUBPOOL).
1	DECIMAL	10	PCRAFM01	FREEMAIN FOR DYNAMIC WORKAREA (XMDDASP SUBPOOL).
1	DECIMAL	2	PCRAGM02	GETMAIN FOR SQA (SP 245).
1	DECIMAL	6	PCRAFM02	FREEMAIN FOR SQA (SP 245).
1	DECIMAL	3	PCRAGM03	GETMAIN FOR PC/AUTH LSQA (SP 255).
1	DECIMAL	7	PCRAFM03	FREEMAIN FOR PC/AUTH LSQA (SP 255).
1	DECIMAL	5	PCRAGM05	GETMAIN FOR PC/AUTH PAGEABLE STORAGE (XMDPSP SUBPOOL).
1	DECIMAL	9	PCRAFM05	FREEMAIN FOR PC/AUTH PAGEABLE STORAGE (XMDPSP SUBPOOL).
1	HEX	97	PCRAUNEX	UNEXPECTED ERROR.
1	HEX	98	PCRAINOP	PC/AUTH SERVICES ARE INOPERABLE (SVTXMSOP IS OFF).
1	HEX	99	PCRADAMG	PC/AUTH CONTROL BLOCK DAMAGE WAS DETECTED.

PCRA Cross Reference

Name	Hex Offset	Hex Value
PCRA	0	
PCRACLUP	2	08
PCRACML	2	40
PCRACMS	2	20
PCRADUMP	3	02
PCRAEERC	0	
PCRAFOOT	C	
PCRAFRRE	2	02
PCRAFRRG	3	04
PCRAKCML	2	10
PCRAMAIN	14	
PCRANTH	3	20
PCRAPERC	3	10
PCRARCUR	2	04
PCRAREAS	1	
PCRAREC2	3	08
PCRARMGR	2	01
PCRARRDA	10	
PCRARSB1	2	80
PCRARSB2	3	01
PCRARSV1	8	
PCRARSV2	D	
PCRARSV3	E	
PCRASERV	0	
PCRASRRA	14	
PCRASTTK	4	
PCRA1ST	3	80
PCRA2ND	3	40

PCT Information

PCT Heading Information

Common Name: ASM Performance Characteristics Table
Macro ID: ILRPCT
DSECT Name: PCT
Owning Component: Auxiliary Storage Manager (SC1CW)
Eye-Catcher ID: PCT
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: YES
Subpool: 245
Key: 0
Data Space: NO
Residency: Above 16 Megabytes virtual
Size: 40 bytes plus a variable number of bytes dependent on the data set size
Created by: ILRASRIM, ILRPGEXP
Pointed to by: PARTPCTQ field of the PART data area
PCTNEXT field of the PCT data area
PAREPCTP field of the PARTE data area
Serialization: None
Function: The PCT provides a single location for device-dependent information used by ASM. One PCT exists for each type of device supported by ASM.

PCT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	PCT	Performance Characteristics Table
0	(0)	CHARACTER	4	PCTID	'PCT ' identifier
4	(4)	CHARACTER	6	PCTDTYPE	Device type (EBCDIC)
10	(A)	CHARACTER	2	PCTRSV3	Reserved
12	(C)	CHARACTER	2	PCTDTYPX	Device type
14	(E)	SIGNED	2	PCTCYLSZ	Slots per cylinder
16	(10)	ADDRESS	4	PCTNEXT	Address of next PCT in the PART PCT queue.
20	(14)	CHARACTER	4	PCTDMASK	Mask to preset non-existent slots
24	(18)	CHARACTER	1	PCTDUSE	Device usage code.
25	(19)	UNSIGNED	1	PCTPCCWM	PCCW multiplier
26	(1A)	UNSIGNED	1	PCTBRST	Burst size
27	(1B)	CHARACTER	3	PCTRSV1	Reserved
30	(1E)	SIGNED	2	PCTSSECN	Number of unique Set Sector values
32	(20)	SIGNED	4	PCTRQTIM	Minimum time to read or write one 4096-byte slot
36	(24)	UNSIGNED	2	PCTMAXTK	Maximum relative track position
38	(26)	UNSIGNED	2	PCTMSSB	Minimum byte variance to insert Set Sector
40	(28)	CHARACTER	*	PCTTABLE	Sector value table

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
40	(28)	STRUCTURE	8	PCTSECT (*)	Sector values
40	(28)	CHARACTER	1	PCTSLTNM	Relative slot number on cylinder
		1...		PCTFOVFL	1 = Overflow track
		.111 1111		PCTSLOT	Slot number
41	(29)	CHARACTER	1	PCTSECNM	Sector value corresponding to slot number
42	(2A)	CHARACTER	2	PCTTRBA	Relative byte on track
44	(2C)	CHARACTER	3	PCTHHR	Head and record for this slot on the cylinder
47	(2F)	CHARACTER	1	PCTRSV2	Reserved

PCT Cross Reference

Name	Hex Offset	Hex Value
PCT	0	
PCTBRST	1A	
PCTCYLSZ	E	
PCTDMASK	14	
PCTDTYPE	4	
PCTDTYPX	C	
PCTDUSE	18	
PCTFOVFL	28	80
PCTHHR	2C	
PCTID	0	
PCTMAXTK	24	
PCTMSSB	26	
PCTNEXT	10	
PCTPCCWM	19	
PCTRQTIM	20	
PCTRSV1	1B	
PCTRSV2	2F	
PCTRSV3	A	
PCTSECNM	29	
PCTSECT	28	
PCTSLOT	28	7F
PCTSLTNM	28	
PCTSSECN	1E	
PCTTABLE	28	
PCTTRBA	2A	

PCTRC Information

PCTRC Heading Information

Common Name: PC/Auth Services System Trace Entry
Macro ID: IHAPCTRC
DSECT Name: GENERAL, PCETCON, PCETCRE, PCETSET, PCAXSET, PCASEXT, PCAXFRE, PCAXRES, PCETDES, PCETDIS, PCLXFRE, PCLXRES
Owning Component: PC/AUTH (SCXMS)
Eye-Catcher ID: None
Storage Attributes: Subpool: 0
Key: 0
Residency: PC/AUTH private area
Size: 20 bytes per entry
Created by: IEAVXECO (ETCON)
IEAVXECR (ETCRE)
IEAVXEDE (ETDES)
IEAVXEDI (ETDIS)
IEAVXLFR (LXFRE)
IEAVXLRE (LXRES)
IEAVXRFE (AXTEX, AXFRE, AND AXRES)
IEAVXSET (ATSET AND AXSET)
Pointed to by: None
Serialization: None
Function: Provides a template for building and documenting PC/Auth services system trace table entries. The first word in each entry appears under the heading: 'Address -' in the formatted trace table. The remaining four words appear under:
Unique-1
Unique-2
Unique-3
Unique-4

PCTRC Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCETCON	ETCON SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0100)
0	(0)	ADDRESS	4	PCETCRET	ETCON CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	4	PCETCNET	NUMBER OF ENTRY TABLES TO BE CONNECTED BY ETCON
8	(8)	ADDRESS	4	PCETCETA	ADDRESS OF FIRST ETIB TO BE CONNECTED BY ETCON
12	(C)	SIGNED	4	PCETCTKN	FIRST ASSIGNED LX ASSOCIATED WITH THE FIRST ENTRY TABLE
16	(10)	UNSIGNED	4	PCETCSEQ	SEQ# FOR THE FIRST ASSIGNED LX
20	(14)	CHARACTER	0	PCETCONE	END OF ETCON SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCETCRE	ETCRE SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0101)
0	(0)	ADDRESS	4	PCETRRET	ETCRE CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	4	PCETRETD	ENTRY TABLE DESCRIPTION OF THE ENTRY TABLE TO BE CREATED BY ETCRE
8	(8)	ADDRESS	4	PCETRTKN	TOKEN ASSIGNED WITH THE NEW ENTRY TABLE
12	(C)	SIGNED	2	PCETRNET	NUMBER OF ENTRY TABLE DESCRIPTIONS CONTAINED IN ENTRY TABLE DESCRIPTION LIST
14	(E)	CHARACTER	6	PCETRRSV	RESERVED
20	(14)	CHARACTER	0	PCETCREE	END OF ETCRE SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	21	PCATSET	ATSET SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0102)
0	(0)	ADDRESS	4	PCATSRET	ATSET CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	4	PCATSRG0	CONTENTS OF INPUT REG 0
4	(4)	BITSTRING	2	PCATSFLG	ATSET OPTION FLAG BYTES
4	(4)	BITSTRING	1	*	RESERVED
5	(5)	1...		PCATSFPT	PT OPERAND INDICATION FLAG (1-PT=YES AND 0-PT=NO)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.1..		PCATSFSS	SSAR OPERAND INDICATION FLAG (1-SSAR=YES AND 0-SSAR=NO)
		..11 1111		*	RESERVED
6	(6)	SIGNED	2	PCATSAX	AX VALUE
8	(8)	CHARACTER	13	PCATSRSV	RESERVED
21	(15)	CHARACTER	0	PCATSETE	END OF ATSET SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCAXSET	AXSET SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0103)
0	(0)	ADDRESS	4	PCAXSRET	AXSET CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	2	PCAXSOAX	ORIGINAL AX VALUE
6	(6)	SIGNED	2	PCAXSNAX	NEW AX VALUE
8	(8)	CHARACTER	12	PCAXSRSV	RESERVED
20	(14)	CHARACTER	0	PCAXSETE	END OF AXSET SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCAXEXT	AXEXT SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0104)
0	(0)	ADDRESS	4	PCAXERET	AXEXT CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	4	PCAXEASD	ASID OF ADDRESS SPACE WHOSE AX IS TO BE EXTRACTED BY AXEXT
8	(8)	SIGNED	4	PCAXEAX	AX VALUE ASSOCIATED WITH INPUT ASID
12	(C)	CHARACTER	8	PCAXERSV	RESERVED
20	(14)	CHARACTER	0	PCAXEXTE	END OF AXEXT SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCAXFRE	AXFRE SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0105)
0	(0)	ADDRESS	4	PCAXFRET	AXFRE CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	2	PCAXFNAX	NUMBER OF AX TO BE FREED BY AXFRE
6	(6)	SIGNED	2	PCAXFAXF	FIRST AX FREED BY AXFRE
8	(8)	CHARACTER	12	PCAXFRSV	RESERVED
20	(14)	CHARACTER	0	PCAXFREE	END OF AXFRE SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCAXRES	AXRES SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0106)
0	(0)	ADDRESS	4	PCAXRRET	AXRES CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	2	PCAXRNAR	NUMBER OF AX TO BE RESERVED BY AXRES
6	(6)	SIGNED	2	PCAXRAXF	FIRST AX RESERVED BY AXRES
8	(8)	CHARACTER	12	PCAXRRSV	RESERVED
20	(14)	CHARACTER	0	PCAXRESE	END OF AXRES SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCETDES	ETDES SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0107)
0	(0)	ADDRESS	4	PCETDRET	ETDES CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	4	PCETDTKN	TOKEN ASSOCIATED WITH THE ENTRY TABLE TO BE DESTROYED BY ETDES
8	(8)	BITSTRING	1	PCETDFLG	ETDES OPTION FLAG BYTE
		1...		PCETDFPG	PURGE OPERAND INDICATION FLAG (1-PURGE=YES AND 0-PURGE=NO)
9	(9)	CHARACTER	11	PCETDRSV	RESERVED
20	(14)	CHARACTER	0	PCETDESE	END OF ETDES SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCETDIS	ETDIS SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0108)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	ADDRESS	4	PCETIRET	ETDIS CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	4	PCETINET	NUMBER OF ENTRY TABLES TO BE DISCONNECTED BY ETDIS
8	(8)	SIGNED	4	PCETITKN	TOKEN ASSOCIATED WITH THE FIRST ENTRY TABLE TO BE DISCONNECTED BY ETDIS
12	(C)	CHARACTER	8	PCETIRSV	RESERVED
20	(14)	CHARACTER	0	PCETDISE	END OF ETDIS SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCLXFRE	LXFRE SYSTEM TRACE ENTRY TEMPLATE (SRVID=X0109)
0	(0)	ADDRESS	4	PCLXFRET	LXFRE CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	4	PCLXFNLX	NUMBER OF LX TO BE FREED BY LXFRE
8	(8)	SIGNED	4	PCLXFFLX	FIRST LX TO BE FREED BY LXFRE
12	(C)	BITSTRING	1	PCLXFFLG	LXFRE OPTION FLAG BYTE
		1...		PCLXFFFR	FORCE OPERAND INDICATION FLAG (1-FORCE=YES AND 0-FORCE=NO)
13	(D)	CHARACTER	7	PCLXFRSV	RESERVED
20	(14)	CHARACTER	0	PCLXFREE	END OF LXFRE SYSTEM TRACE ENTRY TEMPLATE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	PCLXRES	LXRES SYSTEM TRACE ENTRY TEMPLATE (SRVID=X010A)
0	(0)	ADDRESS	4	PCLXRRET	LXRES CALLERS RETURN ADDRESS (ZERO TO INDICATE PC ENTERED ROUTINE)
4	(4)	SIGNED	4	PCLXRNLX	NUMBER OF LX TO BE RESERVED BY LXRES
8	(8)	SIGNED	4	PCLXRLXA	FIRST LX ASSIGNED BY LXRES
12	(C)	BITSTRING	1	PCLXRFLG	LXRES OPTION FLAG BYTE
		1...		PCLXRFSY	SYSTEM OPERAND INDICATION FLAG (1-SYSTEM=YES AND 0-SYSTEM=NO)
13	(D)	CHARACTER	7	PCLXRRSV	RESERVED
20	(14)	CHARACTER	0	PCLXRESE	END OF LXRES SYSTEM TRACE ENTRY TEMPLATE

PCTRC Cross Reference

Name	Hex Offset	Hex Value
PCATSAX	6	
PCATSET	0	
PCATSETE	15	
PCATSFLG	4	
PCATSFPT	5	80
PCATSFSS	5	40
PCATSRET	0	
PCATSRG0	4	
PCATSRSV	8	
PCAXEASD	4	
PCAXEAX	8	
PCAXERET	0	
PCAXERSV	C	
PCAXEXT	0	
PCAXEXTE	14	
PCAXFAXF	6	
PCAXFNAX	4	
PCAXFRE	0	
PCAXFREE	14	
PCAXFRET	0	
PCAXFRSV	8	
PCAXRAXF	6	
PCAXRES	0	
PCAXRESE	14	
PCAXRNAR	4	
PCAXRRET	0	
PCAXRRSV	8	
PCAXSET	0	
PCAXSETE	14	
PCAXSNAX	6	
PCAXSOAX	4	
PCAXSRET	0	
PCAXSRSV	8	
PCETCETA	8	
PCETCNET	4	
PCETCON	0	
PCETCONE	14	
PCETCRE	0	
PCETCREE	14	
PCETCRET	0	
PCETCSEQ	10	
PCETCTKN	C	
PCETDES	0	
PCETDESE	14	
PCETDFLG	8	
PCETDFPG	8	80
PCETDIS	0	
PCETDISE	14	
PCETDRET	0	
PCETDRSV	9	
PCETDTKN	4	
PCETINET	4	
PCETIRET	0	
PCETIRSV	C	
PCETITKN	8	
PCETRETD	4	
PCETRNET	C	
PCETRRET	0	
PCETRRSV	E	
PCETRTKN	8	
PCLXFFFR	C	80
PCLXFFLG	C	
PCLXFFLX	8	
PCLXFNLX	4	

PCTRC Cross Reference

Name	Hex Offset	Hex Value
PCLXFRE	0	
PCLXFREE	14	
PCLXFRET	0	
PCLXFRSV	D	
PCLXRES	0	
PCLXRESE	14	
PCLXRFLG	C	
PCLXRFSY	C	80
PCLXRLXA	8	
PCLXRNLX	4	
PCLXRRET	0	
PCLXRRSV	D	

PEL Information

PEL Programming Interface information

Programming Interface information

PEL

End of Programming Interface information

PEL Heading Information

Common Name: GRS ENQ/DEQ/RESERVE Parameter Element List
Macro ID: ISGPEL
DSECT Name: PEL
Owning Component: Global Resource Serialization (SCSDS)
Eye-Catcher ID: None
Storage Attributes: Subpool: Any valid subpool in the private or common area
Key: User's key
Size: 12 bytes (PelBasic)
+ length(PelPrefx) if used
+ length(PelUCAAA) if ENQ with RESERVE
Created by: ENQ/DEQ/RESERVE macro expansion.
Pointed to by: The pointer is maintained by the user of the macro.
Serialization: None
Function: Contains the necessary information to process an ENQ, DEQ, or RESERVE macro request.

PEL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PEL	PARAMETER ELEMENT LIST, NOTE THAT THE PEL DIVIDED INTO TWO SECTIONS - A FIXED PEL AND THE RNAME WHICH IS VARIABLE IN LENGTH.
0	(0)	CHARACTER	8	PELPREFX (0)	PARAMETER ELEMENT PREFIX
0	(0)	ADDRESS	4	PELTCB	IF BOTH TCB AND ECB ARE CODED, CONTAINS THE TCB ADDRESS
4	(4)	ADDRESS	4	PELDUAL	PEL PREFIX WORK (TCB ADDRESS OR ECB ADDRESS)
8	(8)	CHARACTER	12	PELBASIC (0)	PARAMETER ELEMENT BASIC SECTION
8	(8)	CHARACTER	4	PELFASTPATHWORD (0)	Word to be checked to see if this Pel is a fast-path candidate
8	(8)	BITSTRING	1	PELLAST (0)	FLAG BYTE 1
		1...		PELEOL	"X'80'" LAST ELEMENT OF LIST
		.1..		PELIGNOR	"X'40'" IGNORE REMAINING BITS OF THIS BYTE
		..1.		PELOCANY	"X'20'" If 1, LOC=ANY was specified on RESERVE or DEQ request. LOC=ANY must be specified when the UCB address provided by the caller is an above-the-line address and is to be treated as such by ISGGQWBI, ISGGSETU, and ISGGNQDQ.
		...1		PELSHR	"X'10'" SHARED RESOURCE REQUEST
		.... 1...		PELSAVE	"X'08'" NEW-FORMAT PEL PREFIX PRECEDES FIRST PEL OF LIST. MUST BE ZERO FOR A DEQ. This flag must be set consistently in all PELs in a list to ensure desired results
		.... .1..		PELNORNL	"X'04'" IF 1, RNL=NO This flag must be set consistently in all PELs in a list to ensure desired results
		.... ..1.		PELGEN2	"X'02'" IF 1, GENERIC=YES This flag must be set consistently in all PELs in a list to ensure desired results
		1		PELTCBF	"X'01'" TCB= WAS SPECIFIED. PELTCBF IS IGNORED IN THE USER PEL IF PELSAVE IS ON. THE TCB= OPERAND IS CONSIDERED TO BE PRESENT IF THE TCB FIELD OF THE NEW-FORMAT PEL-PREFIX IS NON-ZERO. This flag must be set consistently in all PELs in a list to ensure desired results
9	(9)	BITSTRING	1	PELMILEN	RNAME LENGTH
10	(A)	BITSTRING	1	PELFLAG (0)	FLAG BYTE 2
		1...		PELSHARE	"X'80'" 0=EXCLUSIVE, 1=SHARE
		.1..		PELSCPE1	"X'40'" SEE COMMENTS BELOW
		..1.		PELSYSMC	"X'20'" OBSOLETE (SET/RESET SYSTEM MUST COMPLETE)
		...1		PELSTPMC	"X'10'" SET/RESET STEP MUST COMPLETE SPECIFIED. This flag must be set consistently in all PELs in a list to ensure desired results
		.... 1...		PELSCPE2	"X'08'" SEE COMMENTS BELOW
		.... .1..		PELRET1	"X'04'" SEE COMMENTS BELOW
		.... ..1.		PELRET2	"X'02'" SEE COMMENTS BELOW
		1		PELRET3	"X'01'" SEE COMMENTS BELOW

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		Comment			
		PELRET1,PELRET2 AND PELRET3 These flags must be set consistently in all PELs in a list to ensure desired results 000- RET=NONE (NO RETURN CODE) 001- RET=HAVE 010- RET=CHNG 011- RET=USE 100- ECB= 101- RESERVED 110- RESERVED 111- RET=TEST PELSCPE1 AND PELSCPE2 00- STEP 01- SYSTEMS AND UCB 10- SYSTEM 11- SYSTEMS			
		End of Comment			
11	(B)	BITSTRING	1	PELRET	RETURN CODE AREA IN USER-S PEL
12	(C)	CHARACTER	8	PELMAJAMINA (0)	Field containing the QName and RName addresses
12	(C)	ADDRESS	4	PELMAJA	ADDRESS OF QNAME
16	(10)	ADDRESS	4	PELMINA	ADDRESS OF RNAME
20	(14)	ADDRESS	4	PELUCBAA	ADDRESS OF POINTER TO UCB. THIS FIELD ONLY EXISTS FOR RESERVE REQUESTS.
		Comment			
		THE FOLLOWING DECLARES DEFINE THE REQUIRED PEL CONSTANTS			
		End of Comment			
20	(14)	X'8'	0	PELMAJSZ	"8" LENGTH OF QNAME
20	(14)	X'C'	0	PELELEM	"12" LENGTH OF A PEL ENTRY (BASIC)
20	(14)	X'18'	0	PEL_LEN	"*-PEL"

PEL Cross Reference

Name	Hex Offset	Hex Value
PEL	0	
PEL_LEN	14	18
PELBASIC	8	
PELDUAL	4	
PELELEM	14	C
PELEOL	8	80
PELFASTPATHWORD	8	
PELFLAG	A	
PELGEN2	8	2
PELIGNOR	8	40
PELLAST	8	
PELMAJA	C	
PELMAJAMINA	C	
PELMAJSZ	14	8
PELMILEN	9	
PELMINA	10	
PELNORNL	8	4
PELOCANY	8	20
PELPREFX	0	
PELRET	B	
PELRET1	A	4
PELRET2	A	2
PELRET3	A	1
PELSAVE	8	8
PELSCPE1	A	40
PELSCPE2	A	8
PELSHARE	A	80
PELSHR	8	10
PELSTPMC	A	10
PELSYSMC	A	20
PELTCB	0	
PELTCBF	8	1
PELUCBAA	14	

PFK Information

PFK Heading Information

Common Name: PROGRAM FUNCTION KEY TABLE MAPPING
Macro ID: IEEVC103
DSECT Name: PFKSTAB
Owning Component: DIDOCS (SC1C4)
Eye-Catcher ID: PFKT
Offset: 0
Length: 4
Storage Attributes: Subpool: 230
Key: 0
Residency: ABOVE|BELOW 16 MEGABYTES
Size: PFK TABLE HEADER: 16 BYTES
PFK TABLE: 3108 BYTES
Created by: IEECB817
Pointed to by: BY = UCM PAGEABLE EXTENSION (UCMPPFKT)
Serialization: COMM TASK LOCAL LOCK
Function: MAPS THE PFK TABLE TO BE USED BY OPERATOR CONSOLES

PFK Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	PFKSTAB	PFK TABLE
0	(0)	CHARACTER	16	PFKHEADR	PFK TABLE HEADER
0	(0)	CHARACTER	4	PFKACRO	ACRONYM "PFKT"
4	(4)	UNSIGNED	1	PFKVERSN	VERSION LEVEL
5	(5)	UNSIGNED	1	*	RESERVED
6	(6)	SIGNED	2	PFKTBNUM	NUMBER OF PFK TABLE DEFINITIONS
8	(8)	SIGNED	4	PFKLEN	LENGTH OF PFK TABLE
12	(C)	CHARACTER	2	PFKMEMB	SUFFIX OF PARMLIB MEMBER
14	(E)	CHARACTER	2	*	RESERVED
16	(10)	CHARACTER	12	PFKENTRY (*)	PFK TABLE ENTRIES
16	(10)	CHARACTER	8	PFKTABNM	PFK TABLE NAME
24	(18)	ADDRESS	4	PFKTABPT	PTR TO PFK TABLE DEFINITION

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	3096	PFKTABLE	
0	(0)	CHARACTER	8	PFKTNAME	NAME OF PFK TABLE
8	(8)	UNSIGNED	4	PFKTLEN	LENGTH OF PFK DEFINITIONS
12	(C)	CHARACTER	8	PFKTWORK	WORK AREA FOR THE K N,PFK CMD PROCESSOR
20	(14)	CHARACTER	128	PFKTAB (4294967320:562118568)	
20	(14)	UNSIGNED	1	PFKTKEY	PFK NUMBER
21	(15)	BITSTRING	1	PFKTFLGS	PFK FLAGS
		1...		PFKTDEF	PFK IS DEFINED
		.1..		PFKTPROC	PFK IS BEING PROCESSED
		..1.		PFKTCON	PFK IS CONVERSATIONAL
		...1		*	RESERVED
		.... 1...		PFKTMST	PFK IS A MASTER KEY. PFKTCMD CONTAINS A LIST OF KEYS
22	(16)	CHARACTER	126	PFKTCMD	PFK COMMAND OR KEYS
3092	(C14)	CHARACTER	1	PFKTEND	END OF PFK DEFINITION
3093	(C15)	CHARACTER	3	*	ADJUST TO DOUBLE WORD BNDY

PFK Constants

Len	Type	Value	Name	Description
Comment				
PFK TABLE CONSTANTS				
End of Comment				
4	CHARACTER	PFKT	PFKT	PFK TABLE ACRONYM
4	DECIMAL	24	PFKTKNUM	NUMBER OF PFKS IN TABLE
4	DECIMAL	126	PFKTMAXL	MAXIMUM LENGTH OF COMMAND
1	HEX	64	PFKTBEND	END OF PFK DEFINITIONS INDICATOR
1	CHARACTER	/	PFKTLEND	END OF KEY LIST INDICATOR
1	HEX	5E	PFKTLSEP	KEY LIST SEPARATOR (SEMI-COLON)
4	DECIMAL	230	PFKTSUBP	SUBPOOL FOR PFK TABLE
1	DECIMAL	1	PFKSP220	VERSION LEVEL IS MVS/XA JBB2220
1	DECIMAL	1	PFKTVRID	VERSION LEVEL - UPDATED FOR SIZE OR INCOMPATIBLE CHANGE

PFK Cross Reference

Name	Hex Offset	Hex Value
PFKACRO	0	
PFKENTRY	10	
PFKHEADR	0	
PFKLEN	8	
PFKMEMB	C	
PFKSTAB	0	
PFKTAB	14	
PFKTABLE	0	
PFKTABNM	10	
PFKTABPT	18	
PFKTBNUM	6	
PFKTCMD	16	
PFKTCON	15	20
PFKTDEF	15	80
PFKTEND	C14	
PFKTFLGS	15	
PFKTKEY	14	
PFKTLEN	8	
PFKTMST	15	08
PFKTNAME	0	
PFKTPROC	15	40
PFKTWORK	C	
PFKVERSN	4	

PFTE Information

PFTE Heading Information

Common Name: PAGE FRAME TABLE ENTRY
Macro ID: IARPFTE
DSECT Name: PFTE
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: None
Storage Attributes: Virtual Storage: Yes
Subpool: N/A (See Residency)
Key: 0
Residency: Extended Read/Write Nucleus
Size: 32 Bytes
Created by: RSM Initialization
Pointed to by: PFTFQPTR field of the PFTE Data Area
PFTBQPTR field of the PFTE Data Area
ESTPFTE field of the ESTE Data Area
RITPFTE field of the RIT Data Area
RITLPFTE field of the RIT Data Area
RITFPFTE field of the RIT Data Area
RITPAFQF field of the RIT Data Area
RITPAFQL field of the RIT Data Area
RITNAFQF field of the RIT Data Area
RITNAFQL field of the RIT Data Area
RITPBFQF field of the RIT Data Area
RITPBFQL field of the RIT Data Area
RITNBFQF field of the RIT Data Area
RITNBFQL field of the RIT Data Area
RITTDFQF field of the RIT Data Area
RITTDFQL field of the RIT Data Area
RITBDFQF field of the RIT Data Area
RITBDFQL field of the RIT Data Area
RITSFQF field of the RIT Data Area
RITSFQL field of the RIT Data Area
RITRSFQF field of the RIT Data Area
RITRSFQL field of the RIT Data Area
RITSBFQF field of the RIT Data Area
RITSBFQL field of the RIT Data Area
RITVRFQF field of the RIT Data Area
RITVRFQL field of the RIT Data Area
RITFVR field of the RIT Data Area
RITLVR field of the RIT Data Area
RITNPFTE field of the RIT Data Area
RITPFTEC field of the RIT Data Area
RITSFFQF field of the RIT Data Area
RITSFFQL field of the RIT Data Area
RITSPFQF field of the RIT Data Area
RITSPFQL field of the RIT Data Area
PCBPFTE field of the PCB Data Area
RABPFQF field of the RAB Data Area
RABPFQL field of the RAB Data Area
RABFFQF field of the RAB Data Area
RABFFQL field of the RAB Data Area
RABDFFQF field of the RAB Data Area
RABDFFQL field of the RAB Data Area
Serialization: Varies
Function: Represents a FRAME to RSM

PFTE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	32	PFTE	
0	(0)	ADDRESS	4	PFTFQPTR	FORWARD PFTE QUEUE POINTER
4	(4)	ADDRESS	4	PFTBQPTR	BACKWARD PFTE QUEUE POINTER

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
8	(8)	CHARACTER	1	PFTQID	QUEUE ID FOR CURRENT QUEUE UNLESS THE PFTE IS ON AN AVAILABLE FRAME QUEUE---- 08=>TOP-DOUBLE-FRAME-QUEUE 09=>BOTTOM-DOUBLE-FRAME-QUEUE 21=>SQA-FRAME-QUEUE 22=>RESERVED-SQA-FRAME-QUEUE 23=>REAL-STG-BUF-FRAME-QUEUE 24=>V=R-WAITING-FRAME-QUEUE 25=>General Defer Frame Queue 40=>SHARED-PAGE-FIXED-FR-QUEUE 41=>SHARED-PAGE-PAGEABLE-FR-QU EUE 81=>PAGEABLE-FRAME-QUEUE 82=>FIXED-FRAME-QUEUE 83=>DEFERRED-FREEMAIN-FR-QUEUE A1=>PAGEABLE-DATA-SPACE-FR-QUE A2=>FIXED-DATA-SPACE-FR-QUEUE A3=>DEFERRED-DELETE-FR-QUEUE E0=>PAGEABLE-RDD-FRAME-QUEUE E1=>FIXED-RDD-FRAME-QUEUE E2=>ORPHAN-FRAME-QUEUE F0=>UNQUEUED..DAT-OFF-NUCLEUS F1=>UNQUEUED..READ-ONLY-NUC. F2=>UNQUEUED..READ/WRITE-NUC. F3=>UNQUEUED..RSMDATA FRAME F4=>UNQUEUED..HW-SYSTEM-AREA F5=>UNQUEUED..ABS.-ZERO-FRAME F6=>UNQUEUED..FIXED-LPA/BLDL FD=>A-FLAWED-PFTE FE=>UNQUEUED..UNINITIALIZED FF=>UNQUEUED-PFTE
9	(9)	UNSIGNED	1	PFTUIC	NUMBER OF UPDATE INTERVALS DURING WHICH FRAME WAS NOT REFERENCED
10	(A)	BITSTRING	1	PFTFLGS2	FLAG BYTE 2 (ALLOCATION FLAGS)
		1...		PFTONAFQ	PFTE IS ON AN AFQ
		.1..		PFTPERM	FRAME IS BACKING PERMANENT STG
		..1.		PFTOFFLN	FRAME IS OFF-LINE
		...1		*	RESERVED
		.... 1...		PFTVRWT	FRAME IS WAITING FOR V=R ALLOC.
		.... .1..		PFTVRALC	FRAME IS ALLOCATED TO V=R
		.... ..1.		PFTDREF	FRAME IS BACKING A DREF PAGE
		1		PFTDSPPG	FRAME IS BACKING A DATA SPACE PAGE
11	(B)	BITSTRING	1	PFTFLGS3	FLAG BYTE 3 (MISC. FLAGS)
		1...		PFTIOCUR	I/O IS CURRENT FOR THIS FRAME
		.1..		PFTVRPLT	THIS FRAME IS CURRENTLY POLLUTING THE V=R AREA
		..1.		PFTVIORU	THIS FRAME IS VIO REUSABLE
		...1		PFTVRINT	FRAME IS V=R INTERCEPTED
		.... 1...		PFTOFINT	FRAME IS OFFLINE INTERCEPTED
		.... .1..		PFTNOREC	INTERCEPTED FRAME SUMMARY BIT- THIS FRAME HAS BEEN INTERCEPTED AND SHOULD NOT BE TAKEN UNLESS IT IS SENT TO AN AVAILABLE FRAME QUEUE. ALSO, THE PAGE ASSOCIATED WITH THE FRAME CANNOT BE REVALIDATED WITH A DIFFERENT FRAME IF A REQUEST FOR THE PAGE IS CURRENTLY ON THE DPQ.
		.... ..1.		PFTIOMC	I/O FOR THIS FRAME MUST COMPLETE INTACT. NEITHER THE FRAME NOR THE DATA MAY BE USED UNTIL THE I/O HAS COMPLETED.
		1		PFTNOPRF	FRAME SHOULD NOT BE STOLEN BY GETFRAME PREF STEAL
12	(C)	CHARACTER	4	PFTFCWRD	FIX COUNT WORD. SERIALIZED BY COMPARE AND SWAP. NOTE: PFTFXCT MUST ALWAYS BE THE LOW ORDER HALFWORD OF THIS FIELD FOR ?INC/DECFXCT TO WORK.
12	(C)	CHARACTER	1	PFTFREID	ID OF QUEUE TO WHICH THIS PFTE IS TO BE RETURNED WHEN FREED-- 01=>PREFERRED-ABOVE-AFQ 02=>NON-PREFERRED-ABOVE-AFQ 03=>PREFERRED-BELOW-AFQ 04=>NON-PREFERRED-BELOW-AFQ 08=>TOP-DOUBLE-FRAME-QUEUE 09=>BOTTOM-DOUBLE-FRAME-QUEUE FF=>NON-FREEABLE-PFTE
13	(D)	BITSTRING	1	PFTFLGS1	FLAG BYTE 1 (PHYSICAL FLAGS). SINCE PFTFCWRD IS DECLARED ABNL, PL/AS WILL GENERATE COMPARE AND SWAP INSTRUCTIONS WHEN UPDATING THESE FLAGS.
		1...		PFTPREF	PFTE IS FOR PREFERRED AREA
		.1..		PFTBELOW	PFTE IS FOR REAL BELOW 16M
		..1.		PFTVR	PFTE IS A V=R CANDIDATE
		...1 1...		*	RESERVED
		.... .1..		PFTSRBSC	SRB HAS BEEN SCHEDULED TO DO FRAME DEALLOCATION.
		.... ..1.		PFTNOUNC	NO UNCORRECTABLE ERRORS HAVE OCCURRED WITHIN THE FRAME. THIS BIT IS MEANINGFUL ONLY WHEN PFTBADFR=1.
		1		PFTBADFR	BAD FRAME - DO NOT REALLOCATE
14	(E)	SIGNED	2	PFTFXCT	FIX COUNT FOR THIS FRAME
16	(10)	CHARACTER	4	PFTSER	PFTE SERIALIZATION WORD
16	(10)	BITSTRING	2	PFTSERFL	Flags portion of PftSer
		1...		PFTRDS	THIS PFTE IS SERIALIZED BY AN RSMDS LOCK
		.1..		PFTSPAGE	THIS PFTE IS IN USE FOR A SHARED PAGE AND IS SERIALIZED BY THE RSMAD/XM/CM/ST LOCK OF COMMON. PFTSDH CONTAINS THE ADDRESS OF THE SDH FOR THE SHARED PAGE GROUP
		..1.		PFTLSQA	FRAME IS BACKING SQA OR LSQA
		...1		PFTMEGAROOED	Shared Segment page
		 1111		*	RESERVED
17	(11)	BITSTRING	1	PFTRVTEX	THE RVTE INDEX IF PFTRDS=1
18	(12)	BITSTRING	2	PFTASID	ASID OF CURRENT OR LAST OWNER

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
20	(14)	ADDRESS	4	PFTVSA	VIRTUAL ADDRESS CURRENTLY OR LAST BACKED BY THIS FRAME
20	(14)	UNSIGNED	4	PFTVIORA	VIO DATA SET PAGE REUSE ARGUMENT - VALID IF PFTVIORU=1
20	(14)	ADDRESS	4	PFTSDH	ADDRESS OF SHARED DATA HEADER - VALID IF PFTSPAGE=1
24	(18)	ADDRESS	4	PFTPCB	ADDRESS OF PCB CURRENTLY BEING USED TO DO I/O FOR THIS FRAME (PFTIOCUR=1) OR PCB LAST USED TO DO THE I/O (PFTIOCUR=0)
24	(18)	BITSTRING	4	PFTMEGAR	Shared Segment information
24	(18)	BITSTRING	1	PFTSEGNO	Segment number of the segment backed by this Shared Segment page table
25	(19)	.... 1...		*	Reserved
25	(19)	BITSTRING	2	PFTUDSNX	UDS index for the UDD that was source for the Shared Segment mapped by this page table.
28	(1C)	CHARACTER	4	PFTPROG	DATA SPACE PROGRAMMING WORD. THIS FIELD IS VALID IF PFTDSPPG IS ON AND THE FRAME IS NOT ON THE DDFQ.
28	(1C)	ADDRESS	4	PFTSPE	Address of the SPE for the view which obtained this PFTE. Valid if PFTSPAGE=1
28	(1C)	ADDRESS	4	PFTTCB	ADDRESS OF THE OWNING TCB IF THE FRAME IS ON THE DDFQ
28	(1C)	ADDRESS	4	PFTESTE	ADDRESS OF THE ESTE FOR THE E-FRAME THAT MAY CONTAIN ANOTHER COPY OF THE DATA IN THIS FRAME

PFTE Constants

Len	Type	Value	Name	Description
		Comment		
		PFTE QUEUE IDS When adding a QID, examine IARQL for possible hits		
		End of Comment		
1	HEX	01	PFTPAFQN	PREFERRED ABOVE AFQ
1	HEX	02	PFTNAFQN	NON-PREFERRED ABOVE AFQ
1	HEX	03	PFTPBFQN	PREFERRED BELOW AFQ
1	HEX	04	PFTNBFQN	NON-PREFERRED BELOW AFQ
1	HEX	08	PFTTDFQN	TOP DOUBLE FRAME QUEUE
1	HEX	09	PFTBDFQN	BOTTOM DOUBLE FRAME QUEUE
1	HEX	21	PFTSFQN	SQA FRAME QUEUE
1	HEX	22	PFTRSFQN	RESERVED SQA FRAME QUEUE
1	HEX	23	PFTSBFQN	REAL STG BUFFER FRAME QUEUE
1	HEX	24	PFTVRFQN	V=R WAITING FRAME QUEUE
1	HEX	25	PFTGDFQN	General Defer Frame Queue
1	HEX	40	PFTSFFQN	SHARED PAGE FIXED FRAME QUEUE
1	HEX	41	PFTSPFQN	SHARED PAGE PAGEABLE FRAME QUEUE
1	HEX	81	PFTPFQN	PAGEABLE FRAME QUEUE
1	HEX	82	PFTFFQN	FIXED FRAME QUEUE
1	HEX	83	PFTDFFQN	DEFERRED FREEMAIN FRAME Q
1	HEX	A1	PFTPDFQN	PAGEABLE DATA SPACE FQ
1	HEX	A2	PFTFDFQN	FIXED DATA SPACE FQ
1	HEX	A3	PFTDDFQN	DEFERED DELETE FRAME Q
1	HEX	E0	PFTPRFQN	PAGEABLE RDD FRAME Q
1	HEX	E1	PFTFRFQN	FIXED RDD FRAME QUEUE
1	HEX	E2	PFTOFQN	ORPHAN FRAME QUEUE
1	HEX	F0	PFTDONN	UNQUEUED- DAT-OFF NUCLEUS
1	HEX	F1	PFTRONN	UNQUEUED- READ ONLY NUC.
1	HEX	F2	PFTRWNN	UNQUEUED- READ/WRITE NUC.
1	HEX	F3	PFTIPCN	UNQUEUED- RSM IPCS USE ONLY
1	HEX	F4	PFTHSAN	UNQUEUED- HW SYSTEM AREA
1	HEX	F5	PFTAZN	UNQUEUED- ABSOLUTE ZERO FR
1	HEX	F6	PFTFXAN	UNQUEUED- FIXED LPA
1	HEX	FC	PFTSADN	RESERVED FOR STAND ALONE DUMP
1	HEX	FD	PFTFLAWN	UNQUEUED- PFTE WAS FOUND FLAWED DURING RECOVERY
1	HEX	FE	PFTUNIN	UNQUEUED- UNINITIALIZED
1	HEX	FF	PFTUNQDN	UNQUEUED
1	HEX	FF	PFTNOFRN	WHEN IN THE PFTFREID FIELD - THIS PFTE CANNOT BE FREED
1	HEX	07	PFTAFQMK	HIGHEST POSSIBLE AVAILABLE FRAME QUEUE ID.
1	HEX	20	PFTRITMK	LOWEST POSSIBLE RIT BASED QUEUE ID (EXCLUDING AFQS AND DOUBLE FRAME QUEUES).
1	HEX	2F	PFTGLMK	Highest possible queue id for a PFTE serialized by the RSMGL lock.
1	HEX	80	PFTRABMK	LOWEST POSSIBLE QUEUE ID FOR AN ADDRESS SPACE RELATED QUEUE (RAB, DAB, OR RDD BASED FRAME QUEUE).
1	HEX	A0	PFTDABMK	LOWEST POSSIBLE QUEUE ID FOR A DAB BASED FRAME QUE
1	HEX	E0	PFTRDDML	LOWEST POSSIBLE QUEUE ID FOR AN RDD BASED FRAME QUE
1	HEX	E7	PFTRDDMH	HIGHEST POSSIBLE QUEUE ID FOR AN RDD BASED FRAME QUE
1	HEX	F0	PFTUNQMK	LOWEST ID POSSIBLE FOR AN UNQUEUED PFTE.

PFTE Cross Reference

Len	Type	Value	Name	Description
Comment				
	PFTE CONSTANTS			
End of Comment				
1	HEX	FE	PFTKMUIC	MAXIMUM UIC VALUE
1	HEX	FF	PFTKBUIC	UIC VALUE USED TO INDICATE A BLOCKED PAGE THAT HAS NEVER BEEN REFERENCED

PFTE Cross Reference

Name	Hex Offset	Hex Value
PFTASID	12	
PFTBADFR	D	01
PFTBELOW	D	40
PFTBQPTR	4	
PFTDREF	A	02
PFTDSPPG	A	01
PFTE	0	
PFTESTE	1C	
PFTFCWRD	C	
PFTFLGS1	D	
PFTFLGS2	A	
PFTFLGS3	B	
PFTFQPTR	0	
PFTFREID	C	
PFTFXCT	E	
PFTIOCUR	B	80
PFTIOMC	B	02
PFTLSQA	10	20
PFTMEGAR	18	
PFTMEGAROOED	10	10
PFTNOPRF	B	01
PFTNOREC	B	04
PFTNOUNC	D	02
PFTOFFLN	A	20
PFTOFINT	B	08
PFTONAFQ	A	80
PFTPCB	18	
PFTPERM	A	40
PFTPREF	D	80
PFTPROG	1C	
PFTQID	8	
PFTRDS	10	80
PFTRVTEX	11	
PFTSDH	14	
PFTSEGNO	18	
PFTSER	10	
PFTSERFL	10	
PFTSPAGE	10	40
PFTSPE	1C	
PFTSRBSC	D	04
PFTTCB	1C	
PFTUDSNX	19	
PFTUIC	9	
PFTVIORA	14	
PFTVIORU	B	20
PFTVR	D	20
PFTVRALC	A	04
PFTVRINT	B	10
PFTVRPLT	B	40
PFTVRWT	A	08
PFTVSA	14	

PICA Information

PICA Programming Interface information

Programming Interface information

PICA

End of Programming Interface information

PICA Heading Information

Common Name: Program Interrupt Control Area
Macro ID: IHAPICA
DSECT Name: PICA
Owning Component: Recovery Termination Manager (SCRTM)
Eye-Catcher ID: None
Storage Attributes: Subpool: User
Key: User
Size: 8 bytes
Created by: A PICA is created and initialized by the executable code produced by the expansion of the SPIE macro during an assembly of the source program.
Pointed to by: PIEPICA field of the PIE data area
Serialization: Local Lock and Task Active mode
Function: Contains: a) The program mask to be used in the PSW.
b) The user SPIE exit routine address.
c) The interruption mask which identifies the program check interruptions which the user SPIE exit routine will service.

PICA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PICA	
0	(0)	SIGNED	4	PICAEXIT (0)	-
0	(0)	BITSTRING	1	PICAPRMK	- PROGRAM MASK TO BE USED IN THE PSW - BITS 0-3 ARE ZERO; BITS 4-7 CONTAIN MASK
1	(1)	ADDRESS	3	PICEXITA	- ADDRESS OF THE USER'S PROGRAM INTER- RUPTION EXIT RTN
4	(4)	SIGNED	4	PICAITMK (0)	- MASK WHICH INDICATES ON WHICH PROGRAM INTERRUPTION TYPES THE EXIT RTN IS TO BE USED - LENGTH IS 4 BYTES.
4	(4)	BITSTRING	1	PICITMK1	-
		1...		PICAEXT	"X'80'" - AN EXTENDED PICA IS IN EFFECT
		.1..		PICACD1	"X'40'" - OPERATION
		..1.		PICACD2	"X'20'" - PRIVILEGED OPERATION
		...1		PICACD3	"X'10'" - EXECUTE
		.... 1...		PICACD4	"X'08'" - PROTECTION
		.... .1..		PICACD5	"X'04'" - ADDRESSING
		.... ..1.		PICACD6	"X'02'" - SPECIFICATION
		1		PICACD7	"X'01'" - DATA INTRPT HANDLED
5	(5)	BITSTRING	1	PICITMK2	-
		1...		PICACD8	"X'80'" - FIXED-POINT OVERFLOW
		.1..		PICACD9	"X'40'" - FIXED-POINT DIVIDE
		..1.		PICACD10	"X'20'" - DECIMAL OVERFLOW
		...1		PICACD11	"X'10'" - DECIMAL DIVIDE
		.... 1...		PICACD12	"X'08'" - EXPONENT OVERFLOW
		.... .1..		PICACD13	"X'04'" - EXPONENT UNDERFLOW
		.... ..1.		PICACD14	"X'02'" - SIGNIFICANCE
		1		PICACD15	"X'01'" - FLOATING-POINT DIVIDE
6	(6)	BITSTRING	1	PICITMK3	-
		.1..		PICACD17	"X'40'" - PAGE TRANSLATION
7	(7)	BITSTRING	1	PICITMK4	-

PICA Cross Reference

Name	Hex Offset	Hex Value
PICA	0	
PICACD1	4	40
PICACD10	5	20
PICACD11	5	10
PICACD12	5	8
PICACD13	5	4
PICACD14	5	2
PICACD15	5	1
PICACD17	6	40
PICACD2	4	20
PICACD3	4	10
PICACD4	4	8
PICACD5	4	4
PICACD6	4	2
PICACD7	4	1
PICACD8	5	80
PICACD9	5	40
PICAEXIT	0	
PICAEXT	4	80
PICAITMK	4	
PICAPRMK	0	
PICEXITA	1	
PICITMK1	4	
PICITMK2	5	
PICITMK3	6	
PICITMK4	7	

PIE Information

PIE Programming Interface information

Programming Interface information

PIE

End of Programming Interface information

PIE Heading Information

Common Name: Program Interruption Element
Macro ID: IHAPIE
DSECT Name: PIE
Owning Component: Recovery Termination Manager (SCRTM)
Eye-Catcher ID: None
Storage Attributes: Subpool: 130 or 250
Key: TCB Key
Size: 32 bytes
Created by: IEAVTESP
Pointed to by: Register 1 upon entry to a SPIE exit routine. Also can be found via the TCBPIE field of the IKJTCB data area.
Serialization: Task Active
Function: The PIE is used to pass program interruption information to a SPIE exit routine.

PIE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PIE	
0	(0)	X'80'	0	BIT0	"128"
0	(0)	X'40'	0	BIT1	"64"
0	(0)	X'20'	0	BIT2	"32"
0	(0)	X'10'	0	BIT3	"16"
0	(0)	X'8'	0	BIT4	"8"
0	(0)	X'4'	0	BIT5	"4"
0	(0)	X'2'	0	BIT6	"2"
0	(0)	X'1'	0	BIT7	"1"
0	(0)	SIGNED	4	PIEPICA (0)	- ADDRESS OF THE CURRENT PICA
0	(0)	BITSTRING	1	PIEFLGS	- FLAG BYTE
		1...		PIENOPI	"BIT0" - IF ONE, INDICATES THAT THE TASK CANNOT ACCEPT FURTHER PI'S
1	(1)	ADDRESS	3	PIEPICAA	- ADDRESS OF THE CURRENT PICA
4	(4)	CHARACTER	8	PIEPSW	- BC MODE PSW STORED AT PROGRAM INTERRUPT TIME @P1C
12	(C)	SIGNED	4	PIEGR14	- SAVE AREA FOR REGISTER 14
16	(10)	SIGNED	4	PIEGR15	- SAVE AREA FOR REGISTER 15
20	(14)	SIGNED	4	PIEGR0	- SAVE AREA FOR REGISTER 0
24	(18)	SIGNED	4	PIEGR1	- SAVE AREA FOR REGISTER 1
28	(1C)	SIGNED	4	PIEGR2	- SAVE AREA FOR REGISTER 2

PIE Cross Reference

Name	Hex Offset	Hex Value
BIT0	0	80
BIT1	0	40
BIT2	0	20
BIT3	0	10
BIT4	0	8
BIT5	0	4
BIT6	0	2
BIT7	0	1
PIE	0	
PIEFLGS	0	
PIEGR0	14	
PIEGR1	18	
PIEGR14	C	
PIEGR15	10	
PIEGR2	1C	
PIENOPI	0	80
PIEPICA	0	
PIEPICAA	1	
PIEPSW	4	

PPD Information

PPD Heading Information

Common Name: Primary Pool Descriptor (VSM Cell Pool)
Macro ID: IGVPPD
DSECT Name: PPD
Owning Component: VSM (SC1CH)
Eye-Catcher ID: PPD
Offset: 0
Length: 4
Storage Attributes: Residency: ESQA or ELSQA, Above 16M line
Size: PPD -- X'0038' bytes
SUBPOOL & KEY 245 OR 255, KEY 0
STORAGE ESTIMATE 1 PER CELL POOL
Created by: IGVCPBLD
Pointed to by: GDAPPDFX, GDAPPDPG, LDAPPD, PPDNEXT, PXTPPD
Serialization: CML/LOCAL, VSMPAG, OR VSMFIX LOCK
Function: This block contains information and base pointers for a cell pool built using the CPOOL VSM service.

PPD Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	56	PPD	
0	(0)	CHARACTER	4	PPDID	CONTROL BLOCK IDENTIFIER
4	(4)	ADDRESS	4	PPDCPID	CELL POOL ID
4	(4)	ADDRESS	4	PPDPXT	ADDRESS OF PRIMARY EXTENT
8	(8)	ADDRESS	4	PPDSPD	POINTER TO TOP SPD IN LIFO QUEUE
8	(8)	ADDRESS	4	PPDMCPA	POINTER TO MULTIHDR CPOOL ANCHOR BLOCK
12	(C)	SIGNED	4	PPDPCNT	PRIMARY CELL COUNT
16	(10)	SIGNED	4	PPDSCNT	SECONDARY CELL COUNT
20	(14)	SIGNED	2	PPDSPID	HALFWORD SUBPOOL ID
20	(14)	CHARACTER	1	*	RESERVED
21	(15)	UNSIGNED	1	PPDSP	SUBPOOL ID
22	(16)	CHARACTER	1	PPDKEY	PROTECTION KEY (IN BITS 4-7)
23	(17)	CHARACTER	1	PPDFLGS	FLAG FIELD
		1...		PPDRLOC	WHEN 1, INDICATES THE REAL(ANY) OPTION OF LOC WAS SPECIFIED
		.11.		PPDVLOC	WHEN 00, LOC=RES. WHEN 01, LOC= BELOW. WHEN 11, LOC=ANY
		...1		PPDTCBF	WHEN 1 TCB WAS SPECIFIED
		.... 1...		PPDKEYF	WHEN 1 KEY WAS SPECIFIED
		.... .1..		PPDHDRF	WHEN 1 HDR WAS SPECIFIED
		11		PPDOWNR	00=Home, 01=Primary, 11=System
24	(18)	ADDRESS	4	PPDTCB	TCB ADDRESS
28	(1C)	SIGNED	4	PPDCSIZE	CELL SIZE
32	(20)	SIGNED	4	PPDPSIZE	SIZE OF PRIMARY EXTENT
36	(24)	SIGNED	4	PPDSSIZE	SIZE OF SECONDARY EXTENT
40	(28)	ADDRESS	4	PPDASCB	ASCB ADDRESS
44	(2C)	ADDRESS	4	PPDNEXT	POINTER TO NEXT PPD ON LDA OR GDA PPD CHAIN
48	(30)	SIGNED	4	PPDINDX	INDEX OF MOST RECENT ENTRY IN MOST RECENT SPD
48	(30)	ADDRESS	4	PPDMPE@	Address of Most recently used MPE for multihdr CPOOL
52	(34)	CHARACTER	1	PPDFLGS2	
		1...		PPDRLO64	WHEN 1, INDICATES REAL(ANY64) OPTION OF LOC WAS SPECIFIED
		.1..		PPDQWORD	When 1, indicates that pool elements need to be on QWORD boundary. It is assumed that the cell size is a multiple of 16
		..1.		PPDPRSRV	When 1, indicates that pool element contents need to be preserved. On initial allocation, they will be zeroed. Anything placed into an element beyond the first 16 bytes will remain unchanged by VSM.
		...1		PPDMULTI	When 1, Indicates MultiHdr Type of pool
		.... 1...		PPDCELSHR	When 1, Indicates MultiHdr Type allowing free cells to be shared between processors
		.... .1..		PPDPGFM1M	When 1, Indicates that LOC=(xx,PAGEFRAMESIZE1MB) was specified on CPOOL BUILD
53	(35)	CHARACTER	3	*	FOR DOUBLEWORD BOUNDARY

PPD Constants

Len	Type	Value	Name	Description
0	BIT	00	PPDOWNR_HOME	
0	BIT	01	PPDOWNR_PRIMARY	
0	BIT	11	PPDOWNR_SYSTEM	

PPD Cross Reference

Name	Hex Offset	Hex Value
PPD	0	
PPDASCB	28	
PPDCELSHR	34	08
PPDCPID	4	
PPDCSIZE	1C	
PPDFLGS	17	
PPDFLGS2	34	
PPDHDRF	17	04
PPDID	0	
PPDINDX	30	
PPDKEY	16	
PPDKEYF	17	08
PPDMCPA	8	
PPDMPE@	30	
PPDMULTI	34	10
PPDNEXT	2C	
PPDOWNR	17	03
PPDPCNT	C	
PPDPGFM1M	34	04
PPDPRSRV	34	20
PPDPSIZE	20	
PPDPXT	4	
PPDQWORD	34	40
PPDRLOC	17	80
PPDRLO64	34	80
PPDSCNT	10	
PPDSP	15	
PPDSPD	8	
PPDSPID	14	
PPDSSIZE	24	
PPDTCB	18	
PPDTCBF	17	10
PPDVLOC	17	60

PPT Information

PPT Programming Interface information

Programming Interface information

PPT

The following fields are **NOT** programming interface information:

- PPT
- PPTCVERS
- PPTENTLN
- PPTENTS
- PPTHDR
- PPTHDRLN
- PPTIB650
- PPTID
- PPTMSGAD
- PPTOLD
- PPTUSED
- PPTVERS

End of Programming Interface information

PPT Heading Information

Common Name: Program Properties Table Mapping Macro
Macro ID: IEFZB610
DSECT Name: PPT, PPT1
Owning Component: Initiator/Subsystem Interface (SC1B6)
Eye-Catcher ID: - PPT: 'PPT '
- PPT1: None
Offset: - PPT: 0
- PPT1: n/a
Length: - PPT: 4 bytes
- PPT1: n/a
Storage Attributes: Subpool: 231 (common)
Key: 0
Size: - PPT: 32 bytes
- PPT1: 16 bytes
FREQUENCY:
- PPT: 1 per MVS Image
- PPT1: 1 per program property table entry
Created by: IEFPPT - Program properties statement processor
Pointed to by: The IEFPPSCN macro should be used to access the PPT table entries.
Serialization: Use of macro IEFPPSCN will protect the user from updates to the table as a result of a SET SCH= command.
Function: Mapping of the Program Properties Table Header and Table Entries

PPT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PPT	
0	(0)	CHARACTER	32	PPTHDR (0)	
0	(0)	CHARACTER	4	PPTID	IDENTIFIER 'PPT '
4	(4)	BITSTRING	1	PPTVERS	VERSION NUMBER
		1		PPTCVERS	"X'01'" CURRENT VERSION NUMBER
5	(5)	BITSTRING	1		RESERVED
6	(6)	SIGNED	2	PPTHDRLN	LENGTH OF PPT HEADER
8	(8)	SIGNED	2	PPTENTLN	LENGTH OF A PPT ENTRY
10	(A)	SIGNED	2	PPTUSED	NUMBER OF USED PPT ENTRIES
12	(C)	SIGNED	2	PPTENTS	TOTAL NUMBER OF PPT ENTRIES
14	(E)	SIGNED	2		RESERVED
16	(10)	BITSTRING	4	PPTMSGAD	ADDRESS OF MSGAREA USED IN IEFPPT
20	(14)	BITSTRING	4	PPTIB650	ADDRESS OF MESSAGE MODULE IEFIB650
24	(18)	ADDRESS	4	PPTOLD	ADDRESS OF NEXT LOGICALLY DELETED PPT
28	(1C)	CHARACTER	4		RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PPT1	
0	(0)	CHARACTER	16	PPTENTRY (0)	MAPPING OF A PPT ENTRY
0	(0)	CHARACTER	8	PPTNAME	PROGRAM NAME
8	(8)	CHARACTER	1	PPTBYTE1	FIRST BYTE OF PROPERTIES
		1...		PPTNCNCL	"X'80'" THIS PROGRAM IS NON-CANCELABLE
		.1..		PPTSKEY	"X'40'" THIS PROGRAM REQUIRES THE SPECIAL PROTECT Y02656 KEY IN PPTKEY Y02656
		..1.		PPTNSWP	"X'20'" THIS PROGRAM IS TO BE AUTHORIZED TO BE Y02669 NON-SWAPPABLE Y02669
		...1		PPTPRIV	"X'10'" THIS PROGRAM IS TO BE 'PRIVILEGED' (WITH Y02655 RESPECT TO THE SYSTEM RESOURCES MANAGER) Y02655
		.... 1...		PPTSYSTK	"X'08'" THIS PROGRAM IS A SYSTEM TASK Y02652
		.... .1..		PPTNDSI	"X'04'" THIS PROGRAM IS NOT TO BE GIVEN DATA SET Y02652 INTEGRITY Y02652
		.... ..1.		PPTNOPAS	"X'02'" BYPASS PASSWORD PROTECTION Y02656
		1		PPTNHUSI	"X'01'" Region and MEMLIMIT values and limits set or affected by the IEFUSI exit are not honored for the program. Set to on when PPT option NOHONORIEFUSIREGION is specified in SCHEDxx. This bit is also set explicitly for some program entries in the IBM default PPT table IEFSDPPT.

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					THE REMAINING BIT IS RESERVED
					End of Comment
9	(9)	CHARACTER	1	PPTKEY	- THIS KEY IS TO BE GIVEN TO THE PROGRAM Y02656 BEING ATTACHED IF PPTSKEY IS ON Y02656
					Comment
					THE EQUATES FOR PROTECT KEYS FOLLOW, TO INSURE THAT Y02651 PROTECT KEYS ARE DEFINED AS THE HIGH ORDER 4 BITS OF Y02651 THE BYTE. KEYS 9-15 ARE FOR V=R PROGRAMS and Key 9 is only used when the Subpool Override is not enabled.
					End of Comment
		....		PPTKEY0	"X'00'" Y02651
		...1		PPTKEY1	"X'10'" Y02651
		..1.		PPTKEY2	"X'20'" Y02651
		..11		PPTKEY3	"X'30'" Y02651
		.1..		PPTKEY4	"X'40'" Y02651
		.1.1		PPTKEY5	"X'50'" Y02651
		.11.		PPTKEY6	"X'60'" Y02651
		.111		PPTKEY7	"X'70'" Y02651
		1...		PPTKEY8	"X'80'" Y02651
		1..1		PPTKEY9	"X'90'"
		1.1.		PPTKEYA	"X'A0'"
		1.11		PPTKEYB	"X'B0'"
		11..		PPTKEYC	"X'C0'"
		11.1		PPTKEYD	"X'D0'"
		111.		PPTKEYE	"X'E0'"
		1111		PPTKEYF	"X'F0'"
10	(A)	BITSTRING	2	PPTCPUA	BIT MASK OF CPU'S ON WHICH THIS PROGRAM CAN Y02669 RUN (SHOULD BE X'FFFF' IF AFFINITY IS NOT Y02669 REQUIRED) Y02669
12	(C)	SIGNED	4	PPTFLGS (0)	FLAG BYTES
12	(C)	CHARACTER	1	PPTPUBYT	Miscellaneous Flags
		1...		PPT2LPU	"X'80'" 2ND LEVEL PREFERRED USAGE
		.1..		PPT1LPU	"X'40'" 1ST LEVEL PREFERRED USAGE
		..1.		PPTN2LP	"X'20'" NOT 2ND LEVEL PREFERRED USAGE
		1		PPTCRPG	"X'01'" CRITICALPAGING specified - program is critical to Hyperswap operation and should not allow any of its pages to be stolen
					Comment
					THE REMAINING BITS ARE RESERVED
					End of Comment
13	(D)	CHARACTER	1	PPTORIG	PPT ENTRY ORIGIN
		1...		PPTDEFLT	"X'80'" FROM IBM SUPPLIED DEFAULT TABLE
					Comment
					THE REMAINING BITS ARE RESERVED
					End of Comment
14	(E)	CHARACTER	2		RESERVED
14	(E)	X'10'	0	PPTLEN	"*-PPT1" LENGTH OF AN ENTRY

PPT Cross Reference

Name	Hex Offset	Hex Value
PPT	0	
PPTBYTE1	8	
PPTCPUA	A	
PPTCRPG	C	1
PPTCVERS	4	1
PPTDEFLT	D	80
PPTENTLN	8	
PPTENTRY	0	
PPTENTS	C	
PPTFLGS	C	
PPTHDR	0	
PPTHDRLN	6	
PPTIB650	14	
PPTID	0	
PPTKEY	9	
PPTKEYA	9	A0
PPTKEYB	9	B0
PPTKEYC	9	C0
PPTKEYD	9	D0
PPTKEYE	9	E0
PPTKEYF	9	F0
PPTKEY0	9	0
PPTKEY1	9	10
PPTKEY2	9	20
PPTKEY3	9	30
PPTKEY4	9	40
PPTKEY5	9	50
PPTKEY6	9	60
PPTKEY7	9	70
PPTKEY8	9	80
PPTKEY9	9	90
PPTLEN	E	10
PPTMSGAD	10	
PPTNAME	0	
PPTNCNCL	8	80
PPTNDSI	8	4
PPTNHUSI	8	1
PPTNOPAS	8	2
PPTNSWP	8	20
PPTN2LP	C	20
PPTOLD	18	
PPTORIG	D	
PPTPRIV	8	10
PPTPUBYT	C	
PPTSKEY	8	40
PPTSYSTK	8	8
PPTUSED	A	
PPTVERS	4	
PPT1	0	
PPT1LPU	C	40
PPT2LPU	C	80

PRA Information

PRA Heading Information

Common Name: Page Service Protect/Unprotect Recording Area (Audit Trail Block)
Macro ID: IARPRA
DSECT Name: PRA
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: None
Storage Attributes: Virtual Storage: Yes
Subpool: 245, ESQA (Fixed Common)
Key: 0
Residency: Anywhere in virtual storage
Size: 40 bytes
Created by: IARPYPRO
Pointed to by: RCEPRTBL
Serialization: Compare and Swap
Function: This control block maps the area set aside for recording information about the issuance of PGSER PROTECT and UNPROTECT by authorized callers.

PRA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	40	PRA	Label for PRA
0	(0)	ADDRESS	4	PRASADDR	Start address of the range
4	(4)	ADDRESS	4	PRAEADDR	End address of the range
8	(8)	CHARACTER	8	PRATIMES	TimeStamp
16	(10)	CHARACTER	8	PRAJOBNM	Jobname
24	(18)	CHARACTER	8	PRAUSER	User ID
32	(20)	ADDRESS	4	PRATCB	TCB Address or 0 for SRB Mode
36	(24)	SIGNED	2	PRAASID	ASID
38	(26)	UNSIGNED	1	PRAFUNC	Function Code (see IHAPSL)
39	(27)	CHARACTER	1	*	Reserved
40	(28)	CHARACTER	0	PRAFINIS	This is the end of the PRA

PRA Cross Reference

Name	Hex Offset	Hex Value
PRA	0	
PRAASID	24	
PRAEADDR	4	
PRAFINIS	28	
PRAFUNC	26	
PRAJOBNM	10	
PRASADDR	0	
PRATCB	20	
PRATIMES	8	
PRAUSER	18	

PRMESTAE Information

PRMESTAE Heading Information

Common Name: MAPPING MACRO FOR COMMON ALLOCATION ESTAE PARMS
Macro ID: IEFZB447
DSECT Name: PRMESTAE
Owning Component: Allocation (SC1B4)
Eye-Catcher ID: None
Storage Attributes: Subpool: 230
Key: Key 1
Residency: Above (32-bit virtual,64-bit real)
Size: 512 Bytes (20,480 for entire structure including Autodata area)
Created by: IEFAB421, IEFAB4C2, IEFAB4C4
Pointed to by: EXITPRMP in the ALCWA
Access the Autodata address via EXITPRMP->PRMEAUTO_PTR.
Note: The Autodata area is pointed to by field PRMEAUTO_PTR in the PRMESTAE structure. The buffer zones in the structure mapping the entire area are not pointed to, and are not intended to be used.
Serialization: None
Function: THIS PARAMETER LIST IS CREATED AND INITIALIZED BY IEFAB421, and also by service modules IEFAB4C2 and IEFAB4C4, AND SUBSEQUENTLY, UPDATED BY VARIOUS ALLOCATION MODULES. THE LIST SERVES AS INPUT TO THE COMMON ALLOCATION ESTAE ROUTINE (IEFAB4E8) IF AN ABEND OCCURS DURING COMMON ALLOCATION PROCESSING. IT ALSO SUPPLIES SOME INFORMATION TO THE UPDATE UCB FRR ROUTINE (IEFAB4E6). THIS LIST CONTAINS FLAGS INDICATING WHAT RESOURCES ARE HELD AT THE TIME OF THE ABEND AND WHAT CLEANUP FUNCTIONS ARE TO BE PERFORMED. IT ALSO SUPPLIES PTRS AND OTHER INFORMATION NEEDED TO PERFORM THE CLEANUP. THE AUTOMATIC DATA AREA SUPPLIED IN THIS LIST IS USED BY IEFAB4E8. This macro includes the mapping for the parameter list itself, the Autodata area for ESTAE Exit Routines to use, and a structure mapping the entire area to storage obtain.

PRMESTAE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	512	PRMESTAE	ESTAE PARAMETER LIST
0	(0)	ADDRESS	4	PRMEAUTO_PTR	Pointer to Autodata area for exit routine
4	(4)	BITSTRING	1	PRMRESC	RESOURCES HELD
		1...		ENQQ4	ENQUEUED ON Q4
		.1..		DDRQ	ENQUEUED ON DDR QUEUE
		..1.		CHNGQ	ENQUEUED ON CHANGE QUEUE
		...1		DSSTAP	DSS TAPE BIT
		.... 1...		DSSUNREC	DSS UNIT RECORD BIT
		.... .1..		TPQ	ENQUEUED ON TP QUEUE
		.... ..1.		MLWTO	DOM MULTILINE WTO
		1		PENDFOOT	PENDING PROCESS ACTIVE
5	(5)	BITSTRING	1	PRMFUNC	FUNCTIONS NEEDED
		1...		GENCLNUP	CALL GENERIC CLEANUP RTN
		.1..		UPDSABCK	ZERO SIOT DSAB PTR
		..1.		DSABCHN	Fix up below the line DSAB chain
		...1		TIOTBLT	TIOT ENTRY BUILT
		.... 1...		DUMPOK	
		.... .1..		FREECORE	FREE QUEUE MANAGER BLOCK
		.... ..1.		VMVCALL	CALL VM&V CLEANUP RTN
		1		PRMRETRY	RETRY REQUESTED
6	(6)	SIGNED	2	PRMASID	ASID
8	(8)	ADDRESS	4	PRMSIOTP	SIOT PTR
12	(C)	ADDRESS	4	PRMUCBP	UCB PTR
16	(10)	ADDRESS	4	PRMQMGP	PTR TO Q-MGR PARMS
20	(14)	SIGNED	4	PRMQMBLN	LENGTH OF Q-MGR BLOCK
24	(18)	ADDRESS	4	PRMQMBP	PTR TO Q-MGR BLOCK TO FREE
28	(1C)	ADDRESS	4	PRMQDBP	PTR TO DSAB QDB
32	(20)	ADDRESS	4	PRMDSQL	Ptr to last DSAB in below the line DSAB queue
36	(24)	ADDRESS	4	PRMDSQF	Ptr to first DSAB in below the line DSAB queue
40	(28)	SIGNED	4	PRMNELM	NO, ELEMENTS IN DSAB QUEUE
44	(2C)	SIGNED	4	PRMWTOID	DOM ID OF MULTILINE WTO

PRMESTAE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
48	(30)	ADDRESS	4	PRMAERBP	PTR TO AERB
52	(34)	ADDRESS	4	PRMJSCBP	PTR TO JSCB
56	(38)	BITSTRING	1	PRMFUNC2	FUNCTION REQUIRED
		1...		WRTBUF	WRITE MESSAGE BUFFER
		.1..		VSERSTOR	Free the storage obtained for the VOLSER table via the GETMAIN macro
		..1.		OFFLSTOR	Free the storage obtained for the Device Offline table via the GETMAIN macro
		...1		MSGBUFF	Free the message buffer obtained via IEEMIFSV
		.... 1...		PRMGPMSK	Indicates that the Group Mask storage must be freed

Comment

Deleted PRMDSTBQ Removed SYSZDSTB ENQ/DEQ support.

End of Comment

		.... .1..		DSABCHA	Fix up above or below the line DSAB chain
		.... ..1.		ALCXTCALLED	Indicates that IEF_ALLC_EVENT exit has been called at the beginning of Allocation in IEFAB421.
		1		*	Not used and available
57	(39)	BITSTRING	1	PRMFOOTS	Footprints are dumped to VRA by IEFAB4ED.
		1...		PRMSECC	Security system has been called when set.
		.1..		PRMSECR	Security system has returned when set.
		..11 1111		*	Reserved.
58	(3A)	BITSTRING	1	PRMRESC2	Resources held #2
		1...		ENQVDEV	Enqueued on VARYDEV. Set by IEFAB488, IEFAB4E8. Used by IEFAB4E8
		.1..		PRM_LOCALOCK	Local lock held
		..1.		PRM_CMSLOCK	CMS Lock held
		...1 1111		*	Reserved.
59	(3B)	CHARACTER	1	*	Reserved.
60	(3C)	ADDRESS	4	PRMALCWA	ADDR OF ALCWA
64	(40)	ADDRESS	4	PRMASPTR	Anchor of the ATS Service Rtn Recovery Blocks - it points to the newest block on the chain
68	(44)	ADDRESS	4	PRMVADDR	Address of the storage obtained for the VOLSER table
72	(48)	SIGNED	4	PRMVSIZE	Size of the storage obtained for the VOLSER table
76	(4C)	ADDRESS	4	PRMOADDR	Address of the storage obtained for the Offline Device table
80	(50)	SIGNED	4	PRMOSIZE	Size of the storage obtained for the Offline Device table
84	(54)	ADDRESS	4	PRMMBTKN	Message buffer token
88	(58)	CHARACTER	2	PRMNUM_WTOID	Number of IEF877E messages issued by IEFAB48A that need to be DOMed (PRMWTOID_Table_ Ptr points to the array of message IDs).
90	(5A)	UNSIGNED	2	PRMARRAY_LENGTH	Length of DOMID array
92	(5C)	ADDRESS	4	PRMWTOID_TABLE_PTR	Pointer to the array of message IDs that need to be DOMed (built by IEFAB48A)
96	(60)	ADDRESS	4	PRMCUCBP	Pointer to the Captured UCB (filled in by IEFAB428 if the UCB to be replaced for SMS was previously captured and needs to be uncaptured)
100	(64)	UNSIGNED	4	PRMGMSIZ	Size of the Group Mask
104	(68)	ADDRESS	4	PRMGMPTR	Pointer to the Group Mask
108	(6C)	ADDRESS	4	PRMDSQLA	Ptr to last DSAB in above or below the line DSAB queue
112	(70)	ADDRESS	4	PRMDSQFA	Ptr to first DSAB in above or below the line DSAB queue
116	(74)	SIGNED	4	PRMNELA	Number of elements on the above or below the line DSAB queue
120	(78)	CHARACTER	392	*	Reserved - Keep PRMESTAE 512 bytes in length (space is available for use).
512	(200)	CHARACTER	0	*	Finish on DWORD Bdy

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4	MSGIDS (*)	Array of message ids that need to be DOMed
0	(0)	SIGNED	4	MSG_ID	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20480	PRMESTAE_AREA	Structure to Getmain containing PRMESTAE parm list and ESTAE Exit Autodata storage on PAGE boundary
0	(0)	CHARACTER	512	PRMESTAE_PARMAREA	Area for actual PRMESTAE parameter list
512	(200)	CHARACTER	3584	PRMESTAE_BUFFER	Buffer zone between Parm area and Autodata area, so as to start AUTODATA area on PAGE boundary
4096	(1000)	CHARACTER	16384	PRMEAUTO	ESTAE Exit Autodata area (4 dataregs worth)

PRMESTAE Cross Reference

Name	Hex Offset	Hex Value
ALCXTCALLED	38	02
CHNGQ	4	20
DDRQ	4	40
DSABCHA	38	04
DSABCHN	5	20
DSSTAP	4	10
DSSUNREC	4	08
DUMPOK	5	08
ENQQ4	4	80
ENQVDEV	3A	80
FREECORE	5	04
GENCLNUP	5	80
MLWTO	4	02
MSG_ID	0	
MSGBUFF	38	10
MSGIDS	0	
OFFLSTOR	38	20
PENDFOOT	4	01
PRM_CMSLOCK	3A	20
PRM_LOCALOCK	3A	40
PRMAERBP	30	
PRMALCWA	3C	
PRMARRAY_LENGTH	5A	
PRMASID	6	
PRMASPTR	40	
PRMCUCBP	60	
PRMDSQF	24	
PRMDSQFA	70	
PRMDSQL	20	
PRMDSQLA	6C	
PRMEAUTO	1000	
PRMEAUTO_PTR	0	
PRMESTAE	0	
PRMESTAE_AREA	0	
PRMESTAE_BUFFER	200	
PRMESTAE_PARMAREA	0	
PRMFOOTS	39	
PRMFUNC	5	
PRMFUNC2	38	
PRMGMPTR	68	
PRMGMSIZ	64	
PRMGPMSK	38	08
PRMJSCBP	34	
PRMMBTKN	54	
PRMNELA	74	
PRMNELM	28	
PRMNUM_WTOID	58	
PRMOADDR	4C	
PRMOSIZE	50	
PRMQDBP	1C	
PRMQMBLN	14	
PRMQMBP	18	
PRMQMGP	10	
PRMRESC	4	
PRMRESC2	3A	
PRMRETRY	5	01
PRMSECC	39	80
PRMSECR	39	40
PRMSIOTP	8	
PRMUCBP	C	
PRMVADDR	44	
PRMVSIZE	48	
PRMWTOID	2C	
PRMWTOID_TABLE_PTR	5C	
TIOTBLT	5	10
TPQ	4	04
UPDSABCK	5	40
VMVCALL	5	02
VSERSTOR	38	40
WRTBUF	38	80

PSA Information

PSA Programming Interface information

Programming Interface information

PSA

ONLY the following fields are part of the programming interface information:

- FLCARCH
- FLCCVT
- FLCFACL
- PSAAOLD
- PSAECVT
- PSAFLAGS
- PSALAA
- PSASVTX
- PSATOLD
- PSATRVT
- PSATX
- PSATXC
- PSAXCVT

End of Programming Interface information

PSA Heading Information

Common Name: Prefixed Save Area
Macro ID: IHAPSA
DSECT Name: PSA
Owning Component: Supervisor Control (SC1C5)
Eye-Catcher ID: None
Storage Attributes: Subpool: 239
Key: 0
Residency: Below 16 MB line
Size: 4096 bytes
Created by: IEAVFX00
IEAVNIP0
IEEVCPRA
Pointed to by: The PSA maps the storage that starts at location 0 for the related processor.
Serialization: Disablement.
None needed for FLCFACL.
Function: Maps fixed hardware and software storage locations for the related processor.

PSA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PSA	
0	(0)	X'0'	0	FLC	"*"
0	(0)	CHARACTER	8	FLCIPPSW (0)	- IPL PSW
0	(0)	BITSTRING	4	FLCRNPSW	-RESTART NEW PSW (AFTER IPL) MDC001
4	(4)	ADDRESS	4		"V(IEAVRSTR)" - SECOND HALF OF RESTART NEW PSW MDC128
4	(4)	X'0'	0	IPLPSW	"FLCIPPSW" --- ALIAS
8	(8)	CHARACTER	8	FLCICCW1 (0)	- IPL CCW1
8	(8)	BITSTRING	8	FLCROPSW	- RESTART OLD PSW (AFTER IPL)
16	(10)	CHARACTER	8	FLCICCW2 (0)	- IPL CCW2
16	(10)	ADDRESS	4	FLCCVT	"V(IEACVT)" - ADDRESS OF CVT (AFTER IPL). THIS OFFSET FIXED BY ARCHITECTURE. (MDC450)
20	(14)	BITSTRING	4		- RESERVED (AFTER IPL) (MDC431)
24	(18)	BITSTRING	8	FLCEOPSW	- EXTERNAL OLD PSW
24	(18)	X'18'	0	EXOPSW	"FLCEOPSW" --- ALIAS
32	(20)	BITSTRING	8	FLCSOPSW	- SVC OLD PSW. THIS OFFSET FIXED BY ARCHITECTURE. (MDC451)
32	(20)	X'20'	0	SVCOPSW	"FLCSOPSW" --- ALIAS
40	(28)	BITSTRING	8	FLCPOPSW	- PROGRAM CHECK OLD PSW
40	(28)	X'28'	0	PIOPSW	"FLCPOPSW" --- ALIAS
48	(30)	BITSTRING	8	FLCMOPSW	- MACHINE CHECK OLD PSW
48	(30)	X'30'	0	MCOPSW	"FLCMOPSW" --- ALIAS
56	(38)	BITSTRING	8	FLCIOPSW	- INPUT/OUTPUT OLD PSW
56	(38)	X'38'	0	IOOPSW	"FLCIOPSW" --- ALIAS
64	(40)	BITSTRING	8		- RESERVED
72	(48)	DBL WORD	8	FLCCVT64 (0)	- 8-byte CVT address
72	(48)	BITSTRING	4		- 1st 4 bytes are 0
76	(4C)	ADDRESS	4	FLCCVT2	"V(IEACVT)" - ADDRESS OF CVT - USED BY DUMP ROUTINES ICB319
80	(50)	BITSTRING	4		- RESERVED
84	(54)	BITSTRING	4		- RESERVED - FLCTRACE DELETED DUE TO SYSTEM TRACE REDESIGN.
88	(58)	BITSTRING	4	FLCENPSW	-EXTERNAL NEW PSW
92	(5C)	ADDRESS	4		"V(IEAQEX00)" - SECOND HALF OF EXTERNAL NEW PSW
92	(5C)	X'58'	0	EXNPSW	"FLCENPSW" --- ALIAS
96	(60)	BITSTRING	4	FLCSNPSW	-SVC NEW PSW
100	(64)	ADDRESS	4		"V(IEAQSC00)" - SECOND HALF OF SVC NEW PSW
100	(64)	X'60'	0	SVCNPSW	"FLCSNPSW" --- ALIAS
104	(68)	BITSTRING	4	FLCPNPSW	- PROGRAM CHECK NEW PSW, DISABLED FOR MACHINE CHECKS.
108	(6C)	ADDRESS	4		"V(IEAQPK00)" - SECOND HALF OF PROGRAM CHECK NEW PSW
108	(6C)	X'68'	0	PINPSW	"FLCPNPSW" --- ALIAS
112	(70)	BITSTRING	4	FLCMNPSW	-MACHINE CHECK NEW PSW MDC003
116	(74)	ADDRESS	4		"V(IGFPMAIN)" - SECOND HALF OF MACHINE CHECK NEW PSW
116	(74)	X'70'	0	MCNPSW	"FLCMNPSW" --- ALIAS
120	(78)	BITSTRING	4	FLCINPSW	-INPUT/OUTPUT NEW PSW
124	(7C)	ADDRESS	4		"V(IEAQIO00)" - SECOND HALF OF I/O NEW PSW
124	(7C)	X'78'	0	IONPSW	"FLCINPSW" --- ALIAS
128	(80)	SIGNED	4	PSAEPARM	- EXTERNAL INTERRUPTION PARAMETER FIELD. (MDC473)
132	(84)	SIGNED	4	PSAEEPSW (0)	- EXTENDED PSW DATA STORED ON EXTERNAL INTERRUPT MDC084
132	(84)	SIGNED	2	PSASPAD	- ISSUING PROCESSOR'S PHYSICAL ADDRESS ON MFA, EMS, OR EXTERNAL CALL INTERRUPT MDC046
134	(86)	SIGNED	2	FLCEICOD	- EXTERNAL INTERRUPTION CODE
134	(86)	X'86'	0	EXCODE	"FLCEICOD" --- ALIAS
136	(88)	SIGNED	4	PSAESPSW (0)	- EXTENDED PSW DATA STORED ON SVC INTERRUPT MDC085

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
136	(88)	BITSTRING	1		- RESERVED - SET TO ZERO
137	(89)	SIGNED	1	FLCSVILC	- SVC INSTRUCTION LENGTH COUNTER - NUMBER OF BYTES. THIS OFFSET FIXED BY ARCHITECTURE. (MDC454)
		.... .111		FLCSILCB	"X'07'" - SIGNIFICANT BITS IN ILC FIELD - LAST BIT IS ALWAYS ZERO MDC080
137	(89)	X'89'	0	SVCILC	"FLCSVILC" --- ALIAS
138	(8A)	SIGNED	2	FLCSVCN	- SVC INTERRUPTION CODE - SVC NUMBER. THIS OFFSET FIXED BY ARCHITECTURE. (MDC455)
138	(8A)	X'8A'	0	SVCNUM	"FLCSVCN" --- ALIAS
140	(8C)	CHARACTER	8	PSAEPPSW (0)	- EXTENDED PSW FOR PROGRAM INTERRUPT MDC086
140	(8C)	BITSTRING	1		- RESERVED - SET TO ZERO
141	(8D)	SIGNED	1	FLCPIILC	- PROGRAM INTERRUPT LENGTH COUNTER - NUMBER OF BYTES IN INSTRUCTION CAUSING PROGRAM INTERRUPTION. THIS OFFSET FIXED BY ARCHITECTURE. (MDC456)
		.... .111		FLCPILCB	"X'07'" - SIGNIFICANT BITS IN ILC FIELD - LAST BIT IS ALWAYS ZERO MDC083
141	(8D)	X'8D'	0	PIILC	"FLCPIILC" --- ALIAS
142	(8E)	SIGNED	2	FLCPICOD (0)	- PROGRAM INTERRUPTION CODE
142	(8E)	X'8E'	0	PICODE	"FLCPICOD" --- ALIAS
142	(8E)	SIGNED	1	PSAEECOD	- EXCEPTION-EXTENSION CODE.
143	(8F)	SIGNED	1	PSAPICOD	- 8-BIT INTERRUPT CODE. THIS OFFSET FIXED BY ARCHITECTURE. (MDC457)
		1...		PSAPIPER	"X'80'" - PER INTERRUPT OCCURRED MDC089
		.1..		PSAPIMC	"X'40'" - MONITOR CALL INTERRUPT OCCURRED MDC090
		..11 1111		PSAPIPC	"X'3F'" - AN UNSOLICITED PROGRAM CHECK HAS OCCURRED IF ANY OF THESE 6 BITS ARE ON MDC091
144	(90)	SIGNED	4	FLCTEA (0)	- TRANSLATION EXCEPTION ADDRESS. THIS OFFSET FIXED BY ARCHITECTURE.
144	(90)	BITSTRING	3		-
		1...		FLCTEAXM	"X'80'" - IF 0 FLCTEA IS RELATIVE TO THE PRIMARY SEGMENT TABLE IF 1 FLCTEA IS RELATIVE TO THE SECONDARY SEGMENT TABLE
147	(93)	BITSTRING	1	FLCDXC (0)	- Data exception code for PI 7
147	(93)	BITSTRING	1	FLCTEAB3	- LAST BYTE OF TEA.
		.... .1..		FLCSOPI	"X'04'" - Suppression on protection flag
		....		FLCTSTDP	"X'00'" - IF 1, THE PRIMARY STD WAS USED.
		1		FLCTSTDA	"X'01'" - IF 1, THE STD WAS AR QUALIFIED.
		.... ..1.		FLCTSTDS	"X'02'" - IF 1, THE SECONDARY STD WAS USED.
		11		FLCTSTDH	"X'03'" - IF 1, THE HOME STD WAS USED.
147	(93)	BITSTRING	0	FLCTEACL	"X'7FFFF000'" Mask to leave only TEA address
148	(94)	BITSTRING	1		- RESERVED - SET TO ZERO
149	(95)	BITSTRING	1	FLCMCNUM	- MONITOR CLASS NUMBER
150	(96)	BITSTRING	1	FLCPERCD	- PROGRAM EVENT RECORDING CODE
151	(97)	BITSTRING	1	FLCATMID	- ATM ID
		1...		FLCPSWB4	"X'80'" PSW.4 part of ATMID
152	(98)	ADDRESS	4	FLCPER	- PER ADDRESS - ESA/390
156	(9C)	BITSTRING	1		- RESERVED - SET TO ZERO
157	(9D)	BITSTRING	3	FLCMTRCD	- MONITOR CODE (ESA/390)
160	(A0)	BITSTRING	1	FLCTEARN	- CONTAINS THE ACCESS REGISTER NUMBER INVOLVED IN THE TRANSLATION EXCEPTION IF BITS 30-31 OF THE TEA='01'.
161	(A1)	BITSTRING	1	FLCPERRN	- CONTAINS THE PER STORAGE ACCESS REGISTER NUMBER.
162	(A2)	BITSTRING	1		- RESERVED.
163	(A3)	BITSTRING	1	FLCARCH	- Architecture information
		1		PSAZARCH	"X'01'" - z/Architecture
		1		PSAESAME	"X'01'" - z/Architecture
164	(A4)	ADDRESS	4	PSAMPL	"V(IHAMPL)" - MPL ADDRESS. THIS OFFSET FIXED BY ARCHITECTURE. (MDC418)
168	(A8)	BITSTRING	344	(0)	- MACHINE CHECK LOGOUT AREA
168	(A8)	BITSTRING	16		- RESERVED (ESA/390)
184	(B8)	BITSTRING	8	FLCIOCDP (0)	- I/O INFORMATION CODE
184	(B8)	BITSTRING	4	FLCSID	- SUBSYSTEM ID
188	(BC)	BITSTRING	4	FLCIOFP	- I/O INTERRUPTION PARAMETER
192	(C0)	BITSTRING	8		- RESERVED
200	(C8)	BITSTRING	16	FLCFACL (0)	- Facilities List. See FLCEFacilitiesList in IHAPSAE for description
200	(C8)	BITSTRING	1	FLCFACL0	Byte 0 of FLCFACL
		1...		FLCFN3	"X'80'" - N3 installed
		.1..		FLCFZARI	"X'40'" - z/Architecture installed
		..1.		FLCFZARA	"X'20'" - z/Architecture active
		.... ..1.		FLCFASLX	"X'02'" - ASN & LX reuse facility installed
201	(C9)	BITSTRING	1	FLCFACL1	Byte 1 of FLCFACL
		1...		FLCFEDAT	"X'80'" DAT features
		.1..		FLCFSRS	"X'40'" Sense-running-status
		..1.		FLCFSSKE	"X'20'" Cond. SSKE instruction installed
		...1		FLCFCTOP	"X'10'" STSI-enhancement
202	(CA)	BITSTRING	1	FLCFACL2	Byte 2 of FLCFACL

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1...		FLCFETF2	"X'80'" Extended Translation facility 2
		.1..		FLCFCRYA	"X'40'" Cryptographic assist
		..1.		FLCFLD	"X'20'" Long Displacement facility
		...1		FLCFLDHP	"X'10'" Long Displacement High Performance
		.... 1...		FLCFHMAS	"X'08'" HFP Multiply Add/Subtract
		.... .1..		FLCFEIMM	"X'04'" Extended immediate when z/Arch
		.... ..1.		FLCFETF3	"X'02'" Extended Translation Facility 3 when z/Arch
		1		FLCFHUN	"X'01'" HFP unnormalized extension
203	(CB)	BITSTRING	1	FLCFACL3	Byte 3 of FLCFACL
		1...		FLCFET2E	"X'80'" ETF2-enhancement 031215
		.1..		FLCFSTKF	"X'40'" STCKF-enhancement
		.... ..1.		FLCFET3E	"X'02'" ETF3-enhancement 040512
		1		FLCFECT	"X'01'" ECT-facility
204	(CC)	BITSTRING	1	FLCFACL4	Byte 4 of FLCFACL
		1...		FLCFCSSF	"X'80'" Compare-and-swap-and-store
		.1..		FLCFCSF2	"X'40'" Compare-and-swap-and-store 2
		..1.		FLCFGIEF	"X'20'" General-Instructions-Extension Facility
		1		FLCFOCM	"X'01'" Obsolete CPU-measurement facility. Use FLCFCMC and FLCFCMS instead.
205	(CD)	BITSTRING	1	FLCFACL5	Byte 5 of FLCFACL
		.1..		FLCFFPSE	"X'40'" Floating-point-support enhancement
		..1.		FLCFDFP	"X'20'" Decimal-floating-point
		...1		FLCFDFPH	"X'10'" Decimal-floating-point high performance
		.... 1...		FLCFPFPO	"X'08'" PFPO instruction 070424
206	(CE)	BITSTRING	1	FLCFACL6	Byte 6 of FLCFACL
207	(CF)	BITSTRING	1	FLCFACL7	Byte 7 of FLCFACL
208	(D0)	BITSTRING	1	FLCFACL8	Byte 8 of FLCFACL
		.1..		FLCFCAAI	"X'40'" Crypto AP-Queue adapter interruption
		...1		FLCFCMC	"X'10'" CPU-measurement counter facility
		.... 1...		FLCFCMS	"X'08'" CPU-measurement sampling facility
		.... .1..		FLCFSCLP	"X'04'" Possible future enhancement
		.... ..1.		FLCFAISI	"X'02'" AISI facility
		1		FLCFAEN	"X'01'" AEN facility
209	(D1)	BITSTRING	1	FLCFACL9	Byte 9 of FLCFACL
		1...		FLCFAIS	"X'80'" AIS facility

Comment

IHAPSAE FLCEFacilitiesList will have any future bit definitions.

End of Comment

210	(D2)	BITSTRING	6		- RESERVED
216	(D8)	BITSTRING	8	FLCCTSA	- CPU-TIMER SAVE AREA (FROM STORE- STATUS OR MACHINE-CHECK) ESA/390
224	(E0)	BITSTRING	8	FLCCCSA	- CLOCK-COMPARATOR SAVE AREA (FROM STORE-STATUS OR MACHINE-CHECK) ESA/390
232	(E8)	BITSTRING	8	FLCMCIC	- MACHINE-CHECK INTERRUPTION CODE
240	(F0)	BITSTRING	8		- RESERVED - SET TO ZERO
248	(F8)	ADDRESS	4	FLCFSA	- FAILING STORAGE ADDRESS
252	(FC)	BITSTRING	4		- RESERVED - SET TO ZERO
256	(100)	BITSTRING	16	FLCFLA	- FIXED LOGOUT AREA. SIZE FIXED BY ARCHITECTURE.
272	(110)	BITSTRING	16	FLCRV110	- RESERVED.
288	(120)	SIGNED	4	FLCARSAV (16)	- ACCESS REGISTER SAVE AREA
352	(160)	BITSTRING	32	FLCFPSAV	- FLOATING POINT REGISTER SAVE AREA
384	(180)	SIGNED	4	FLCGRSAV (16)	- GENERAL REGISTER SAVE AREA
448	(1C0)	SIGNED	4	FLCCRSAV (16)	- CONTROL REGISTER SAVE AREA
512	(200)	DBL WORD	8	FLCHDEND (0)	- END OF HARDWARE ASSIGNMENTS
512	(200)	CHARACTER	4	PSAPSA	- CONTROL BLOCK ACRONYM IN EBCDIC
516	(204)	SIGNED	2	PSACPUPA	- PHYSICAL CPU ADDRESS (CHANGED DURING ACR) (MDC130) YM3489
518	(206)	SIGNED	2	PSACPULA	- LOGICAL CPU ADDRESS
520	(208)	ADDRESS	4	PSAPCCAV	- VIRTUAL ADDRESS OF PCCA
524	(20C)	ADDRESS	4	PSAPCCAR	- REAL ADDRESS OF PCCA
528	(210)	ADDRESS	4	PSALCCAV	- VIRTUAL ADDRESS OF LCCA
532	(214)	ADDRESS	4	PSALCCAR	- REAL ADDRESS OF LCCA
536	(218)	ADDRESS	4	PSATNEW	- TCB pointer. Field maintained for code compatability with previous MVS releases. DO NOT USE.
536	(218)	X'218'	0	IEATCBP	"PSATNEW" - ALIAS
540	(21C)	ADDRESS	4	PSATOLD	- Pointer to current TCB or zero if in SRB mode. Field fixed by architecture
544	(220)	ADDRESS	4	PSAANEW	"V(IEAMASCB)" ASCB pointer. Field maintained for code compatability with previous MVS releases. DO NOT USE.
548	(224)	ADDRESS	4	PSAAOLD	- Pointer to the home (current) ASCB. Architecture is dependent on the offset of this field.
552	(228)	BITSTRING	4	PSASUPER (0)	- SUPERVISOR CONTROL WORD. THIS OFFSET FIXED BY ARCHITECTURE. (MDC462)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
552	(228)	BITSTRING	1	PSASUP1	- FIRST BYTE OF PSASUPER
		1...		PSAIO	"X'80'" - I/O FLIH
		.1..		PSASVC	"X'40'" - SVC FLIH
		..1.		PSAEXT	"X'20'" - EXTERNAL FLIH
		...1		PSAPI	"X'10'" - PROGRAM CHECK FLIH
		.... 1...		PSALOCK	"X'08'" - LOCK ROUTINE
		.... .1..		PSADISP	"X'04'" - DISPATCHER
		.... ..1.		PSATCTL	"X'02'" - TCTL RECOVERY FLAG (MDC310)
		1		PSATYPE6	"X'01'" - TYPE 6 SVC IN CONTROL (MDC311)
553	(229)	BITSTRING	1	PSASUP2	- SECOND BYTE OF PSASUPER
		1...		PSAIPCRI	"X'80'" - REMOTE IMMEDIATE SIGNAL SERVICE ROUTINE (IEAVERI)
		.1..		PSASVCR	"X'40'" - SUPER FRR USES FOR SVC FLIH RECURSION TRACKING
		..1.		PSASVCRR	"X'20'" - SVC RECOVERY RECURSION INDICATOR. OWNER: SUPERVISOR CONTROL. SERIALIZATION: DISABLEMENT.
		.... .1..		PSAACR	"X'04'" - AUTOMATIC CPU RECONFIGURATION (ACR) IN CONTROL MDC119
		.... ..1.		PSARTM	"X'02'" - RECOVERY TERMINATION MONITOR (RTM) IN CONTROL MDC120
		1		PSALCR	"X'01'" - USED BY RTM TO SERIALIZE CALLS OF THE SUPERIVSOR ANALYSIS ROUTER
554	(22A)	BITSTRING	1	PSASUP3	- THIRD BYTE OF PSASUPER
		1...		PSAIOSUP	"X'80'" - IF ON, A MAINLINE IOS COMPONENT SUCH AS CHANNEL SCHEDULER HAS ENTERED A PHYSICALLY DISABLED STATE WITHOUT REGARD TO LOCKING REQUIREMENTS MDC027
		...1		PSASPR	"X'10'" - SUPER FRR IS ACTIVE (MDC305)
		.... 1...		PSAESTA	"X'08'" - SVC 60 RECOVERY ROUTINE ACTIVE (MDC312)
		.... .1..		PSARSM	"X'04'" - REAL STORAGE MANAGER (RSM) ENTERED FOR PAGE FIX (MDC321)
		.... ..1.		PSAULCMS	"X'02'" - LOCK MANAGER UNCONDITIONAL LOCAL OR CMS LOCK ROUTINES (MDC469)
		1		PSASLIP	"X'01'" - IEAVTSLP RECURSION CONTROL BIT (MDC471)
555	(22B)	BITSTRING	1	PSASUP4	- FOURTH BYTE OF PSASUPER
		1...		PSALDWT	"X'80'" - BLWLDWT IS IN CONTROL TO LOAD A RESTARTABLE OR NON-RESTARTABLE WAIT STATE CODE OWNERSHIP: LDWT
		.1..		PSASMF	"X'40'" - SMF SUSPEND/RESET (MDC599)
		..1.		PSAESAR	"X'20'" - SUPERVISOR ANALYSIS ROUTER IS ACTIVE
		...1		PSAMCH	"X'10'" - Machine Check Handler is active.
556	(22C)	BITSTRING	12	PSARV22C	- RESERVED
568	(238)	BITSTRING	2	PSA_WORKUNIT_PROCCLASSATDISP	-
568	(238)	BITSTRING	1	PSA_WORKUNIT_PROCCLASSATDISP_BYTE0	
569	(239)	BITSTRING	1	PSA_WORKUNIT_PROCCLASSATDISP_BYTE1	
570	(23A)	BITSTRING	2	PSAPROCCLASS	- PROCESSOR WUQ Offset.
570	(23A)	BITSTRING	2	PSA_BYLPAR_PROCCLASS	- PROCESSOR WUQ Offset.
570	(23A)	BITSTRING	1	PSAPROCCLASS_BYTE0	
571	(23B)	BITSTRING	1	PSAPROCCLASS_BYTE1	This field is for IBM use only. OWNERSHIP: SUPERVISOR CONTROL SERIALIZATION: READ = NONE WRITE = NO WRITE ALLOWED See PSAProcClass_xxx constants.
		....		PSAPROCCLASS_CP	"X'0000'" Standard CP. 0 is offset to SWUQ
		.... ..1.		PSAPROCCLASS_ZAAP	"X'0002'" zAAP.
		.... .1..		PSAPROCCLASS_ZIIP	"X'0004'" zIIP.
		.... .1..		PSAPROCCLASS_SUP	"X'0004'" zIIP.
571	(23B)	X'2'	0	PSAPROCCLASSCONVERTER	"2" Procclass conversion factor
571	(23B)	X'4'	0	PSAMAXPROCCLASS	"4" PSA Max procclass
571	(23B)	X'2'	0	PSAMAXPROCCLASSINDEX	"PSAMaxProcClass/PSAProcClassConverter" Maximum ProcClass index. A ProcClass beings at 0 and ends at this number. Currently: Index 0 - CP ProcClassIndex Index 1 - zAAP ProcClassIndex Index 2 - zIIP ProcClassIndex
570	(23A)	BITSTRING	1	PSA_BYLPAR_PROCCLASS_BYTE0	
571	(23B)	BITSTRING	1	PSA_BYLPAR_PROCCLASS_BYTE1	
572	(23C)	BITSTRING	1	PSAPTYPE	- PROCESSOR TYPE INDICATOR OWNERSHIP: SUPERVISOR CONTROL SERIALIZATION: READ = NONE WRITE = DISABLEMENT.
		.1..		PSAIFA	"X'40'" Indicates Special Processor
		.1..		PSA_BYLPAR_ZAAP	"X'40'"
		.1..		PSA_BYLPAR_IFA	"X'40'"
		..1.		PSAIFADS	"X'20'" zAAP (IFA) that is different speed than CP

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		PSADSCRP	"X'10'" Discretionary Processor
		.... 1...		PSAZIIP	"X'08'" zIIP
		.... 1...		PSA_BYLPAR_ZIIP	"X'08'"
		.... 1...		PSASUP	"X'08'" zIIP
		.... 1...		PSA_BYLPAR_SUP	"X'08'"
		.... .1..		PSAZIIPDS	"X'04'" zIIP that is different speed than CP
		.... .1..		PSASUPDS	"X'04'" zIIP that is different speed than CP
573	(23D)	BITSTRING	1	PSAILS	- INTERRUPT HANDLER LINKAGE STACK INDICATORS.
		1...		PSAILSIO	"X'80'" - THE I/O FLIH IS USING THE INTERRUPT HANDLER LINKAGE STACK.
		.1..		PSAILSEX	"X'40'" - THE EXTERNAL FLIH IS USING THE INTERRUPT HANDLER LINKAGE STACK.
		..1.		PSAILSPC	"X'20'" - THE PROGRAM FLIH IS USING THE INTERRUPT HANDLER LINKAGE STACK.
		...1		PSAILSDS	"X'10'" - THE DISPATCHER IS USING THE INTERRUPT HANDLER LINKAGE STACK.
		.... 1...		PSAILSRS	"X'08'" - THE RESTART FLIH IS USING THE INTERRUPT HANDLER LINKAGE STACK.
		.... .1..		PSAILSOR	"X'04'" - EXIT IS USING THE INTERRUPT HANDLER LINKAGE STACK.
		.... ..1.		PSAILST6	"X'02'" - TYPE 6 SVC IS USING THE INTERRUPT HANDLER LINKAGE STACK.
		1		PSAILSLK	"X'01'" - THE INTERRUPT HANDLER LINKAGE STACK IS ACTIVE BECAUSE THE RSM LOCK OR A LOCK HIGHER THAN THE RSM LOCK IS HELD.
574	(23E)	BITSTRING	2	PSALSVCI	- LAST SVC ISSUED ON THIS PROCESSOR PRIOR TO ENABLEMENT BY THE SVC FLIH. OWNERSHIP: SUPERVISOR CONTROL SERIALIZATION: DISABLEMENT
576	(240)	BITSTRING	1	PSAFLAGS	- SYSTEM FLAGS This field is PI for bits PSATX and PSATXC only OWNERSHIP: SUPERVISOR CONTROL SERIALIZATION: DISABLEMENT SERIALIZATION: None for PI bits
		1...		PSAAEIT	"X'80'" - ADDRESSING ENVIRONMENT IS IN TRANSITION. INDICATES THAT THE SPACE TYPE (ADDRESS SPACE OR SUBSPACE) ASSOCIATED WITH PASN OR SASN IS UNKNOWN. Was PSAFPAC, PSAFPPE 2
		.... 1...		PSATX	"X'08'" Equivalent to CVTTX
		.... .1..		PSATXC	"X'04'" Equivalent to CVTTXC
577	(241)	BITSTRING	10	PSARV241	RESERVED FOR FUTURE USE - SC1C5.
587	(24B)	BITSTRING	1	PSASCAFF	$$SCAFFOLD
		1...		PSAEMEMA	"X'80'" $$SCAFFOLD: z/Architecture
588	(24C)	ADDRESS	4	PSALKCRF	LINKAGE STACK POINTER SAVE AREA. USED WHEN THE RSM OR ANY LOCK ABOVE THE RSM LOCK IS HELD.
592	(250)	DBL WORD	8	(0)	- ALIGN PSAMPSW TO DOUBLE WORD
592	(250)	BITSTRING	8	PSAMPSW	- SETLOCK MODEL PSW
		.... ..1.		PSAPIOM	"X'02'" INPUT/OUTPUT INTERRUPT MASK
		1		PSAPEXM	"X'01'" EXTERNAL INTERRUPT MASK
600	(258)	BITSTRING	8	PSAICNT	- Number of instructions executed at dispatch time
608	(260)	SIGNED	4	PSATCLIN	- STOSM PSASLSA,X'00' INSTRUCTION USED BY IEAVETCL and IEAVSCHA. OWNERSHIP: SUPERVISOR CONTROL. SERIALIZATION: DISABLEMENT.
612	(264)	SIGNED	4	PSAINTIN	- STOSM PSASLSA,X'00' INSTRUCTION USED BY IEAVEINT. OWNERSHIP: SUPERVISOR CONTROL. SERIALIZATION: DISABLEMENT.
616	(268)	SIGNED	4	PSAIPCIN	- STOSM PSASLSA,X'00' INSTRUCTION USED BY IPC ROUTINES. OWNERSHIP: SUPERVISOR CONTROL. SERIALIZATION: DISABLEMENT.
616	(268)	X'269'	0	PSAIPCSM	"PSAIPCIN+1,1,C'X'" - LABEL FOR SYSTEM MASK USED IN ABOVE INSTRUCTION. OWNERSHIP: SUPERVISOR CONTROL.
620	(26C)	SIGNED	4	PSAEMS2S	- STOSM PSASLSA,X'00' INSTRUCTION USED BY IEAVEMS0. OWNERSHIP: SUPERVISOR CONTROL. SERIALIZATION DISABLEMENT.
620	(26C)	X'26D'	0	PSAEMS2M	"PSAEMS2S+1,1,C'X'" - LABEL OF SYSTEM MASK USED IN ABOVE INSTRUCTION. OWNERSHIP: SUPERVISOR CONTROL.
624	(270)	SIGNED	4	PSASTOSM	- STOSM PSASLSA,X'00' INSTRUCTION. IN ORDER TO USE THIS FIELD, MOVE THE SYSTEM MASK TO PSASTSSM AND IMMEDIATELY ISSUE EX 0,PSASTOSM. THE SYSTEM MASK FIELD (PSASTSSM) IS NOT PRESERVED ACROSS CALLS AND SHOULD NOT BE USED TO SAVE THE SYSTEM MASK. OWNERSHIP: NA. SERIALIZATION: DISABLEMENT.
624	(270)	X'271'	0	PSASTSSM	"PSASTOSM+1,1,C'X'" - LABEL FOR SYSTEM MASK USED IN ABOVE INSTRUCTION. OWNERSHIP: NA.
628	(274)	SIGNED	4	PSAHLHIS	- SAVE AREA FOR PSAHLHI MDC050
632	(278)	BITSTRING	1	PSARECUR	- RESTART FLIH RECURSION INDICATOR. IF X'00', FLIH NOT IN CONTROL. IF X'FF', FLIH IN CONTROL, ENTRY IS RECURSIVE. MDC093
633	(279)	BITSTRING	1	PSARSSM	- STNSM AREA FOR IEAVERES
634	(27A)	BITSTRING	1	PSASNSM2	- STNSM AREA FOR IEAVTRT1 (MDC470)
635	(27B)	BITSTRING	1	PSARTM1S	- BITS 0-7 OF THE CURRENT PSW ARE STORED HERE WHENEVER PSARTM1R IS EXECUTED IN RTM. (MDC613)
636	(27C)	ADDRESS	4	PSALWTSA	- REAL ADDRESS OF SAVE AREA USED WHEN A RESTARTABLE WAIT STATE IS LOADED OWNERSHIP: LDWT

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
640	(280)	CHARACTER	116	PSACLHT (0)	- CPU LOCKS TABLE (MDC314)
640	(280)	CHARACTER	80	PSACLHT1 (0)	- SPIN LOCKS TABLE
640	(280)	ADDRESS	4	PSADISPL	"V(DISPLOCK)" - GLOBAL DISPATCHER LOCK (MDC315)
644	(284)	ADDRESS	4	PSAASML	- AUXILIARY STORAGE MANAGEMENT (ASM) LOCK MDC002
648	(288)	ADDRESS	4	PSASALCL	"V(SALCLOCK)" - SPACE ALLOCATION LOCK (MDC316)
652	(28C)	ADDRESS	4	PSAIOSSL	- IOS SYNCHRONIZATION LOCK MDC010
656	(290)	ADDRESS	4	PSARSMDL	- ADDRESS OF THE RSM DATA SPACE LOCK
660	(294)	ADDRESS	4	PSAIOSUL	- IOS UNIT CONTROL BLOCK LOCK MDC005
664	(298)	ADDRESS	4	PSARVLK1	- RESERVED FOR LOCK EXPANSION
668	(29C)	ADDRESS	4	PSARV29C	- RESERVED FOR LOCK EXPANSION
672	(2A0)	ADDRESS	4	PSARV2A0	- RESERVED FOR LOCK EXPANSION
676	(2A4)	ADDRESS	4	PSATPACL	- TCAM'S TPACBDEB LOCK MDC009
680	(2A8)	ADDRESS	4	PSAOPTL	"V(OPTLOCK)" - OPTIMIZER LOCK (MDC317)
684	(2AC)	ADDRESS	4	PSARSMGL	- RSM GLOBAL LOCK
688	(2B0)	ADDRESS	4	PSAVFIXL	"V(VFIXLOCK)" VSM FIXED SUBPOOLS LOCK
692	(2B4)	ADDRESS	4	PSAASMGL	- ASM GLOBAL LOCK
696	(2B8)	ADDRESS	4	PSARSMSL	- RSM STEAL LOCK
700	(2BC)	ADDRESS	4	PSARSMXL	- RSM CROSS MEMORY LOCK
704	(2C0)	ADDRESS	4	PSARSMAL	- RSM ADDRESS SPACE LOCK
708	(2C4)	ADDRESS	4	PSAVPAGL	"V(VPAGLOCK)" VSM PAGEABLE SUBPOOLS LOCK
712	(2C8)	ADDRESS	4	PSARSMCL	RSM COMMON LOCK
712	(2C8)	X'13'	0	PSALKS1	"19" COUNT OF LOCKS IN CLHT1
716	(2CC)	ADDRESS	4	PSARVLK2	RESERVED FOR LOCK EXPANSION
720	(2D0)	CHARACTER	16	PSACLHT2 (0)	SHARED EXCLUSIVE LOCKS TABLE
720	(2D0)	ADDRESS	4	PSARSML	"V(RSMLOCK)" RSM GLOBAL FUNCTION/RECOVERY LOCK
		1...		PSARSMEX	"X'80'" - BIT 0 OF PSARSML. IF ON, THE RSM LOCK IS HELD EXCLUSIVE.
724	(2D4)	ADDRESS	4	PSATRCEL	"V(TRCELOCK)" TRACE BUFFER MANAGEMENT LOCK
		1...		PSATRCEX	"X'80'" - BIT 0 OF PSATRCEL. IF ON THE TRACE LOCK IS HELD EXCLUSIVE.
728	(2D8)	ADDRESS	4	PSAIOSL	"V(IOSLOCK)" - IOS LOCK
		1...		PSAIOSEX	"X'80'" - BIT 0 OF PSAIOSL. IF ON THE IOS LOCK IS HELD EXCLUSIVE.
728	(2D8)	X'3'	0	PSALKS2	"3" COUNT OF LOCKS IN CLHT2
732	(2DC)	ADDRESS	4	PSARVLK4	- RESERVED FOR LOCK EXPANSION
736	(2E0)	CHARACTER	8	PSACLHT3 (0)	SPECIAL LOCKS TABLE
736	(2E0)	ADDRESS	4	PSACPUL	CPU TABLE LOCKS
736	(2E0)	X'1'	0	PSALKS3	"1" COUNT OF LOCKS IN CLHT3
740	(2E4)	ADDRESS	4	PSARVLK5	- RESERVED FOR LOCK EXPANSION
744	(2E8)	CHARACTER	12	PSACLHT4 (0)	SUSPEND LOCKS TABLE
744	(2E8)	ADDRESS	4	PSACMSL	- CROSS MEMORY SERVICES LOCK (MDC463)
748	(2EC)	ADDRESS	4	PSALOCAL	- LOCAL LOCK
748	(2EC)	X'2'	0	PSALKS4	"2" COUNT OF LOCKS IN CLHT4
752	(2F0)	ADDRESS	4	PSARVLK6	- RESERVED FOR LOCK EXPANSION
756	(2F4)	ADDRESS	4	PSALCPUA	- LOGICAL CPU ADDRESS FOR LOCK INSTRUCTION. THIS OFFSET FIXED BY ARCHITECTURE. (MDC421)
760	(2F8)	SIGNED	4	PSAHLHI (0)	- HIGHEST LOCK HELD INDICATOR. THIS OFFSET FIXED BY ARCHITECTURE. (MDC464)
760	(2F8)	SIGNED	4	PSACLHS (0)	- CPU LOCKS HELD STRING MDC122
760	(2F8)	BITSTRING	1	PSACLHS1	- FIRST BYTE OF PSACLHS. (MDC384)
		1...		PSACPULI	"X'80'" - CPU LOCK INDICATOR
		...1		PSASUM	"X'10'" - SUMMARY BIT. IF ON, AT LEAST ONE LOCK IN PSACLHSE IS HELD BY THIS PROCESSOR.
		.... 1...		PSARSMLI	"X'08'" - RSM LOCK INDICATOR
		.... .1..		PSATRCEI	"X'04'" - TRACE LOCK INDICATOR
		.... ..1.		PSAIOSI	"X'02'" - IOS LOCK INDICATOR
761	(2F9)	BITSTRING	1	PSACLHS2	- SECOND BYTE OF PSACLHS. (MDC385)
		...1		PSARSMCI	"X'10'" - RSM COMMON LOCK INDICATOR
		.... 1...		PSARSMGI	"X'08'" - RSM GLOBAL LOCK INDICATOR
		.... .1..		PSAVFIXI	"X'04'" - VSM FIX LOCK INDICATOR
		.... ..1.		PSAASMGI	"X'02'" - ASM GLOBAL LOCK INDICATOR
		1		PSARSMSI	"X'01'" - RSM STEAL LOCK INDICATOR
762	(2FA)	BITSTRING	1	PSACLHS3	- THIRD BYTE OF PSACLHS (MDC386)
		1...		PSARSMXI	"X'80'" - RSM CROSS MEMORY LOCK INDICATOR
		.1..		PSARSMAI	"X'40'" - RSM ADDRESS SPACE LOCK INDICATOR
		..1.		PSAVPAGI	"X'20'" - VSM PAGE LOCK INDICATOR
		...1		PSADSPLI	"X'10'" - DISPATCHER LOCK INDICATOR (MDC387)
		.... 1...		PSAASMLI	"X'08'" - ASM LOCK INDICATOR (MDC388)
		.... .1..		PSASALLI	"X'04'" - SPACE ALLOCATION LOCK INDICATOR (MDC389)
		.... ..1.		PSAIOSLI	"X'02'" - IOS SYNCHRONIZATION LOCK INDICATOR (MDC390)
		1		PSARSMDI	"X'01'" - RSM DATA SPACE LOCK INDICATOR
763	(2FB)	BITSTRING	1	PSACLHS4	- FOURTH BYTE OF PSACLHS (MDC392)
		1...		PSAIOULI	"X'80'" - IOS UCB LOCK INDICATOR (MDC393)
		.... 1...		PSATPALI	"X'08'" - TPACBDEB LOCK INDICATOR (MDC397)
		.... .1..		PSASRMLI	"X'04'" - SYSTEM RESOURCE MANAGER (SRM) LOCK INDICATOR (MDC398)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... ..1.		PSACMSLI	"X'02'" - CROSS MEMORY SERVICES LOCK INDICATOR (CMS, CMSSMF, CMSEQDQ, CMSLATCH) (MDC399)
		1		PSALCLLI	"X'01'" - LOCAL LOCK INDICATOR (MDC400)
764	(2FC)	ADDRESS	4	PSALITA	"V(IEAVELT1)" - ADDRESS OF LOCK INTERFACE TABLE. THIS OFFSET FIXED BY ARCHITECTURE. (MDC465)
768	(300)	BITSTRING	8	PSASTOR8	- 8-BYTE value for master's STO
776	(308)	SIGNED	4	PSACR0	- SAVE AREA FOR CONTROL REGISTER 0
780	(30C)	BITSTRING	1	PSAMCHFL	- MCH RECURSION FLAGS
781	(30D)	BITSTRING	1	PSASYMSK	- THIS FIELD WILL BE USED IN CONJUNCTION WITH THE STNSM INSTRUCTION TO PLACE IOS CHANNEL SCHEDULER INTO A DISABLED STATE AND SIMULTANEOUSLY SAVE THE SYSTEM MASK OF THE CALLER MDC022
782	(30E)	BITSTRING	1	PSAACTCD	- ACTION CODE SUPPLIED BY OPERATOR AFTER SYSTEM HAS LOADED RESTARTABLE WAIT STATE AND BEFORE THE RESTART KEY IS DEPRESSED. VALUE DEPENDS ON RESTARTABLE WAIT STATE CODE. UNPREDICTABLE DURING NORMAL SYSTEM OPERATION. OWNERSHIP: LDWT
783	(30F)	BITSTRING	1	PSAMCHIC	- MCH INITIALIZATION COMPLETE FLAGS MDC098
784	(310)	ADDRESS	4	PSAWKRAP	- REAL ADDRESS OF VARY CPU PARAMETER LIST MDC106
788	(314)	ADDRESS	4	PSAWKVAP	- VIRTUAL ADDRESS OF VARY CPU PARAMETER LIST MDC107
792	(318)	SIGNED	2	PSAVSTAP	- WORK AREA FOR VARY CPU MDC108
794	(31A)	SIGNED	2	PSACPUSA	- PHYSICAL CPU ADDRESS (STATIC) (MDC131) YM3489
796	(31C)	SIGNED	4	PSASTOR	- MASTER MEMORY'S SEGMENT TABLE ORIGIN REGISTER (STOR) VALUE
800	(320)	BITSTRING	90	PSAIDAWK	- WORK SAVE AREA FOR private DEBUG TOOL.
890	(37A)	SIGNED	2	PSARET	- BSM 0,14 BRANCH RETURN TO CALLER USED BY ROUTINES INVOKED BY IOS
892	(37C)	SIGNED	2	PSARETCD	- BSM 0,14 BRANCH RETURN TO CALLER WITH RETURN CODE IN REGISTER 15, USED BY ROUTINES INVOKED BY IOS
894	(37E)	BITSTRING	2	PSARV37E	- RESERVED
896	(380)	CHARACTER	64	PSARSVT (0)	- RECOVERY STACK VECTOR TABLE MDC064
896	(380)	CHARACTER	64	PSARSVTE (0)	- RECOVERY STACK VECTOR TABLE MDC065
896	(380)	ADDRESS	4	PSACSTK	- ADDRESS OF CURRENTLY USED FUNCTIONAL RECOVERY ROUTINE (FRR) STACK MDC061
900	(384)	ADDRESS	4	PSANSTK	- ADDRESS OF NORMAL FRR STACK MDC062
904	(388)	ADDRESS	4	PSASSTK	- ADDRESS OF SVC-I/O-DISPATCHER FRR STACK MDC063
908	(38C)	ADDRESS	4	PSASSAV	- ADDRESS OF INTERRUPTED STACK SAVED BY SVC, I/O, DISPATCHER MDC066
912	(390)	ADDRESS	4	PSAMSTK	- ADDRESS OF MCH FRR STACK MDC067
916	(394)	ADDRESS	4	PSAMSAV	- ADDRESS OF INTERRUPTED STACK SAVED BY MCH MDC068
920	(398)	ADDRESS	4	PSAPSTK	- ADDRESS OF PROGRAM CHECK FLIH FRR STACK MDC069
924	(39C)	ADDRESS	4	PSAPSAV	- ADDRESS OF INTERRUPTED STACK SAVED BY PROGRAM CHECK FLIH MDC070
928	(3A0)	ADDRESS	4	PSAESTK1	- ADDRESS OF EXTERNAL FLIH FRR STACK FOR NON-RECURSIVE ENTRIES MDC071
932	(3A4)	ADDRESS	4	PSAESAV1	- ADDRESS OF INTERRUPTED STACK SAVED BY EXTERNAL FLIH FOR NON-RECURSIVE ENTRIES MDC072
936	(3A8)	ADDRESS	4	PSAESTK2	- ADDRESS OF EXTERNAL FLIH FRR STACK FOR FIRST LEVEL RECURSIONS MDC073
940	(3AC)	ADDRESS	4	PSAESAV2	- ADDRESS OF INTERRUPTED STACK SAVE BY EXTERNAL FLIH FOR FIRST LEVEL RECURSIONS MDC074
944	(3B0)	ADDRESS	4	PSAESTK3	- ADDRESS OF EXTERNAL FLIH FRR STACK FOR SECOND LEVEL RECURSIONS AND ACR MDC075
948	(3B4)	ADDRESS	4	PSAESAV3	- ADDRESS OF INTERRUPTED STACK SAVED BY EXTERNAL FLIH FOR SECOND LEVEL RECURSIONS MDC076
952	(3B8)	ADDRESS	4	PSARSTK	- ADDRESS OF RESTART FLIH FRR STACK MDC077
956	(3BC)	ADDRESS	4	PSARSAV	- ADDRESS OF INTERRUPTED STACK SAVED BY RESTART FLIH MDC078
960	(3C0)	DBL WORD	8	(0)	- ALIGN PSALWPSW TO DOUBLE WORD
960	(3C0)	BITSTRING	8	PSALWPSW	- PSW OF WORK INTERRUPTED WHEN A RESTARTABLE WAIT STATE IS LOADED OWNERSHIP: LDWT
968	(3C8)	DBL WORD	8	PSARV3C8	Reserved
976	(3D0)	ADDRESS	4	PSATSTK	- ADDRESS OF RTM RECOVERY STACK. SERIALIZATION: NONE - THE FIELD IS INITIALIZED AT IPL/VARY CPU ONLINE TIME ONLY. OWNER: RTM.
980	(3D4)	ADDRESS	4	PSATSAV	- ADDRESS OF ERROR STACK SAVED BY RTM WHEN SWITCHING TO RTM RECOVERY STACK. OWNERSHIP: RTM
984	(3D8)	ADDRESS	4	PSAASTK	- ADDRESS OF ACR FRR STACK. OWNERSHIP: ACR
988	(3DC)	ADDRESS	4	PSAASAV	- ADDRESS OF INTERRUPT STACK SAVED BY ACR. OWNERSHIP: ACR
992	(3E0)	DBL WORD	8	(0)	- ALIGN PSARTPSW TO DOUBLE WORD
992	(3E0)	BITSTRING	8	PSARTPSW	- RESUME PSW FOR RTM SETRP RETRY OPTION OWNERSHIP: RTM
1000	(3E8)	BITSTRING	8	PSARV3E8	- RESERVED
1008	(3F0)	SIGNED	4	(0)	- ALIGN PSASFACC TO FULL WORD MDC123
1008	(3F0)	BITSTRING	4	PSASFACC	- SETFRR ABEND COMPLETION CODE USED WHEN A SETFRR ADD IS ISSUED AGAINST A FULL FRR STACK MDC123

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
1012	(3F4)	SIGNED	4	PSALSFCC	- L 1,PSASFACC INSTRUCTION TO LOAD REGISTER 1 WITH THE SETFRR ABEND COMPLETION CODE IN PSASFACC
1016	(3F8)	SIGNED	2	PSASVC13	- AN SVC 13 INSTRUCTION
1018	(3FA)	BITSTRING	1	PSAFPFL	- See LCCAFPFL
1019	(3FB)	BITSTRING	1	PSAINTE	- FLAGS FOR CPU TIMER (MDC466)
		1...		PSANUIN	"X'80'" - CPU TIMER CANNOT BE USED (MDC467)
1020	(3FC)	SIGNED	4	PSARTM1R	- STOSM PSARTM1S,X'00' INSTRUCTION EXECUTED BEFORE RTM GOES TO THE RETRY ROUTINE FOR THE FRRS. OWNERSHIP: RTM. SERIALIZATION: DISABLEMENT.
1020	(3FC)	X'3FD'	0	PSARTM1M	"PSARTM1R+1,1,C'X'" - LABEL FOR SYSTEM MASK USED IN ABOVE INSTRUSTION. OWNERSHIP: RTM.
1024	(400)	BITSTRING	8	PSARV400	- Reserved
1032	(408)	ADDRESS	4	PSAATCVT	- ADDRESS OF VTAM ATCVT. INITIALIZED BY VTAM. (MDC300)
1036	(40C)	ADDRESS	4	PSAWTCOD	- WAIT STATE CODE LOADED OWNERSHIP: LDWT
1040	(410)	ADDRESS	4	PSASCWA	- ADDRESS OF SUPERVISOR CONTROL CPU RELATED WORK SAVE AREA
1044	(414)	ADDRESS	4	PSARSMSA	- ADDRESS OF RSM CPU RELATED WORK SAVE AREA
1048	(418)	DBL WORD	8	(0)	- ALIGN PSASCPSW TO DOUBLE WORD (MDC325)
1048	(418)	BITSTRING	4	PSASCPSW	- MODEL PSW OWNERSHIP: SUPERVISOR CONTROL. SERIALIZATION: DISABLED.
1052	(41C)	ADDRESS	4		- MODEL PSW SECOND HALF (MDC325)
1056	(420)	DBL WORD	8	(0)	- ALIGN PSASMPSW TO DOUBLE WORD (MDC326)
1056	(420)	BITSTRING	4	PSASMPSW	- SRB DISPATCH PSW (MDC326)
1060	(424)	ADDRESS	4		- DISPATCH PSW SECOND HALF (MDC326)
1064	(428)	DBL WORD	8	(0)	- ALIGN PSAPCPSW TO DOUBLE WORD YM0943
1064	(428)	BITSTRING	16	PSAPCPSW	= TEMPORARY OLD PSW STORAGE FOR PROGRAM FLIH (MDC129)
1080	(438)	BITSTRING	8	PSARV438	= Reserved
1088	(440)	DBL WORD	8	(0)	- Align PSARSP16 to double word
1088	(440)	BITSTRING	16	PSAMCX16	- MCH exit PSW16
1104	(450)	BITSTRING	16	PSARSP16	- Resume PSW field for restart interrupt handler
1120	(460)	BITSTRING	16	PSAPSWSV16	- PSW SAVE AREA FOR DISPATCHER AND ACR
1120	(460)	DBL WORD	8		- Part of PSAPSWSV16
1128	(468)	BITSTRING	8	PSAPSWSV	- PSW SAVE AREA FOR DISPATCHER AND ACR (MDC319)
1136	(470)	DBL WORD	8	(0)	- ALIGN PSACPUT TO DOUBLE WORD (MDC328)
1136	(470)	BITSTRING	8	PSACPUT	- SUPERVISOR CPU TIMER SAVE AREA (MDC328)
1144	(478)	SIGNED	4	PSAPCFUN (0)	- PROGRAM FLIH RECURSION FLAGS (MDC613)
1144	(478)	BITSTRING	1	PSAPCFB1	- FUNCTION VALUE (MDC484)
		1		PSAPCMC	"X'01'" - MC INTERRUPT (MDC605)
		.... ..1.		PSAPCPF	"X'02'" - PAGE FAULT
		11		PSAPCPS	"X'03'" - PER/SPACE SWITCH INTERRUPT
		.... .1..		PSAPCAD	"X'04'" - ADDRESSING EXCEPTION (MDC488)
		.... .1.1		PSAPCTR	"X'05'" - TRANSLATION EXCEPTION (MDC489)
		.... .11.		PSAPCPC	"X'06'" - PROGRAM CHECK (MDC490)
		.... .111		PSAPCTRC	"X'07'" - TRACE INTERRUPT
		.... 1...		PSAPCAF	"X'08'" - NEW VALUE FOR PROGRAM INTERRUPT FLAG. ASYMMETRIC FEATURE OPERATION EXCEPTION.
		.... 1..1		PSAPCLS	"X'09'" - LINKAGE STACK INTERRUPT FUNCTION VALUE FOR PROGRAM FLIH.
		.... 1.1.		PSAPCART	"X'0A'" - ACCESS REGISTER TRANSLATION INTERRUPT VALUE FOR PROGRAM FLIH.
		.... 1.11		PSAPCDPF	"X'0B'" - DISABLED PAGE/SEGMENT FAULT
		.... 11..		PSAPCDAR	"X'0C'" - DISABLED ART PIC X'2B' FUNCTION VALUE FOR PROGRAM FLIH.
		.... 11.1		PSAPCPRT	"X'0D'" - Protection exception function value
		.... 11.1		PSAPCMAX	"X'0D'" - MAXIMUM VALID FUNCTION VALUE
1145	(479)	BITSTRING	1	PSAPCFB2	- FUNCTION FLAGS (MDC491)
		1...		PSAPCTRR	"X'80'" - TRACE INTERRUPT RECURSION HANDLER FLAG.
		.1..		PSAPCMT	"X'40'" - TRACE RECURSION FLAG (MDC493)
1146	(47A)	BITSTRING	1	PSAPCFB3	- RECURSION FLAGS (MDC494)
		1...		PSAPCP1	"X'80'" - FIRST LEVEL PROGRAM CHECK (MDC495)
		.1..		PSAPCP2	"X'40'" - SECOND LEVEL PROGRAM CHECK (MDC496)
		..1.		PSAPCDE	"X'20'" - DAT ERROR CONDITION (MDC497)
		...1		PSAPCLV	"X'10'" - 0=REGISTERS IN LCCA, 1=REGISTERS NOT IN LCCA. (MDC498)
		.... 1...		PSAPCP3	"X'08'" - THIRD LEVEL PROGRAM CHECK (MDC604)
		.... .1..		PSAPCP4	"X'04'" - FOURTH LEVEL PROGRAM CHECK (MDC604)
		.... ..1.		PSAPCPFR	"X'02'" - RECURSIVE PAGE FAULT INDICATOR
		1		PSAPCAVR	"X'01'" - RECURSIVE ASTE VALIDITY INDICATOR
1147	(47B)	BITSTRING	1	PSAPCFB4	- RECURSION FLAGS
		1...		PSAPCDNV	"X'80'" - DUCT validity indicator
		.1..		PSAPCLSR	"X'40'" - IEAVLSIH has invoked IARPTEPR and recursion into RSM is not permitted.
1148	(47C)	SIGNED	2	PSAPCPS2	- PASID AT TIME OF SECOND LEVEL INTERRUPT (MDC604)
1150	(47E)	BITSTRING	2	PSARV47E	- RESERVED
1152	(480)	BITSTRING	24	PSAPCWKA	- Work area for PC FLIH. Must be qword-aligned
1176	(498)	SIGNED	2	PSAPCPS3	- PASID AT TIME OF THIRD LEVEL INTERRUPT (MDC604)
1178	(49A)	SIGNED	2	PSAPCPS4	- PASID AT TIME OF FOURTH LEVEL INTERRUPT

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
1180	(49C)	SIGNED	4	PSAMODEW (0)	- Word label to address PSAMODE.
1180	(49C)	BITSTRING	1		- RESERVED - FIRST BYTE OF PSAMODEW
1181	(49D)	BITSTRING	1	PSAMFLGS	- SECOND BYTE OF PSAMODEW (MDC604)
		1...		PSANSS	"X'80'" - ENABLED UNLOCKED TASK WITH FRR (MDC605)
		.1..		PSAPRSRB	"X'40'" - Preemptable-class SRB
1182	(49E)	BITSTRING	1	PSAMODEH	- SECOND HALFWORD OF PSAMODEW. FIRST BYTE MUST BE ZERO FOR I/O AND EXTERNAL FLIHS. (MDC613)
1183	(49F)	BITSTRING	1	PSAMODE	- SYSTEM MODE INDICATOR AND DISPLACEMENT INTO TABLES FOR EXTERNAL AND I/O FLIHS
		....		PSATASKM	"X'00'" - TASK MODE VALUE (MDC338)
		.... .1..		PSASRBM	"X'04'" - SRB MODE VALUE (MDC339)
		.... 1...		PSAWAITM	"X'08'" - WAIT MODE VALUE (MDC340)
		...1		PSADISPM	"X'10'" - DISPATCHER MODE VALUE (MDC342)
		..1.		PSAPSRBM	"X'20'" - PSEUDO SRB MODE FLAG BIT. THIS BIT MAY BE ON WITH ANY OF ABOVE MODE VALUES. (MDC343)
1184	(4A0)	BITSTRING	3		- RESERVED
1187	(4A3)	BITSTRING	1	PSASTNSM	- STNSM TARGET USED BY EXIT PROLOGUE (MDC346)
1188	(4A4)	SIGNED	4	PSALKJW	- LOCAL LOCK RELEASE SRB JOURNAL WORD (MDC612)
1192	(4A8)	DBL WORD	8	PSADZERO (0)	- DOUBLEWORD OF ZERO (MDC612)
1192	(4A8)	SIGNED	4	PSAFZERO	- FULLWORD OF ZERO (MDC612)
1196	(4AC)	SIGNED	4		- FULLWORD OF ZERO (MDC612)
1200	(4B0)	SIGNED	4	PSALKJW2	- CMS LOCK RELEASE JOURNAL WORD. (MDC613)
1204	(4B4)	ADDRESS	4	PSALKPT	"V(IEALKPT)" - SETLOCK TEST,TYPE=HIER PARAMETER LIST TABLE. OWNERSHIP: LOCK MANAGER. SERIALIZATION: NONE.
1208	(4B8)	ADDRESS	4	PSALAA	- LE Anchor Area. Owner: LE
1212	(4BC)	ADDRESS	4	PSALIT2	"V(IEAVELT2)" - POINTER TO THE EXTENDED LOCK INTERFACE TABLE.
1216	(4C0)	ADDRESS	4	PSAECLTP	"V(IEACLTE)" - POINTER TO THE EXTENDED CURRENT LOCKS HELD TABLE.
1220	(4C4)	SIGNED	4	PSACLHSE (0)	- CURRENT LOCKS HELD STRING EXTENSION
1220	(4C4)	BITSTRING	1	PSALHEB0	- BYTE 0 OF THE CURRENT LOCK HELD STRING EXTENSION.
		1...		PSABLSDI	"X'80'" - BMFLSD LOCK INDICATOR.
		.1..		PSAXDSI	"X'40'" - XCFDS LOCK INDICATOR.
		..1.		PSAXRESI	"X'20'" - XCFRES LOCK INDICATOR.
		...1		PSAXQI	"X'10'" - XCFQ LOCK INDICATOR.
		.... 1...		PSAESETI	"X'08'" - ETRSET LOCK INDICATOR.
		.... .1..		PSAIXSCI	"X'04'" - IXLSCH LOCK INDICATOR.
		.... ..1.		PSAIXSHI	"X'02'" - IXLSHR LOCK INDICATOR.
		1		PSAIXDSI	"X'01'" - IXLDS LOCK INDICATOR.
1221	(4C5)	BITSTRING	1	PSALHEB1	- BYTE 1 OF THE CURRENT LOCK HELD STRING EXTENSION.
		1...		PSAIXLLI	"X'80'" - IXLSHELL LOCK INDICATOR.
		.1..		PSAULUTI	"X'40'" - IOSULUT LOCK INDICATOR.
		..1.		PSAIXLRI	"X'20'" - IXLREQST LOCK INDICATOR.
		...1		PSAWLMRI	"X'10'" - WLMRES LOCK INDICATOR
		.... 1...		PSAWLMQI	"X'08'" - WLMQ LOCK INDICATOR.
		.... .1..		PSACNTXI	"X'04'" - CONTEXT LOCK INDICATOR
		.... ..1.		PSAREGSI	"X'02'" - REGSRV LOCK INDICATOR.
		1		PSASSDLI	"X'01'" - SSD LOCK INDICATOR.
1222	(4C6)	BITSTRING	1	PSALHEB2	- BYTE 2 OF THE CURRENT LOCK HELD STRING EXTENSION.
		1...		PSAGRSLI	"X'80'" - GRSINT lock indicator
		.1..		PSAPSLK1	"X'40'" - HCWPSLK1 lock indicator
		..1.		PSAPNLK1	"X'20'" - HCWPNLK1 lock indicator
		...1		PSAIOLK1	"X'10'" - HCWIOLK1 lock indicator
		.... 1...		PSAPXLK1	"X'08'" - HCWPXLK1 lock indicator
		.... .1..		PSADRLK3	"X'04'" - HCWDRLK3 lock indicator
		.... ..1.		PSADRLK2	"X'02'" - HCWDRLK2 lock indicator
		1		PSADRLK1	"X'01'" - HCWDRLK1 lock indicator
1223	(4C7)	BITSTRING	1	PSALHEB3	- BYTE 3 OF THE CURRENT LOCK HELD STRING EXTENSION.
		1...		PSASRMEI	"X'80'" - SRMENQ lock indicator
1224	(4C8)	BITSTRING	8	PSARV4C8	- RESERVED FOR FUTURE LOCK EXPANSION.
1232	(4D0)	BITSTRING	184	PSARV4D0	- RESERVED.
1416	(588)	BITSTRING	1	PSAHWFB	- HARDWARE FLAG BYTE.
1417	(589)	BITSTRING	1	PSACR0CB	- CR0 CONTROL BYTE USED BY PROTPSA MACRO (MDC425)
		...1		PSAENABL	"X'10'" - TO ENABLE PSA PROTECTION (MDC428)
		....		PSADSABL	"X'00'" - TO DISABLE PSA PROTECTION (MDC429)
1418	(58A)	BITSTRING	2	PSARV58A	- RESERVED
1420	(58C)	SIGNED	4	PSACR0SV	- CR0 SAVE AREA USED BY PROTPSA MACRO (MDC426)
		...1		PSACR0EN	"X'10'" - IF 0, PSA PROTECT DISABLED. IF 1, PSA PROTECT ENABLED. BIT IS IN HIGH-ORDER BYTE OF PSACR0SV. (MDC432)
		1...		PSACR0ED	"X'80'" DAT features. Bit is in PSACR0SV+1
		.... 1...		PSACR0AL	"X'08'" - IF 1, ASN & LX Reuse facility is enabled. Bit is in PSACR0SV+1
		.... .1..		PSACR0FP	"X'04'" - IF 1, extended floating point is enabled. Bit is in PSACR0SV+1
		.... ..1.		PSACR0VI	"X'02'" - IF 1, vector instructions are enabled. Bit is in PSACR0SV+1
1424	(590)	SIGNED	4	PSAPCCR0	- PROGRAM CHECK FLIH CR0 SAVE AREA (MDC427)
1428	(594)	SIGNED	4	PSARCR0	- RESTART FLIH CR0 SAVE AREA (MDC434)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		PSARPEN	"X'10'" - IF 0, PSA PROTECT DISABLED. IF 1, PSA PROTECT ENABLED. BIT IS IN HIGH-ORDER BYTE OF PSARCR0. (MDC435)
1432	(598)	DBL WORD	8	PSASTKE (0)	- CURRENT STACK CONTROL WORD FOR SRB'S AND TYPE 6 SVC'S.
1432	(598)	SIGNED	2	PSATKN	- CURRENT STACK TOKEN (MDC610)
1434	(59A)	SIGNED	2	PSAASD	- CURRENT STACK ADDRESS SPACE DESIGNATOR (MDC610)
1436	(59C)	SIGNED	4	PSASEL	- CURRENT STACK ELEMENTS ADDRESS (MDC610)
1440	(5A0)	DBL WORD	8	(0)	- ALIGN PSASKPSW TO A DOUBLEWORD (MDC604)
1440	(5A0)	BITSTRING	4	PSASKPSW	PCLINK STACK/UNSTACK MODEL PSW (MDC604)
1444	(5A4)	ADDRESS	4	PSASKPS2	- PCLINK PSW ADDRESS (MDC604)
1448	(5A8)	ADDRESS	4	PSACPCLS	- PCLINK WORKAREA - CURRENT STACK HEADER ADDRESS
1452	(5AC)	BITSTRING	4	PSARV5AC	- RESERVED.
1456	(5B0)	ADDRESS	4	PSASCFS	- ADDRESS OF THE SUPERVISOR CONTROL FLIH SAVEAREA.
1460	(5B4)	ADDRESS	4	PSAPAWA	- ADDRESS OF PC/AUTH WORK AREA.
1464	(5B8)	BITSTRING	1	PSASCFB	- SUPERVISOR CONTROL FLAG BYTE.
		1...		PSAIOPR	"X'80'" - INDICATES IF INTERRUPTED TASK SHOULD BE PREEMPTED. USED BY THE I/O FLIH.
		.1..		PSAIORTY	"X'40'" - I/O FLIH RECOVERY FLAG. IF 1, CONTINUE RETRY PROCESSING INSTEAD OF ABENDING
1465	(5B9)	BITSTRING	3	PSARV5B9	- RESERVED
1468	(5BC)	BITSTRING	4	PSACR0M1	MASK OF CR0 WITH EXTERNAL MASK BITS OFF - USED BY WINDOW.
1472	(5C0)	BITSTRING	4	PSACR0M2	MASK OF CR0 WITH ONLY EXTERNAL MASK BITS ON - USED BY WINDOW.
1476	(5C4)	BITSTRING	4	PSARV5C4	- RESERVED
1480	(5C8)	BITSTRING	8	PSA_CR0EMASKOFFEXTINT	Mask of bits to turn off all external interrupts in grande CR0
1488	(5D0)	BITSTRING	8	PSA_CR0EMASKONEXTINT	Mask of bits to turn on all external interrupts in grande CR0
1496	(5D8)	BITSTRING	8	PSA_CR0ESAVEAREA (0)	Save area for grande CR0
1496	(5D8)	BITSTRING	4	PSA_CR0ESAVEAREA_HW	High word save area for high word of CR0
1500	(5DC)	BITSTRING	4	PSA_CR0ESAVEAREA_LW	Low word save area for low word of CR0
1504	(5E0)	BITSTRING	112	PSARV5E0	- RESERVED
1616	(650)	DBL WORD	8	PSA_TIME_ON_CP	- Current SRB's accumulated CPU time on a standard CP. This field must immediately precede PSATIME This field is valid only when there is at least one zAAP/zIIP installed.
1624	(658)	DBL WORD	8	PSATIME	- CURRENT SRB'S ACCUMULATED CPU TIME
1632	(660)	SIGNED	4	PSASRSAV	- ADDRESS OF CURRENT FRR STACK SAVED BY STOP/RESET. (MDC605)
1636	(664)	BITSTRING	12	PSAESC8	- Save area for IEAVESC8
1648	(670)	BITSTRING	8	PSADEXMW	- Work area for dispatcher CR3/4
1656	(678)	BITSTRING	64	PSADSARS	- DISPATCHER ACCESS REGISTER SAVE AREA
1720	(6B8)	DBL WORD	8	PSA_PCFLIH_TRACE_INTERRUPT_CPUT	- Trace interrupt CPU timer saved by IEAVEPCO
1728	(6C0)	DBL WORD	8	PSADTSAV	- CPU TIMER VALUE AT LAST DISPATCH, SRBTIMER REQUEST, CPUTIMER EXPIRATION, OR STATUS SAVE OR RESTORE.
1728	(6C0)	BITSTRING	1	PSAFF6C0 (0)	INITIALIZE FIELD PSADTSAV
1736	(6C8)	DBL WORD	8	(0)	
1736	(6C8)	BITSTRING	16	PSADEXMS (0)	- DISPATCHER CONTROL REGISTER 3 AND 4 SAVE AREA (MDC610)
1736	(6C8)	DBL WORD	8	PSADCR3 (0)	- DISPATCHER CONTROL REGISTER 3 SAVE AREA (MDC610)
1736	(6C8)	SIGNED	4	PSADSINS	- DISPATCHER Secondary ASTE Inst# S/A
1740	(6CC)	SIGNED	4	PSADPKSA (0)	- PKM and SASID
1740	(6CC)	SIGNED	2	PSADPKM	- DISPATCHER PROGRAM KEY MASK SAVE AREA (MDC610)
1742	(6CE)	SIGNED	2	PSADSAS	- DISPATCHER SECONDARY ASID SAVE AREA (MDC610)
1744	(6D0)	DBL WORD	8	PSADCR4 (0)	- DISPATCHER CONTROL REGISTER 4 SAVE AREA (MDC610)
1744	(6D0)	SIGNED	4	PSADPINS	- DISPATCHER Primary ASTE Inst# S/A
1748	(6D4)	SIGNED	4	PSADAXPA (0)	- AX and PASID
1748	(6D4)	SIGNED	2	PSADAX	- DISPATCHER AUTHORIZATION INDEX SAVE AREA. (MDC613)
1750	(6D6)	SIGNED	2	PSADPAS	- DISPATCHER PRIMARY ASID SAVE AREA. (MDC610)
1752	(6D8)	DBL WORD	8	PSAUSEND (0)	END FIRST SET OF ASSIGNED FIELDS SAVED BY ACR.
1752	(6D8)	BITSTRING	200	PSARV6D8	- RESERVED
1952	(7A0)	DBL WORD	8	PSAECVT	Address of ECVT
1960	(7A8)	DBL WORD	8	PSAXCVT	Address of XCVT
1968	(7B0)	DBL WORD	8	(0)	- ALIGN PSADATLK ON DOUBLE WORD
1968	(7B0)	BITSTRING	1	PSADATLK (48)	- AREA FOR DAT-OFF ASSIST LINKAGE CODE
2016	(7E0)	ADDRESS	4	PSADATOF	- REAL STORAGE ADDRESS OF THE DAT-OFF LINKAGE TABLE WHICH IS INITIALIZED BY NIP FOR DAT-ON/DAT-OFF LINKAGE
2020	(7E4)	SIGNED	4	PSADATLN	- LENGTH OF THE DAT-OFF INDEX TABLE (IEAVEDFT)
2024	(7E8)	BITSTRING	4	PSARV7E8	- RESERVED FOR SYSTEM TRACE.
2024	(7E8)	BITSTRING	1	PSAFF7E8 (0)	INITIALIZE FIELD PSARV7E8
2028	(7EC)	BITSTRING	1	PSATRACE	- SYSTEM TRACE FLAGS.
		1...		PSATROFF	"X'80'" - IF ON, SYSTEM TRACE SUSPENDED ON THIS PROCESSOR BECAUSE WAIT TASK DISPATCHED.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
2029	(7ED)	BITSTRING	3	PSARV7ED	- RESERVED FOR SYSTEM TRACE.
2032	(7F0)	ADDRESS	4	PSATBVTR	- REAL ADDRESS OF SYSTEM TRACE BUFFER VECTOR TABLE (TBVT) REPRESENTING THE CURRENT SYSTEM TRACE BUFFER FOR THIS PROCESSOR. OWNERSHIP: SYSTEM TRACE. SERIALIZATION: DISABLEMENT FOR EXTERNAL INTERRUPTS ON THIS PROCESSOR OR THE TRACE SPIN LOCK.
2036	(7F4)	ADDRESS	4	PSATBVTV	- VIRTUAL ADDRESS CORRESPONDING TO PSATBVTR.
2040	(7F8)	ADDRESS	4	PSATRVT	"V(IEAVETVT)" - ADDRESS OF SYSTEM TRACE VECTOR TABLE.
2044	(7FC)	ADDRESS	4	PSATOT	"V(IEAVETOT)" - ADDRESS OF SYSTEM TRACE OPERAND TABLE.
				Comment	
				FETCH PROTECTED KEY 0 AREA LOCATIONS 800 TO FFF HEX	
				End of Comment	
2048	(800)	DBL WORD	8	PSAUS2ST (0)	START SECOND SET OF ASSIGNED FIELDS SAVED BY ACR.
2048	(800)	BITSTRING	16	PSACDSAV (0)	CALLDISP REGISTER SAVE AREA FOR REGISTERS 14 - 1
2048	(800)	SIGNED	4	PSACDSAE	CALLDISP REGISTER 14 SAVE AREA
2052	(804)	SIGNED	4	PSACDSAF	CALLDISP REGISTER 15 SAVE AREA
2056	(808)	SIGNED	4	PSACDSA0	CALLDISP REGISTER 0 SAVE AREA
2060	(80C)	SIGNED	4	PSACDSA1	CALLDISP REGISTER 1 SAVE AREA
2064	(810)	SIGNED	4	PSAGSPSW	GLOBAL SCHEDULE SYSTEM MASK SAVE AREA. OWNERSHIP: SUPERVISOR CONTROL. SERIALIZATION: DISABLEMENT.
2068	(814)	SIGNED	4	PSAGSRGS	GLOBAL SCHEDULE REGISTER SAVE AREA. OWNERSHIP: SUPERVISOR CONTROL. SERIALIZATION: DISABLEMENT.
2072	(818)	BITSTRING	4	PSARV818	- RESERVED
2076	(81C)	SIGNED	4	PSASV01R	IEAVTRG1 register 1 save area.
2080	(820)	SIGNED	4	PSASV14R	IEAVTRG1 register 14 save area.
2084	(824)	SIGNED	4	PSAEMS2R	- REGISTER SAVE AREA OWNERSHIP: MEMORY SWITCH. SERIALIZATION: DISABLED.
2088	(828)	BITSTRING	64	PSATRSAV (0)	- TRACE REGISTER SAVE AREA.
2088	(828)	SIGNED	4	PSATRGR0	- TRACE REGISTER 0 SAVE AREA.
2092	(82C)	SIGNED	4	PSATRGR1	- TRACE REGISTER 1 SAVE AREA.
2096	(830)	SIGNED	4	PSATRGR2	- TRACE REGISTER 2 SAVE AREA.
2100	(834)	SIGNED	4	PSATRGR3	- TRACE REGISTER 3 SAVE AREA.
2104	(838)	SIGNED	4	PSATRGR4	- TRACE REGISTER 4 SAVE AREA.
2108	(83C)	SIGNED	4	PSATRGR5	- TRACE REGISTER 5 SAVE AREA.
2112	(840)	SIGNED	4	PSATRGR6	- TRACE REGISTER 6 SAVE AREA.
2116	(844)	SIGNED	4	PSATRGR7	- TRACE REGISTER 7 SAVE AREA.
2120	(848)	SIGNED	4	PSATRGR8	- TRACE REGISTER 8 SAVE AREA.
2124	(84C)	SIGNED	4	PSATRGR9	- TRACE REGISTER 9 SAVE AREA.
2128	(850)	SIGNED	4	PSATRGRA	- TRACE REGISTER 10 SAVE AREA.
2132	(854)	SIGNED	4	PSATRGRB	- TRACE REGISTER 11 SAVE AREA.
2136	(858)	SIGNED	4	PSATRGRC	- TRACE REGISTER 12 SAVE AREA.
2140	(85C)	SIGNED	4	PSATRGRD	- TRACE REGISTER 13 SAVE AREA.
2144	(860)	SIGNED	4	PSATRGRE	- TRACE REGISTER 14 SAVE AREA.
2148	(864)	SIGNED	4	PSATRGRF	- TRACE REGISTER 15 SAVE AREA.
2152	(868)	BITSTRING	4	PSATRSV1	- Trace Save 1
2156	(86C)	BITSTRING	4	PSATRSVS	- Trace Save for SLIP/PER
2160	(870)	BITSTRING	8	PSATRSV2	- Trace Save 2
2168	(878)	BITSTRING	40	PSARV878	- RESERVED.
2208	(8A0)	BITSTRING	8	PSAGSAVH	- Register save area used by dispatcher
2216	(8A8)	DBL WORD	8	(0)	- ALIGN PSAGSAV TO DOUBLE WORD
2216	(8A8)	BITSTRING	64	PSAGSAV	- REGISTER SAVE AREA USED BY DISPATCHER AND SCHEDULE
2216	(8A8)	BITSTRING	1	PSAFF8A8 (0)	INITIALIZE FIELD PSAGSAV
2280	(8E8)	SIGNED	4	PSASCRG1	- GLOBAL SCHEDULE REGISTER SAVE AREA
2284	(8EC)	SIGNED	4	PSASCRG2	- GLOBAL SCHEDULE REGISTER SAVE AREA
2288	(8F0)	SIGNED	4	PSAGPREG (3)	- REGISTER SAVE AREA FOR SVC FLIH AND SCHEDULE
2300	(8FC)	SIGNED	4	PSARSREG	- RESTART FLIH REGISTER SAVE
2304	(900)	SIGNED	4	PSAPCGR8	- PROGRAM FLIH REGISTER 8 SAVE AREA
2308	(904)	SIGNED	4	PSAPCGR9	- PROGRAM FLIH REGISTER 9 SAVE AREA
2312	(908)	DBL WORD	8	PSAPCGAB (0)	PROGRAM FLIH REG 10-11 SAVE AREA
2312	(908)	SIGNED	4	PSAPCGRA	- PROGRAM FLIH REGISTER 10 SAVE AREA
2316	(90C)	SIGNED	4	PSAPCGRB	- PROGRAM FLIH REGISTER 11 SAVE AREA
2320	(910)	DBL WORD	8	(0)	- ALIGN PSALKSA TO DOUBLE WORD
2320	(910)	BITSTRING	64	PSALKSA (0)	- IEAVELK REGISTER SAVE AREA OWNERSHIP: SUPERVISOR CONTROL SERIALIZATION: DISABLEMENT
2320	(910)	SIGNED	4	PSALKR0	- IEAVELK REGISTER 0 SAVE AREA
2324	(914)	SIGNED	4	PSALKR1	- IEAVELK REGISTER 1 SAVE AREA
2328	(918)	SIGNED	4	PSALKR2	- IEAVELK REGISTER 2 SAVE AREA
2332	(91C)	SIGNED	4	PSALKR3	- IEAVELK REGISTER 3 SAVE AREA
2336	(920)	SIGNED	4	PSALKR4	- IEAVELK REGISTER 4 SAVE AREA
2340	(924)	SIGNED	4	PSALKR5	- IEAVELK REGISTER 5 SAVE AREA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
2344	(928)	SIGNED	4	PSALKR6	- IEAVELK REGISTER 6 SAVE AREA
2348	(92C)	SIGNED	4	PSALKR7	- IEAVELK REGISTER 7 SAVE AREA
2352	(930)	SIGNED	4	PSALKR8	- IEAVELK REGISTER 8 SAVE AREA
2356	(934)	SIGNED	4	PSALKR9	- IEAVELK REGISTER 9 SAVE AREA
2360	(938)	SIGNED	4	PSALKR10	- IEAVELK REGISTER 10 SAVE AREA
2364	(93C)	SIGNED	4	PSALKR11	- IEAVELK REGISTER 11 SAVE AREA
2368	(940)	SIGNED	4	PSALKR12	- IEAVELK REGISTER 12 SAVE AREA
2372	(944)	SIGNED	4	PSALKR13	- IEAVELK REGISTER 13 SAVE AREA
2376	(948)	SIGNED	4	PSALKR14	- IEAVELK REGISTER 14 SAVE AREA
2380	(94C)	SIGNED	4	PSALKR15	- IEAVELK REGISTER 15 SAVE AREA
2384	(950)	DBL WORD	8	(0)	- ALIGN PSASLSA TO DOUBLE WORD
2384	(950)	BITSTRING	72	PSASLSA	- SINGLE LEVEL SAVE AREA USED BY DISABLED ROUTINES WITH NO DEPENDENCY THAT THE SAVE AREA REMAIN INTACT ACROSS A CALL. THIS AREA IS NOT MAINTAINED BY RESTART PROCESSING THAT RESULTS IN AN ABEND OF OF THE INTERRUPTED ROUTINE.
2384	(950)	BITSTRING	1	PSAFF950 (0)	INITIALIZE FIELD PSASLSA
2456	(998)	BITSTRING	64	PSAJSTSA	- SAVE AREA FOR JOB STEP TIMING ROUTINE. OWNERSHIP: SUPERVISOR CONTROL. SERIALIZATION: DISABLEMENT.
2456	(998)	BITSTRING	1	PSAFF998 (0)	INITIALIZE FIELD PSAJSTSA
2520	(9D8)	DBL WORD	8	PSAUS2ND (0)	END SECOND SET OF ASSIGNED FIELDS SAVED BY ACR.
2520	(9D8)	DBL WORD	8	(0)	- ALIGN PSASLKSA TO DOUBLE WORD
2520	(9D8)	BITSTRING	64	PSASLKSA (0)	- IEAVESLK REGISTER SAVE AREA OWNERSHIP: SUPERVISOR CONTROL SERIALIZATION: DISABLEMENT
2520	(9D8)	SIGNED	4	PSASLKR0	- IEAVESLK REGISTER 0 SAVE AREA
2524	(9DC)	SIGNED	4	PSASLKR1	- IEAVESLK REGISTER 1 SAVE AREA
2528	(9E0)	SIGNED	4	PSASLKR2	- IEAVESLK REGISTER 2 SAVE AREA
2532	(9E4)	SIGNED	4	PSASLKR3	- IEAVESLK REGISTER 3 SAVE AREA
2536	(9E8)	SIGNED	4	PSASLKR4	- IEAVESLK REGISTER 4 SAVE AREA
2540	(9EC)	SIGNED	4	PSASLKR5	- IEAVESLK REGISTER 5 SAVE AREA
2544	(9F0)	SIGNED	4	PSASLKR6	- IEAVESLK REGISTER 6 SAVE AREA
2548	(9F4)	SIGNED	4	PSASLKR7	- IEAVESLK REGISTER 7 SAVE AREA
2552	(9F8)	SIGNED	4	PSASLKR8	- IEAVESLK REGISTER 8 SAVE AREA
2556	(9FC)	SIGNED	4	PSASLKR9	- IEAVESLK REGISTER 9 SAVE AREA
2560	(A00)	SIGNED	4	PSASLKRA	- IEAVESLK REGISTER 10 SAVE AREA
2564	(A04)	SIGNED	4	PSASLKRB	- IEAVESLK REGISTER 11 SAVE AREA
2568	(A08)	SIGNED	4	PSASLKRC	- IEAVESLK REGISTER 12 SAVE AREA
2572	(A0C)	SIGNED	4	PSASLKRD	- IEAVESLK REGISTER 13 SAVE AREA
2576	(A10)	SIGNED	4	PSASLKRE	- IEAVESLK REGISTER 14 SAVE AREA
2580	(A14)	SIGNED	4	PSASLKRF	- IEAVESLK REGISTER 15 SAVE AREA
2584	(A18)	BITSTRING	36	PSARVA18	- RESERVED
2620	(A3C)	BITSTRING	64	PSASCSAV	IEAVESC0 save area
2684	(A7C)	BITSTRING	1	PSASFLGS	Schedule flags Ownership: Supervisor Control Serialization: Disablement
		1...		PSASCHDA	"X'80'" Schedule is active
		.1..		PSAMCHA	"X'40'" Machine Check is active
		..1.		PSARSTA	"X'20'" Restart is active
		...1		PSAEGRA	"X'10'" Global Recovery is active
		.... 1...		PSARTMA	"X'08'" Selected RTM functions are active
		.... .1..		PSADONTGETWEB	"X'04'" A WEB or WEBQLOCK is held. IEAVESC0 should not obtain a WEB.
2685	(A7D)	BITSTRING	1	PSAMISCF	Miscellaneous flags set ONLY at IPL. Ownership: Supervisor Control Serialization: None
		1...		PSAALR	"X'80'" Equivalent to CVTALR
2686	(A7E)	BITSTRING	2	PSARVA7E	Reserved for future use - SC1C5
2688	(A80)	BITSTRING	188	PSARVA80	- RESERVED
2876	(B3C)	ADDRESS	4	PSAGSCH7	"V(IEAVESC7)" - ENABLED GLOBAL SCHEDULE ENTRY POINT
2880	(B40)	ADDRESS	4	PSAGSCH8	"V(IEAVESC8)" - DISABLED GLOBAL SCHEDULE ENTRY POINT
2884	(B44)	ADDRESS	4	PSALSCH1	"V(IEAVESC1)" - ENABLED SCHEDULE ENTRY POINT (MDC371)
2888	(B48)	ADDRESS	4	PSALSCH2	"V(IEAVESC2)" - DISABLED SCHEDULE ENTRY POINT (MDC372)
2892	(B4C)	ADDRESS	4	PSASVT	"V(IEAVESVT)" - ADDRESS OF SUPERVISOR VECTOR TABLE (MDC373)
2896	(B50)	ADDRESS	4	PSASVTX	"V(IEAVSVTX)" Address of Supervisor Vector Table extension. SERIALIZATION: None OWNERSHIP: Supervisor Control
2900	(B54)	BITSTRING	8	PSAFAFRR (0)	Fast FRR fields. These fields are for IBM use only.
2900	(B54)	ADDRESS	4	PSAFFRR	Fast FRR address. This field is for IBM use only. Serialization: CPU Lock, PSAFFRRS must be set before PSAFFRR Ownership: RTM
2904	(B58)	ADDRESS	4	PSAFFRRS	Fast FRR stack. This field is for IBM use only. Serialization: CPU Lock, PSAFFRRS must be set before PSAFFRR Ownership: RTM
2908	(B5C)	BITSTRING	36	PSARVB5C	- Reserved
2944	(B80)	DBL WORD	8	(0)	- ALIGN PSASTAK TO DOUBLE WORD MDC118
2944	(B80)	BITSTRING	1	PSASTAK (88)	- NORMAL FRR STACK
4056	(FD8)	BITSTRING	1	PSARVFD8 (40)	- RESERVED FOR EXPANSION OF PSASTAK
4096	(1000)	DBL WORD	8	PSAEND (0)	- END OF PSA (MDC612)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dec)	Description

Comment

```
  IHAPSAE_1:;
  START OF SPECIFICATIONS
   PROPRIETARY_STATEMENT
 01 PROPRIETARY STATEMENT=
  LICENSED MATERIALS - PROPERTY OF IBM
  5650-ZOS COPYRIGHT IBM CORP. 2000, 2013
  STATUS= HBB7790
   END_OF_PROPRIETARY_STATEMENT
01 DESCRIPTIVE NAME: PSA Extension (z/Architecture)
02 ACRONYM: PSAE
01 MACRO NAME: IHAPSAE
01 EXTERNAL CLASSIFICATION:
01 NOTPI: BASE
01 PI: FIELDS FlceFacilitiesList
01 END OF EXTERNAL CLASSIFICATION:
01 DSECT NAME:
    FLCESAME
01 COMPONENT: SUPERVISOR CONTROL (SC1C5)
01 EYE-CATCHER: NONE
01 STORAGE ATTRIBUTES:
02 SUBPOOL: N/A
02 KEY: N/A
02 RESIDENCY: N/A
01 SIZE:
            FLCESAME -- X'0200' bytes
01 CREATED BY:
      IEAVFX00
      IEAVNIP0
      IEEVCPRA
01 POINTED TO BY:
      The PSAE maps the storage that starts at location 0 for the
      related processor.
01 SERIALIZATION:
      Disablement.
      None needed for FlceFacilitiesList.
01 FUNCTION:
02 Maps the z/Architecture format of the first page of the PSA.
02 This macro is automatically included when IHAPSA is included.
01 METHOD OF ACCESS:
02 ASM:
     IHAPSAE
         DSECT=YES|NO -- Request DSECT definition
         PSAE=YES|NO|COND -- Request PSAE mapping
                     USING on PSAE
    Default: DSECT=YES,PSAE=YES
    Notes: name=YES => expand
           name=NO => do not expand
           name=COND => expand only if DSECT=YES
02 PL/X:
     %INCLUDE SYSLIB(IHAPSAE)
01 DELETED BY: N/A
01 FREQUENCY: N/A
01 DEPENDENCIES: None
01 NOTES: The FLCX can also be used to map the second 512 bytes
         of the area saved by Store Status at Address.
01 DISTRIBUTION LIBRARY: AMACLIB
```

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
01 CHANGE ACTIVITY:					
$L0=64BITCBG HBB7703 971101 PD00XB: AR 8537					
$L1=64BITSUP HBB7703 971101 PD00XB: AR 8537					
$P1= HBB7703 990331 PDHC : AR-8660-02 and nits					
$L2=LXREU HBB7709 030801 PD00XB: ASN-and-LX-Reuse Facility					
$L3=WILDB HBB7720 040303 PD00XB: Breaking Event Address					
$P2=MExxxxx HBB7720 041018 PD00XB: Larger Facility List					
$P3=ME02847 HBB7720 050203 PD00XB: STCKF					
$L4=ECT HBB7730 050407 PD00XB: Extract CPU Time					
$L5=AFFDISP HBB7730 050501 PD00GD: Affinity Dispatcher					
$L6=ME05881 HBB7730 060220 U2IAXZ: sense-running-status					
$01=OA11783 HBB7720 060203 PD00HL: Conditional SSKE					
$L7=DCRB165 HBB7740 060425 PD00I6: CPOOL MultiHdr Support					
$L8=LARGEPG HBB7740 060915 PD002D: Large Page Support					
$P4=ME09727 HBB7740 070319 PD00XB: More FlceFacilitiesList					
$02=OA20922 HBB7740 070730 PD002C: Add FLCEEDATFEAT					
$L9=HIS HBB7750 070830 PD00D0: HIS FLCE Bits					
$LA=MExxxxx HBB7760 080401 PD00XB: FlceCryptoAPQAI					
$LB=LI1113 HBB7760 080714 PDHC: FlceAISI					
$P5=ME15014 HBB7760 081223 PDHC: Move FlceAISI, add FlceAEN and FlceAIS					
$LC=ME15840 HBB7770 090327 PD00KP: SCLP function to PSA/PSAE					
$LD=ME16076 HBB7770 090501 PD00KP: Add parsing facility bit, move FlceTCSF					
$LE=ME16847 HBB7770 090804 PD00XZ: FlceIPTERange					
$LF=ME17350 HBB7770 091002 PD00IN: FlceNonQKeySetting					
$LG=ME17371 HBB7770 091006 PD00XB: FlceCMPEF					
$LH=ME17542 HBB7770 091008 PD00XB: AEFSI					
$LI=SVA2438 HBB7770 091215 PD002D: SLIP ZAD Support					
$LJ=ME18513 HBB7780 100121 PD00XB: FlceMSA4					
$LK=ME21106 HBB7780 110115 PD00XB: FacilitiesList.45					
$03=OA36150 HBB7780 110405 PD00D0: FlceDistinctOperands Comment					
$LL=HISSERV HBB7790 110120 PD00BS: Enhanced Monitor Facility					
$LN=RI HBB7790 110320 PD00BS: FlceRI					
$LO=ME22179 HBB7790 110801 PD00XB: Comment update					
Caution: Update IEAVPPSA upon adding fields to first 4K of PSA					
END OF SPECIFICATIONS					
%					
				End of Comment	
0	(0)	DBL WORD	8	FLCESAME (0)	FLCE 0x: defined by architecture
0	(0)	CHARACTER	8	FLCEIPPSW	FLCE 0x: IPL PSW
8	(8)	CHARACTER	8	FLCEICCW1	FLCE 8x: IPL CCW1
16	(10)	CHARACTER	8	FLCEICCW2	FLCE 10x: IPL CCW1
24	(18)	CHARACTER	104	FLCER018	FLCE 18x: reserved
128	(80)	CHARACTER	4	FLCEEPARM	FLCE 80x: External interruption parameter
132	(84)	CHARACTER	2	FLCECPUAD	FLCE 84x: CPU address
134	(86)	CHARACTER	2	FLCEEICODE	FLCE 86x: External interruption code
136	(88)	CHARACTER	4	FLCESDATA	FLCE 88x: Additional SVC interruption data
136	(88)	CHARACTER	2	FLCESDATABYTE0	FLCE 88x:
136	(88)	CHARACTER	1		FLCE 88x: Reserved
137	(89)	BITSTRING	1	FLCESILC	FLCE 89x: SVC interruption length code
				Comment	
Bit definitions:					
				End of Comment	
		.... .111		FLCESILCB	"X'07'" FLCE 89x: Significant bits in ILC. Last bit is always zero
138	(8A)	CHARACTER	2	FLCESICODE	FLCE 8Ax: SVC interruption code
140	(8C)	CHARACTER	4	FLCEPDATA	FLCE 8Cx: Additional Program interruption data
140	(8C)	CHARACTER	2	FLCEPDATABYTE0	FLCE 8Cx:
140	(8C)	CHARACTER	1		FLCE 8Cx: Reserved
141	(8D)	BITSTRING	1	FLCEPILC	FLCE 8Dx: Program interruption length code
				Comment	
Bit definitions:					
				End of Comment	
		.... .111		FLCEPILCB	"X'07'" FLCE 8Dx: Significant bits in ILC. Last bit is always zero

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
142	(8E)	CHARACTER	2	FLCEPICODE	FLCE 8Ex: Program interruption code
142	(8E)	BITSTRING	1	FLCEPICODE0	FLCE 8Ex: Exception extension code
143	(8F)	BITSTRING	1	FLCEPICODE1	FLCE 8Fx: 8-bit interruption code
					Comment
					Bit definitions:
					End of Comment
		1...		FLCEPIPER	"X'80'" FLCE 8Fx: PER interruption code
		.1..		FLCEPIMC	"X'40'" FLCE 8Fx: Monitor Call interruption code
		..11 1111		FLCEPIPC	"X'3F'" FLCE 8Fx: An unsolicited program interruption has occurred if any of these bits are on
144	(90)	CHARACTER	4	FLCEPIINFORMATION	
					FLCE 90x:
144	(90)	CHARACTER	3		
147	(93)	BITSTRING	1	FLCEDXC	FLCE 93x: Data exception code for PI 7
148	(94)	CHARACTER	2	FLCEMCNUM	FLCE 94x: Monitor class number
150	(96)	CHARACTER	2	FLCEPERCODE	FLCE 96x: PER code
150	(96)	BITSTRING	1	FLCEPERCODE0	FLCE 96x: Byte 0
					Comment
					Bit definitions:
					End of Comment
		1...		FLCEPERSB	"X'80'" FLCE 96x: PER successful branch event
		.1..		FLCEPERIF	"X'40'" FLCE 96x: PER instruction fetch event
		..1.		FLCEPERSA	"X'20'" FLCE 96x: PER storage alteration event
		.... 1...		FLCEPERSAR	"X'08'" FLCE 96x: PER storage alteration using real event
		.... .1..		FLCEPERZAD	"X'04'" FLCE 96x: PER zero address detection
		.... ..1.		FLCEPERTRANSACTIONEND	
					"X'02'"
151	(97)	BITSTRING	1	FLCEPERATMID	FLCE 97x: PER addressing and translation mode ID
					Comment
					Bit definitions:
					End of Comment
		1...		FLCEPERPSW4	"X'80'" FLCE 97x: PER PSW bit 4
		.1..		FLCEPERATMIDVALID	
					"X'40'" FLCE 97x: When 1, the ATMID bits are valid
		..1.		FLCEPERPSW32	"X'20'" FLCE 97x: PER PSW bit 32
		...1		FLCEPERPSW5	"X'10'" FLCE 97x: PER PSW bit 5
		.... 1...		FLCEPERPSW16	"X'08'" FLCE 97x: PER PSW bit 16
		.... .1..		FLCEPERPSW17	"X'04'" FLCE 97x: PER PSW bit 17
		11		FLCEPERASCEID	
					"X'03'" FLCE 97x: PER ASCE identification. If a storage alteration event when DAT is on, identifies the ASCE used: '00' - primary ASCE '01' - AR-specified AR. '10' - secondary ASCE '11' - home ASCE
152	(98)	CHARACTER	8	FLCEPER	FLCE 98x: PER address
152	(98)	CHARACTER	4	FLCEPERW0	FLCE 98x: PER address word 0
156	(9C)	ADDRESS	4	FLCEPERW1	FLCE 9Cx: PER address word 1
160	(A0)	BITSTRING	1	FLCEEAID	FLCE A0x: Exception access ID (The AR number involved in the translation exception when bits 30-31 of the TEA='01'). On a PIC 2C when ALRF is installed, additional bits are set
					Comment
					Bit definitions:
					End of Comment
		1...		FLCEEAID0	"X'80'" Bit 0 of EAID. Zero
		.1..		FLCEEAID1	"X'40'" Bit 1 of EAID. Zero
		..1.		FLCEEAID2	"X'20'" Bit 2 of EAID. Set only when PIC 2C for PTI or for PASN translation on PR
		...1		FLCEEAID3	"X'10'" Bit 3 of EAID. Set only when PIC 2C for SSAIR or for SASN translation on PR
		 1111		FLCEEAID_ARNUM	
					"X'0F'" AR number. Zero when Bit 1 or Bit 2 is set
161	(A1)	BITSTRING	1	FLCEPERAID	FLCE A1x: PER access ID (the access register number involved in the PER storage alteration event)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
162	(A2)	BITSTRING	1	FLCEOPACID	FLCE A2x:
163	(A3)	CHARACTER	1	FLCEAMDID	FLCE A3x: Architecture mode ID (See FLCARCH in IHAPSA)
				Comment	
					Bit definitions:
				End of Comment	
		1		FLCELOEME	"X'01'" Logout is Z/Architecture
164	(A4)	ADDRESS	4	FLCEMPL	FLCE A4x: MPL address
168	(A8)	CHARACTER	8	FLCETEID	FLCE A8x: Translation exception identification
168	(A8)	CHARACTER	8	FLCETEA	FLCE A8x: Translation exception address
168	(A8)	CHARACTER	6		
174	(AE)	BITSTRING	1	FLCETEA6	FLCE AEx: Byte 6 of FlceTEA
				Comment	
					Bit definitions:
				End of Comment	
		.... 11..		FLCEAEFSI	"X'0C'" Access-exception Fetch/Store indicator: 00 -- not determined. 01 -- store. 10 -- fetch. 11 -- reserved
175	(AF)	BITSTRING	1	FLCETEA7	FLCE AFx: Byte 7 of FlceTEA
				Comment	
					Bit definitions:
				End of Comment	
		.... 1...		FLCEPEALC	"X'08'" FLCE AFx: Protection exception due to access-list control
		.... .1..		FLCESOPI	"X'04'" FLCE AFx: Suppress on protection indication
		11		FLCETEASTD	"X'03'" FLCE AFx: Segment table designation for TEA: '00' - primary STD '01' - STD was AR-qualified '10' - secondary STD '11' - home STD
168	(A8)	CHARACTER	8	FLCETEASNINFO	
					FLCE A8x: ASN Info
168	(A8)	CHARACTER	6		
174	(AE)	SIGNED	2	FLCETEASN	FLCE AEx: ASN
168	(A8)	CHARACTER	8	FLCETEPCINFO	FLCE A8x: PC Info
168	(A8)	CHARACTER	4		
172	(AC)	SIGNED	4	FLCEPCNUM	FLCE ACx: PC#. Bits 0-10 are 0, bit 11 is 1, and the PC# is in bits 12-31
176	(B0)	CHARACTER	8	FLCEMONITORCODE	
					FLCE B0x: Monitor Code
184	(B8)	CHARACTER	4	FLCESSID	FLCE B8x: Subsystem ID word
188	(BC)	CHARACTER	4	FLCEIOINTPARM	
					FLCE BCx: I/O interruption parameter
192	(C0)	CHARACTER	4	FLCEIOINTID	FLCE C0x: I/O interruption ID
196	(C4)	CHARACTER	4	FLCER0C4	FLCE C4x: Reserved
200	(C8)	CHARACTER	16	FLCEFACILITIESLIST	
					FLCE C8x: Facilities list stored by STFLE. See macro IHAFACL for a more complete definition of the facilities list. If the facilities list exceeds 128 bits, only the area mapped by IHAFACL will contain those additional bits
200	(C8)	BITSTRING	1	FLCEFACILITIESLISTBYTE0	
					FLCE C8x
				Comment	
					Bit definitions:
				End of Comment	
		1...		FLCEZARCHN3	"X'80'" Instructions marked "N3" in the instruction summary are available on the CPU in ESA/390 mode
		1...		FLCEESAMEN3	"X'80'" Instructions marked "N3" in the instruction summary are available on the CPU in ESA/390 mode
		.1..		FLCEZARCHINSTALLED	
					"X'40'" The z/Architecture mode is installed on the CPU
		.1..		FLCEESAMEINSTALLED	
					"X'40'" The z/Architecture mode is installed on the CPU
		..1.		FLCEZARCH	"X'20'" The z/Architecture mode is active on the CPU
		..1.		FLCEESAME	"X'20'" The z/Architecture mode is active on the CPU
		...1		FLCEIDTEINSTALLED	
					"X'10'" IDTE is installed
		.... 1...		FLCEIDTECLEARINGCOMBINEDSEGMENT	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					"X'08'" IDTE does clearing of combined entries upon segment-table entry invalidation
		.... .1..		FLCEIDTECLEARINGCOMBINEDREGION	
					"X'04'" IDTE does clearing of combined entries upon region-table entry invalidation
		.... ..1.		FLCEASNANDLXREUSEINSTALLED	
					"X'02'" The ASN and LX reuse facility is installed on the CPU
		1		FLCESTFLE	"X'01'" STFLE instruction is available
201	(C9)	BITSTRING	1	FLCEFACILITIESLISTBYTE1	
					FLCE C9x
				Comment	
		Bit definitions:			
				End of Comment	
		1...		FLCEEDATFEAT	"X'80'" DAT features
		.1..		FLCESENSERUNNINGSTATUS	
					"X'40'" sense-running-status facility
		..1.		FLCECONDSSKEINSTALLED	
					"X'20'" The conditional SSKE instruction is installed
		...1		FLCECONFIGURATIONTOPOLOGY	
					"X'10'" STSI-enhancement for configuration topology
		.... 1...		FLCECQCIF	"X'08'" 110524
		.... .1..		FLCEIPTERANGE	
					"X'04'" IPTE-range facility is installed
		.... ..1.		FLCENONQKEYSETTING	
					"X'02'" Nonquiescing key-setting facility is installed
		1		FLCEAPFT	"X'01'" The APFT facility is installed / 091111
202	(CA)	BITSTRING	1	FLCEFACILITIESLISTBYTE2	
					FLCE CAx
				Comment	
		Bit definitions:			
				End of Comment	
		1...		FLCEETF2	"X'80'" Extended translation facility 2 is present
		.1..		FLCECRYPTOASSIST	
					"X'40'" The cryptographic assist is present
		.1..		FLCEMESSAGESECURITYASSIST	
					"X'40'" The message security assist is present
		..1.		FLCELONGDISPLACEMENT	
					"X'20'" The long displacement facility is installed in the z/Architecture mode
		...1		FLCELONGDISPLACEMENTHP	
					"X'10'" The long displacement facility has high performance. Bit FlceLongDisplacement will also be on.
		.... 1...		FLCEHFPMAS	"X'08'" The HFP Multiply add/subtract facility is installed
		.... .1..		FLCEEXTENDEDIMMEDIATE	
					"X'04'" The extended immediate facility is installed in the z/Architecture mode
		.... ..1.		FLCEETF3	"X'02'" The extended translaction facility 3 is installed in the z/Architecture mode
		1		FLCEHFPUNNORMEXTENSION	
					"X'01'" The HFP unnormalized extension facility is installed
203	(CB)	BITSTRING	1	FLCEFACILITIESLISTBYTE3	
					FLCE CBx
				Comment	
		Bit definitions:			
				End of Comment	
		1...		FLCEETF2E	"X'80'" ETF2 enhancement is present 031215
		.1..		FLCESTCKF	"X'40'" STCKF enhancement is present
		..1.		FLCEPARSE	"X'20'" Parsing enhancement facility is present
		.... 1...		FLCETCSF	"X'08'" TOD clock steering facility
		.... ..1.		FLCEETF3E	"X'02'" ETF3 enhancement is present 040512
		1		FLCEECTF	"X'01'" Extract Cpu Time facility
204	(CC)	BITSTRING	1	FLCEFACILITIESLISTBYTE4	
					FLCE CCx

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
				Comment	
					Bit definitions:
				End of Comment	
		1...		FLCECSSF	"X'80'" Compare-and-swap-and-store facility
		.1..		FLCECSSF2	"X'40'" Compare-and-swap-and-store facility 2
		..1.		FLCEGENERALINSTEXTENSION	"X'20'" General-Instructions- Extension Facility
		.... 1...		FLCEENHANCEDMONITOR	"X'08'" The Enhanced Monitor facility is supported.
		1		FLCEOBSOLETECPUMEASUREMENT	"X'01'" Obsolete. Meant CPU-measurement facility supported. Use FlceCpuMeasurementCounter & FlceCpuMeasurementSampling
205	(CD)	BITSTRING	1	FLCEFACILITIESLISTBYTE5	FLCE CDx
				Comment	
					Bit definitions:
				End of Comment	
		1...		FLCESETPROGRAMPARM	"X'80'" Set-Program-Parameter facility is supported
		.1..		FLCEFPSEF	"X'40'" Floating-point-support enhancement facility
		..1.		FLCEDFPF	"X'20'" Decimal-floating-point facility
		...1		FLCEDFPFHP	"X'10'" Decimal-floating-point facility high performance
		.... 1...		FLCEPFPO	"X'08'" PFPO instruction 070424
		.... .1..		FLCEDISTINCTOPERANDS	"X'04'" z196 is the first machine with this facility bit on.
		.... .1..		FLCEHIGHWORD	"X'04'"
		.... .1..		FLCELOADSTOREONCONDITION	"X'04'"
		.... .1..		FLCEPOPULATIONCOUNT	"X'04'"
		1		FLCECMPEF	"X'01'" Possible future enhancement
206	(CE)	BITSTRING	1	FLCEFACILITIESLISTBYTE6	FLCE CEx
				Comment	
					Bit definitions:
				End of Comment	
		.1..		FLCEMISCINSTEXT	"X'40'" Bit 49 - Miscellaneous instruction extensions facility.
		.1..		FLCEEXECUTIONHINT	"X'40'" Bit 49 - Execution hint facility.
		.1..		FLCELOADANDTRAP	"X'40'" Bit 49 - Load and trap facility.
		..1.		FLCECONSTRAINEDTX	"X'20'" Bit 50 - Constrained Transactional execution facility
207	(CF)	BITSTRING	1	FLCEFACILITIESLISTBYTE7	FLCE CFx
208	(D0)	BITSTRING	1	FLCEFACILITIESLISTBYTE8	FLCE D0x bits 64-71
				Comment	
					Bit definitions:
				End of Comment	
		1...		FLCERI	"X'80'" FlceRI
		.1..		FLCECRYPTOAPQAI	"X'40'" Crypto AP-Queue adapter interruption
		...1		FLCECPUMEASUREMENTCOUNTER	"X'10'" CPU-measurement counter facility
		.... 1...		FLCECPUMEASUREMENTSAMPLING	"X'08'" CPU-measurement sampling facility
		.... .1..		FLCESCLP	"X'04'" Possible future enhancement
		.... ..1.		FLCEAISI	"X'02'" AISI facility, bit 70
		1		FLCEAEN	"X'01'" AEN facility, bit 71

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
209	(D1)	BITSTRING	1	FLCEFACILITIESLISTBYTE9	FLCE D1x bits 72-79
		Comment			
Bit definitions:					
		End of Comment			
		1...		FLCEAIS	"X'80'" AIS facility, bit 72
		.1..		FLCETRANSACTIONALEXECUTION	"X'40'" Bit 73 - Transactional execution facility
		.... .1..		FLCEMSA4	"X'04'" MSA4 facility, bit 77
		.... ..1.		FLCEEDAT2	"X'02'" Bit 78 - Enhanced Dat-2
210	(D2)	BITSTRING	1	FLCEFACILITIESLISTBYTEA	FLCE D2x
211	(D3)	BITSTRING	1	FLCEFACILITIESLISTBYTEB	FLCE D3x
212	(D4)	BITSTRING	1	FLCEFACILITIESLISTBYTEC	FLCE D4x
213	(D5)	BITSTRING	1	FLCEFACILITIESLISTBYTED	FLCE D5x
214	(D6)	BITSTRING	1	FLCEFACILITIESLISTBYTEE	FLCE D6x
215	(D7)	BITSTRING	1	FLCEFACILITIESLISTBYTEF	FLCE D7x
216	(D8)	CHARACTER	16	FLCER0D8	FLCE D8x: Reserved
232	(E8)	CHARACTER	8	FLCEMCIC	FLCE E8x: Machine check interruption code
240	(F0)	CHARACTER	4	FLCEMCICE	FLCE F0x: Machine check interruption code extension
244	(F4)	CHARACTER	4	FLCEEDCODE	FLCE F4x: External damage code
248	(F8)	CHARACTER	8	FLCEFSA	FLCE F8x: Failing storage address
256	(100)	ADDRESS	8	FLCEEMFCTRARRAYADDR	FLCE 100x: The enhanced monitor facility counter array origin
264	(108)	SIGNED	4	FLCEEMFCTRARRAYSIZE	FLCE 108x: The enhanced monitor facility counter array dimension
268	(10C)	SIGNED	4	FLCEEMFEXCEPTIONCNT	FLCE 10Cx: The enhanced monitor facility exception count
272	(110)	CHARACTER	8	FLCEBEA	FLCE 110x: Breaking event address
280	(118)	CHARACTER	8	FLCER118	FLCE 118x: Reserved
288	(120)	CHARACTER	16	FLCEROPSW	FLCE 120x: Restart old PSW
304	(130)	CHARACTER	16	FLCEEOPSW	FLCE 130x: External old PSW
320	(140)	CHARACTER	16	FLCESOPSW	FLCE 140x: SVC old PSW
336	(150)	CHARACTER	16	FLCEPOPSW	FLCE 150x: Program old PSW
352	(160)	CHARACTER	16	FLCEMOPSW	FLCE 160x: Machine check old PSW
368	(170)	CHARACTER	16	FLCEIOPSW	FLCE 170x: I/O old PSW
384	(180)	CHARACTER	32	FLCER180	FLCE 180x: reserved
416	(1A0)	CHARACTER	16	FLCERNPSW	FLCE 1A0x: Restart new PSW
432	(1B0)	CHARACTER	16	FLCEENPSW	FLCE 1B0x: External new PSW
448	(1C0)	CHARACTER	16	FLCESNPSW	FLCE 1C0x: SVC new PSW
464	(1D0)	CHARACTER	16	FLCEPNPSW	FLCE 1D0x: Program new PSW
480	(1E0)	CHARACTER	16	FLCEMNPSW	FLCE 1E0x: Machine check new PSW
496	(1F0)	CHARACTER	16	FLCEINPSW	FLCE 1F0x: I/O new PSW
496	(1F0)	X'200'	0	FLCESAME_LEN	"*-FLCESAME"

PSA Cross Reference

Name	Hex Offset	Hex Value
EXCODE	86	86
EXNPSW	5C	58
EXOPSW	18	18
FLC	0	0
FLCARCH	A3	0
FLCARSAV	120	0
FLCATMID	97	0
FLCCCSA	E0	0
FLCCRSAV	1C0	0
FLCCTSA	D8	0
FLCCVT	10	
FLCCVT2	4C	
FLCCVT64	48	
FLCDXC	93	
FLCEAEFSI	AE	C
FLCEAEN	D0	1

Name	Hex Offset	Hex Value
FLCEAIS	D1	80
FLCEAISI	D0	2
FLCEAMDID	A3	
FLCEAPFT	C9	1
FLCEASNANDLXREUSEINSTALLED	C8	2
FLCEBEA	110	
FLCECMPEF	CD	1
FLCECONDSSKEINSTALLED	C9	20
FLCECONFIGURATIONTOPOLOGY	C9	10
FLCECONSTRAINEDTX	CE	20
FLCECPUAD	84	
FLCECPUMEASUREMENTCOUNTER		

Name	Hex Offset	Hex Value
	D0	10
FLCECPUMEASUREMENTSAMPLING		
	D0	8
FLCECQCIF	C9	8
FLCECRYPTOAPQAI		
	D0	40
FLCECRYPTOASSIST		
	CA	40
FLCECSSF	CC	80
FLCECSSF2	CC	40
FLCEDFPF	CD	20
FLCEDFPFHP	CD	10
FLCEDISTINCTOPERANDS		
	CD	4
FLCEDXC	93	
FLCEEAID	A0	
FLCEEAID_ARNUM		
	A0	F
FLCEEAID0	A0	80
FLCEEAID1	A0	40
FLCEEAID2	A0	20
FLCEEAID3	A0	10
FLCEECTF	CB	1
FLCEEDATFEAT	C9	80
FLCEEDAT2	D1	2
FLCEEDCODE	F4	
FLCEEICODE	86	
FLCEEMFCTRARRAYADDR		
	100	
FLCEEMFCTRARRAYSIZE		
	108	
FLCEEMFEXCEPTIONCNT		
	10C	
FLCEENHANCEDMONITOR		
	CC	8
FLCEENPSW	1B0	
FLCEEOPSW	130	
FLCEEPARM	80	
FLCEESAME	C8	20
FLCEESAMEINSTALLED		
	C8	40
FLCEESAMEN3	C8	80
FLCEETF2	CA	80
FLCEETF2E	CB	80
FLCEETF3	CA	2
FLCEETF3E	CB	2
FLCEEXECUTIONHINT		
	CE	40
FLCEEXTENDEDIMMEDIATE		
	CA	4
FLCEFACILITIESLIST		
	C8	
FLCEFACILITIESLISTBYTEA		
	D2	
FLCEFACILITIESLISTBYTEB		
	D3	
FLCEFACILITIESLISTBYTEC		
	D4	
FLCEFACILITIESLISTBYTED		
	D5	
FLCEFACILITIESLISTBYTEE		
	D6	
FLCEFACILITIESLISTBYTEF		
	D7	
FLCEFACILITIESLISTBYTE0		
	C8	
FLCEFACILITIESLISTBYTE1		
	C9	
FLCEFACILITIESLISTBYTE2		
	CA	
FLCEFACILITIESLISTBYTE3		
	CB	
FLCEFACILITIESLISTBYTE4		
	CC	
FLCEFACILITIESLISTBYTE5		
	CD	
FLCEFACILITIESLISTBYTE6		
	CE	
FLCEFACILITIESLISTBYTE7		
	CF	
FLCEFACILITIESLISTBYTE8		
	D0	
FLCEFACILITIESLISTBYTE9		
	D1	
FLCEFPSEF	CD	40
FLCEFSA	F8	
FLCEGENERALINSTEXTENSION		
	CC	20
FLCEHFPMAS	CA	8
FLCEHFPUNNORMEXTENSION		
	CA	1
FLCEHIGHWORD	CD	4
FLCEICCW1	8	
FLCEICCW2	10	
FLCEICOD	86	0
FLCEIDTECLEARINGCOMBINEDREGION		
	C8	4
FLCEIDTECLEARINGCOMBINEDSEGMENT		
	C8	8
FLCEIDTEINSTALLED		
	C8	10
FLCEINPSW	1F0	
FLCEIOINTID	C0	
FLCEIOINTPARM		
	BC	
FLCEIOPSW	170	
FLCEIPPSW	0	
FLCEIPTERANGE		
	C9	4
FLCELOADANDTRAP		
	CE	40
FLCELOADSTOREONCONDITION		
	CD	4
FLCELOEME	A3	1
FLCELONGDISPLACEMENT		
	CA	20
FLCELONGDISPLACEMENTHP		
	CA	10
FLCEMCIC	E8	
FLCEMCICE	F0	
FLCEMCNUM	94	
FLCEMESSAGESECURITYASSIST		
	CA	40
FLCEMISCINSTEXT		
	CE	40
FLCEMNPSW	1E0	
FLCEMONITORCODE		
	B0	
FLCEMOPSW	160	
FLCEMPL	A4	
FLCEMSA4	D1	4
FLCENONQKEYSETTING		
	C9	2
FLCENPSW	58	40C0000
FLCEOBSOLETECPUMEASUREMENT		
	CC	1
FLCEOPACID	A2	
FLCEOPSW	18	0
FLCEPARSE	CB	20
FLCEPCNUM	AC	
FLCEPDATA	8C	
FLCEPDATABYTE0		
	8C	
FLCEPEALC	AF	8
FLCEPER	98	
FLCEPERAID	A1	
FLCEPERASCEID		
	97	3

Name	Hex Offset	Hex Value
FLCEPERATMID	97	
FLCEPERATMIDVALID	97	40
FLCEPERCODE	96	
FLCEPERCODE0	96	
FLCEPERIF	96	40
FLCEPERPSW16	97	8
FLCEPERPSW17	97	4
FLCEPERPSW32	97	20
FLCEPERPSW4	97	80
FLCEPERPSW5	97	10
FLCEPERSA	96	20
FLCEPERSAR	96	8
FLCEPERSB	96	80
FLCEPERTRANSACTIONEND	96	2
FLCEPERW0	98	
FLCEPERW1	9C	
FLCEPERZAD	96	4
FLCEPFPO	CD	8
FLCEPICODE	8E	
FLCEPICODE0	8E	
FLCEPICODE1	8F	
FLCEPIINFORMATION	90	
FLCEPILC	8D	
FLCEPILCB	8D	7
FLCEPIMC	8F	40
FLCEPIPC	8F	3F
FLCEPIPER	8F	80
FLCEPNPSW	1D0	
FLCEPOPSW	150	
FLCEPOPULATIONCOUNT	CD	4
FLCERI	D0	80
FLCERNPSW	1A0	
FLCEROPSW	120	
FLCER0C4	C4	
FLCER0D8	D8	
FLCER018	18	
FLCER118	118	
FLCER180	180	
FLCESAME	0	
FLCESAME_LEN	1F0	200
FLCESCLP	D0	4
FLCESDATA	88	
FLCESDATABYTE0	88	
FLCESENSERUNNINGSTATUS	C9	40
FLCESETPROGRAMPARM	CD	80
FLCESICODE	8A	
FLCESILC	89	
FLCESILCB	89	7
FLCESNPSW	1C0	
FLCESOPI	AF	4
FLCESOPSW	140	
FLCESSID	B8	
FLCESTCKF	CB	40
FLCESTFLE	C8	1
FLCETCSF	CB	8
FLCETEA	A8	
FLCETEASN	AE	
FLCETEASNINFO	A8	
FLCETEASTD	AF	3
FLCETEA6	AE	
FLCETEA7	AF	
FLCETEID	A8	
FLCETEPCINFO	A8	
FLCETRANSACTIONALEXECUTION	D1	40
FLCEZARCH	C8	20
FLCEZARCHINSTALLED	C8	40
FLCEZARCHN3	C8	80
FLCFACL	C8	
FLCFACL0	C8	0
FLCFACL1	C9	0
FLCFACL2	CA	0
FLCFACL3	CB	0
FLCFACL4	CC	0
FLCFACL5	CD	0
FLCFACL6	CE	0
FLCFACL7	CF	0
FLCFACL8	D0	0
FLCFACL9	D1	0
FLCFAEN	D0	1
FLCFAIS	D1	80
FLCFAISI	D0	2
FLCFASLX	C8	2
FLCFCAAI	D0	40
FLCFCMC	D0	10
FLCFCMS	D0	8
FLCFCRYA	CA	40
FLCFCSF2	CC	40
FLCFCSSF	CC	80
FLCFCTOP	C9	10
FLCFDFP	CD	20
FLCFDFPH	CD	10
FLCFECT	CB	1
FLCFEDAT	C9	80
FLCFEIMM	CA	4
FLCFETF2	CA	80
FLCFETF3	CA	2
FLCFET2E	CB	80
FLCFET3E	CB	2
FLCFFPSE	CD	40
FLCFGIEF	CC	20
FLCFHMAS	CA	8
FLCFHUN	CA	1
FLCFLA	100	0
FLCFLD	CA	20
FLCFLDHP	CA	10
FLCFN3	C8	80
FLCFOCM	CC	1
FLCFPFPO	CD	8
FLCFPSAV	160	0
FLCFSA	F8	
FLCFSCLP	D0	4
FLCFSRS	C9	40
FLCFSSKE	C9	20
FLCFSTKF	CB	40
FLCFZARA	C8	20
FLCFZARI	C8	40
FLCGRSAV	180	0
FLCHDEND	200	
FLCICCW1	8	
FLCICCW2	10	
FLCINPSW	78	40C0000
FLCIOCDP	B8	
FLCIOFP	BC	0
FLCIOPSW	38	0
FLCIPPSW	0	
FLCMCIC	E8	0
FLCMCNUM	95	0
FLCMNPSW	70	80000
FLCMOPSW	30	0
FLCMTRCD	9D	0
FLCPER	98	
FLCPERCD	96	0
FLCPERRN	A1	0
FLCPICOD	8E	
FLCPIILC	8D	0
FLCPILCB	8D	7
FLCPNPSW	68	A0000
FLCPOPSW	28	0

Name	Hex Offset	Hex Value
FLCPSWB4	97	80
FLCRNPSW	0	40E0000
FLCROPSW	8	0
FLCRV110	110	0
FLCSID	B8	0
FLCSILCB	89	7
FLCSNPSW	60	40C0000
FLCSOPI	93	4
FLCSOPSW	20	0
FLCSVCN	8A	0
FLCSVILC	89	0
FLCTEA	90	
FLCTEAB3	93	0
FLCTEACL	93	FFF000
FLCTEARN	A0	0
FLCTEAXM	90	80
FLCTSTDA	93	1
FLCTSTDH	93	3
FLCTSTDP	93	0
FLCTSTDS	93	2
IEATCBP	218	218
IONPSW	7C	78
IOOPSW	38	38
IPLPSW	4	0
MCNPSW	74	70
MCOPSW	30	30
PICODE	8E	8E
PIILC	8D	8D
PINPSW	6C	68
PIOPSW	28	28
PSA	0	
PSA_BYLPAR_IFA		
	23C	40
PSA_BYLPAR_PROCCLASS		
	23A	0
PSA_BYLPAR_PROCCLASS_BYTE0		
	23A	
PSA_BYLPAR_PROCCLASS_BYTE1		
	23B	
PSA_BYLPAR_SUP		
	23C	8
PSA_BYLPAR_ZAAP		
	23C	40
PSA_BYLPAR_ZIIP		
	23C	8
PSA_CR0EMASKOFFEXTINT		
	5C8	FFFFFFFD
PSA_CR0EMASKONEXTINT		
	5D0	2
PSA_CR0ESAVEAREA		
	5D8	
PSA_CR0ESAVEAREA_HW		
	5D8	0
PSA_CR0ESAVEAREA_LW		
	5DC	0
PSA_PCFLIH_TRACE_INTERRUPT_CPUT		
	6B8	
PSA_TIME_ON_CP		
	650	0
PSA_WORKUNIT_PROCCLASSATDISP		
	238	0
PSA_WORKUNIT_PROCCLASSATDISP_BYTE0		
	238	
PSA_WORKUNIT_PROCCLASSATDISP_BYTE1		
	239	
PSAACR	229	4
PSAACTCD	30E	0
PSAAEIT	240	80
PSAALR	A7D	80
PSAANEW	220	
PSAAOLD	224	
PSAASAV	3DC	
PSAASD	59A	0
PSAASMGI	2F9	2
PSAASMGL	2B4	
PSAASML	284	
PSAASMLI	2FA	8
PSAASTK	3D8	
PSAATCVT	408	
PSABLSDI	4C4	80
PSACDSAE	800	FFFFFFFF
PSACDSAF	804	FFFFFFFF
PSACDSAV	800	
PSACDSA0	808	FFFFFFFF
PSACDSA1	80C	FFFFFFFF
PSACLHS	2F8	
PSACLHSE	4C4	
PSACLHS1	2F8	0
PSACLHS2	2F9	0
PSACLHS3	2FA	0
PSACLHS4	2FB	0
PSACLHT	280	
PSACLHT1	280	
PSACLHT2	2D0	
PSACLHT3	2E0	
PSACLHT4	2E8	
PSACMSL	2E8	
PSACMSLI	2FB	2
PSACNTXI	4C5	4
PSACPCLS	5A8	
PSACPUL	2E0	
PSACPULA	206	0
PSACPULI	2F8	80
PSACPUPA	204	0
PSACPUSA	31A	0
PSACPUT	470	0
PSACR0	308	0
PSACR0AL	58C	8
PSACR0CB	589	0
PSACR0ED	58C	80
PSACR0EN	58C	10
PSACR0FP	58C	4
PSACR0M1	5BC	FFFF018C
PSACR0M2	5C0	FE73
PSACR0SV	58C	0
PSACR0VI	58C	2
PSACSTK	380	
PSADATLK	7B0	0
PSADATLN	7E4	FFFFFFFF
PSADATOF	7E0	
PSADAX	6D4	FFFF
PSADAXPA	6D4	
PSADCR3	6C8	
PSADCR4	6D0	
PSADEXMS	6C8	
PSADEXMW	670	
PSADISP	228	4
PSADISPL	280	
PSADISPM	49F	10
PSADONTGETWEB		
	A7C	4
PSADPAS	6D6	FFFF
PSADPINS	6D0	FFFFFFFF
PSADPKM	6CC	FFFF
PSADPKSA	6CC	
PSADRLK1	4C6	1
PSADRLK2	4C6	2
PSADRLK3	4C6	4
PSADSABL	589	0
PSADSARS	678	0
PSADSAS	6CE	FFFF
PSADSCRP	23C	10
PSADSINS	6C8	FFFFFFFF
PSADSPLI	2FA	10
PSADTSAV	6C0	
PSADZERO	4A8	
PSAECLTP	4C0	
PSAECVT	7A0	

Name	Hex Offset	Hex Value
PSAEECOD	8E	0
PSAEEPSW	84	
PSAEGRA	A7C	10
PSAEMEMA	24B	80
PSAEMS2M	26C	26D
PSAEMS2R	824	FFFFFFFF
PSAEMS2S	26C	
PSAENABL	589	10
PSAEND	1000	
PSAEPARM	80	0
PSAEPPSW	8C	
PSAESAME	A3	1
PSAESAR	22B	20
PSAESAV1	3A4	
PSAESAV2	3AC	
PSAESAV3	3B4	
PSAESC8	664	0
PSAESETI	4C4	8
PSAESPSW	88	
PSAESTA	22A	8
PSAESTK1	3A0	
PSAESTK2	3A8	
PSAESTK3	3B0	
PSAEXT	228	20
PSAFAFRR	B54	
PSAFFRR	B54	
PSAFFRRS	B58	
PSAFF6C0	6C0	FFFFFFFF
PSAFF7E8	7E8	FFFFFFFF
PSAFF8A8	8A8	FFFFFFFF
PSAFF950	950	FFFFFFFF
PSAFF998	998	FFFFFFFF
PSAFLAGS	240	0
PSAFPFL	3FA	0
PSAFZERO	4A8	0
PSAGPREG	8F0	FFFFFFFF
PSAGRSLI	4C6	80
PSAGSAV	8A8	
PSAGSAVH	8A0	
PSAGSCH7	B3C	
PSAGSCH8	B40	
PSAGSPSW	810	FFFFFFFF
PSAGSRGS	814	FFFFFFFF
PSAHLHI	2F8	
PSAHLHIS	274	0
PSAHWFB	588	0
PSAICNT	258	0
PSAIDAWK	320	0
PSAIFA	23C	40
PSAIFADS	23C	20
PSAILS	23D	0
PSAILSDS	23D	10
PSAILSEX	23D	40
PSAILSIO	23D	80
PSAILSLK	23D	1
PSAILSOR	23D	4
PSAILSPC	23D	20
PSAILSRS	23D	8
PSAILST6	23D	2
PSAINTE	3FB	0
PSAINTIN	264	
PSAIO	228	80
PSAIOLK1	4C6	10
PSAIOPR	5B8	80
PSAIORTY	5B8	40
PSAIOSEX	2D8	80
PSAIOSI	2F8	2
PSAIOSL	2D8	
PSAIOSLI	2FA	2
PSAIOSSL	28C	
PSAIOSUL	294	
PSAIOSUP	22A	80
PSAIOULI	2FB	80
PSAIPCIN	268	
PSAIPCRI	229	80
PSAIPCSM	268	269
PSAIXDSI	4C4	1
PSAIXLLI	4C5	80
PSAIXLRI	4C5	20
PSAIXSCI	4C4	4
PSAIXSHI	4C4	2
PSAJSTSA	998	0
PSALAA	4B8	
PSALCCAR	214	
PSALCCAV	210	
PSALCLLI	2FB	1
PSALCPUA	2F4	
PSALCR	229	1
PSALDWT	22B	80
PSALHEB0	4C4	0
PSALHEB1	4C5	0
PSALHEB2	4C6	0
PSALHEB3	4C7	0
PSALITA	2FC	
PSALIT2	4BC	
PSALKCRF	24C	
PSALKJW	4A4	0
PSALKJW2	4B0	0
PSALKPT	4B4	
PSALKR0	910	FFFFFFFF
PSALKR1	914	FFFFFFFF
PSALKR10	938	FFFFFFFF
PSALKR11	93C	FFFFFFFF
PSALKR12	940	FFFFFFFF
PSALKR13	944	FFFFFFFF
PSALKR14	948	FFFFFFFF
PSALKR15	94C	FFFFFFFF
PSALKR2	918	FFFFFFFF
PSALKR3	91C	FFFFFFFF
PSALKR4	920	FFFFFFFF
PSALKR5	924	FFFFFFFF
PSALKR6	928	FFFFFFFF
PSALKR7	92C	FFFFFFFF
PSALKR8	930	FFFFFFFF
PSALKR9	934	FFFFFFFF
PSALKSA	910	
PSALKS1	2C8	13
PSALKS2	2D8	3
PSALKS3	2E0	1
PSALKS4	2EC	2
PSALOCAL	2EC	
PSALOCK	228	8
PSALSCH1	B44	
PSALSCH2	B48	
PSALSFCC	3F4	
PSALSVCI	23E	0
PSALWPSW	3C0	0
PSALWTSA	27C	
PSAMAXPROCCLASS	23B	4
PSAMAXPROCCLASSINDEX	23B	2
PSAMCH	22B	10
PSAMCHA	A7C	40
PSAMCHFL	30C	0
PSAMCHIC	30F	0
PSAMCX16	440	0
PSAMFLGS	49D	0
PSAMISCF	A7D	0
PSAMODE	49F	0
PSAMODEH	49E	0
PSAMODEW	49C	
PSAMPL	A4	
PSAMPSW	250	C0000
PSAMSAV	394	
PSAMSTK	390	
PSANSS	49D	80
PSANSTK	384	

Name	Hex Offset	Hex Value
PSANUIN	3FB	80
PSAOPTL	2A8	
PSAPAWA	5B4	
PSAPCAD	478	4
PSAPCAF	478	8
PSAPCART	478	A
PSAPCAVR	47A	1
PSAPCCAR	20C	
PSAPCCAV	208	
PSAPCCR0	590	0
PSAPCDAR	478	C
PSAPCDE	47A	20
PSAPCDNV	47B	80
PSAPCDPF	478	B
PSAPCFB1	478	0
PSAPCFB2	479	0
PSAPCFB3	47A	0
PSAPCFB4	47B	0
PSAPCFUN	478	
PSAPCGAB	908	
PSAPCGRA	908	FFFFFFFF
PSAPCGRB	90C	FFFFFFFF
PSAPCGR8	900	FFFFFFFF
PSAPCGR9	904	FFFFFFFF
PSAPCLS	478	9
PSAPCLSR	47B	40
PSAPCLV	47A	10
PSAPCMAX	478	D
PSAPCMC	478	1
PSAPCMT	479	40
PSAPCPC	478	6
PSAPCPF	478	2
PSAPCPFR	47A	2
PSAPCPRT	478	D
PSAPCPS	478	3
PSAPCPSW	428	0
PSAPCPS2	47C	0
PSAPCPS3	498	0
PSAPCPS4	49A	0
PSAPCP1	47A	80
PSAPCP2	47A	40
PSAPCP3	47A	8
PSAPCP4	47A	4
PSAPCTR	478	5
PSAPCTRC	478	7
PSAPCTRR	479	80
PSAPCWKA	480	0
PSAPEXM	250	1
PSAPI	228	10
PSAPICOD	8F	0
PSAPIMC	8F	40
PSAPIOM	250	2
PSAPIPC	8F	3F
PSAPIPER	8F	80
PSAPNLK1	4C6	20
PSAPROCCLASS	23A	0
PSAPROCCLASS_BYTE0	23A	
PSAPROCCLASS_BYTE1	23B	
PSAPROCCLASS_CP	23B	0
PSAPROCCLASS_SUP	23B	4
PSAPROCCLASS_ZAAP	23B	2
PSAPROCCLASS_ZIIP	23B	4
PSAPROCCLASSCONVERTER	23B	2
PSAPRSRB	49D	40
PSAPSA	200	D7E2C140
PSAPSAV	39C	
PSAPSLK1	4C6	40

Name	Hex Offset	Hex Value
PSAPSRBM	49F	20
PSAPSTK	398	
PSAPSWSV	468	0
PSAPSWSV16	460	0
PSAPTYPE	23C	0
PSAPXLK1	4C6	8
PSARCR0	594	0
PSARECUR	278	0
PSAREGSI	4C5	2
PSARET	37A	
PSARETCD	37C	
PSARPEN	594	10
PSARSAV	3BC	
PSARSM	22A	4
PSARSMAI	2FA	40
PSARSMAL	2C0	
PSARSMCI	2F9	10
PSARSMCL	2C8	
PSARSMDI	2FA	1
PSARSMDL	290	
PSARSMEX	2D0	80
PSARSMGI	2F9	8
PSARSMGL	2AC	
PSARSML	2D0	
PSARSMLI	2F8	8
PSARSMSA	414	
PSARSMSI	2F9	1
PSARSMSL	2B8	
PSARSMXI	2FA	80
PSARSMXL	2BC	
PSARSP16	450	0
PSARSREG	8FC	FFFFFFFF
PSARSSM	279	0
PSARSTA	A7C	20
PSARSTK	3B8	
PSARSVT	380	
PSARSVTE	380	
PSARTM	229	2
PSARTMA	A7C	8
PSARTM1M	3FC	3FD
PSARTM1R	3FC	
PSARTM1S	27B	0
PSARTPSW	3E0	0
PSARVA18	A18	0
PSARVA7E	A7E	0
PSARVA80	A80	0
PSARVB5C	B5C	0
PSARVFD8	FD8	0
PSARVLK1	298	
PSARVLK2	2CC	
PSARVLK4	2DC	
PSARVLK5	2E4	
PSARVLK6	2F0	
PSARV2A0	2A0	
PSARV22C	22C	0
PSARV241	241	0
PSARV29C	29C	
PSARV3C8	3C8	
PSARV3E8	3E8	0
PSARV37E	37E	0
PSARV4C8	4C8	0
PSARV4D0	4D0	0
PSARV400	400	0
PSARV438	438	0
PSARV47E	47E	0
PSARV5AC	5AC	0
PSARV5B9	5B9	0
PSARV5C4	5C4	0
PSARV5E0	5E0	0
PSARV58A	58A	0
PSARV6D8	6D8	0
PSARV7ED	7ED	0
PSARV7E8	7E8	
PSARV818	818	0

Name	Hex Offset	Hex Value
PSARV878	878	0
PSASALCL	288	
PSASALLI	2FA	4
PSASCAFF	24B	0
PSASCFB	5B8	0
PSASCFS	5B0	
PSASCHDA	A7C	80
PSASCPSW	418	C0000
PSASCRG1	8E8	FFFFFFFF
PSASCRG2	8EC	FFFFFFFF
PSASCSAV	A3C	0
PSASCWA	410	
PSASEL	59C	0
PSASFACC	3F0	8007D000
PSASFLGS	A7C	0
PSASKPSW	5A0	C0000
PSASKPS2	5A4	
PSASLIP	22A	1
PSASLKRA	A00	FFFFFFFF
PSASLKRB	A04	FFFFFFFF
PSASLKRC	A08	FFFFFFFF
PSASLKRD	A0C	FFFFFFFF
PSASLKRE	A10	FFFFFFFF
PSASLKRF	A14	FFFFFFFF
PSASLKR0	9D8	FFFFFFFF
PSASLKR1	9DC	FFFFFFFF
PSASLKR2	9E0	FFFFFFFF
PSASLKR3	9E4	FFFFFFFF
PSASLKR4	9E8	FFFFFFFF
PSASLKR5	9EC	FFFFFFFF
PSASLKR6	9F0	FFFFFFFF
PSASLKR7	9F4	FFFFFFFF
PSASLKR8	9F8	FFFFFFFF
PSASLKR9	9FC	FFFFFFFF
PSASLKSA	9D8	
PSASLSA	950	
PSASMF	22B	40
PSASMPSW	420	70C0000
PSASNSM2	27A	0
PSASPAD	84	0
PSASPR	22A	10
PSASRBM	49F	4
PSASRMEI	4C7	80
PSASRMLI	2FB	4
PSASRSAV	660	FFFFFFFF
PSASSAV	38C	
PSASSDLI	4C5	1
PSASSTK	388	
PSASTAK	B80	0
PSASTKE	598	
PSASTNSM	4A3	0
PSASTOR	31C	0
PSASTOR8	300	0
PSASTOSM	270	
PSASTSSM	270	271
PSASUM	2F8	10
PSASUP	23C	8
PSASUPDS	23C	4
PSASUPER	228	
PSASUP1	228	0
PSASUP2	229	0
PSASUP3	22A	0
PSASUP4	22B	0
PSASVC	228	40
PSASVCR	229	40
PSASVCRR	229	20
PSASVC13	3F8	
PSASVT	B4C	
PSASVTX	B50	
PSASV01R	81C	0
PSASV14R	820	0
PSASYMSK	30D	0
PSATASKM	49F	0
PSATBVTR	7F0	
PSATBVTV	7F4	
PSATCLIN	260	
PSATCTL	228	2
PSATIME	658	0
PSATKN	598	0
PSATNEW	218	
PSATOLD	21C	
PSATOT	7FC	
PSATPACL	2A4	
PSATPALI	2FB	8
PSATRACE	7EC	0
PSATRCEI	2F8	4
PSATRCEL	2D4	
PSATRCEX	2D4	80
PSATRGRA	850	FFFFFFFF
PSATRGRB	854	FFFFFFFF
PSATRGRC	858	FFFFFFFF
PSATRGRD	85C	FFFFFFFF
PSATRGRE	860	FFFFFFFF
PSATRGRF	864	FFFFFFFF
PSATRGR0	828	FFFFFFFF
PSATRGR1	82C	FFFFFFFF
PSATRGR2	830	FFFFFFFF
PSATRGR3	834	FFFFFFFF
PSATRGR4	838	FFFFFFFF
PSATRGR5	83C	FFFFFFFF
PSATRGR6	840	FFFFFFFF
PSATRGR7	844	FFFFFFFF
PSATRGR8	848	FFFFFFFF
PSATRGR9	84C	FFFFFFFF
PSATROFF	7EC	80
PSATRSAV	828	
PSATRSVS	86C	0
PSATRSV1	868	0
PSATRSV2	870	0
PSATRVT	7F8	
PSATSAV	3D4	
PSATSTK	3D0	
PSATX	240	8
PSATXC	240	4
PSATYPE6	228	1
PSAULCMS	22A	2
PSAULUTI	4C5	40
PSAUSEND	6D8	
PSAUS2ND	9D8	
PSAUS2ST	800	
PSAVFIXI	2F9	4
PSAVFIXL	2B0	
PSAVPAGI	2FA	20
PSAVPAGL	2C4	
PSAVSTAP	318	0
PSAWAITM	49F	8
PSAWKRAP	310	
PSAWKVAP	314	
PSAWLMQI	4C5	8
PSAWLMRI	4C5	10
PSAWTCOD	40C	
PSAXCVT	7A8	
PSAXDSI	4C4	40
PSAXQI	4C4	10
PSAXRESI	4C4	20
PSAZARCH	A3	1
PSAZIIP	23C	8
PSAZIIPDS	23C	4
SVCILC	89	89
SVCNPSW	64	60
SVCNUM	8A	8A
SVCOPSW	20	20

PSL Information

PSL Programming Interface information

Programming Interface information

PSL

ONLY the following fields are part of the programming interface information:

- PSLAST
- PSLCHAIN
- PSLEND
- PSLNULL
- PSLSTRT

End of Programming Interface information

PSL Heading Information

Common Name: Page Service List Entry
Macro ID: IHAPSL
DSECT Name: PSL
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: None
Storage Attributes: Virtual Storage: Yes
Subpool: USER SPECIFIED.
Key: USER SPECIFIED.
Residency: USER SPECIFIED.
Size: 12 bytes
Created by: Caller
Pointed to by: R1 on entry to PGSER Macro Service Routine
Serialization: USER SPECIFIED.
Function: A page service list is a parameter list requesting paging services. Each list entry either (1) specifies a range of addresses to be operated on, or (2) specifies the address of the next list entry to be processed, or (3) is null. The first entry also indicates which paging service is to be performed on all ranges specified in the list.

PSL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PSL	, PSLPTR
0	(0)	ADDRESS	4	PSLSTRT	31-BIT START ADDRESS OF THE VIRTUAL AREA OR A POINTER TO THE NEXT PSL. BIT 0 IS RESERVED AND MUST BE 0.
4	(4)	ADDRESS	4	PSLEND	IF PSLSTRT IS THE START ADDRESS, THEN PSLEND IS THE 31-BIT ADDRESS OF THE FINAL BYTE OF THE VIRTUAL AREA. BIT 0 IS RESERVED AND MUST BE 0. IF PSLSTRT IS A POINTER TO THE NEXT PSL, THEN PSLEND IS RESERVED.
8	(8)	BITSTRING	1	PSLFLGS1	FLAGS SET BY CALLER
		1...		PSLAST	"X'80'" IF 1, THEN THIS IS THE LAST PSL IN THE CONCATENATION OF PSLS. (NOTE THAT PSLAST IS IGNORED IF PSLCHAIN=1 AND PSLNULL=0).
		.1..		PSLNULL	"X'40'" IF 1, THEN NO PAGE SERVICE PROCESSING IS PERFORMED FOR THE RANGE OF ADDRESSES SPECIFIED IN PSLSTRT, PSLEND. ADDITIONALLY, IF PSLNULL=1, THEN THE PSLCHAIN FIELD IS IGNORED. PSLNULL=1 DOES NOT AFFECT THE PROCESSING OF THE PSLFLGS2, PSLFUNC, PSLRTN FIELDS.
		..1.		PSLCHAIN	"X'20'" IF 1, THEN PSLSTRT IS A POINTER TO THE NEXT PSL TO BE PROCESSED AND PSLEND IS RESERVED. PSLCHAIN IS IGNORED IF PSLNULL=1. IF PSLNULL=0, PSLCHAIN=1, AND PSLAST=1, THEN PSLAST IS IGNORED AND PSLSTRT IS USED TO POINT TO THE NEXT PSL TO PROCESS.
9	(9)	BITSTRING	1	PSLRTN	RESERVED
10	(A)	BITSTRING	2	PSLFCTL (0)	PAGE SERVICE FUNCTION SPECIFICATION FIELD.
10	(A)	BITSTRING	1	PSLFUNC	RESERVED. SET BY PGSER MACRO INSTRUCTION IN FIRST OR ONLY PSL IN LIST OF PSLS. MEANING NOT AFFECTED BY CONTENTS OF PSLFLGS1. THE PSLFUNC FIELD IN THE FIRST OR ONLY PSL IN THE LIST OF PSLS SPECIFIES THE PAGE SERVICE WHICH IS TO BE INVOKED TO PROCESS ALL THE RANGE(S) OF ADDRESSES WHICH ARE SPECIFIED IN THE PSLS IN THE LIST. PSLFUNC IS IGNORED IN ANY PSLS IN THE LIST SUBSEQUENT TO THE FIRST PSL.
		1		PSLFFIX	"X'01'" FUNCTION REQUESTED IS PAGE FIX
		.... ..1.		PSLFFREE	"X'02'" FUNCTION REQUESTED IS PAGE FREE
		11		PSLFANYW	"X'03'" FUNCTION REQUESTED IS ANYWHER
		.... .1..		PSLFLOAD	"X'04'" FUNCTION REQUESTED IS PAGE LOAD
		.... .1.1		PSLFOUT	"X'05'" FUNCTION REQUESTED IS PAGE OUT
		.... .11.		PSLFRELS	"X'06'" FUNCTION REQUESTED IS PAGE RELEASE
		.... .111		PSLFPROT	"X'07'" FUNCTION REQUEST IS PAGE PROTECT
		.... 1...		PSLFUNP	"X'08'" FUNCTION REQUEST IS PAGE UNPROTECT
		.... 1..1		PSLPCIEFIX	"X'09'" FUNCTION REQUEST IS PCIE PAGE FIX
		.... 1.1.		PSLPCIEFREE	"X'0A'" FUNCTION REQUEST IS PCIE PAGE FREE
11	(B)	BITSTRING	1	PSLFLGS2	RESERVED. SET BY PGSER MACRO INSTRUCTION IN FIRST OR ONLY PSL IN LIST OF PSLS. MEANING NOT AFFECTED BY CONTENTS OF PSLFLGS1. THE PSLFLGS2 FIELD IN THE FIRST OR ONLY PSL IN THE LIST OF PSLS SPECIFIES MODIFIERS TO BE APPLIED TO THE PAGE SERVICE SPECIFIED IN PSLFUNC IN PROCESSING THE RANGE(S) OF ADDRESSES WHICH ARE SPECIFIED IN THE LIST. PSLFLGS2 IS IGNORED IN ANY PSLS IN THE LIST SUBSEQUENT TO THE FIRST PSL.
		.1..		PSLRLSE	"X'40'" IF 1, RELEASE=Y WAS CODED ON PGSER MACRO
		..1.		PSLKEPRL	"X'20'" IF 1, KEEPREL=Y WAS CODED ON PGSER MACRO
		...1		PSLANYW	"X'10'" IF 1, ANYWHER=Y WAS CODED ON PGSER MACRO

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... 1...		PSLONG	"X'08'" IF 1, LONG=Y WAS CODED OR DEFAULTED ON PGSER MACRO
		.... .1..		PSLBACK	"X'04'" IF 1, BACKOUT=Y WAS CODED OR DEFAULTED ON PGSER MACRO
		.... ..1.		PSLL2G	"X'02'" IF 1, LIMIT(2G) WAS CODED ON THE PGSER MACRO
12	(C)	SIGNED	4	PSLFINIS (0)	THIS IS THE END OF THE PSL
12	(C)	X'C'	0	PSLLEN	"PSLFINIS-PSL" LENGTH OF A PSL

PSL Cross Reference

Name	Hex Offset	Hex Value
PSL	0	
PSLANYW	B	10
PSLAST	8	80
PSLBACK	B	4
PSLCHAIN	8	20
PSLEND	4	
PSLFANYW	A	3
PSLFCTL	A	
PSLFFIX	A	1
PSLFFREE	A	2
PSLFINIS	C	
PSLFLGS1	8	
PSLFLGS2	B	
PSLFLOAD	A	4
PSLFOUT	A	5
PSLFPROT	A	7
PSLFRELS	A	6
PSLFUNC	A	
PSLFUNP	A	8
PSLKEPRL	B	20
PSLLEN	C	C
PSLL2G	B	2
PSLNULL	8	40
PSLONG	B	8
PSLPCIEFIX	A	9
PSLPCIEFREE	A	A
PSLRLSE	B	40
PSLRTN	9	
PSLSTRT	0	

PVT Information

PVT Programming Interface information

Programming Interface information

PVT

INCLUDE ONLY

End of Programming Interface information

PVT Heading Information

Common Name: RSM Page Vector Table
Macro ID: IHAPVT
DSECT Name: PVT
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: PVT
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: Yes
Subpool: R/O Nucleus
Key: 0
Size: 1912 bytes
Created by: IAXMP
Pointed to by: CVTPVTP field of the CVT data area
Serialization: None
Function: Information used internally by RSM

PVT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PVT	
				Comment	
		- - ADDRESSES OF EXTERNAL DATA FIELDS - -			
				End of Comment	
0	(0)	CHARACTER	1	PVTID (4)	PVT Control Block Identifier
4	(4)	ADDRESS	4	PVTRIT	"V(IARMRRIT)" Address of the start of the RIT
8	(8)	ADDRESS	4	PVTPFTA	"V(IARMRPFT)" Address of PFT Address in RIT
12	(C)	ADDRESS	4	PVTPCNA	"V(IARMRPCN)" Address of data space PC numbr array
16	(10)	ADDRESS	4	PVTEXTPT	"V(PVTEXT)" Address of PVT Extension
20	(14)	ADDRESS	4	PVTRSH	"V(IARMPRSH)" Address of Recovery Refresh Table
24	(18)	ADDRESS	4	PVTESTA	"V(IARMREST)" Address of Extended Store Table - ESA only
28	(1C)	CHARACTER	1	(12)	Reserved area for additional data addresses
				Comment	
		- - ADDRESSES OF VDAC EXTERNAL ENTRY POINTS - -			
				End of Comment	
40	(28)	ADDRESS	4	PVTVVTPT	"V(PVTVVTAB)" Address of VDAC Vector Table
44	(2C)	ADDRESS	4	PVTKURPR	"V(IARKURPR)" Address of VDAC REPRIME Entry
48	(30)	ADDRESS	4	PVTKGRES	"V(IARKGRES)" Address of VDAC RESET Entry
52	(34)	ADDRESS	4	PVTKQASC	"V(IARKQASC)" Address of VDAC ASSOCIATE Entry
56	(38)	ADDRESS	4	PVTKDIS	"V(IARKDIS)" Address of VDAC DISASSOCIATE Entry
60	(3C)	ADDRESS	4	PVTKCMIT	"V(IARKCMIT)" Address of VDAC COMMIT Entry
				Comment	
		- - ADDRESSES OF NON-VDAC EXTERNAL ENTRY POINTS - -			
				End of Comment	
64	(40)	ADDRESS	4	PVTGIOCM	"V(IARGIOCM)" Address of General I/O Completion
68	(44)	ADDRESS	4	PVTUTRV	"V(IARUTRV)" Translate REAL-TO-VIRTUAL Routine
72	(48)	ADDRESS	4	PVTPSIB	"V(IARPSIV)" Paging Services- VSL Branch Entry Point
76	(4C)	ADDRESS	4	PVTXPRSB	"V(IARXPRSB)" Real Storage Buffer Routine
80	(50)	ADDRESS	4	PVTXIBAD	"V(IARXIBAD)" Bad Frame Routine
84	(54)	ADDRESS	4	PVTXCRMF	"V(IARXCRMF)" Frame counting service for RMF
88	(58)	ADDRESS	4	PVTERCF	"V(IARERCF)" Storage Reconfiguration Rtn for Extended Store - ESA only
92	(5C)	ADDRESS	4	PVTXWVFC	"V(IARXWVFC)" Virtual Fetch data set creation - ESA only
96	(60)	ADDRESS	4	PVTXVFA	"V(IARXVFA)" Virtual Fetch Assign - ESA only
100	(64)	ADDRESS	4	PVTSSDEL	"V(IARSSDEL)" Delete secondary working set pages
				Comment	
		The following PAGE FIX/FREE Fast Path Entry Point addresses are all POINTER DEFINED:			
				End of Comment	
104	(68)	ADDRESS	4	PVTPNL	"V(IARPNL)" PAGE FREE Fast Path - List Format
108	(6C)	ADDRESS	4	PVTPNR	"V(IARPNR)" PAGE FREE Fast Path - Register Format

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
112	(70)	ADDRESS	4	PVTPQLB	"V(IARPQLB)" PAGE FIX Fast Path - List Format
116	(74)	ADDRESS	4	PVTPQRB	"V(IARPQRB)" PAGE FIX Fast Path - Register Format
120	(78)	ADDRESS	4	PVTPQLNB	"V(IARPQLNB)" PAGE FIX Fast Path - List Format without BACKOUT
124	(7C)	ADDRESS	4	PVTPQRNB	"V(IARPQRNB)" PAGE FIX Fast Path - Register Format WITHOUT BACKOUT
128	(80)	ADDRESS	4	PVTXPLCK	"V(IARXPLCK)" Lock Interface in IARXP
132	(84)	ADDRESS	4	PVTXXFP	"V(IARXXFP)" External Interface Routine
136	(88)	ADDRESS	4	PVTUFP	"V(IARUFP)" Find Page
140	(8C)	ADDRESS	4	PVTXCNTF	"V(IARXCNTF)" Counting Routine
144	(90)	ADDRESS	4	PVTUCNVT	"V(IARUCNVT)" Convert Routine
148	(94)	ADDRESS	4	PVTXRCF	"V(IARXRCF)" Storage Reconfiguration Routine for Real Storage
152	(98)	ADDRESS	4	PVTUALF	"V(IARUALF)" PFTE Manager- GETFRAME Routine
156	(9C)	ADDRESS	4	PVTUMVF	"V(IARUMVF)" PFTE Manager- MOVEFRAM Routine
160	(A0)	ADDRESS	4	PVTSIN	"V(IARSIN)" SWAP-IN Processor
164	(A4)	ADDRESS	4	PVTSOUT	"V(IARSOUT)" SWAP-OUT Processor
168	(A8)	ADDRESS	4	PVTVFRMN	"V(IARVFRMN)" VSM FREEMAIN Exit to RSM
172	(AC)	ADDRESS	4	PVTUINV	"V(IARUINV)" POINTER DEFINED address of PTLB Routine
176	(B0)	ADDRESS	4	PVTSURST	"V(IARSURST)" Swap Restart Entry Point
180	(B4)	ADDRESS	4	PVTEAEXT	"V(IAREAEXT)" MIGRATION Scheduler Entry Point - ESA only
184	(B8)	ADDRESS	4	PVTXWRLS	"V(IARXWRLS)" Virtual Fetch Release - ESA only
188	(BC)	ADDRESS	4	PVTDLCON	"V(IARDLCON)" DSPCALL CONVERT Interface Routine
192	(C0)	ADDRESS	4	PVTDZLIM	"V(IARDZLIM)" DSPLIMIT Service Routine
196	(C4)	ADDRESS	4	PVTXQVDC	"V(IARXQVDC)" VDAC Counting Routine
200	(C8)	ADDRESS	4	PVTCCDSL	"V(IARCCDSL)" DSPCALL DSPLIST service routine
204	(CC)	ADDRESS	4	PVTYLGRP	"V(IARYLGRP)" VIO Release Logical Group
208	(D0)	ADDRESS	4	PVTCQMVP	"V(IARCQMVP)" HSPSERV MVPG Service Routine
212	(D4)	ADDRESS	4	PVTCJCPY	"V(IARCJCPY)" SDUMP Copy Service Routine
216	(D8)	ADDRESS	4	PVTP3PFX	"V(IARP3PFX)" RSMPIN super fast pin entry point
220	(DC)	ADDRESS	4	PVTP3PFR	"V(IARP3PFR)" RSMPIN super fast unpin entry point
224	(E0)	ADDRESS	4	PVTPZFFR	"V(IARPZFFR)" RSMPIN fast pin/unpin/recover routine
228	(E4)	CHARACTER	1	(0)	Reserved space for Entry Points
228	(E4)	CHARACTER	1	PVTRCVTT (132)	- - Recovery Exit Vector Table - -

Comment

- - ENTRY POINTS REQUIRED FOR S/370 COMPATIBILITY - -

End of Comment

1896	(768)	ADDRESS	4	PVTPPSIX	"V(IARPSIX)" PGFIX BRANCH ENTRY (R FORMAT)
1900	(76C)	ADDRESS	4	PVTPPSIY	"V(IARPSIY)" PGFIX BRANCH ENTRY (L FORMAT)
1904	(770)	ADDRESS	4	PVTPPSIZ	"V(IARPSIX)" PGFIX BRANCH ENTRY (R FORMAT)
1908	(774)	ADDRESS	4	PVTPPSIF	"V(IARPSIF)" PGFREE BRANCH ENTRY
1912	(778)	CHARACTER	1	PVTRIDXT (0)	- - Recovery ID Index Table - -

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PVTVVTAB	
0	(0)	SIGNED	4	(0)	Beginning of VDAC vector table on on word boundary
0	(0)	ADDRESS	4		Reserved
4	(4)	ADDRESS	4	PVTLGRES	"V(IARLGRES)" Address of VDAC Dataspace RESET Entry Point
8	(8)	ADDRESS	4	PVTLQASC	"V(IARLQASC)" Address of VDAC Dataspace ASSOCIATE Entry Point
12	(C)	ADDRESS	4	PVTLDIS	"V(IARLDIS)" Address of VDAC Dataspace DISASSOCIATE Entry Point
16	(10)	ADDRESS	4	PVTLCMIT	"V(IARLCMIT)" Address of VDAC data space COMMIT Entry Point
20	(14)	ADDRESS	4	PVTLNCON	"V(IARLNCON)" Address of VDAC CONTROL Entry Point
24	(18)	ADDRESS	4	PVTKKCHL	"V(IARKKCHL)" Address of VDAC CHANGELIST Entry Point
28	(1C)	ADDRESS	4	PVTLOCHL	"V(IARLOCHL)" Address of VDAC Dataspace CHANGELIST Entry Point

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	PVTEXT	
0	(0)	SIGNED	4	(0)	Beginning of the PVT Extension
0	(0)	ADDRESS	4		Length of a Data Space ASTE
4	(4)	ADDRESS	4	PVTHSCNV	"V(IARHSCNV)" Address of IARSSCNV (subspace) convert
8	(8)	ADDRESS	4	PVTYCFA	"V(IARYCFA)" Address of IARYCFA Subspace double frame interface entry point
12	(C)	ADDRESS	4	PVTCCDSW	"V(IARCCDSW)" DSPCALL DSPLISTW service routine
16	(10)	ADDRESS	4	PVTBRLKP	"V(IARBRLKP)" Address of LKPG entry point in IARBR
20	(14)	ADDRESS	4	PVTWTRV	"V(IARWTRV)" Address of RSA-to-VSA Convert Entry Point (branch entry)
24	(18)	ADDRESS	4	PVTIRSRV	"V(IARIRSRV)" Address of IARIRSRV (IARVSERV branch entry)
28	(1C)	ADDRESS	4	PVTRRCV	"V(IARRRC)" RSM recover router
32	(20)	ADDRESS	4	PVTSAEXC	"V(IARSAEXC)" Frame exchange routine
36	(24)	ADDRESS	4	PVTBRFCT	"V(IARBRFCT)" Address of IARBRFCT

PVT Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
40	(28)	ADDRESS	4	PVTUMCPU	"V(IARUMCPU)" Address of IARUMCPU
44	(2C)	ADDRESS	4	PVTPQRF	"V(IARPQRF)" PGSER FIX Fast Path - Register Format with function code/options
48	(30)	ADDRESS	4	PVTUMPF	"V(IARUMPF)" Address of IARUMPF
52	(34)	ADDRESS	4	PVTVUCOM	"V(IARVUCOM)" Address of IARVUCOM
56	(38)	ADDRESS	4	PVTVVASP	"V(IARVVASP)" Address of IARVVASP
60	(3C)	ADDRESS	4	PVTBOBT	"V(IARBOBT)" Address of IARBOBT
64	(40)	ADDRESS	4	PVTBOASM	"V(IARBOASM)" Address of IARBOASM
68	(44)	ADDRESS	4	PVTBFREE	"V(IARBFREE)" Address of IARBFREE
72	(48)	ADDRESS	4	PVTBBAD	"V(IARBBAD)" Address of IARBBAD
76	(4C)	ADDRESS	4	PVTBQNXT	"V(IARBQNXT)" Address of IARBQNXT
80	(50)	ADDRESS	4	PVTBADDI	"V(IARBADDI)" Address of IARBADDI
84	(54)	ADDRESS	4	PVTBQIST	"V(IARBQIST)" Address of IARBQIST
88	(58)	ADDRESS	4	PVTBQAIU	"V(IARBQAIU)" Address of IARBQAIU
92	(5C)	ADDRESS	4	PVTBDPRE	"V(IARBDPRE)" Address of IARBDPRE
96	(60)	ADDRESS	4	PVTBDCAN	"V(IARBDCAN)" Address of IARBDCAN
100	(64)	ADDRESS	4	PVTBQIAU	"V(IARBQIAU)" Address of IARBQIAU
104	(68)	ADDRESS	4	PVTBDPBC	"V(IARBDPBC)" Address of IARBDPBC
108	(6C)	ADDRESS	4	PVTBDCHK	"V(IARBDCHK)" Address of IARBDCHK
112	(70)	ADDRESS	4	PVTX1CHK	"V(IARX1CHK)" Address of IARX1CHK
116	(74)	ADDRESS	4	PVTBOASD	"V(IARBOASD)" Address of IARBOASD
120	(78)	ADDRESS	4	PVTXSS	"V(IARXSS)" Address of IARXSS

PVT Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
PVT	0		PVTPNL	68	
PVTBADDI	50		PVTPNR	6C	
PVTBBAD	48		PVTPPSIF	774	
PVTBDCAN	60		PVTPPSIX	768	
PVTBDCHK	6C		PVTPPSIY	76C	
PVTBDPBC	68		PVTPPSIZ	770	
PVTBDPRE	5C		PVTPQLB	70	
PVTBFREE	44		PVTPQLNB	78	
PVTBOASD	74		PVTPQRB	74	
PVTBOASM	40		PVTPQRF	2C	
PVTBOBT	3C		PVTPQRNB	7C	
PVTBQAIU	58		PVTPSIB	48	
PVTBQIAU	64		PVTPZFFR	E0	
PVTBQIST	54		PVTP3PFR	DC	
PVTBQNXT	4C		PVTP3PFX	D8	
PVTBRFCT	24		PVTRCVTT	E4	
PVTBRLKP	10		PVTRIDXT	778	
PVTCCDSL	C8		PVTRIT	4	
PVTCCDSW	C		PVTRRCV	1C	
PVTCJCPY	D4		PVTRSH	14	
PVTCQMVP	D0		PVTSAEXC	20	
PVTDLCON	BC		PVTSIN	A0	
PVTDZLIM	C0		PVTSOUT	A4	
PVTEAEXT	B4		PVTSSDEL	64	
PVTERCF	58		PVTSURST	B0	
PVTESTA	18		PVTUALF	98	
PVTEXT	0		PVTUCNVT	90	
PVTEXTPT	10		PVTUFP	88	
PVTGIOCM	40		PVTUINV	AC	
PVTHSCNV	4		PVTUMCPU	28	
PVTID	0		PVTUMPF	30	
PVTIRSRV	18		PVTUMVF	9C	
PVTKCMIT	3C		PVTUTRV	44	
PVTKDIS	38		PVTVFRMN	A8	
PVTKGRES	30		PVTVUCOM	34	
PVTKKCHL	18		PVTVVASP	38	
PVTKQASC	34		PVTVVTAB	0	
PVTKURPR	2C		PVTVVTPT	28	
PVTLCMIT	10		PVTWTRV	14	
PVTLDIS	C		PVTXCNTF	8C	
PVTLGRES	4		PVTXCRMF	54	
PVTLNCON	14		PVTXIBAD	50	
PVTLOCHL	1C		PVTXPLCK	80	
PVTLQASC	8		PVTXPRSB	4C	
PVTPCNA	C		PVTXQVDC	C4	
PVTPFTA	8		PVTXRCF	94	

Name	Hex Offset	Hex Value
PVTXSS	78	
PVTXVFA	60	
PVTXWRLS	B8	
PVTXWVFC	5C	
PVTXXFP	84	
PVTX1CHK	70	
PVTYCFA	8	
PVTYLGRP	CC	

PXT Information

PXT Heading Information

Common Name: VSM Cell Pool Primary Extent
Macro ID: IGVPXT
DSECT Name: PXT
Owning Component: Virtual Storage Manager (SC1CH)
Eye-Catcher ID: None
Storage Attributes: Subpool: User supplied
Key: User supplied
Residency: User supplied
Size: PXTBASE = 40 bytes
PXTXBASE = 32+ bytes
Created by: IGVCPBLD
Pointed to by: PPDPXT
Serialization: Compare Double and Swap
LOCAL/CML lock for local cell pools
VSMPAG for pageable global cell pools
VSMFIX for fixed global cell pools
Function: Describes the primary cell pool extent.

PXT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	PXT	
0	(0)	CHARACTER	40	PXTBASE	BASE PORTION OF PXT
0	(0)	CHARACTER	24	PXTHDR	USER SUPPLIED HEADER
24	(18)	CHARACTER	8	PXTCDSDW	COMPARE DOUBLE AND SWAP DOUBLEWORD
24	(18)	SIGNED	4	PXTSYNC	SYNCRHONIZATION COUNT
28	(1C)	ADDRESS	4	PXTCPTR	PTR TO 1ST CELL IN POOL
32	(20)	ADDRESS	4	PXTPPD	POINTER TO PPD
36	(24)	BITSTRING	2	PXTFLGS	FLAGS
36	(24)	BITSTRING	1	PXTFLGS1	
		1...		PXTMULTI	1 = MULTIHDR CELL POOL
37	(25)	BITSTRING	1	PXTFLGS2	
38	(26)	CHARACTER	2	*	RSVD, FOR POOL ALIGNMENT
40	(28)	CHARACTER	0	PXTPOOL	CELLS OF POOL. Note that this is truly 8 bytes past this label when the pool is BNDRY=QWORD
40	(28)	CHARACTER	24	PXTXBASE	BASE PORTION OF PXTX, ONLY applicable for Multiple Header Cellpools
40	(28)	CHARACTER	8	PXTXCNTL	Control Data
40	(28)	SIGNED	4	PXTXMAXCELLS	Maximum number of Cells Allowed IN Cell Pool
44	(2C)	SIGNED	4	PXTXALLOCCNT	Current Count of Cells allocated to Cell Pool
48	(30)	CHARACTER	8	PXTXCNTS	Counts Data
48	(30)	SIGNED	4	PXTXCOMPRESSCNT	Count of Compressed Cells
52	(34)	SIGNED	4	PXTXRELEASECNT	Count of Succesful Releases to Free Chain
56	(38)	BITSTRING	4	PXTXFLGS	Flag Word
56	(38)	BITSTRING	1	PXTXFLGS1	
		1...		PXTXEXPAND	Expansion has been done for this header
57	(39)	BITSTRING	1	PXTXFLGS2	
58	(3A)	BITSTRING	1	PXTXFLGS3	
59	(3B)	BITSTRING	1	PXTXFLGS4	
60	(3C)	SIGNED	2	PXTXCPUID	CPU ID of Header
62	(3E)	CHARACTER	2	PXTXRSV2	Reserved Space for PXTXBASE
64	(40)	CHARACTER	*	PXTXXTRA	Extra Portion of PXTX, length will depend on size of CPU Cache Line

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	PXTXC	PXTX Cell Mapping
0	(0)	ADDRESS	4	PXTXCNXT	Address of next Free Cells
4	(4)	SIGNED	4	PXTXCFRC	Count of Free Elements
8	(8)	CHARACTER	8	PXTXCRSV	Reserved Space

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	PXTXP	CELL Prefix Mapping

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	SIGNED	2	PXTXPCPUID	CPU ID for Cell
2	(2)	CHARACTER	14	*	Reserved

PXT Constants

Len	Type	Value	Name	Description
2	DECIMAL	256	PXT#CPUHDRLEN	Length of Header, Must be the same as EcvtCacheLineSize or BUILD will Fail
2	DECIMAL	8	PXT#CPUHDRLENLOG2	Log(2) value for PXT#CPUHDRLEN which is used for shifting CPU ID to calculate header offset
4	DECIMAL	4096	PXT#CELLSZ4K	Cell size value = 4K. Used to determine which multiplication factor (PXT#MF_SmCell or PXT#MF_LgCell) to use
4	DECIMAL	256	PXT#MF_SMCELL	Multiplication factor used to calculate the threshold above which cell sharing is allowed to occur -- for cells with size less than 4K
4	DECIMAL	128	PXT#MF_LGCELL	Multiplication factor used to calculate the threshold above which cell sharing is allowed to occur -- for cells with size 4K or more
4	DECIMAL	0	PXT#NOCELLS	Return value indicating no free cells are availble because MAXCELLS limit is reached
4	DECIMAL	1	PXT#CPOOLEXT	Return value indicating the cell pool either has been extended or is not empty (a cell has been freed)

PXT Cross Reference

Name	Hex Offset	Hex Value
PXT	0	
PXTBASE	0	
PXTCDSDW	18	
PXTCPTR	1C	
PXTFLGS	24	
PXTFLGS1	24	
PXTFLGS2	25	
PXTHDR	0	
PXTMULTI	24	80
PXTPOOL	28	
PXTPPD	20	
PXTSYNC	18	
PXTXALLOCCNT	2C	
PXTXBASE	28	
PXTXC	0	
PXTXCFRC	4	
PXTXCNTL	28	
PXTXCNTS	30	
PXTXCNXT	0	
PXTXCOMPRESSCNT	30	
PXTXCPUID	3C	
PXTXCRSV	8	
PXTXEXPAND	38	80
PXTXFLGS	38	
PXTXFLGS1	38	
PXTXFLGS2	39	
PXTXFLGS3	3A	
PXTXFLGS4	3B	
PXTXMAXCELLS	28	
PXTXP	0	
PXTXPCPUID	0	
PXTXRELEASECNT	34	
PXTXRSV2	3E	
PXTXXTRA	40	

QDB Information

QDB Heading Information

Common Name: Queue Descriptor Block
Macro ID: IHAQDB
DSECT Name: QDB
Owning Component: Allocation (SC1B4)
Eye-Catcher ID: QDB
Offset: 0
Length: 4
Storage Attributes: Subpool: 236 or 237
Key: 1
Residency: SIZE: minimum 52 bytes
Size: minimum 52 bytes
NOTES: None
FLAG FMID DATE ID COMMENT
$L1=ODSLM HBB7705 001031 PDOO: OPEN DATA SET LIMIT RELIEF
$L2=ALLCHT HBB7750 070112 PDEV: DSAB Metadata
$P2=ME10263 HBB7750 080307 PDHV: Separate QDB from DSABQDB
Created by: IEFAB4FC
Since this is a generic queue header, it may also be created by other sources.
Pointed to by: JSCDSABQ field of the JSCB data area
LCTDSABQ field of the LCT data area
Since this is a generic queue header, it May also be pointed to by other sources.
Serialization:
Function: This is a generic queue header block, and contains first and last element pointers to the queue, as well as the number of elements in the queue, and other identifying information about the queue.

QDB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	QDB	
0	(0)	CHARACTER	4	QDBQDB	- ACRONYM IN EBCDIC -QDB-
4	(4)	BITSTRING	2	QDBATTR	- QUEUE ATTRIBUTES
		.... .1..		QDBELEMA	"X'04'" AT LEAST ONE ELEMENT IS ABOVE THE LINE
6	(6)	SIGNED	2	QDBRV001	- RESERVED
8	(8)	SIGNED	4	QDBNELMS	- NUMBER OF ELEMENTS ON QUEUE
12	(C)	ADDRESS	4	QDBFELMP	- POINTER TO FIRST ELEMENT
16	(10)	ADDRESS	4	QDBLELMP	- POINTER TO LAST ELEMENT
20	(14)	SIGNED	2	QDBFPTDS	- FORWARD POINTER DISPLACEMENT
22	(16)	SIGNED	2	QDBBPTDS	- BACKWARD POINTER DISPLACEMENT
24	(18)	SIGNED	2	QDBPRSZ	- PRIORITY FIELD SIZE
26	(1A)	SIGNED	2	QDBPRDS	- PRIORITY FIELD DISPLACEMENT
28	(1C)	ADDRESS	4	QDBRV002	- RESERVED
32	(20)	SIGNED	4	QDBNELMA	- NUMBER OF ELEMENTS ON ABOVE OR BELOW THE LINE QUEUE
36	(24)	ADDRESS	4	QDBFELMA	- POINTER TO FIRST ABOVE OR BELOW THE LINE DSAB
40	(28)	ADDRESS	4	QDBLELMA	- POINTER TO LAST ABOVE OR BELOW THE LINE DSAB
44	(2C)	SIGNED	2	QDBFPTDA	- ABOVE OR BELOW THE LINE FORWARD POINTER DISPLACEMENT
46	(2E)	SIGNED	2	QDBBPTDA	- ABOVE OR BELOW THE LINE BACKWARD POINTER DISPLACEMENT
48	(30)	SIGNED	4	QDBECPID	ELEMENT CELL POOL ID

QDB Cross Reference

Name	Hex Offset	Hex Value
QDB	0	
QDBATTR	4	
QDBBPTDA	2E	
QDBBPTDS	16	
QDBECPID	30	
QDBELEMA	4	4
QDBFELMA	24	
QDBFELMP	C	
QDBFPTDA	2C	
QDBFPTDS	14	
QDBLELMA	28	
QDBLELMP	10	
QDBNELMA	20	
QDBNELMS	8	
QDBPRDS	1A	
QDBPRSZ	18	
QDBQDB	0	
QDBRV001	6	
QDBRV002	1C	

QIO Information

QIO Heading Information

Common Name: QMNGRIO Work Area
Macro ID: IHAQIO
DSECT Name: IHAQIO
Owning Component: Scheduler Work Area Manager (SC1B5)
Eye-Catcher ID: None
Storage Attributes: Subpool: Any subpool
Key: Any key
Size: 256 bytes
Created by: Routines that invoke QMNGRIO
Pointed to by: QMIOP
Function: Contains the QMPA.

QIO Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	IHAQIO	
0	(0)	CHARACTER	36	QIOQMPA	- Q MGR PARAMETER AREA
36	(24)	CHARACTER	76	QIOECIOB (0)	ECB/IOB SPACE
36	(24)	SIGNED	4	QIOECB	- EVENT CONTROL BLOCK
40	(28)	DBL WORD	8	QIOIOB (0)	- INPUT/OUTPUT BLOCK
40	(28)	CHARACTER	2	QIOIFLGS	- IOB FLAG BYTES
42	(2A)	CHARACTER	2	QIOISNS	- IOB SENSE BYTES
44	(2C)	SIGNED	4	QIOIECB (0)	-
44	(2C)	CHARACTER	1	QIOICC	- IOB ECB COMPLETION CODE
45	(2D)	ADDRESS	3	QIOIECBA	- IOB ECB ADDRESS
48	(30)	CHARACTER	1	QIOIFLG3	- IOB FLAG BYTE 3
49	(31)	CHARACTER	7	QIOICSW	- SEVEN LOW ORDER BYTES OF LAST CSW
56	(38)	SIGNED	4	QIOIST (0)	-
56	(38)	CHARACTER	1	QIOISIO	- SIO CONDITION CODE
57	(39)	ADDRESS	3	QIOISTRT	- CCW CHAIN POINTER
60	(3C)	SIGNED	4	QIOIDCB (0)	-
60	(3C)	CHARACTER	1	QIOIRSVD	-
61	(3D)	ADDRESS	3	QIOIDCBA	- IOB DCB ADDRESS
64	(40)	CHARACTER	8	QIOIREST	- SPACE TO IOB END
72	(48)	CHARACTER	8	QIOISEEK	- SEEK/SEARCH MBBCCHHR
80	(50)	DBL WORD	8	QIOISET (0)	- SET SECTOR CCW
80	(50)	CHARACTER	1	QIOISETO	- SET SECTOR OP CODE
81	(51)	ADDRESS	3	QIOISETA	- SET SECTOR DATA ADDRESS
84	(54)	CHARACTER	1	QIOISETF	- SET SECTOR FLAGS
85	(55)	CHARACTER	1	QIOISETR	- SET SECTOR RESERVED
86	(56)	SIGNED	2	QIOISETL	- SET SECTOR LENGTH
88	(58)	DBL WORD	8	QIOISCH (0)	- SEARCH CCW
88	(58)	CHARACTER	1	QIOISCHO	- SEARCH OP CODE
89	(59)	ADDRESS	3	QIOISCHA	- SEARCH DATA ADDRESS
92	(5C)	CHARACTER	1	QIOISCHF	- SEARCH FLAGS
93	(5D)	CHARACTER	1	QIOISCHR	- SEARCH RESERVED
94	(5E)	SIGNED	2	QIOISCHL	- SEARCH LENGTH
96	(60)	DBL WORD	8	QIOITIC (0)	- TIC CCW
96	(60)	CHARACTER	1	QIOITICO	- TIC OP CODE
97	(61)	ADDRESS	3	QIOITICA	- TIC DATA ADDRESS
100	(64)	CHARACTER	1	QIOITICF	- TIC FLAGS
101	(65)	CHARACTER	1	QIOITICR	- TIC RESERVED
102	(66)	SIGNED	2	QIOITICL	- TIC LENGTH
104	(68)	DBL WORD	8	QIOIO (0)	- I/O CCW
104	(68)	CHARACTER	1	QIOIOO	- I/O OP CODE
105	(69)	ADDRESS	3	QIOIOA	- I/O DATA ADDRESS
108	(6C)	CHARACTER	1	QIOIOF	- I/O FLAGS
109	(6D)	CHARACTER	1	QIOIOR	- I/O RESERVED
110	(6E)	SIGNED	2	QIOIOL	- I/O LENGTH
112	(70)	SIGNED	4	QIOJOB (0)	- QMPA JOB INFO LIST
112	(70)	SIGNED	4	QIOFILL1	- FULL WORK OF ZEROS
116	(74)	ADDRESS	4	QIONAMEA	- POINTER TO JOB NAME
120	(78)	ADDRESS	4	QIOSWADS	- POINTER TO SWADS DCB
124	(7C)	SIGNED	4	QIOPREXP (0)	- QMPA EXTERNAL PARM AREA PREFIX
124	(7C)	SIGNED	2	QIOFILL2	-
126	(7E)	SIGNED	2	QIORECL	- RECORD LENGTH
128	(80)	SIGNED	4	QIOXPA (0)	- QMPA EXTERNAL PARM AREA
128	(80)	ADDRESS	4	QIOCOREA	- IN-CORE ADDRESS OF RECORD

QIO Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
132	(84)	CHARACTER	4	QIOTTR0	- RELATIVE DISK ADDRESS OF RECORD
136	(88)	CHARACTER	120	QIOQMWRK	WORK SPACE FOR QUEUE MANAGER

QIO Cross Reference

Name	Hex Offset	Hex Value
IHAQIO	0	
QIOCOREA	80	
QIOECB	24	
QIOECIOB	24	
QIOFILL1	70	
QIOFILL2	7C	
QIOICC	2C	
QIOICSW	31	
QIOIDCB	3C	
QIOIDCBA	3D	
QIOIECB	2C	
QIOIECBA	2D	
QIOIFLGS	28	
QIOIFLG3	30	
QIOIO	68	
QIOIOA	69	
QIOIOB	28	
QIOIOF	6C	
QIOIOL	6E	
QIOIOO	68	
QIOIOR	6D	
QIOIREST	40	
QIOIRSVD	3C	
QIOISCH	58	
QIOISCHA	59	
QIOISCHF	5C	
QIOISCHL	5E	
QIOISCHO	58	
QIOISCHR	5D	
QIOISEEK	48	
QIOISET	50	
QIOISETA	51	
QIOISETF	54	
QIOISETL	56	
QIOISETO	50	
QIOISETR	55	
QIOISIO	38	
QIOISNS	2A	
QIOIST	38	
QIOISTRT	39	
QIOITIC	60	
QIOITICA	61	
QIOITICF	64	
QIOITICL	66	
QIOITICO	60	
QIOITICR	65	
QIOJOB	70	
QIONAMEA	74	
QIOPREXP	7C	
QIOQMPA	0	
QIOQMWRK	88	
QIORECL	7E	
QIOSWADS	78	
QIOTTR0	84	
QIOXPA	80	

QMIDS Information

QMIDS Programming Interface information

Programming Interface information

QMIDS

End of Programming Interface information

QMIDS Heading Information

Common Name: Constants for SWA block IDs and acronyms
Macro ID: IEFQMIDS
DSECT Name: N/A
Owning Component: SWA Manager (SC1B5)
Eye-Catcher ID: N/A
Offset: N/A
Length: N/A
Storage Attributes: Subpool: N/A
Key: N/A
Residency: N/A
Size: N/A
FREQUENCY = N/A
Created by: N/A
Pointed to by: N/A
Serialization: N/A
Function: Provides constants for SWA block IDs and acronyms

QMIDS Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0		
		....		SWJCTID	"X'00'" JOB CONTROL TABLE (JCT) BLOCK ID
		1		SWACTID	"X'01'" ACCOUNT CONTROL TABLE (ACT) BLOCK ID
		.... ..1.		SWSCTID	"X'02'" STEP CONTROL TABLE (SCT) BLOCK ID
		11		SWSIOTID	"X'03'" STEP INPUT/OUTPUT TABLE (SIOT) BLOCK ID
		.... .1..		SWSIOXID	"X'04'" SIOT EXTENSION (SIOX) BLOCK ID
		.... .111		SWDSNTID	"X'07'" DATA SET NAMES TABLE (DSNT) BLOCK ID
		.... 1.1.		SWPOTID	"X'0A'" PROCEDURE OVERRIDE TABLE (POT) BLOCK ID
		.... 11..		SWSCTXID	"X'0C'" STEP CONTROL TABLE EXT (SCTX) BLOCK ID
		 1111		SWDSENID	"X'0F'" DATA SET ENQUEUE TABLE (DSEN) BLOCK ID
		...1 1.11		SWJMRID	"X'1B'" JOB MANAGEMENT RECORD (JMR) BLOCK ID
		...1 11..		SWJFCBID	"X'1C'" JOB FILE CONTROL BLOCK (JFCB) ID
		...1 11.1		SWJFCXID	"X'1D'" JOB FILE CONTROL BLOCK EXT (JFCX) ID
		..1.		SWPDIDID	"X'20'"
		..1. ...1		SWPDIBID	"X'21'" PASSED DATASET INFORMATION BLOCK (PDIB) ID
		..1. ..1.		SWPDIQID	"X'22'"
		..1. ..11		SWGDGNID	"X'23'" GDG NAMES TABLE (GDGN) BLOCK ID
		..1. .1.1		SWIWABID	"X'25'" INTERPRETER WORK ARE BLOCK (IWAB) ID
		..1. .11.		SWVUTID	"X'26'" VOLUME UNLOAD TABLE (VUT) BLOCK ID
		..1. .111		SWDDNTID	"X'27'" DDNAMES TABLE (DDNT) BLOCK ID
		..1. 1...		SWAMPXID	"X'28'" AMP KEYWORD EXTENSION (AMPX) BLOCK ID
		..1. 1..1		SWJFCEID	"X'29'" JOB FILE CONTROL BLOCK EXT (JFCE) ID
		..11		SWJCTXID	"X'30'" JOB CONTROL TABLE EXT (JCTX) BLOCK ID
		..11 ...1		SWSSWAID	"X'31'" SUBSYSTEM WORKAREA (SSWA) BLOCK ID
		..11 ..1.		SWSWBID	"X'32'" SCHEDULER WORK BLOCK (SWB) BLOCK ID
		..11 .1.1		SWSIOTBL	"X'35'" STEP INPUT/OUTPUT TABLE (SIOT BELOW) BLOCK ID
		..11 .11.		SWJFCBBL	"X'36'" JOB FILE CONTROL BLOCK (JFCB BELOW) BLOCK ID
		..11 .111		SWJFCEBL	"X'37'" JOB FILE CONTROL BLOCK EXT (JFCE BELOW) BLOCK ID
		..11 1...		SWIFBID	"X'38'" IF RELATIONAL (IFB) BLOCK ID

QMIDS Cross Reference

Name	Hex Offset	Hex Value
SWACTID	0	1
SWAMPXID	0	28
SWDDNTID	0	27
SWDSENID	0	F
SWDSNTID	0	7
SWGDGNID	0	23
SWIFBID	0	38
SWIWABID	0	25
SWJCTID	0	0
SWJCTXID	0	30
SWJFCBBL	0	36
SWJFCBID	0	1C
SWJFCEBL	0	37
SWJFCEID	0	29
SWJFCXID	0	1D
SWJMRID	0	1B
SWPDIBID	0	21

Name	Hex Offset	Hex Value
SWPDIDID	0	20
SWPDIQID	0	22
SWPOTID	0	A
SWSCTID	0	2
SWSCTXID	0	C
SWSIOTBL	0	35
SWSIOTID	0	3
SWSIOXID	0	4
SWSSWAID	0	31
SWSWBID	0	32
SWVUTID	0	26

QMPA Information

QMPA Programming Interface Information

Programming Interface Information

QMPA

ONLY the following fields are part of the programming interface:

- QMACLEX
- QMEPAX
- QMPACL
- QMPACLX
- QMPNC
- QMPOP
- QMREAD
- QMWRTE

End of Programming Interface Information

QMPA Heading Information

Common Name: SWA MANAGER PARAMETER AREA
Macro ID: IEFQMNGR
DSECT Name: IOPAQRAMS
Owning Component: Scheduler Work Area Manager (SC1B5)
Eye-Catcher ID: None
Storage Attributes: Subpool: Any
Key: Any
Size: 36 bytes located on a word boundary
Created by: Caller of Move Mode SWA Manager
Pointed to by: Register 1, JSCBQMPI in the active JSCB
Serialization: None
Function: Provides mapping of SWA Manager Parameter Area.

QMPA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	IOPARAMS	
0	(0)	CHARACTER	4	QMNAM	RESERVED
0	(0)	X'0'	0	QMCAN	"QMNAM" RESERVED
4	(4)	CHARACTER	2	QMVERS	VERSION NUMBER
4	(4)	X'2'	0	QMCURVER	"2" QMPA VERSION 2
6	(6)	CHARACTER	2	QMLGTH	QMPA LENGTH
8	(8)	CHARACTER	1	QMPOP	FUNCTION CODE PARAMETER FUNCTION CODE VALUES
8	(8)	X'1'	0	QMASGN	"1" ASSIGN
8	(8)	X'0'	0	QMASGS	"0" ASSIGN/START
8	(8)	X'2'	0	QMWRTA	"2" WRITE AND ASSIGN
8	(8)	X'3'	0	QMWRTE	"3" WRITE
8	(8)	X'4'	0	QMREAD	"4" READ
8	(8)	X'5'	0	QMREDALL	"5" READALL/MOVE
8	(8)	X'6'	0	QMWRTALL	"6" WRITEALL/MOVE
8	(8)	X'7'	0	QMDTYP	"7" RESERVED
8	(8)	X'8'	0	QMDELE	"8" DELETE
9	(9)	CHARACTER	2	QMFLT	RESERVED
11	(B)	CHARACTER	1	QMTST	RESERVED
12	(C)	CHARACTER	2	QMTLN	RESERVED
14	(E)	CHARACTER	1	QMNOT	RESERVED
15	(F)	CHARACTER	1	QMTPY	RESERVED
16	(10)	CHARACTER	1	QMSTA	JOB STATUS BYTE
		1...		QMACLEX	"X'80'" PASSING 4-BYTE EPA ADDRESS
		.1..		QMEPAX	"X'40'" PASSING 16 BYTE EPAS
		..1.		QMSJNL	"X'20'" IF SET TO ONE, JOURNAL BLOCKS
		...1		QMCONDGM	"X'10'" IF SET TO ONE, DO COND. GETMAIN
		.... 1...		QMBLDVAT	"X'08'" IF SET TO ONE, BUILD VAT TABLE
17	(11)	CHARACTER	1	QMPRI	RESERVED
18	(12)	CHARACTER	2	QMLNK	RESERVED
20	(14)	SIGNED	4	QMPACLX	4-BYTE PTR TO EXTERNAL PARAMETER LIST
24	(18)	SIGNED	4	QMADD	ADDRESS OF ADDRESS TABLE (QMAT)
28	(1C)	SIGNED	4	QMSTO	ADDRESS OF STORAGE TABLE (QMST)
32	(20)	CHARACTER	4	QMPCL	PTR TO EXTRN PARM LIST
32	(20)	X'20'	0	QMPCM	"QMPCL" NO. OF RCRDS TO ASSIGN
32	(20)	X'20'	0	QMPNC	"QMPCL" NO. OF RCRDS TO READ/WRITE
32	(20)	X'21'	0	QMPACL	"QMPCL+1" PTR TO EXTRN PARM LIST

Comment

THE FOLLOWING FIELD NAMES ARE USED ONLY FOR SWA FUNCTIONS

End of Comment

0	(0)	CHARACTER	1	QMSWSP	SWA SUBPOOL NUMBER
12	(C)	SIGNED	4	QMRBN	BLOCK NUMBER FOR THIS JOB

QMPA Cross Reference

Name	Hex Offset	Hex Value
IOPARAMS	0	
QMACLEX	10	80
QMADD	18	
QMASGN	8	1
QMASGS	8	0
QMBLDVAT	10	8
QMCAN	0	0
QMCONDGM	10	10
QMCURVER	4	2
QMDELE	8	8
QMDTYP	8	7
QMEPAX	10	40
QMFLT	9	
QMLGTH	6	
QMLNK	12	
QMNAM	0	
QMNOT	E	
QMPACL	20	21
QMPACLX	14	
QMPCL	20	
QMPCM	20	20
QMPNC	20	20
QMPOP	8	
QMPRI	11	
QMRBN	C	
QMREAD	8	4
QMREDALL	8	5
QMSJNL	10	20
QMSTA	10	
QMSTO	1C	
QMSWSP	0	
QMTLN	C	
QMTPY	F	
QMTST	B	
QMVERS	4	
QMWRTA	8	2
QMWRTALL	8	6
QMWRTE	8	3

QSRCD Information

QSRCD Heading Information

Common Name: ASM Quick Start Record
Macro ID: ILRQSRCD
DSECT Name: QSR
Owning Component: Auxiliary Storage Manager (SC1CW)
Eye-Catcher ID: QSRECORD
Offset: 0
Length: 8
Storage Attributes: Virtual Storage: YES
Subpool: 245
Key: 0
Data Space: NO
Residency: Above 16 Megabytes virtual
Size: 8192 bytes
Created by: ILRASRIM
Pointed to by: n/a
Serialization: The QSR is serialized via ENQ and DEQ (qname=SYSZPGAD,rname=PAGEADD) when used by ILRPGEXP.
No serialization during initialization.
Function: Contains all the information necessary to rebuild the Quick Startable LPA (PLPA) on a quick or warm start IPL. The QSR is used by the ASM RIM and QSR initialization routines. The RIM allocates the QSR on cold starts, and QSR initialization builds the QSR once PLPA has been loaded. The RIM reads the QSR on quick/warm starts. The QSR entries contain pointers to XQSRs (ILRXQSRDs) that contain the information necessary to rebuild the external page table entries for PLPA. The XQSRs are written to the PLPA data set.

QSRCD Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8192	QSR	Quick Start Record
0	(0)	CHARACTER	192	QSRHDR	QSR header
0	(0)	CHARACTER	8	QSRIDNT	Control block identifier, set to C'QSRECORD'
8	(8)	SIGNED	4	QSRVVMDI	Hash value for PLPA directory. This address must be on a 4096 byte boundary
12	(C)	ADDRESS	4	QSRPLPAS	Low virtual address -- start address of PLPA.
16	(10)	ADDRESS	4	QSRPLPAE	Address of first byte beyond top (end) of PLPA.
20	(14)	BITSTRING	1	QSRFLAGS	QSR flag byte
		1...		QSRPLPAF	PLPA data set full flag. 1 = PLPA became full during system initialization, 0 = PLPA not full yet
		.1..		QSRCOMMF	Common data full flag. 1 = Common data set became full during system initialization, 0 = Common data set not full yet
		..11 1111		*	Reserved
21	(15)	CHARACTER	3	*	Reserved
24	(18)	CHARACTER	8	QSRSYNCH	Time stamp for QSR record
32	(20)	ADDRESS	4	QSRXQSR	XQSR pointer
36	(24)	SIGNED	4	QSRXNUM	Number of XQSRs for PLPA
40	(28)	CHARACTER	8	QSRPRODI	FMID for the release that wrote this QSR record
48	(30)	CHARACTER	144	QSRRSV	Reserved
192	(C0)	CHARACTER	8000	QSRMAP	8000-byte map of PLPA XQSR LSIDs, made up of 4-byte entries

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4	QSRENTRY	QSR entry containing LSIDs for PLPA page. The entries are built in ascending order of virtual address, with each entry containing one LSID. The first zero entry indicates the end of the entries in use.
0	(0)	SIGNED	4	QSRLSID	Logical slot ID for PLPA data set copy of PLPA XQSR
0	(0)	CHARACTER	1	QSRPTNN	PART number portion of LSID, identifying page data set
1	(1)	CHARACTER	3	QSRSLOT	Slot number portion of LSID identifying slot within the PLPA page data set

QSRCD Cross Reference

Name	Hex Offset	Hex Value
QSR	0	
QSRCOMMF	14	40
QSRENTRY	0	
QSRFLAGS	14	
QSRHDR	0	
QSRIDNT	0	
QSRLSID	0	
QSRMAP	C0	
QSRPLPAE	10	
QSRPLPAF	14	80
QSRPLPAS	C	
QSRPRODI	28	
QSRPTNN	0	
QSRRSV	30	
QSRSLOT	1	
QSRSYNCH	18	
QSRVVMDI	8	
QSRXNUM	24	
QSRXQSR	20	

QVOD Information

QVOD Heading Information

Common Name: QUEUE VERIFICATION OUTPUT DATA AREA
Macro ID: IHAQVOD
DSECT Name: QVODHDR
Owning Component: SUPERVISOR CONTROL (SC1C5)
Eye-Catcher ID: None
Storage Attributes: Subpool: User subpool
Key: User key
Residency: User defined
Size: Variable
Created by: Caller of Queue Verifier
Pointed to by: QVPLODA field of the QVPL data area.
Serialization: Supplied by the caller of module IEAVEQV0, IEAVEQV4 or IEAVEQV5.
Function: Provides diagnostic information to the Queue Verifier.
Describes all errors found and corrective actions taken.

QVOD Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	QVOD	
Comment					
HEADER					
End of Comment					
0	(0)	SIGNED	4	QVODHDR (0)	HEADER TO DATA AREA
0	(0)	CHARACTER	1	QVODRES1	RESERVED BYTE 1
1	(1)	CHARACTER	1	QVODSIZE	TOTAL AVAILABLE SIZE IN BYTES
2	(2)	CHARACTER	1	QVODRES2	RESERVED BYTE 2
3	(3)	CHARACTER	1	QVODUSED	NUMBER OF BYTES USED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	QVODCMMN	COMMON PORTION OF DATA AREA
0	(0)	CHARACTER	1	QVODKL (0)	KEY/LENGTH
0	(0)	CHARACTER	1	QVODKEY	KEY=VRAQVOD
1	(1)	CHARACTER	1	QVODLEN	LENGTH OF QVODR15+ENTRIES
2	(2)	CHARACTER	4	QVODR15 (0)	SAME CONTENTS AS REG 15 ON RETURN
2	(2)	CHARACTER	1	QVODFLGS	FLAG BYTE
		1...		QVODOVFL	"X'80'" IF TOP BIT ON, AN OVERFLOW OF RECORDING INFORMATION HAS OCCURRED
		.1..		QVODR15R	"X'40'" IF BIT IS ON, ENOUGH ROOM EXISTS FOR THE KEY/LENGTH FIELDS AND REGISTER 15.
		..11 1111		QVODRES3	"X'3F'" RESERVED BITS
3	(3)	CHARACTER	1	QVODNREC	NUMBER OF ERRORS RECORDED
4	(4)	CHARACTER	1	QVODNDET	NUMBER OF ERRORS DETECTED
5	(5)	CHARACTER	1	QVODRCOD	RETURN CODE
6	(6)	CHARACTER	16	QVODFENT (0)	FIRST ERROR ENTRY

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	QVODENT	ERROR ENTRY FORMAT
0	(0)	CHARACTER	4	QVODCDAT (0)	CONSTANT DATA
0	(0)	CHARACTER	1	QVODERRC	ERROR CODE (SEE TABLE NAMED "QUEUE VERIFY ERROR CODES" IN THE COMPONENT DIAGNOSIS: SUPERVISOR CONTROL BOOK)
1	(1)	CHARACTER	1	QVODERRX	EXTENDED ERROR CODE (SEE TABLE NAMED "QUEUE VERIFY ERROR CODES" IN THE COMPONENT DIAGNOSIS: SUPERVISOR CONTROL BOOK)
2	(2)	CHARACTER	1	QVODTYPE	ENTRY POINT ID IN IEAVEQVX
3	(3)	CHARACTER	1	QVODELEN	REPORT LENGTH FOR IEAVEQVX
4	(4)	CHARACTER	12	QVODVDAT (0)	VARIABLE DATA
4	(4)	CHARACTER	4	QVODVW1	VARIABLE DATA WORD 1
8	(8)	CHARACTER	4	QVODVW2	VARIABLE DATA WORD 2
12	(C)	CHARACTER	4	QVODVW3	VARIABLE DATA WORD 3
12	(C)	X'10'	0	QVODEND	"*" END OF QVOD

QVOD Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
12	(C)	X'10'	0	QVODENSZ	"QVODEND-QVODENT" SIZE OF QVOD

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	QVODENTX	ERROR ENTRY FORMAT
0	(0)	CHARACTER	4	QVODXDAT (0)	CONSTANT DATA
0	(0)	CHARACTER	1	QVODXERR	ERROR CODE (SEE TABLE NAMED "QUEUE VERIFY ERROR CODES" IN THE COMPONENT DIAGNOSIS: SUPERVISOR CONTROL BOOK)
1	(1)	CHARACTER	1	QVODXERX	EXTENDED ERROR CODE (SEE TABLE NAMED "QUEUE VERIFY ERROR CODES" IN THE COMPONENT DIAGNOSIS: SUPERVISOR CONTROL BOOK)
2	(2)	CHARACTER	1	QVODXTYP	ENTRY POINT ID IN IEAVEQVX
3	(3)	CHARACTER	1	QVODXELN	REPORT LENGTH FOR IEAVEQVX.
4	(4)	CHARACTER	28	QVODXVD (0)	VARIABLE DATA
4	(4)	CHARACTER	4	QVODXVW1	VARIABLE DATA WORD 1
8	(8)	CHARACTER	4	QVODXVW2	VARIABLE DATA WORD 2
12	(C)	CHARACTER	4	QVODXVW3	VARIABLE DATA WORD 3
16	(10)	CHARACTER	4	QVODXR14	RESERVED ---- WORD 4
20	(14)	CHARACTER	4	QVODXVW5	VARIABLE DATA WORD 5
24	(18)	CHARACTER	4	QVODXVW6	VARIABLE DATA WORD 6
28	(1C)	CHARACTER	4	QVODXVW7	VARIABLE DATA WORD 7
28	(1C)	X'20'	0	QVODXEND	"*" END OF MULTI-SPACE OUTPUT AREA
28	(1C)	X'20'	0	QVODXSZ	"QVODXEND-QVODENTX" SIZE OF MULTI-SPACE OUTPUT AREA

QVOD Cross Reference

Name	Hex Offset	Hex Value
QVOD	0	
QVODCDAT	0	
QVODCMMN	0	
QVODELEN	3	
QVODEND	C	10
QVODENSZ	C	10
QVODENT	0	
QVODENTX	0	
QVODERRC	0	
QVODERRX	1	
QVODFENT	6	
QVODFLGS	2	
QVODHDR	0	
QVODKEY	0	
QVODKL	0	
QVODLEN	1	
QVODNDET	4	
QVODNREC	3	
QVODOVFL	2	80
QVODRCOD	5	
QVODRES1	0	
QVODRES2	2	
QVODRES3	2	3F
QVODR15	2	
QVODR15R	2	40
QVODSIZE	1	
QVODTYPE	2	
QVODUSED	3	
QVODVDAT	4	
QVODVW1	4	
QVODVW2	8	
QVODVW3	C	
QVODXDAT	0	
QVODXELN	3	
QVODXEND	1C	20
QVODXERR	0	
QVODXERX	1	
QVODXR14	10	
QVODXSZ	1C	20
QVODXTYP	2	
QVODXVD	4	
QVODXVW1	4	
QVODXVW2	8	
QVODXVW3	C	
QVODXVW5	14	
QVODXVW6	18	
QVODXVW7	1C	

QVPL Information

QVPL Heading Information

Common Name: QUEUE VERIFICATION PARAMETER LIST
Macro ID: IHAQVPL
DSECT Name: LCCX
Owning Component: Supervisor Control (SC1C5)
Eye-Catcher ID: None
Storage Attributes: Subpool: Any
Key: Any
Residency: Above or below 16M
Size: Varies
Created by: User of the service
Pointed to by: A register
Serialization: None
Function: Parameter list for an internal service.

QVPL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	40	QVPL	
0	(0)	CHARACTER	40	QVPLTYP3	
				Comment	
				DOUBLE-THREADED OR ACCESS REGISTER QUALIFIED QUEUE	
				End of Comment	
0	(0)	CHARACTER	33	QVPLTYP2	
				Comment	
				SINGLE-THREADED, HDR & TLR QUEUE	
				End of Comment	
0	(0)	CHARACTER	28	QVPLTYP1	
				Comment	
				SINGLE-THREADED, HEADER QUEUE	
				End of Comment	
				Comment	
				TYPE ONE QUEUE -- SINGLE THREADED, HEADER QUEUE	
				End of Comment	
0	(0)	ADDRESS	4	QVPLEVR	ADDR OF ELEMENT VERIFY RTN
4	(4)	ADDRESS	4	QVPLODA	ADDR OF OUTPUT DATA AREA
8	(8)	ADDRESS	4	QVPLWKA	ADDR OF WORK AREA FOR QUEUE VERIFY NOTE: THERE ARE CONSTANTS, QVPLWAL1-QVPLWAL6, INITIALIZED TO THE SIZE OF THE WORK AREA'S FOR ENTRY POINTS, IEAVEQV1-IEAVEQV6, RESPECTIVELY
12	(C)	ADDRESS	4	QVPLNOEL	VALUE IN HEADER WHEN NO ELTS ON QUEUE
16	(10)	ADDRESS	4	QVPLHDR	ADDRESS OF QUEUE HEADER
20	(14)	CHARACTER	4	QVPLHF	FORWARD POINTER DESCRIPTORS
20	(14)	CHARACTER	1	QVPLFLGH	HEADER FLAG FIELD
		1...		QVPLHD3	IF 1, HEADER= 3 BYTE FIELD IF 0, HEADER = 4 BYTES
		.1..		QVPLEXT	IF 1, EXTENDED QUEUE-VERIFIER PARAMETER LIST
		..11 1111		QVPLRES1	REST OF BYTE RESERVED
21	(15)	CHARACTER	1	QVPLFLGF	FORWARD PTR FLAG FIELD
		1...		QVPLFP3	IF 1, FWD PTR = 3 BYTE FIELD IF 0, FWD PTR = 4 BYTES
		.111 1111		QVPLRES2	REMAINDER OF BYTE RESERVED
22	(16)	SIGNED	2	QVPLFPTR	OFFSET IN BYTES OF FORWARD CHAIN POINTER
24	(18)	ADDRESS	4	QVPLLELM	VALUE IN FORWARD POINTER OF LAST ELEMENT
28	(1C)	CHARACTER	0	QVPLEND1	END OF TYPE 1 QVPL

QVPL Constants

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		Comment			
		TYPE TWO QUEUE -- SINGLE THREADED, HDR & TRLR QUEUE			
		End of Comment			
28	(1C)	ADDRESS	4	QVPLTRLR	ADDR OF QUEUE TRAILER
32	(20)	CHARACTER	1	QVPLTBE	TRLR & BKWD PTR DESCRIPTORS
32	(20)	CHARACTER	1	QVPLFLGT	TRAILER FLAG FIELD
		1...		QVPLTR3	IF 1, TRLR = 3 BYTE FIELD IF 0,TRLR = 4 BYTES
		.111 1111		QVPLRES3	REMAINDER OF BYTE RESERVED
33	(21)	CHARACTER	0	QVPLEND2	END OF TYPE 2 QVPL
		Comment			
		TYPE THREE QUEUE -- DOUBLE THREADED, HDR & TRLR QUEUE			
		End of Comment			
33	(21)	CHARACTER	7	*	DEFINED IN DECLARE FOR QVPLE2

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
32	(20)	STRUCTURE	8	QVPLE2	
32	(20)	CHARACTER	4	QVPLTB	
32	(20)	CHARACTER	1	*	
33	(21)	CHARACTER	1	QVPLFLGB	BACKWARD PTR FLAG FIELD
		1...		QVPLBP3	IF 1,BKWD PTR=3 BYTE FIELD IF 0,BKWD PTR= 4 BYTES
		.111 1111		QVPLRES4	REMAINDER OF BYTE RESERVED
34	(22)	SIGNED	2	QVPLBPTR	OFFSET IN BYTES OF BACKWARD CHAIN POINTER
36	(24)	ADDRESS	4	QVPLFELM	VALUE IN BACKWARD CHAIN OF FIRST ELEMENT
40	(28)	CHARACTER	0	QVPLEND	END OF TYPE 3 QVPL

QVPL Constants

Len	Type	Value	Name	Description
4	DECIMAL	40	QVPLWAL1	COMPILE TIME VARIABLE EQUAL TO WORK AREA SIZE FOR ENTRY IEAVEQV1
4	DECIMAL	40	QVPLWAL2	COMPILE TIME VARIABLE EQUAL TO WORK AREA SIZE FOR ENTRY IEAVEQV2
4	DECIMAL	40	QVPLWAL3	COMPILE TIME VARIABLE EQUAL TO WORK AREA SIZE FOR ENTRY IEAVEQV3
4	DECIMAL	160	QVPLWAL4	COMPILE TIME VARIABLE EQUAL TO WORK AREA SIZE FOR ENTRY IEAVEQV4
4	DECIMAL	160	QVPLWAL5	COMPILE TIME VARIABLE EQUAL TO WORK AREA SIZE FOR ENTRY IEAVEQV5
4	DECIMAL	160	QVPLWAL6	COMPILE TIME VARIABLE EQUAL TO WORK AREA SIZE FOR ENTRY IEAVEQV6
4	DECIMAL	160	QVPLMWKA	MAX WORK AREA SIZE
4	DECIMAL	40	QVPLMPL	MAX PLIST SIZE WITHOUT A QVPLX BEING NEEDED. SEE IHAQVPLX FOR CONSTANT IF QVPLX IS REQUIRED NOTE: NOT NEEDED BUT LEFT FOR COMPATIBILITY

QVPL Cross Reference

Name	Hex Offset	Hex Value
QVPL	0	
QVPLBPTR	22	
QVPLBP3	21	80
QVPLEND	28	
QVPLEND1	1C	
QVPLEND2	21	
QVPLEVR	0	
QVPLEXT	14	40
QVPLE2	20	
QVPLFELM	24	
QVPLFLGB	21	
QVPLFLGF	15	
QVPLFLGH	14	
QVPLFLGT	20	
QVPLFPTR	16	
QVPLFP3	15	80
QVPLHDR	10	
QVPLHD3	14	80
QVPLHF	14	
QVPLLELM	18	
QVPLNOEL	C	
QVPLODA	4	
QVPLRES1	14	3F
QVPLRES2	15	7F
QVPLRES3	20	7F
QVPLRES4	21	7F
QVPLTB	20	
QVPLTBE	20	
QVPLTRLR	1C	
QVPLTR3	20	80
QVPLTYP1	0	
QVPLTYP2	0	
QVPLTYP3	0	
QVPLWKA	8	

QWA Information

QWA Heading Information

Common Name: QUEUE WORK AREA
Macro ID: ISGQWA
DSECT Name: QWA
Owning Component: Global Resource Serialization (SCSDS)
Eye-Catcher ID: Local QWA - LQWA
Global QWA - GQWA
Private QWA - PQWA (used for PC Entered Enq/Deq/Res)
Space QWA - SQWA (used by LNQDQ to reduce CMSEQDQ contention when possible)
Offset: 0
Length: 4
Storage Attributes: Subpool: 245
Key: 0
Residency: Below 16M line
Size: 1200 BYTES
Created by: THE LOCAL/GLOBAL QWA-S HAVE BEEN DEFINED BY THE GRS RIM, ISGNCBIM.
Private QWAs (PQWA) ARE CREATED BY ISGGPC WHEN processing a PC ENQ/DEQ/RES request.
Pointed to by: LOCAL QWA - GVTLQWA
GLOBAL QWA - GVTGQWA
Private QWA - pointed to out of ISGGPC dynamic area only.
Serialization: LOCAL QWA - CMS ENQ/DEQ CLASS LOCK.
GLOBAL QWA - GRS LOCAL LOCK.
SVRB QWA - REQUESTOR'S LOCAL LOCK.
Private QWA- Instance of ISGGPC
Function: USED AS A COMMON WORK AREA FOR THE ENQ/DEQ/RESERVE PROCESSING ROUTINES.
NOTE THAT THE QWA MAY BE MAPPED TO THE FOLLOWING STORAGE AREAS.
1. LOCAL QWA - USED WHEN PROCESSING A LOCAL RESOURCE.
2. GLOBAL QWA - USED WHEN PROCESSING A GLOBAL RESOURCE.
3. SVRB QWA - USED DURING COMPLETION PROCESSING. THIS AREA MAPS TO THE RB EXTENDED SAVEAREA.
4. PRIVATE QWA - USED WHEN PROCESSING A PC ENQ ENQ/DEQ/RES request. ISGGPC gets ONE OF THESE PER-REQUEST AS IT IS NOT HOLDING THE REQUIRED LOCKS NEEDED FOR THE LQWA/GQWA.

QWA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	1200	QWA	QUEUE WORK AREA
0	(0)	ADDRESS	4	QWANCELL	pointer to next Qwa when being obtained or freed
0	(0)	CHARACTER	4	QWAID	CONTROL BLOCK IDENTIFIER
4	(4)	CHARACTER	48	QWABASIC	QWA BASIC SECTION - THIS IS THE ONLY SECTION THAT CAN MAP TO THE RB EXTENDED SAVEAREA OR THE RMPL WORK AREA
4	(4)	ADDRESS	4	QWAPELA	INPUT PEL ADDRESS
8	(8)	UNSIGNED	1	QWAKEY	REQUESTOR-S KEY
		1111		QWAKEYNB	QWA KEY NIBBLE
		 1111		*	RESERVED
9	(9)	UNSIGNED	1	QWARETRY	ID FOR RETRY ADDRESS
10	(A)	CHARACTER	2	QWARSVD3	RESERVED
12	(C)	ADDRESS	4	QWAPT1	PT OPERAND 1
16	(10)	ADDRESS	4	QWAPT2	PT OPERAND 2
20	(14)	CHARACTER	28	QWARSA	REQUEST SAVE AREA - THIS AREA IS MOVED TO THE QWBHRSA WHEN A GLOBAL RESOURCE IS REQUESTED
20	(14)	ADDRESS	4	QWAMRBQ	POINTER TO FIRST MESSAGE IN MRB QUEUE
24	(18)	UNSIGNED	1	QWAERR	FIRST DIGIT OF ABEND CODE
25	(19)	BITSTRING	1	QWAMFGS	MISC FLAG BITS

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1...		QWAFFDM	FAST-DEQ-MARK-FLAG. IF 1, BEING PERFORMED ON BEHALF OF A TASK WHICH RECEIVES A RETCODE OF OF ZERO WHEN IT ISSUED A DEQ. THE DEQ WAS HANDLED BY FAST-DEQ.
		.1..		QWAFDQS	FAST-DEQ-QWB-SEEN FLAG. WHEN 1, THIS FAST DEQ REQUEST-S QWB HAS BEEN SEEN BY ISGGRP00 AND CAN BE FREED BY ISGGNQDQ BACK-END OR HAS BEEN SEEN BY ISGGNQDQ BACK-END AND CAN BE FREED BY ISGGRP00
		..1.		*	WARNING QwbQxbOG maps here so do not use
		...1 11..		*	RESERVED
		.... ..1.		QWALISTREQ	List request.
		1		QWAPCENQ	ENQ/DEQ..LINKAGE=SYSTEM request or ISGENQ request. Not an SVC entered ENQ/DEQ/.. request. This is set via an ISGGPC path.@PFC
26	(1A)	UNSIGNED	2	QWAPFLGS	SAVED PEL FLAGS
26	(1A)	UNSIGNED	1	QWAPLAST	SAVED PELLAST FLAG BYTE
		1...		QWAEOL	PELEOL
		.1..		QWAIGNOR	PELIGNOR
		..1.		QWARES1	PELRES1
		...1		QWASHR	PELSHR
		.... 1...		QWASAVE	PELSAVE
		.... .1..		QWAGEN1	PELGEN1
		.... ..1.		QWAGEN2	PELGEN2
		1		QWATCBF	PELTCBF
27	(1B)	UNSIGNED	1	QWAPFLAG	SAVED PELFLAG FLAG BYTE
		1...		QWASHARE	PELSHARE
		.1..		QWASCPE1	PELSCPE1
		..1.		QWASYSMC	PELSYSMC
		...1		QWASTPMC	PELSTPMC
		.... 1...		QWASCPE2	PELSCPE2
		.... .1..		QWARET1	PELRET1
		.... ..1.		QWARET2	PELRET2
		1		QWARET3	PELRET3
28	(1C)	BITSTRING	1	QWAFLAG1	QWA PROCESS FLAGS

Comment

THE FOLLOWING FLAGS ARE INITIALIZED IN THE QWA BY THE ENQ/DEQ/RESERVE MAINLINE ROUTINE. WHEN A GLOBAL RESOURCE REQUEST IS PROCESSED BY THE GRP, THE DATA IS MOVED TO THE QWB HEADER (QWBHFLG1). WHEN THE ENQ/DEQ/RESERVE SVRB IS POST'D, THE INFORMATION IS MOVED BACK TO THE QWA. THEREFORE THE BIT DEFINITIONS OF QWBHFLG1 MUST MATCH THE BIT DEFINITION OF QWAFLAG1.

End of Comment

		1...		QWASTLC	STEAL PROCESSING IS NOW COMPLETE, I.E., STEAL QWB(S) HAVE BEEN PLACED ON THE REQUEST QUEUE IF NECESSARY
		.1..		QWASMC	INDICATES SET SMC STATUS (on ENQ request only)
		.1..		QWARMC	INDICATES RESET SMC STATUS (on DEQ or purge request only)
		..1.		QWAMTDQ	MASID-target DEQ. A QEL was DEQ-ed while it is the target of another QEL that has a non-zero QELMASID.
		...1		QWASPOST	INDICATES SPOST IS NECESSARY
		.... 1...		QWAINT	INDICATES AN INTERNALLY GENERATED REQUEST
		.... .1..		QWALNGWT	A LONG-WAIT IS NECESSARY
		.... ..1.		QWAINGRS	Primary=GRS (space-switching PC to GRS has occurred)
		1		QWAPURG	INDICATE ISGGDEQP HAS PURGED THE QWB THAT WAS MAPPED TO THIS QWA
29	(1D)	BITSTRING	1	QWAFLAG2	QWA STATUS FLAGS

Comment

THE FOLLOWING FLAGS ARE INITIALIZED IN THE QWA BY THE ENQ/DEQ/RESERVE MAINLINE ROUTINE. WHEN A GLOBAL RESOURCE IS REQUESTED, MAINLINE FRONT-END PROCESSING WILL MOVE THIS FLAG BYTE TO QWBHFLG2. THEREFORE THE BIT DEFINITIONS OF QWAFLAG2 MUST MATCH THE BIT DEFINITIONS OF QWBHFLG2.

End of Comment

		1...		QWAMIXR	MIXED RESOURCE REQUEST
		.1..		QWATCBFA	REQUESTING TASK WAS ABENDING WHEN THE REQUEST WAS RECEIVED
		..1.		QWAAUTH	REQUESTOR IS AUTHORIZED
		...1		QWAGLBL	GLOBAL RESOURCES DEFINED IN THE QWB
		.... 1...		QWAECBF	ECB= SPECIFIED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.... .1..		QWASVC56	ON, denotes an ENQ/RESERVE/ISGENQ OBTAIN request. OFF, denotes DEQ/ISGENQ RELEASE request. The SVC in name is misleading. It could be an ENQ/RESERVE with LINKAGE= SYSTEM (QWAPCENQ would be ON) or an ISGENQ OBTAIN request (QWAISGENQ and QWAPCENQ would be on)
		.... ..1.		QWAABDMC	THE TASK OR ADDRESS SPACE HAS TERMINATED WHILE IN MUST COMPLETE
		1		QWASYNCC	SYNCHRONIZATION COMPLETE
30	(1E)	UNSIGNED	2	QWAGRES	FOR ENQ REQUESTS, THE NUMBER OF GLOBAL RESOURCES FOR WHICH NO QEL WAS PUT IN QUEUE. FOR DEQ REQUESTS, THE NUMBER OF GLOBAL RESOURCES FOR WHICH A QEL WAS REMOVED FROM QUEUE
32	(20)	ADDRESS	4	QWAECBA	ECB ADDRESS - THIS FIELD IS REPLACED BY QWAQWBA WHEN THE QWABASIC SECTION MAPS TO THE SVRB EXTENDED SAVEAREA.
32	(20)	ADDRESS	4	QWAQWBA	DUAL USE FIELD. THIS FIELD WILL ONLY EXIST IN THE SVRB QWA WHEN AN ENQ/DEQ REQUESTOR IS SUSPENDED. IF A LOCAL RESOURCE IS BEING PROCESSED, THIS FIELD CONTAINS ZEROES. IF A GLOBAL RESOURCE IS BEING PROCESSED THIS FIELD CONTAINS THE ADDRESS OF THE FIRST QWB DEFINING THE REQUEST. THIS ENSURES THE QWB ADDRESS IS MADE AVAILABLE TO THE MAINLINE ESTAE ROUTINE SHOULD AN ERROR OCCUR OVER THE GLOBAL SUSPENSION.
36	(24)	ADDRESS	4	QWATCBA	REQUESTOR-S (OR DIRECTED) TCB ADDRESS
40	(28)	ADDRESS	4	QWASVRBA	SVRB ADDRESS FOR THIS REQUEST
44	(2C)	ADDRESS	4	QWAQXB	ADDRESS OF QXB
				Comment	
		END OF RSA SECTION			
				End of Comment	
48	(30)	BITSTRING	1	QWAFLAG3	REQUEST PROCESSING FLAGS - THESE FLAGS ARE NOT TRANSPOSED TO THE QWB.
		1...		QWACMS	CMS LOCK HELD
		.1..		QWAFRR	FRR ESTABLISHED
		..1.		QWAREQLL	REQUESTOR-S LOCAL LOCK
		...1		QWAGRSLL	GRS LOCAL LOCK
		.... 1...		QWA3ERSQ	EARLY-RESERVE-QUEUE FLAG. IF 1, THE ENQ/DEQ REQUEST HAS A GLOBAL RESOURCE WITH THE SAME NAME AS AN EARLY-RESERVE LOCAL RESOURCE. EVERY QWB OF THE REQUEST MUST BE PLACED ON THE EARLY-RESERVE QUEUE.
		.... .1..		QWARQDMG	REQUEST DAMAGED FLAG. IF 1, THE QWB FOR THIS REQUEST WAS DAMAGED SINCE THE QWBHSYID FIELD DID NOT CONTAIN A VALID SYSID. IF THE REQUEST REPRESENTS AN ENQ, THE ENQ PROCESSING ROUTINE WILL SET THE QCBNOENQ FLAG IN EACH REQUESTED QCB AND WILL ADD A QEL TO THE QCB CHAIN.
		.... ..1.		*	Reserved. Not used
		1		QWALSTRQ	LIST REQUEST - IF 1, REQUEST WAS PART OF A MULTIPLE RESOURCE REQUEST FROM THIS SYSTEM
49	(31)	BITSTRING	1	QWAFLAG4	REQUEST PROCESSING FLAGS - THESE FLAGS ARE NOT TRANSPOSED TO THE QWB.
		1...		QWABADML	BAD MINOR LENGTH SPECIFIED
		.1..		QWADMGE	TRIGGERS Q-DAMAGE MESSAGE
		..1.		QWAWAITN	WAITING QEL FOUND (NOT ECB)
		...1		QWA1DEQ	AT LEAST 1 QEL DEQUEUED
		.... 1...		QWAISGENQ	ISGENQ request. Set on ISGGPC path. QWAPCENQ will also be on. Note that QWASVC56 denotes if it is an obtain/release
		.... .1..		QWAWAIT	WAIT WITHIN ENQ/DEQ
		.... ..1.		QWAMVCP	ISSUE MVCP - EITHER USER IS NOT AUTHORIZED OR THE INPUT PEL COULD NOT BE CONTAINED IN THE SQA QWB
		1		QWANOENQ	TURN OFF ALL ENQ-S
50	(32)	BITSTRING	1	QWAFLAG5	REQUEST PROCESSING FLAGS - THESE FLAGS ARE NOT TRANSPOSED TO THE QWB
		1...		QWAGLBLQ	THE GLOBAL ASCB QEL QUEUE IS BEING SEARCHED
		.1..		QWARMFP	RMF HAS BEEN CALLED
		..1.		QWAHOLD	ISSUE ENQHOLD SYSEVENT
		...1		QWAQXBO	QXB OBTAINED
		.... 1...		QWACSYID	REQUEST WAS INITIATED FROM THE CURRENT SYSTEM
		.... .1..		QWAPHLDR	QSCAN PLACE-HOLDER QCB IS BEING PURGED.
		.... ..1.		QWAMOD24	REQUEST WAS INITIATED IN 24-BIT AMODE
		1		QWAGBLRS	TASK OWNS GLOBAL RESOURCES
51	(33)	BITSTRING	1	QWAFLAG6	QWA STATUS FLAGS
		1...		QWAR15SW	NON-ZERO RETURN CODE PRESENT
		.1..		QWA6ECBZ	ECB-ZERO FLAG. ECB-OPERAND WAS SPECIFIED, WITH ECB-ADDRESS OF ZERO. SET FLAG QELECBZ.
		..1.		QWA6GERT	Global ENQ RET=TEST PELs exist for this request

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		QWACALLGERTS	Indicates whether ISGGLUPC should call ISGGERTS.
		.... 1...		QWAGLOBALSQUEUED	Indicates that ISGGLU has or is about to place the global Qwb onto GvtReqQ
		.... .1..		QWARNLSCHANGED	Indicates that ISGGLU detected that the RNLs had changed across the window where lock were dropped and the request thus needs to be redriven and the queued1 exit notified
		.... ..1.		QWAQXBOG	Qxb obtained by a GRP
		1		QWAPRNEEDED	ISGGQWBI did a stacking PC or a BAKR, so XENDUP in ISGGNQDQ needs to do the PR to unstack
52	(34)	CHARACTER	0	QWAEND1	END BASIC SECTION
		Comment			
		NOTE THAT THE FOLLOWING FIELDS ARE NOT INCLUDED IN THE SVRB QWA			
		End of Comment			
52	(34)	CHARACTER	140	QWARDA	QWA REQUEST DATA AREA
52	(34)	CHARACTER	16	QWARSA2	QWA REQUEST DATA AREA
52	(34)	CHARACTER	8	QWAJOBNM	JOBNAME/USERID OF REQUESTOR
60	(3C)	UNSIGNED	4	QWAORIGN	ORIGIN OF REQUESTOR
60	(3C)	UNSIGNED	2	QWASYSID	SYSID OF REQUESTOR
62	(3E)	UNSIGNED	2	QWAASID	ASID OF REQUESTOR
64	(40)	ADDRESS	4	QWAASCB	IF ENQ/DEQ/RESERVE, ADDRESS OF REQUESTOR-S ASCB. NOTE THAT IF THE HIGH-ORDER BIT IS SET, AN ISGQSCAN INFORMATION ROUTINE EXISTS FOR THE ADDRESS SPACE. IF A PURGE REQUEST, ADDRESS OF TARGET ASCB
		Comment			
		END OF RSA2 SECTION			
		End of Comment			
68	(44)	SIGNED	4	QWALOCLR	COUNT OF LOCAL RESOURCES REQUESTED
72	(48)	SIGNED	4	QWAGLBLR	COUNT OF GLOBAL RESOURCES REQUESTED
76	(4C)	SIGNED	4	QWAQWBS	COUNT OF QWB-S REQUIRED TO CONTAIN A GLOBAL RESOURCE REQUEST.
80	(50)	SIGNED	4	QWAFREEC	COUNT OF QCB/QEL/QXB-S TO BE FREED
84	(54)	SIGNED	4	QWACPELR	COUNT OF PEL ENTRIES REMAINING TO BE MOVED TO THE PRIVATE AREA QWB(S)
88	(58)	SIGNED	4	QWAPRMSZ	TOTAL SIZE OF INPUT PEL
92	(5C)	SIGNED	4	QWANMESZ	TOTAL SIZE OF QNAME/RNAME-S IN PEL
96	(60)	UNSIGNED	2	QWAQWBSZ	AVAILABLE BYTES IN A PRIVATE AREA QWB
98	(62)	UNSIGNED	2	QWACSYS	CURRENT SYSID (0 FOR A LOCAL RESOURCE REQUEST)
100	(64)	ADDRESS	4	QWAQWBHS	ADDRESS OF THE QWB HEADER AND SMPL. IF A LOCAL RESOURCE IS BEING PROCESSED, CONTAINS THE ADDRESS OF THE SQA QWB. IF A GLOBAL RESOURCE IS BEING PROCESSED, CONTAINS THE ADDRESS OF A PRIVATE AREA QWB.
104	(68)	ADDRESS	4	QWAQWBF	ADDRESS OF FIRST QWB ON THE REQUEST HOLD QUEUE
108	(6C)	ADDRESS	4	QWAQWBL	ADDRESS OF LAST QWB ON THE REQUEST HOLD QUEUE
112	(70)	ADDRESS	4	QWAFQEL	ADDRESS OF FIRST INITIALIZED QEL FOR THE CURRENT REQUEST
116	(74)	ADDRESS	4	QWACOQWB	CURRENT OUTPUT QWB ADDRESS, I.E., THE PRIVATE AREA QWB(S) TO CONTAIN THE GLOBAL RESOURCE(S)
120	(78)	ADDRESS	4	QWACIQWB	CURRENT INPUT QWB ADDRESS, I.E., THE SQA QWB OR SQA QWB EXTENSION
124	(7C)	ADDRESS	4	QWANSLOT	NEXT QWB SLOT
128	(80)	ADDRESS	4	QWAHASH	HASH TABLE SLOT OF INPUT RESOURCE NAME
132	(84)	ADDRESS	4	QWAFQWB	ADDRESS OF FIRST QWB DEFINING THE GLOBAL REQUEST.
136	(88)	ADDRESS	4	QWAPPELE	PREVIOUS PEL ENTRY
140	(8C)	ADDRESS	4	QWAGSA	ADDRESS OF LOCAL OR GLOBAL GSA
144	(90)	CHARACTER	20	QWADPL	DEQ PURGE LIST
164	(A4)	ADDRESS	4	QWACNFY@	Address of contention notification parameter list
168	(A8)	ADDRESS	4	QWANQAR@	Pointer to copy of QWA, SQA QWB, ISGGRX dynamic area, in the user address space
172	(AC)	BITSTRING	2	QWAEXITS	Exit processing indicators
172	(AC)	BITSTRING	1	QWAEXITSTATUS	
		1...		QWAEXITSTATUSKNOWN	When set, exits have been checked
		.1..		QWANEEDTOCALLQ1EXIT	When set, the batch exit was or is about to be called and thus recovery will need to call the queued1 exit in the event of a failure between the batch exit and queued1 exit calls.
		..1.		QWAMUSTCALLBATCHCND	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					When set, the PreBatch exit, called in ISGLNQDQ, detected a request to call the BatchCnd exit. Don't recall the PreBatch exit and do call the BatchCnd exit during normal ENQ/DEQ processing
		...1		QWACALLCLEANUP	
					When set, the PreBatch exit indicated not to call BatchCnd therefore NQAR must be deleted
173	(AD)	BITSTRING	1	QWALIVEEXITS	indicates which exits, if any, exist
		1...		QWANXNQ	When set, there is an ISGNQXIT exit routine
		.1..		QWANXBX	When set, there is an ISGNQXITBATCH exit routine
		..1.		QWANXQ1X	When set, there is an ISGNQXITQUEUED1 exit routine
		...1		QWANXFQ	When set, there is an ISGNQXITFAST exit routine
		.... 1...		QWANXLQD	When set, there is an ISGENDOFLQCB exit routine
		.... .1..		QWANXPB	When set, there is an ISGNQXITPREBATCH exit routine
		.... ..1.		QWANXCB	When set, there is an ISGNQXITBATCHCND exit routine
174	(AE)	UNSIGNED	2	QWAABENDCD	Abend Code presented to exit
176	(B0)	ADDRESS	4	QWAGVTAD	ADDRESS OF GVT
180	(B4)	CHARACTER	12	QWAPLISTS	
180	(B4)	CHARACTER	12	QWARSVSP	ISGGRSVS Parm List
180	(B4)	ADDRESS	4	QWAQWB@	Pointer to QWB
184	(B8)	ADDRESS	4	QWAQEL@	Pointer to QEL
188	(BC)	ADDRESS	4	QWAPEL@	Pointer to PEL
192	(C0)	CHARACTER	0	QWAEND2	END OF AREA CLEARED
192	(C0)	SIGNED	4	QWAWORK1	GENERAL PURPOSE WORKAREA

Comment

SAVEAREAS FOLLOW. NOTE THE FOLLOWING PROTOCOL FOR USE OF THESE
SAVEAREAS. SAVEAREAS 1-3 CAN BE USED BY ANY ROUTINE WITH CORRECT
SERIALIZATION BUT CANNOT BE USED BETWEEN MODULES. (ISGGNQDQ,
ISGGQWBC, AND ISGGPGRP ARE COUNTED AS ONE MODULE IN APPLYING
THIS RULE.)
ISGGNQDQ PLACES THE ADDRESS OF QWASAVE1 IN REGISTER 13 BEFORE
CALLING ISGGQWBC OR ISGGPGRP. THESE MODULES MUST NOT USE
QWASAVE1.
SAVEAREA 4 IS USED BY ISGGRP00 TO INTERFACE WITH ISGGNQDQ
AND ISGGDEQP, AND BY ISGGNQDQ TO
CALL ISGGQWBI.
(NOTE: THE SAVEAREA IS USED FOR ISGGQWBI IN THE
FRONT-END OF ISGGNQDQ, AND IS USED BY ISGGRP00
IN THE BACK-END PROCESSING DONE BY ISGGNQDQ.)
SAVEAREA 5 IS USED BY ISGGDEQP TO INTERFACE WITH ISGGNQDQ
AND BY ISGGQWBI IN CALLING EXTERNAL ROUTINES.
(ISGGDEQP PLACES THE ADDRESS OF QWASAVE5 IN REGISTER 13 BEFORE
CALLING ENTRY-POINT ISGGDQ00 OF ISGGNQDQ. ISGGQWBI IS NOT
CALLED ON THIS PATH, WHICH IS BACK-END PROCESSING.)
Note: The above comments may not be correct anymore. They don't
seem to have been updated since before GRSSTAR

End of Comment

196	(C4)	CHARACTER	72	QWASAVE1	SAVEAREA 1 - LEVEL 1 S.A.
268	(10C)	CHARACTER	144	QWASAVE2_3	This field is used by ISGGRSV to save 64-bit regs
268	(10C)	CHARACTER	72	QWASAVE2	SAVEAREA 2 - LEVEL 2 S.A.
340	(154)	CHARACTER	72	QWASAVE3	SAVEAREA 3 - LEVEL 3 S.A.
412	(19C)	CHARACTER	72	QWASAVE4	SAVEAREA 4 - FOR GRP00
484	(1E4)	CHARACTER	72	QWASAVE5	SAVEAREA 5 - USED ONLY BY ISGGDEQP AND ISGGQWBI
556	(22C)	ADDRESS	4	QWAS1R14	REG 14 SUBROUTINE SAVEAREA 1
560	(230)	ADDRESS	4	QWAS2R14	REG 14 SUBROUTINE SAVEAREA 2
564	(234)	ADDRESS	4	QWAS3R14	REG 14 SUBROUTINE SAVEAREA 3
568	(238)	ADDRESS	4	QWAS4R14	REG 14 SUBROUTINE SAVEAREA 4
572	(23C)	ADDRESS	4	QWAE1R13	REG 13 ENTRY POINT SAVEAREA 1
576	(240)	ADDRESS	4	QWAGRP13	SAVEAREA TO CONTAIN THE SAVEAREA ADDRESS PROVIDED BY THE ATTACH OF GRP.
580	(244)	CHARACTER	52	QWATRMRM	ENQ/DEQ TERMINATION RESOURCE MANAGER WORK AREA.
580	(244)	CHARACTER	8	QWASTPNM	STEPNAME OF TERMINATING TASK
588	(24C)	BITSTRING	1	QWARMFLG	RESOURCE MANAGER FLAGS
		1...		QWAJSTEP	WHEN 1, JOBSTEP IS TERMINATING
		.1..		QWARMRV7	RESERVED
		..1.		QWARMRV6	RESERVED
		...1		QWARMRV5	RESERVED
		.... 1...		QWARMRV4	RESERVED
		.... .1..		QWARMRV3	RESERVED
		.... ..1.		QWARMRV2	RESERVED
		1		QWARMRV1	RESERVED
589	(24D)	CHARACTER	3	QWARMR01	RESERVED

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
592	(250)	CHARACTER	4	QWACCODE	COMPLETION CODE
592	(250)	BITSTRING	3	QWACOMPC	SYSTEM COMPLETION CODE IS FIRST 12 BITS. USER COMPLETION CODE IS LAST 12 BITS.
595	(253)	BITSTRING	1	QWACCRV1	RESERVED
596	(254)	ADDRESS	4	QWARB	CURRENT RB
600	(258)	CHARACTER	32	QWARUBTM	REGISTER UPDATE BLOCK FOR ISGGTRM1 RECOVERY
632	(278)	CHARACTER	64	*	Reserved not used
696	(2B8)	CHARACTER	12	QWAPGROA	OUTPUT AREA PRODUCED BY ISGGPGRP.
696	(2B8)	BITSTRING	1	QWAFLAG7	FIRST FLAG-BYTE FROM ISGGPGRP.
		1...		QWA7OWNR	REQUESTOR OWNS RESOURCE, OR REQUESTOR IS NOT ON QEL-CHAIN BUT WILL OWN RESOURCE WHEN ITS QEL IS ADDED TO THE QEL-CHAIN.
		.1..		QWA7AURC	ADJUST-UCB-RESERVE-COUNT. IF 1, THE CALLER SHOULD INCREASE THE UCB-RESERVE COUNT (ENQ/RESERVE) OR REDUCE THE COUNT (DEQ). (THIS ADJUSTMENT SHOULD BE DONE ONLY IF THE CALLER FINDS THAT THE RESERVE-CONVERSION RNLE DOES NOT SUPPRESS THE RESERVE) VALID ONLY IN ORIGINATING SYSTEM.
		..1.		QWA7CHGA	EXCLUSIVE-CONTROL ALLOWED. IF ENQ RET=CHNG: THE MATCH-QEL IS THE ONLY QEL THAT OWNS THE RESOURCE. IF MASID-ENQ: EXCLUSIVE-CONTROL IS ALLOWED IMMEDIATELY. IF MASID-ENQ CASE: VALID ONLY IN ORIGINATING SYSTEM.
		...1		QWA7HOLD	ISSUE SYSEVENT-HOLD(S).
		.... 1...		QWA7POST	POST A QEL OR QELS.
		.... .1..		QWA7RLSE	ISSUE SYSEVENT-RLSE(S).
		.... ..1.		QWA7COEX	COEXISTENCE RETURN-CODE FLAG. IF 1, THIS SYSTEM COULD NOT HONOR AN EXCLUSIVE ENQ WITH MASID OPERAND BECAUSE THE RESOURCE IS SHARED. VALID ONLY IN ORIGINATING SYSTEM.
		1		QWA7ABMR	MASID-RESTRICTION VIOLATED. ENQ WITH MASID VIOLATES A RESTRICTION, OR DEQ RELEASES A RESOURCE USED AS A MASID-TARGET. VALID ONLY IN ORIGINATING SYSTEM.
697	(2B9)	BITSTRING	1	QWAFLAG8	SECOND FLAG-BYTE FROM ISGGPGRP.
		1...		QWA8EXSH	EXC/SHR. 1 MEANS RC=8 ENQ SHOULD REPORT SHR CONTROL. VALID ONLY IN ORIGINATING SYSTEM.
		.1..		QWA8DCVT	Delayed convert request being POSTed. Indicates that the QEL pointed to by QWAPSTAD is a MASID convert-to-exclusive request that can now be given access to the resource without actually owning it.
		..1.		QWA8CNST	Start of contention
		...1		QWA8CNCH	Change in contention
		.... 1...		QWA8CNEN	End of contention
		.... .1..		QWA8RSV3	RESERVED
		.... ..1.		QWA8RSV2	RESERVED
		1		QWA8RSV1	RESERVED
698	(2BA)	SIGNED	2	QWAGPMAS	MASID-VALUE TO BE PLACED IN NEW QEL (IF ANY).
700	(2BC)	ADDRESS	4	QWAMQLAD	ADDRESS OF MATCH-QEL.
704	(2C0)	ADDRESS	4	QWADSTAD	DEFERRED-STEAL ADDRESS. ADDRESS OF A QEL THAT CAN NOW BE STOLEN WHEN A MASIDQEL IS DEQ-ED. VALID ONLY IN ORIGINATING SYSTEM.
708	(2C4)	UNSIGNED	1	QWAPGRFN	ISGGPGRP FUNCTION-CODE
709	(2C5)	CHARACTER	1	QWARSVD5	RESERVED
710	(2C6)	SIGNED	2	QWAFMTVL	VALUE OF FORMAT-BYTE THAT PRECEDES FIRST PEL, OR ZERO
712	(2C8)	SIGNED	4	QWASEHCT	COUNT OF SYSEVENT-HOLDS TO BE ISSUED.
716	(2CC)	SIGNED	4	QWAPSTCT	COUNT OF POSTS TO BE ISSUED.
720	(2D0)	SIGNED	4	QWASERCT	COUNT OF SYSEVENT-RLSES TO BE ISSUED.
724	(2D4)	ADDRESS	4	QWASEHAD	ADDRESS OF FIRST QEL TO BE TARGET OF SYSEVENT-HOLD.
728	(2D8)	ADDRESS	4	QWAPSTAD	ADDRESS OF FIRST QEL TO BE TARGET OF POST.
732	(2DC)	ADDRESS	4	QWASERAD	ADDRESS OF FIRST QEL TO BE TARGET OF SYSEVENT-RLSE.
736	(2E0)	CHARACTER	32	QWACLR2B	BEGINNING OF SECOND QWA SECTION THAT IS CLEARED AT BEGINNING OF ENQ OR DEQ REQUEST
736	(2E0)	CHARACTER	24	QWANWPEL	BEGINNING OF PARAMETERS FROM NEW-FORMAT PEL-PREFIX. THIS FIELD IS SENT TO OTHER SYSTEMS IN FIELDS QWBXRSA3 AND QWBXR3LN OF MAPPING MACRO ISGQWB.
736	(2E0)	ADDRESS	4	QWANPTCB	TCB-ADDRESS OR ZERO.
740	(2E4)	ADDRESS	4	QWANPECB	ECB-ADDRESS OR ZERO.
744	(2E8)	ADDRESS	4	QWANPMAS	MASID-OPERAND OR ZERO.
748	(2EC)	ADDRESS	4	QWANPMTC	MTCB-OPERAND OR ZERO.
752	(2F0)	CHARACTER	8	QWARSVC4	RESERVED.
760	(2F8)	CHARACTER	0	QWANPEND	END OF PARAMETERS.
760	(2F8)	BITSTRING	1	QWAFLAG9	FLAG-BYTE
		1...		QWA9CNPP	COPY NEW-FORMAT PEL-PREFIX FLAG. USED BY ISGGQWBI.
		.1..		QWA9DSTL	DEFERRED-STEAL NEEDED. USED BY XDEQQEL SUBROUTINE OF ISGGNQDQ.
		..1.		QWA9RSV6	RESERVED
		...1		QWA9RSV5	RESERVED
		.... 1...		QWA9RSV4	RESERVED
		.... .1..		QWA9RSV3	RESERVED

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... ..1.		QWA9RSV2	RESERVED
		1		QWA9RSV1	RESERVED
761	(2F9)	CHARACTER	7	QWARSVD6	RESERVED
768	(300)	ADDRESS	4	QWAFRRPARMAREA@	Address of FRRParm area used by ISGLNQDQ to give ISGGFRR0 information such as MODID, and the address of the QWA being used.
772	(304)	ADDRESS	4	QWAQSQHTENT	Address of ASID hash table for the home address space. Only set STEP requests
776	(308)	CHARACTER	72	*	Reserved not used
848	(350)	BITSTRING	16	QWABUILTETOD	Time that the QWA was initialize by either ISGLNQDQ, ISGGNQDQ, or ISGGPC. This time represents the time that the request was made. It makes its way into the QXBETOD and then the ENQTOKEN for ISGENQ requests
864	(360)	CHARACTER	12	QWATCBATTOKNINFO	Ttoken info associated with QWATCBA TCB
864	(360)	BITSTRING	8	QWATCBATTKNSTKN	From TTKNSTKN. This is STOKEN of the assigned address space.
872	(368)	BITSTRING	4	QWATCBATTKNCNT	From TTKNCNT
876	(36C)	CHARACTER	120	QWAGENWORKAREA	Workarea used by ISGGQWBC and ISGGNQDQ
876	(36C)	BITSTRING	8	QWASAVEDRSVCODES	Used by ISGGNQDQ
876	(36C)	BITSTRING	4	QWASAVEDRSVRETCODE	Return code from ISGGRSV saved by ISGGNQDQ on DEQ passing control to waiters only
880	(370)	BITSTRING	4	QWASAVEDRSVRSNCODE	Reason code from ISGGRSV saved by ISGGNQDQ on DEQ passing control to waiters only
884	(374)	CHARACTER	0	QWASAVEDRSVRETCODEEND	
996	(3E4)	CHARACTER	88	*	Reserved
1084	(43C)	CHARACTER	28	QWAISGLNQDQCOMMUNICATIONAREA	Data passed from ISGLNQDQ to mainline ENQ/DEQ when exit status has been determined
1084	(43C)	ADDRESS	4	QWAICANQAR@	Address of NQAR obtained for use by exit routines (copy of QwaNqar@)
1088	(440)	CHARACTER	4	*	Reserved (previously QwaICARet@)
1092	(444)	CHARACTER	16	*	Reserved (previously QwaICAReqToken)
1108	(454)	BITSTRING	2	QWAICAEXITS	Copy of exit flags (copy of QwaExits)
1110	(456)	CHARACTER	2	*	Reserved
1112	(458)	CHARACTER	8	QWALRNLC	Time stamp of last RNL change obtained from GvtLRnlC and subsequently checked later to see if an RNL change occurred across the window where locks were dropped in ISGGPC
1120	(460)	SIGNED	4	*	unused
1124	(464)	SIGNED	4	*	unused
1128	(468)	UNSIGNED	4	QWAGRSALET	set by ISGGPC and used by ISGGNX to reference the Qwa in the GRS address space after the CMSET to home.
1132	(46C)	SIGNED	4	QWAQXBRECDECCNT	used by ISGGPC recovery to decrement the Qxb list count
1136	(470)	CHARACTER	16	QWAREQTOKEN	Request token for exits
1152	(480)	UNSIGNED	4	QWAEXITTYPE	Exit to call, either Batch, Queued1, EndOfLcb, or EnqExit
1156	(484)	UNSIGNED	4	QWA#XITPELS	Number of non-step PELS for exit processing
1160	(488)	UNSIGNED	4	QWATOTALRNAMELENGTH	total number of bytes of storage needed to contain all of the non-step rnames
1164	(48C)	ADDRESS	4	QWARET@	Next sequential instruction following the ENQ/DEQ request
1168	(490)	UNSIGNED	4	QWASAEAX	Reg value used to restore uses EAX.
1172	(494)	ADDRESS	4	QWALNQDQADDDYNAREA@	Address of an additional work area that ISGLNQDQ uses when calling ISGSALC, ISGSDAL, and possibly others. See ISGLNQDQ for how it is used.
1176	(498)	CHARACTER	12	QWACMSXM	XMSAVE area for CMSET
1188	(4A4)	ADDRESS	4	QWAUCBP@	UCB prefix address - used for DEQ of a RESERVE
1192	(4A8)	UNSIGNED	4	QWARSNCD	Reason code for QWAERR
1196	(4AC)	UNSIGNED	4	QWADEQSAVEDLWCDECVALUE	Used by ISGGNQDQ to save the value to decrement the QXBLWC by
1200	(4B0)	CHARACTER	0	QWAEND3	END QWA

QWA Constants

Len	Type	Value	Name	Description
		Comment		
		Declare possible QWA eyecatcher values		
		End of Comment		
4	CHARACTER	LQWA	KLQWAID	Local QWA
4	CHARACTER	GQWA	KGQWAID	Global QWA
4	CHARACTER	PQWA	KPQWAID	Private QWA i.e. ISGGPC
4	CHARACTER	SQWA	KSQWAID	Local Lock serialized quick space QWA
4	DECIMAL	1	QWAPGFMS	QWAPGRFN FUNCTION IS MASID-SCAN
4	DECIMAL	2	QWAPGFEQ	QWAPGRFN FUNCTION IS ENQ
4	DECIMAL	3	QWAPGFDQ	QWAPGRFN FUNCTION IS DEQ
4	DECIMAL	1200	QWA_KLENGTH	
		Comment		
		The following QwaExitType function codes that are used to tell ISGGRX entry points why they are being called.		
		End of Comment		
4	DECIMAL	1	QWA_KBATCH	
4	DECIMAL	2	QWA_KQUEUED1	
4	DECIMAL	3	QWA_KENDOFLQCB	
4	DECIMAL	4	QWA_KENQEXIT	
4	DECIMAL	5	QWA_KPREBATCH	
4	DECIMAL	6	QWA_KCLEANUP	
4	DECIMAL	7	QWA_KISGENQPOST	Special ISGENQ ECB posting hook for ISGGNQDQ. Really nothing to do with exit processing
4	DECIMAL	8	QWA_KDELETE64CELLS	for gfrr0 to call to delete 64 but rname cells off sqa qwb
4	DECIMAL	9	QWA_KINCCONCOUNT	Increment concurrent request count (lnqdq usage)
4	DECIMAL	10	QWA_KFUZZYINCLST	Upfront check of concurrent request count for potentially many requests (gnqdq front-end usage)
4	DECIMAL	11	QWA_KFORCEDINC	Unconditionally increment concurrent request count (gnqdq back-end usage)
4	DECIMAL	12	QWA_KDECCONCOUNT	Decrement concurrent request count (common back-end usage)

QWA Cross Reference

Name	Hex Offset	Hex Value
QWA	0	
QWA#XITPELS	484	
QWAABDMC	1D	02
QWAABENDCD	AE	
QWAASCB	40	
QWAASID	3E	
QWAAUTH	1D	20
QWABADML	31	80
QWABASIC	4	
QWABUILTETOD	350	
QWACALLCLEANUP	AC	10
QWACALLGERTS	33	10
QWACCODE	250	
QWACCRV1	253	
QWACIQWB	78	
QWACLR2B	2E0	
QWACMS	30	80
QWACMSXM	498	
QWACNFY@	A4	
QWACOMPC	250	
QWACOQWB	74	
QWACPELR	54	
QWACSYID	32	08
QWACSYS	62	
QWADEQSAVEDLWCDECVALUE	4AC	
QWADMGE	31	40
QWADPL	90	
QWADSTAD	2C0	
QWAECBA	20	
QWAECBF	1D	08
QWAEND1	34	
QWAEND2	C0	
QWAEND3	4B0	
QWAEOL	1A	80
QWAERR	18	
QWAEXITS	AC	
QWAEXITSTATUS	AC	
QWAEXITSTATUSKNOWN	AC	80
QWAEXITTYPE	480	
QWAE1R13	23C	
QWAFDQS	19	40
QWAFFDM	19	80
QWAFLAG1	1C	
QWAFLAG2	1D	
QWAFLAG3	30	
QWAFLAG4	31	

Name	Hex Offset	Hex Value
QWAFLAG5	32	
QWAFLAG6	33	
QWAFLAG7	2B8	
QWAFLAG8	2B9	
QWAFLAG9	2F8	
QWAFMTVL	2C6	
QWAFQEL	70	
QWAFQWB	84	
QWAFREEC	50	
QWAFRR	30	40
QWAFRRPARMAREA@	300	
QWAGBLRS	32	01
QWAGENWORKAREA	36C	
QWAGEN1	1A	04
QWAGEN2	1A	02
QWAGLBL	1D	10
QWAGLBLQ	32	80
QWAGLBLR	48	
QWAGLOBALSQUEUED	33	08
QWAGPMAS	2BA	
QWAGRES	1E	
QWAGRP13	240	
QWAGRSALET	468	
QWAGRSLL	30	10
QWAGSA	8C	
QWAGVTAD	B0	
QWAHASH	80	
QWAHOLD	32	20
QWAICAEXITS	454	
QWAICANQAR@	43C	
QWAID	0	
QWAIGNOR	1A	40
QWAINGRS	1C	02
QWAINT	1C	08
QWAISGENQ	31	08
QWAISGLNQDQCOMMUNICATIONAREA	43C	
QWAJOBNM	34	
QWAJSTEP	24C	80
QWAKEY	8	
QWAKEYNB	8	F0
QWALISTREQ	19	02
QWALIVEEXITS	AD	
QWALNGWT	1C	04
QWALNQDQADDDYNAREA@	494	
QWALOCLR	44	
QWALRNLC	458	
QWALSTRQ	30	01
QWAMFGS	19	
QWAMIXR	1D	80
QWAMOD24	32	02
QWAMQLAD	2BC	
QWAMRBQ	14	
QWAMTDQ	1C	20
QWAMUSTCALLBATCHCND	AC	20
QWAMVCP	31	02
QWANCELL	0	
QWANEEDTOCALLQ1EXIT	AC	40
QWANMESZ	5C	
QWANOENQ	31	01
QWANPECB	2E4	
QWANPEND	2F8	
QWANPMAS	2E8	
QWANPMTC	2EC	
QWANPTCB	2E0	
QWANQAR@	A8	
QWANSLOT	7C	
QWANWPEL	2E0	
QWANXBX	AD	40
QWANXCB	AD	02
QWANXFQ	AD	10
QWANXLQD	AD	08
QWANXNQ	AD	80
QWANXPB	AD	04
QWANXQ1X	AD	20
QWAORIGN	3C	
QWAPCENQ	19	01
QWAPEL@	BC	
QWAPELA	4	
QWAPFLAG	1B	
QWAPFLGS	1A	
QWAPGRFN	2C4	
QWAPGROA	2B8	
QWAPHLDR	32	04
QWAPLAST	1A	
QWAPLISTS	B4	
QWAPPELE	88	
QWAPRMSZ	58	
QWAPRNEEDED	33	01
QWAPSTAD	2D8	
QWAPSTCT	2CC	
QWAPT1	C	
QWAPT2	10	
QWAPURG	1C	01
QWAQEL@	B8	
QWAQSQHTENT	304	
QWAQWB@	B4	
QWAQWBA	20	
QWAQWBF	68	
QWAQWBHS	64	
QWAQWBL	6C	
QWAQWBS	4C	
QWAQWBSZ	60	
QWAQXB	2C	
QWAQXBO	32	10
QWAQXBOG	33	02
QWAQXBRECDECCNT	46C	
QWARB	254	
QWARDA	34	
QWAREQLL	30	20
QWAREQTOKEN	470	
QWARES1	1A	20
QWARET@	48C	
QWARETRY	9	
QWARET1	1B	04
QWARET2	1B	02
QWARET3	1B	01
QWARMC	1C	40
QWARMFLG	24C	
QWARMFP	32	40
QWARMRV1	24C	01
QWARMRV2	24C	02
QWARMRV3	24C	04
QWARMRV4	24C	08
QWARMRV5	24C	10
QWARMRV6	24C	20
QWARMRV7	24C	40
QWARMR01	24D	
QWARNLSCHANGED	33	04
QWARQDMG	30	04
QWARSA	14	
QWARSA2	34	
QWARSNCD	4A8	
QWARSVC4	2F0	
QWARSVD3	A	
QWARSVD5	2C5	
QWARSVD6	2F9	
QWARSVSP	B4	
QWARUBTM	258	
QWAR15SW	33	80

QWA Cross Reference

Name	Hex Offset	Hex Value
QWASAEAX	490	
QWASAVE	1A	08
QWASAVEDRSVCODES	36C	
QWASAVEDRSVRETCODE	36C	
QWASAVEDRSVRETCODEEND	374	
QWASAVEDRSVRSNCODE	370	
QWASAVE1	C4	
QWASAVE2	10C	
QWASAVE2_3	10C	
QWASAVE3	154	
QWASAVE4	19C	
QWASAVE5	1E4	
QWASCPE1	1B	40
QWASCPE2	1B	08
QWASEHAD	2D4	
QWASEHCT	2C8	
QWASERAD	2DC	
QWASERCT	2D0	
QWASHARE	1B	80
QWASHR	1A	10
QWASMC	1C	40
QWASPOST	1C	10
QWASTLC	1C	80
QWASTPMC	1B	10
QWASTPNM	244	
QWASVC56	1D	04
QWASVRBA	28	
QWASYNCC	1D	01
QWASYSID	3C	
QWASYSMC	1B	20
QWAS1R14	22C	
QWAS2R14	230	
QWAS3R14	234	
QWAS4R14	238	
QWATCBA	24	
QWATCBATTKNCNT	368	
QWATCBATTKNSTKN	360	
QWATCBATTOKNINFO	360	
QWATCBF	1A	01
QWATCBFA	1D	40
QWATOTALRNAMELENGTH	488	
QWATRMRM	244	
QWAUCBP@	4A4	
QWAWAIT	31	04
QWAWAITN	31	20
QWAWORK1	C0	
QWA1DEQ	31	10
QWA3ERSQ	30	08
QWA6ECBZ	33	40
QWA6GERT	33	20
QWA7ABMR	2B8	01
QWA7AURC	2B8	40
QWA7CHGA	2B8	20
QWA7COEX	2B8	02
QWA7HOLD	2B8	10
QWA7OWNR	2B8	80
QWA7POST	2B8	08
QWA7RLSE	2B8	04
QWA8CNCH	2B9	10
QWA8CNEN	2B9	08
QWA8CNST	2B9	20
QWA8DCVT	2B9	40
QWA8EXSH	2B9	80
QWA8RSV1	2B9	01
QWA8RSV2	2B9	02
QWA8RSV3	2B9	04
QWA9CNPP	2F8	80
QWA9DSTL	2F8	40
QWA9RSV1	2F8	01
QWA9RSV2	2F8	02
QWA9RSV3	2F8	04
QWA9RSV4	2F8	08
QWA9RSV5	2F8	10
QWA9RSV6	2F8	20

RAB Information

RAB Heading Information

Common Name: RSM ADDRESS SPACE BLOCK
Macro ID: IARRAB
DSECT Name: RAB
Owning Component: REAL STORAGE MANAGER (SC1CR)
Eye-Catcher ID: RAB
Offset: 8
Length: 3 bytes
Storage Attributes: Virtual Storage: YES
Subpool: 245, EXTENDED SQA (FIXED COMMON) or Nucleus
Key: 0
Data Space: NO
Residency: MUST be above 16 Megabytes virtual
Size: 280 bytes (without the RAX)
Created by: IAXMA
Pointed to by: RABFQPTR FIELD OF THE RAB DATA AREA
RABBQPTR FIELD OF THE RAB DATA AREA
RITCRAB FIELD OF THE RIT DATA AREA
RITRABQF FIELD OF THE RIT DATA AREA
RITRABQL FIELD OF THE RIT DATA AREA
RITRRAB FIELD OF THE RIT DATA AREA
RITRCUR FIELD OF THE RIT DATA AREA
ASCBRSMA FIELD OF THE ASCB DATA AREA
Serialization: FIELD DEPENDENT
Function: CONTAINS RSM ADDRESS SPACE RELATED INFORMATION

RAB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	944	RAB	
0	(0)	ADDRESS	4	RABFQPTR	FORWARD RAB QUEUE POINTER
4	(4)	ADDRESS	4	RABBQPTR	BACKWARD RAB QUEUE POINTER
8	(8)	CHARACTER	4	RABID	RAB CONTROL BLOCK IDENTIFIER
8	(8)	CHARACTER	3	RABIDRAB	EBCDIC CHARACTERS R A B
11	(B)	CHARACTER	1	RABIDTYP	EBCDIC CHARACTER TO IDENTIFY THE TYPE OF ADDRESS SPACE. C FOR COMMON AREA RAB, R FOR RASP RAB, BLANK FOR ALL OTHERS
12	(C)	BITSTRING	1	RABFLGS1	FLAG BYTE 1
		1...		RABNOTRS	TRAS TO THIS ADDRESS SPACE SHOULD NOT BE DONE
		.1..		RABASIT	ADDR SPACE CREATE IN PROGRESS
		..1.		RABSWPR	SWAP (OUT OR IN) IN PROGRESS
		...1		RABSWOUT	ADDRESS SPACE IS SWAPPED OUT
		.... 1...		RABNLSQA	LSQA IS NOT ACCESSIBLE
		.... .1..		RABNPGT	SWAP-IN OF A PGT/XPT FAILED
		.... ..1.		RABBADPT	ADDRESS SPACE CONTAINS A PGT/XPT IN A BAD FRAME
		1		RABFLAW	THIS RAB WAS FOUND FLAWED DURING RECOVERY PROCESSING AND WAS DEQUEUED FROM THE RAB QUEUE
13	(D)	BITSTRING	1	RABFLGS2	FLAG BYTE 2
		1...		RAB2LPU	SECOND LEVEL PREFERRED USER
		.1..		RAB1LPU	FIRST LEVEL PREFERRED USER
		..1.		RABPAGDS	LSQA SWAPPED TO PAGING DATA SET
		...1		RABREQSW	A SWAP HAS BEEN REQUESTED FOR THIS ADDRESS SPACE
		.... 1...		RABVRPTD	PAGE TABLES HAVE BEEN DEALLOCATED FOR THE LAST V=R JOB REQUEST IN THIS ADDRESS SPACE
		.... .1..		RABLSQAO	AN LSQA PAGE HAS BEEN PAGED OUT
		.... ..1.		RABTRMIP	ADDRESS TERMINATION IN PROGRESS
		1		RABRVRPL	RVR POOL HAS BEEN BUILT
14	(E)	BITSTRING	1	RABFLGS3	FLAG BYTE 3
		1...		RABBADVP	THE DATA IN ONE OR MORE VDAC PAGES MAY NOT BE AT THE CORRECT LEVEL (SEE RABVFLGA AND RABVFLGB).
		.1..		RABSWEST	ADDRESS SPACE SWAPPED TO EXTENDED STORAGE (ESA only, NOt used for ESAME)
		..1.		RABDRIM	MIGRATION OF DREF PAGES IS INHIBITED
		...1		RABRMPND	RECONFIGURATION MIGRATION IS PENDING FOR THIS ADDRESS SPACE
		.... 1...		RABLSWAP	THIS ADDRESS SPACE IS LOGICALLY SWAPPED OUT
		.... .1..		RABSSIPF	SELF-STEAL IN PROGRESS BIT
		.... ..1.		RABSWIOC	1, ALL PAGING I/O HAS COMPLETED SO THAT THE SWAPOUT OF THE WORKING SET MAY BEGIN.
		1		RABUDDCV	UDD COUNT VERIFICATION SRB TO BE SCHEDULED FOR THIS ADDRESS SPACE

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
15	(F)	BITSTRING	1	RABFLGS4	FLAG BYTE 4
		1...		RABVIOPL	VIO LPID POOL HAS BEEN BUILD
		.1..		RABSWASM	SWAP HAS CALLED ASM
		..1.		RABHVQCV	HVFQ COUNT VERIFICATION SRB TO BE SCHEDULED FOR THIS ADDRESS SPACE
		...1		RABSWEXC	In-real swap frame exchange (IARSASRB) is active
		.... 1...		RABREALSW	In-real swap is active for this address space
		.... .1..		RABSENDPAGETOAUX	For steal/pageout processing send the page to aux instead of making the page into a steal candidate - used by test
		11		*	RESERVED
16	(10)	SIGNED	2	RABC0DCT	REMAINING NUMBER OF TIMES THAT A C0D ERROR OF A TYPE FLAGGED IN RABC0DFL WILL BE LOGGED
18	(12)	BITSTRING	1	RABC0DFL	ERROR FLAGS
		1...		RABSTPIN	STORAGE PIN OCCURRED
		.1..		RABLSCON	LONG/SHORT FIX CONFLICT ERROR OCCURRED
		..1.		RABECBNP	AN ECB COULD NOT BE POSTED AS EXPECTED
		...1		RABFOENF	FOE NOT FOUND FOR A NON-ZERO TCB PAGE-FREE
		.... 1...		RABASPIN	ADDRESS SPACE HAS BEEN PINNED
		.... .1..		RABSTLPE	STEAL DISCOVERED A FRAME WITH INCONSISTENT INFORMATION
		11		*	RESERVED
19	(13)	BITSTRING	1	RABVFLGA	VDAC ERROR FLAGS- FLAGS INDICATE WHY RABBADVP IS ON.
		1...		RABVTTD	BADVP ERROR IN DISASSOC. DURING NON-XMEM TCB TERMINATTION.
		.1..		RABVTTXD	BADVP ERROR IN DISASSOC. DURING XMEM TCB TERMINATTION.
		..1.		RABVATD	BADVP ERROR IN DISASSOC. DURING ADDR SPACE TERMINATTION.
		...1		RABVDXD	BADVP ERROR IN DISASSOC. DURING DEFER EXIT PROCESSING.
		.... 1...		RABVTTC	BADVP ERROR IN COMMIT DURING NON-XMEM TCB TERMINATTION.
		.... .1..		RABVTTXC	BADVP ERROR IN COMMIT DURING XMEM TCB TERMINATTION.
		.... ..1.		RABVATC	BADVP ERROR IN COMMIT DURING ADDR SPACE TERMINATTION.
		1		RABVDXC	BADVP ERROR IN COMMIT DURING DEFER EXIT PROCESSING.
20	(14)	BITSTRING	1	RABVFLGB	VDAC ERROR FLAGS- FLAGS INDICATE WHY RABBADVP IS ON.
		1...		RABVPXD	BADVP ERROR IN PRIMING DURING DEFER EXIT PROCESSING
		.111 1111		*	RESERVED
21	(15)	CHARACTER	1	*	RESERVED
22	(16)	BITSTRING	2	RABASID	ADDRESS SPACE ID
24	(18)	ADDRESS	4	RABASCB	ADDRESS OF ASCB
28	(1C)	UNSIGNED	4	RABLOCK	LOCK WORD
32	(20)	ADDRESS	4	RABSGT	ADDRESS OF SGT
36	(24)	ADDRESS	4	RABLPPGT	ADDRESS OF 1ST LOW STORAGE PGT
40	(28)	ADDRESS	4	RABXPPGT	APPARENT ORIGIN OF FIRST EXTENDED PRIVATE AREA PGT
44	(2C)	ADDRESS	4	RABSWFXC	NUMBER OF FIXED PAGES BELOW 16 MEG TO BE SWAPPED-IN FOR THIS ADDRESS SPACE
48	(30)	ADDRESS	4	RABSWFCB	ADDRESS OF SWAP FCB
52	(34)	ADDRESS	4	RABSFT	ADDRESS OF SFT HEADER
56	(38)	SIGNED	4	RABSFTSZ	SIZE OF SFT IN BYTES
60	(3C)	ADDRESS	4	RABSFTEL	ADDRESS OF LAST SFT ENTRY USED
64	(40)	ADDRESS	4	RABSLT	ADDRESS OF FIRST POSSIBLE SLT
68	(44)	ADDRESS	4	RABSLTQ	ADDRESS OF 1ST AVAIL SLT
72	(48)	SIGNED	4	RABASLTC	COUNT OF AVAILABLE SLTS
76	(4C)	CHARACTER	4	RABFLAGSABN	Set of flags to use when RSMAD level serialization is NOT held.
76	(4C)	BITSTRING	1	RABFLAGSABN1	
		1...		RABSCMEVAC	This address space (or one of its data spaces) is using SCM that is being configured offline
		.1..		RABSCMEVACSRBSCHEDULED	IARX1SRB was scheduled to this address space and may be running
		..1.		RABSCMEVACNOTCOMPLETE	IARX1SRB was either unable to be scheduled to this address space or interrupted w/ PURGEDQ
80	(50)	ADDRESS	8	RABFCUR	PFQ PREF STEAL CURSOR OR ZERO
88	(58)	CHARACTER	4	*	RESERVED
92	(5C)	ADDRESS	4	RABMCOQF	POINTER TO FIRST MOMB ON THE QUEUE OF COMMON MOMBs WITH OWNER = THIS ASID
96	(60)	ADDRESS	4	RABMCOQL	POINTER TO LAST MOMB ON THE QUEUE OF COMMON MOMBs WITH OWNER = THIS ASID
100	(64)	ADDRESS	4	RABLDPQF	POINTER TO FIRST PCB ON THE LOCAL DEFERRED PCB QUEUE
104	(68)	ADDRESS	4	RABLDPQL	POINTER TO LAST PCB ON THE LOCAL DEFERRED PCB QUEUE
108	(6C)	ADDRESS	4	RABNPQF	POINTER TO FIRST PCB ON THE NOTIFICATION PCB QUEUE. NPQ contains X-Mem, Common, Shared, and Non-XMem high virtual i/o requests
112	(70)	ADDRESS	4	RABNPQL	POINTER TO LAST PCB ON THE NOTIFICATION PCB QUEUE. NPQ contains X-Mem, Common, Shared, and Non-XMem high virtual i/o requests
116	(74)	ADDRESS	4	RABRPH	POINTER TO RPH (RPI HEADER)@L9A
120	(78)	ADDRESS	4	*	RESERVED
124	(7C)	ADDRESS	4	RABFCQF	POINTER TO FIRST FCB ON THE FIX CONTROL QUEUE
128	(80)	ADDRESS	4	RABFCQL	POINTER TO LAST FCB ON THE FIX CONTROL QUEUE

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
132	(84)	ADDRESS	4	RABFFOEQ	POINTER TO FIRST FOE ON THE FREE FOE QUEUE
136	(88)	ADDRESS	4	RABCFEQ	POINTER TO FIRST CFE ON ADDRESS SPACE'S COMMON FOE QUEUE
140	(8C)	ADDRESS	4	RABSCMEVACIOFCB@	Address of the Config SCM offline I/O function area associated w/ this address space
144	(90)	ADDRESS	4	*	RESERVED
148	(94)	ADDRESS	4	RABTOKEN	TOKEN REPRESENTING THE INSTANCE OF MIGRATION FOR WHICH A MIGSWAP SYSEVENT (NON-PURGE) WAS ISSUED FOR THIS ADDRESS SPACE.
152	(98)	ADDRESS	8	RABSTATICFRAMEQHEADERAREAADDR	The address of the address space frame queue header area in the PFT CADS
160	(A0)	CHARACTER	4	RABRVRID	RVR POOL ID
164	(A4)	ADDRESS	4	RABRVRQF	POINTER TO FIRST RVR ON THE RVR QUEUE
168	(A8)	ADDRESS	4	RABRVRQL	POINTER TO LAST RVR ON THE RVR QUEUE
172	(AC)	ADDRESS	4	RABRVRLO	ADDRESS OF THAT RVR WHICH HAS THE LOWEST VIRTUAL ADDR OF ALL RVRS USED. (FOR DUMPING)
176	(B0)	ADDRESS	4	RABRVRHI	ADDRESS OF THAT RVR WHICH HAS THE HIGHEST VIRTUAL ADDR OF ALL RVRS USED. (FOR DUMPING)
180	(B4)	ADDRESS	4	RABDCQF	POINTER TO FIRST FCB ON THE DISASSOCIATE CONTROL QUEUE
184	(B8)	ADDRESS	4	RABDCQL	POINTER TO LAST FCB ON THE DISASSOCIATE CONTROL QUEUE
188	(BC)	ADDRESS	4	RABCCQF	POINTER TO FIRST FCB ON THE COMMIT CONTROL QUEUE
192	(C0)	ADDRESS	4	RABCCQL	POINTER TO LAST FCB ON THE COMMIT CONTROL QUEUE
196	(C4)	ADDRESS	4	RABNCQF	POINTER TO FIRST FCB ON THE NOTIFICATION CONTROL QUEUE
200	(C8)	ADDRESS	4	RABNCQL	POINTER TO LAST FCB ON THE NOTIFICATION CONTROL QUEUE
204	(CC)	ADDRESS	4	RABCPQF	POINTER TO FIRST PCB ON THE COMMIT PCB QUEUE
208	(D0)	ADDRESS	4	RABCPQL	POINTER TO LAST PCB ON THE COMMIT PCB QUEUE
212	(D4)	ADDRESS	4	RABPCQF	POINTER TO FIRST FCB ON THE PRIMING CONTROL QUEUE
216	(D8)	ADDRESS	4	RABPCQL	POINTER TO LAST FCB ON THE PRIMING CONTROL QUEUE
220	(DC)	ADDRESS	4	RABDAB	ADDRESS OF THE DAB (OR ZERO IF NO OWNED DATA SPACES)
224	(E0)	UNSIGNED	4	RABDBLDF	DEFAULT VALUE FOR THE NUMBER OF BLOCKS ON DSPSERV CREATE FOR THIS ADDRESS SPACE.
228	(E4)	UNSIGNED	4	RABDMXEX	MAXIMUM NUMBER OF USER KEY DATA SPACES THAT MAY EXIST AT ONE TIME FOR THIS ADDRESS SPACE
232	(E8)	UNSIGNED	4	RABDMXSZ	MAXIMUM NUMBER OF MEGABYTES OF USER KEY DATA SPACE ALLOWED FOR THIS ADDRESS SPACE
236	(EC)	ADDRESS	4	RABSAEQF	POINTER TO FIRST SAE ON THE STACK ANCHOR ELEMENT QUEUE
240	(F0)	ADDRESS	4	RABSAEQL	POINTER TO LAST SAE ON THE STACK ANCHOR ELEMENT QUEUE
244	(F4)	SIGNED	4	RABBFXSV	VALUE OF RAXBELFX WHEN LOGICAL SWAP IS INITIALTED
248	(F8)	ADDRESS	4	*	RESERVED - USED TO BE RABFCUR
252	(FC)	SIGNED	4	RABPINCT	COUNT OF RSMPIN LEVEL 1'S IN PROGRESS. IF GREATER THEN ZERO AT LOGICAL SWAP TIME, THE ADDR SPACE WILL BE MEMTERMED. UPDATED VIA COMPARE AND SWAP IN THE LEVEL 1 RSMPIN SERVICE.
256	(100)	CHARACTER	8	RABSTKN	STOKEN OF ADDRESS SPACE
256	(100)	ADDRESS	4	RABSTKN2	
260	(104)	ADDRESS	4	RABSTKN1	
264	(108)	ADDRESS	4	RABSRTH	ADDRESS OF THE SUBSPACE RANGE TABLE HEADER
268	(10C)	ADDRESS	4	RABSPEQF	POINTER TO FIRST SPE ON THE SHARED PAGE ELEMENT QUEUE
272	(110)	ADDRESS	4	RABSPEQL	POINTER TO LAST SPE ON THE SHARED PAGE ELEMENT QUEUE
276	(114)	SIGNED	4	RABMUSV	Maximum number of unauthorized shared views this address space can create
280	(118)	SIGNED	4	RABTUSV	Total number of unauthorized shared views this address space has in existence
284	(11C)	CHARACTER	4	RABVIOID	VIO LPID POOL ID (ESAME only)
288	(120)	ADDRESS	4	RABVIOQF	POINTER TO FIRST VIO LPID BLOCK VIO LPID QUEUE (ESAME only)
292	(124)	ADDRESS	4	RABVIOQL	POINTER TO LAST VIO LPID BLOCK VIO LPID QUEUE (ESAME only)
296	(128)	ADDRESS	4	RABVIOLO	ADDRESS OF THAT VIO LPID BLOCK WHICH HAS THE LOWEST VIRTUAL ADDR OF ALL VIO LPID BLOCKS USED.(FOR DUMPING) (ESAME only)
300	(12C)	ADDRESS	4	RABVIOHI	ADDRESS OF THAT VIO LPID BLOCK WHICH HAS THE HIGHEST VIRTUAL ADDR OF ALL VIO LPID BLOCKS USED.(FOR DUMPING) (ESAME only)
304	(130)	ADDRESS	4	*	
308	(134)	ADDRESS	4	RABLVAB	pointer to large virtual anchor block
312	(138)	CHARACTER	4	*	Reserved
316	(13C)	ADDRESS	8	RABTOPPFTE	Top Region Table PFTE, this is zero when no region tables exist for the AS.
324	(144)	CHARACTER	4	*	Reserved
328	(148)	CHARACTER	8	RABTOPVSA	Top VSA mapped by Top Region for this address space, this is zero when no region tables exist for the AS, or Top VSA for shared area for CRAB
336	(150)	UNSIGNED	8	RABV64PRIVMOTKN	
344	(158)	CHARACTER	20	*	Reserved
364	(16C)	ADDRESS	4	RABSIBQF	Address of first SIB address on the SIB queue
368	(170)	ADDRESS	4	RABSIBQL	Address of last SIB address on the SIB queue
372	(174)	CHARACTER	4	*	Reserved
376	(178)	CHARACTER	0	RABRAXD	RAX AREA

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
		RAX - RSM ADDRESS SPACE BLOCK EXTENSION			
					End of Comment
376	(178)	CHARACTER	4	RAXID	RAX CONTROL BLOCK ID
380	(17C)	CHARACTER	4	RAXCSWRD	RAX COMPARE AND SWAP WORD
380	(17C)	BITSTRING	1	RAXFLGS1	FLAG BYTE 1
		1...		RAXESSW	SWAP THIS ADDRESS SPACE TO EXTENDED STORE - SET BY SRM (ESA MODE ONLY. NOT USED FOR ESAME)
		.1..		RAXBLPEA	BLOCK PAGING FROM EXPANDED STORAGE IS ACTIVE. (ESA MODE ONLY. NOT USED FOR ESAME)
		..1.		RAXSSCRE	SRM WANTS RSM SWAP OUT TO CALL IARSSCRE
		...1		RAXSORFL	SWAP OUT FAILED DUE TO LACK OF RESOURCES
		.... 1...		RAXDAVQL	SRM WANTS RSM TO DEFER 1ST REFERENCE PAGE FAULTS IN THIS SPACE DURING AVQLOW
		.... .1..		RAXLARGEFRAMEAUTH	RACF authorized for Large pages
381	(17D)	UNSIGNED	1	RAXTRIMSTATUS	Trim status indicator
382	(17E)	CHARACTER	2	RAXRSV1	RESERVED
384	(180)	SIGNED	4	RAXESCT	NUMBER OF PAGES ON EXTENDED STORAGE. THIS FIELD APPLIES TO THE COMMON RAX. (ESA MODE ONLY, NOT USED FOR ESAME MODE)
388	(184)	SIGNED	4	RAXQUOT	STORAGE ISOLATION QUOTA OF MAIN STORAGE FRAMES AND EXTENDED STORAGE E-FRAMES. THIS FIELD APPLIES TO THE COMMON RAX. FOR ESA MODE, IT ALSO INCLUDES EXTENDED STORAGE E-FRAMES.
392	(188)	SIGNED	4	RAXSWSM	NUNBER OF MIGRATED SECONDARY WORKING SET PAGES.
396	(18C)	SIGNED	4	RAXDRM	NUMBER OF DREF PAGES THAT HAVE BEEN MIGRATED OR ARE IN THE PROCESS OF BEING MIGRATED
400	(190)	SIGNED	4	RAXDRMIP	NUMBER OF DREF PAGES WITH MIGRATION IN PROGRESS
404	(194)	UNSIGNED	4	RAXUKDSS	NUMBER OF BLOCKS (4K BYTES) OF USER KEY DATA SPACE IN EXISTENCE FOR THIS ADDRESS SPACE.
408	(198)	UNSIGNED	4	RAXDSHWM	HIGH WATER MARK (IN MEGABYTES) OF USER KEY DATA SPACE CREATED FOR THIS ADDRESS SPACE. THIS FIELD IS PROVIDED FOR SMF AND MAY BE RESET ONLY BY THE SMF COMPONENT.
412	(19C)	SIGNED	4	RAXDBFRM	NUMBER OF DOUBLE FRAME PAIRS CURRENTLY IN USE BY THIS ADDRESS SPACE
416	(1A0)	SIGNED	4	RAXVIOCT	NUMBER OF VIO DATA SET PAGES ON EXPANDED STORAGE. (ESA MODE ONLY, NOT USED FOR ESAME MODE)
420	(1A4)	SIGNED	4	RAXFMCT	NUMBER OF FRAMES CURRENTLY IN USE BY THIS ADDRESS SPACE. It does not include 2G frames. THIS FIELD APPLIES TO THE COMMON RAX.
424	(1A8)	UNSIGNED	2	RAXHWRDA	RESERVED L8C
426	(1AA)	UNSIGNED	2	RAXFXSTL	NUMBER OF FIXED PAGES IN THIS LOGICALLY SWAPPED ADDR SPACE THAT WERE BACKED BY REAL BELOW 16 MEGABYTES (FOR ESA MODE THE PAGES ARE CURRENTLY STOLEN TO EXPANDED)
428	(1AC)	SIGNED	4	RAXHSPCT	NUMBER OF HIPERSPACE PAGES CURRENTLY ON EXPANDED STORAGE FOR THIS ADDRESS SPACE (THIS COUNT IS ALSO INCLUDED IN RAXESCT) (ESA MODE ONLY, NOT USED FOR ESAME MODE)
432	(1B0)	SIGNED	4	RAXCSTAR	WORKING SET MANAGEMENT CENTRAL STORAGE TARGET NUMBER OF FRAMES
436	(1B4)	CHARACTER	16	RAXFBV	STRUCTURE NAME
436	(1B4)	SIGNED	4	RAXFBV1	NUMBER OF FRAMES IN UIC INTERVAL 1 AS SET BY SRM VIA THE RCEFRV FIELDS.
440	(1B8)	SIGNED	4	RAXFBV2	NUMBER OF FRAMES IN UIC INTERVAL 2 AS SET BY SRM VIA THE RCEFRV FIELDS.
444	(1BC)	SIGNED	4	RAXFBV3	NUMBER OF FRAMES IN UIC INTERVAL 3 AS SET BY SRM VIA THE RCEFRV FIELDS.
448	(1C0)	SIGNED	4	RAXFBV4	NUMBER OF FRAMES IN UIC INTERVAL 4 AS SET BY SRM VIA THE RCEFRV FIELDS.
452	(1C4)	SIGNED	4	RAXOVBLK	NUMBER OF IMPLICITLY OVER- BLOCKED FRAMES
456	(1C8)	SIGNED	4	RAXBELFX	NUMBER PAGES IN THIS ADDRESS SPACE EXPLICITLY FIXED AND CURRENTLY BACKED WITH REAL BELOW 16 MEGABYTES
460	(1CC)	SIGNED	4	RAXSWSS	COUNT OF SECONDARY WORKING PAGES
464	(1D0)	SIGNED	4	RAXTOTSV	TOTAL NUMBER OF SHARED PAGE VIEWS IN THIS ADDRESS SPACE
468	(1D4)	SIGNED	4	RAXSVINR	TOTAL NUMBER OF SHARED PAGES IN CENTRAL STORAGE THAT ARE VALID IN THIS ADDRESS SPACE
472	(1D8)	UNSIGNED	4	RAXSPVLC	CONSTANTLY INCREASING COUNT OF SHARED PAGE VALIDATIONS IN THIS ADDRESS SPACE
476	(1DC)	SIGNED	4	RAXSPSNG	NUMBER OF SHARED PAGE SINGLETONS IN THIS ADDRESS SPACE
480	(1E0)	SIGNED	4	RAXTOTFX	TOTAL NUMBER OF FIXED PAGES IN THIS ADDRESS SPACE (DOES NOT INCLUDE SHARED PAGES OR 2G PAGES)
484	(1E4)	SIGNED	4	RAXHRECT	NUMBER OF HIPERSPACE PAGES ON REAL (ESAME ONLY)

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
488	(1E8)	SIGNED	4	RAXVIORC	NUMBER OF VIO DATASET PAGES IN THE VIO REAL CACHE (ESAME ONLY)
492	(1EC)	SIGNED	4	RAXSPGPI	TOTAL NUMBER OF SHARED PAGES BROUGHT IN FROM AUXILIARY STORAGE BY THIS ADDRESS SPACE
496	(1F0)	SIGNED	4	RAXCSTNO	NUMBER OF FRAMES USED TO BACK CASTOUT=NO CACHE HIPERSPACES IN THIS ADDRESS SPACE (ESAME ONLY)
500	(1F4)	SIGNED	4	RAXABVFX	NUMBER OF PAGES IN THIS ADDRESS SPACE FIXED AND CURRENTLY BACKED WITH REAL STORAGE BETWEEN 16M AND 2G
504	(1F8)	SIGNED	4	RAXLSQA	NUMBER OF FIXED LSQA PAGES BACKED IN REAL STORAGE
508	(1FC)	SIGNED	4	RAXDREFR	NUMBER OF LSQA DREF PAGES AND DATA SPACE DREF PAGES IN REAL STORAGE
512	(200)	SIGNED	4	RAXBFQFX	NUMBER OF PAGES ON A FIXED QUEUE AND BACKED BELOW 16M IN REAL
516	(204)	SIGNED	4	RAXQDFRM	NUMBER OF QUAD GROUPS CURRENTLY IN USE BY THE ADDRESS SPACE (ESAME ONLY)
520	(208)	SIGNED	4	RAXAGED	Number of frames that have been aged
524	(20C)	SIGNED	4	RAXQDFRMSAVED	Value of RAXQDFRM saved during swap
528	(210)	CHARACTER	40	RAXV64B	Counts for 64Bit Virtual Support
528	(210)	CHARACTER	8	RAXLVMEMLIM	Address Space Memory limit in MB
536	(218)	CHARACTER	8	RAXLVABYTES	Number of bytes allocated from large virtual memory in memory objects
544	(220)	CHARACTER	8	RAXLVHBYTES	number of bytes hidden within large virtual memory objects
552	(228)	CHARACTER	8	RAXLVGBYTES	high water mark for number of usable bytes within large virtual memory objects
560	(230)	UNSIGNED	1	RAXLVMEMLIMS	Source of Address Space Memory limit
561	(231)	UNSIGNED	3	*	Reserved for future use
564	(234)	UNSIGNED	4	RAXLVNMOMB	number of memory objects allocated
568	(238)	CHARACTER	8	RAXFFSRBTS	Time stamp when FF-SRB was last rescheduled
576	(240)	CHARACTER	32	RAXV64C	Counts for 64Bit Virtual Support
576	(240)	CHARACTER	8	RAXLVSHRBYTES	Number of shared bytes from large virtual memory in memory objects
584	(248)	CHARACTER	8	RAXLVSHRGBYTES	high water mark for number of shared bytes within large virtual memory objects
592	(250)	CHARACTER	8	RAXLVSHRNMOMB	number of shared memory objects allocated
600	(258)	CHARACTER	8	RAXHVSHRPAGEVALIDATIONS	number of page validations for high virtual shared
600	(258)	CHARACTER	4	*	
604	(25C)	SIGNED	4	RAXHVSHRPAGEVALIDATIONS31	number of page validations for high virtual shared
608	(260)	SIGNED	4	RAXHVDATFMCT	Number of frames used for high virtual DAT structure (common RAB only).
612	(264)	CHARACTER	4	RAXCSWRD2	RAX COMPARE AND SWAP WORD
612	(264)	BITSTRING	1	RAXSWAPFLAGS	
		1...		RAXREALSWAPCANCEL	RESERVED
		.1..		RAXPAGEABLESHORTAGEPHASE1	
		..11 1111		*	
613	(265)	BITSTRING	1	RAXSWAPREASON	RESERVED
		1...		RAXREALSWAPINTER	
		.1..		RAXREALSWAPDDP	
		..1.		RAXREALSWAPPREF	
		...1		RAXREALSWAP16MSHRT	
		.... 1...		RAXREALSWAP2GSHRT	
		.... .111		*	
614	(266)	CHARACTER	2	*	
616	(268)	SIGNED	4	RAXFXABVSTL	NUMBER OF FIXED PAGES IN THIS LOGICALLY SWAPPED ADDR SPACE THAT WERE BACKED BY REAL BETWEEN 16M and 2G
620	(26C)	SIGNED	4	RAXFXTOTSTL	NUMBER OF FIXED PAGES IN THIS LOGICALLY SWAPPED ADDR SPACE THAT WERE BACKED BY REAL BELOW 16M BETWEEN 16M and 2G

Comment

4 RAXRSV3 CHAR(16), Add back some reserved space when shipping Large Page support

End of Comment

624	(270)	UNSIGNED	8	RAXLARGEMEMORYOBJECTS	Number of Large Memory Objects allocated by this address space
624	(270)	CHARACTER	4	*	
628	(274)	SIGNED	4	RAXLARGEMEMORYOBJECTS31	
632	(278)	UNSIGNED	8	RAXLARGEPAGESBACKEDINREAL	Number of Large Pages (1MB pages) backed in real storage owned by this address space
632	(278)	CHARACTER	4	*	
636	(27C)	SIGNED	4	RAXLARGEPAGESBACKEDINREAL31	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
640	(280)	UNSIGNED	8	RAXHVCOMMONBYTES	Amount of 64-Bit Common allocated with this ASID as the owner.
648	(288)	UNSIGNED	8	RAXHVCOMMONHWMBYTES	High Water mark for the amount of 64-bit common bytes allocated with this ASID as the owner
656	(290)	UNSIGNED	8	RAXHVCOMMONNMOMB	Number of 64-bit memory objects allocated with this ASID as the owner
664	(298)	UNSIGNED	8	RAXHVPAGESINREAL	Number of real storage frames used to back 64-bit private storage. It does not include 2G frames.@0BC
672	(2A0)	UNSIGNED	8	RAXHVAUXSLOTS	Number of aux slots used to back 64-bit private storage. Includes only dasd storage
680	(2A8)	UNSIGNED	8	RAXHVGPAGESINREAL	High water mark for the number of real storage frames used to back 64-bit private storage
688	(2B0)	UNSIGNED	8	RAXHVGAUXSLOTS	High water mark
688	(2B0)	UNSIGNED	8	RAXHVGAUX	High water mark for the number of aux slots and SCM blockids used to back 64-bit private storage.
696	(2B8)	CHARACTER	8	RAXPPTNAME	Program name associated with this address space
704	(2C0)	BITSTRING	1	RAXFLGS2	Flag Byte 2
		1...		RAXCRITICALPAGING	Pages are not allowed to be stolen from this address space (set by IEFPPT or IEFSD101)
		.1..		RAXCRITICALPAGESSTOLEN	Pages were stolen from this address space while it was marked critical
		..1.		RAXASOWNSNONCRITICALDS	At least one non-critical dataspace has been created by the address space
		...1		RAXNONCRITICALDSSTOLEN	Pages were stolen from a non-critical dataspace owned by a critical address space
		 1111		*	
705	(2C1)	BITSTRING	1	RAXCRITICALBITS	
		1...		RAX_IAXUO_HIGHSTOLEN	Bit indicating that critical pages stolen in IAXUO high steal processing
		.1..		RAX_IAXUO_GLOBALSTOLEN	Bit indicating that critical pages stolen in IAXUO global steal processing
		..11		*	Reserved
		.... 1...		RAX_IAXUE_IAXUO	Bit indicating that critical pages stolen in IAXUE processing (IAXUO call)
		.... .1..		RAX_IAXUE_IAXPP	Bit indicating that critical pages stolen in IAXUE processing (IAXPP call)
		.... ..1.		RAX_IAXUE_IAXDF	Bit indicating that critical pages stolen in IAXUE processing (IAXDF call)
		1		RAX_IAXUE_IAXIX	Bit indicating that critical pages stolen in IAXUE processing (IAXIX call)
706	(2C2)	BITSTRING	1	RAXCRITICALBITS2	
		1...		RAX_IAXUE_IAXKL	Bit indicating that critical pages stolen in IAXUE processing (IAXKL call)
		.1..		RAX_IAXUE_IAXPB	Bit indicating that critical pages stolen in IAXUE processing (IAXPB call)
		..1.		RAX_IAXUE_IAXPE	Bit indicating that critical pages stolen in IAXUE processing (IAXPE call)
		...1		RAX_IAXUE_IAXPZ	Bit indicating that critical pages stolen in IAXUE processing (IAXPZ call)
		.... 1...		RAX_IAXUE_IAXUR	Bit indicating that critical pages stolen in IAXUE processing (IAXUR call)
		.... .1..		RAX_IAXUE_IAXVZ	Bit indicating that critical pages stolen in IAXUE processing (IAXVZ call)
		.... ..1.		RAX_IAXUE_IAXV1	Bit indicating that critical pages stolen in IAXUE processing (IAXV1 call)
		1		RAX_IAXUE_UNKNOWN	Bit indicating that critical pages stolen in IAXUE processing (unknown)
707	(2C3)	BITSTRING	1	RAXCRITICALBITS3	
		1...		RAX_IAXUA_RSFQ1STOLEN	Bit indicating that critical pages stolen in IAXUA processing RSFQ1
		.1..		RAX_IAXUA_BDFQSTOLEN	Bit indicating that critical pages stolen in IAXUA processing BDFQ
		..1.		RAX_IAXUA_RSFQ2STOLEN	Bit indicating that critical pages stolen in IAXUA processing RSFQ2
		...1		RAX_IAXUA_VRSTOLEN	Bit indicating that critical pages stolen in IAXUA processing V=R
		.... 1...		RAX_IAXUA_PFTSTOLEN	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Bit indicating that critical pages stolen in IAXUA processing PFTSCAN
		.... .1..		RAX_IAXUA_RABSTOLEN	
					Bit indicating that critical pages stolen in IAXUA processing RABSCAN
		.... ..1.		RAX_IAXUA_SBFQSTOLEN	
					Bit indicating that critical pages stolen in IAXUA processing SBFQ
		1		RAX_IAXUA_RVTESTOLEN	
					Bit indicating that critical pages stolen in IAXUA processing RVTE
708	(2C4)	BITSTRING	1	RAXCRITICALBITS4	
		1...		RAX_IAXYT_IAXCD	
					Bit indicating that critical pages stolen in IAXYT processing (IAXCD call)
		.1..		RAX_IAXYT_IAXFH	
					Bit indicating that critical pages stolen in IAXYT processing (IAXFH call)
		..1.		RAX_IAXYT_IAXFP	
					Bit indicating that critical pages stolen in IAXYT processing (IAXFP call)
		...1		RAX_IAXYT_IAXFV	
					Bit indicating that critical pages stolen in IAXYT processing (IAXFV call)
		.... 1...		RAX_IAXYT_IAXFY	
					Bit indicating that critical pages stolen in IAXYT processing (IAXFY call)
		.... .1..		RAX_IAXYT_IAXVO	
					Bit indicating that critical pages stolen in IAXYT processing (IAXVO call)
		.... ..1.		RAX_IAXYT_IAXXS	
					Bit indicating that critical pages stolen in IAXYT processing (IAXXS call)
		1		RAX_IAXYT_UNKNOWN	
					Bit indicating that critical pages stolen in IAXYT processing (unknown)
709	(2C5)	BITSTRING	1	RAXCRITICALBITS5	
		1...		RAX_IAXUD_PAGESTOLEN	
					Bit indicating that critical pages stolen in IAXUD page processing
		.1..		RAX_IAXUD_SWAPSTOLEN	
					Bit indicating that critical pages stolen in IAXUD swap processing
		..1.		RAX_IAXUD_SCANPSTOLEN	
					Bit indicating that critical pages stolen in IAXUD scan page processing
		...1		RAX_IAXUD_SCANSSTOLEN	
					Bit indicating that critical pages stolen in IAXUD scan swap processing
		.... 1...		RAX_IAXYG_PAGESTOLEN	
					Bit indicating that critical pages stolen in IAXYG page processing
		.... .1..		RAX_IAXYG_SWAPSTOLEN	
					Bit indicating that critical pages stolen in IAXYG swap processing
		.... ..1.		RAX_IAXYG_AREASSTOLEN	
					Bit indicating that critical pages stolen in IAXYG area scan processing
		1		RAX_IAXYG_ANYSSTOLEN	
					Bit indicating that critical pages stolen in IAXYG any scan processing
710	(2C6)	BITSTRING	1	RAXFLGS3	FLAG BYTE 3
		1...		RAX_HIGH_VIRT_GETSTOR	
					Bit indicating that high virtual getstor is issued and storage is obtained
711	(2C7)	CHARACTER	1	RAXRSV4	Reserved
712	(2C8)	SIGNED	4	RAXPLFRM	Number of pageable large frame groups currently used by this address space
716	(2CC)	SIGNED	4	RAXPLHWM	High Water Mark for the number of pageable large frame groups used by this address space
720	(2D0)	UNSIGNED	8	RAXPMMSS	Number of failed attempts to back storage with pageable large frames by this address space (pref)
728	(2D8)	UNSIGNED	8	RAXPLSID	Number of system-initiated demotions from pageable large frames groups to 4k page frames for this address space
736	(2E0)	UNSIGNED	8	RAXPLRID	Number of request-initiated demotions from pageable large frames groups to 4k page frames for this address space
744	(2E8)	UNSIGNED	8	RAXNMMSS	Number of failed attempts to back storage with pageable large frames by this address space (non-pref)
752	(2F0)	SIGNED	4	RAXPLXRM	Number of pageable large frame groups currently fixed by this address space
756	(2F4)	SIGNED	4	RAXLARGECOMMONMEMORYOBJECTS	Number of common large memory objects owned by this address space. Serialized by C/S.
760	(2F8)	UNSIGNED	8	RAXLARGECOMMONPAGES	Number of common large pages owned by this address space. Serialized by CSG.
768	(300)	CHARACTER	24	RAXRSV5	Reserved
792	(318)	UNSIGNED	8	RAXHVAUXSCM	Number of SCM blockids used to back 64 bit private storage. Serialized by the RSMAD lock
800	(320)	UNSIGNED	8	RAXRSV51	Unused
808	(328)	UNSIGNED	8	RAXTOTPIDASD	Total page-ins from DASD for pages in this address space. Excludes SWAP-IN, VIO, AND HIPERSPACE PAGE-INS. Serialized by the RSMAD lock
816	(330)	UNSIGNED	8	RAXTOTPISCM	Total page-ins from SCM for pages in this address space. Excludes SWAP-IN, VIO, AND HIPERSPACE PAGE-INS. Serialized by the RSMAD lock

RAB Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
				Comment	
! Note: OuxbPOut contains the total number of page outs (i.e. ! dasd + SCM).					
				End of Comment	
824	(338)	UNSIGNED	8	RAXTOTPODASD	Total page-outs to DASD. Excludes Swap-Out, VIO PAGE-OUT, VIO MOVEOUT and HIPERSPACE PAGES. Serialized by the RSMAD lock
832	(340)	UNSIGNED	8	RAXTOTPOSCM	Total page-outs to SCM. Excludes Swap-Out, VIO PAGE-OUT, VIO MOVEOUT and HIPERSPACE PAGES. Serialized by the RSMAD lock
840	(348)	UNSIGNED	8	RAXTOTPI1MSCM	Total page-ins from SCM for 1M pages in this address space. Excludes SWAP-IN, VIO, AND HIPERSPACE PAGE-INS. Serialized by the RSMAD lock
848	(350)	UNSIGNED	8	RAXTOTPO1MSCM	Total page-outs of 1M pages to SCM. Excludes Swap-Out, VIO PAGE-OUT, VIO MOVEOUT and HIPERSPACE PAGES. Serialized by the RSMAD lock
856	(358)	CHARACTER	72	RAXRSV6	Reserved for HBB7780
928	(3A0)	UNSIGNED	8	RAX2GMEMORYOBJECTS	Number of 2G Memory Objects allocated by this address space
928	(3A0)	CHARACTER	4	*	
932	(3A4)	SIGNED	4	RAX2GMEMORYOBJECTS31	
936	(3A8)	UNSIGNED	8	RAX2GPAGESBACKEDINREAL	Number of 2G pages backed in real storage owned by this address space
936	(3A8)	CHARACTER	4	*	
940	(3AC)	SIGNED	4	RAX2GPAGESBACKEDINREAL31	
944	(3B0)	CHARACTER	0	RAXEND	KEEP RAX A MULT. OF 8 BYTES 6@LID

RAB Constants

Len	Type	Value		Name	Description
1	DECIMAL		1	RAXLVSMF	MEMLIMIT set by SMF either in SMFPRMxx or by use of SMF default value=0
1	DECIMAL		2	RAXLVJCL	MEMLIMIT set by the JCL
1	DECIMAL		3	RAXLVREG0	MEMLIMIT Unlimited based on REGION=0 specification
1	DECIMAL		4	RAXLVUSI	MEMLIMIT set by IEFUSI
1	DECIMAL		5	RAXLVOMVS	MEMLIMIT set by UNIX OMVS segment
1	DECIMAL		6	RAXLVSETR	MEMLIMIT set by UNIX setrlimit
1	DECIMAL		7	RAXLVSPW	MEMLIMIT set by UNIX spawn
1	DECIMAL		8	RAXLVSETO	MEMLIMIT set by UNIX SETOMVS command
1	DECIMAL		9	RAXLVAUTH	MEMLIMIT set by authorized application modification
1	DECIMAL		10	RAXLVURG	Special case of MEMLIMIT getting set in IEFSMFIE (IEFUSI set REGION size)
1	HEX	FF		RAXLVBAD	Error setting MEMLIMIT (for debug purposes)
				Comment	
RAX constants for indicating trim status (RaxTrimStatus)					
				End of Comment	
1	DECIMAL		0	RAXNOTRIM	Trimming not in progress
1	DECIMAL		1	RAXTRIMPHASE1	Phase 1 in progress
1	DECIMAL		2	RAXTRIMPHASE1DONE	Phase 1 is complete
1	DECIMAL		3	RAXTRIMPHASE2	Phase 2 is in progress
				Comment	
RAX constants for MOMB DUMP Priorities used by C and Java					
				End of Comment	
1	DECIMAL		1	RAXDUMPPRIORHIGHEST	
1	DECIMAL		5	RAXDUMPPRIORCSTACK	
1	DECIMAL		15	RAXDUMPPRIORCHEAP	
1	DECIMAL		15	RAXDUMPPRIORJAVASTACK	
1	DECIMAL		20	RAXDUMPPRIORJAVASHAREDCLASSDATA	
1	DECIMAL		30	RAXDUMPPRIORJAVAHEAP	
1	DECIMAL		50	RAXDUMPPRIORJAVAAOTJITEDCODE	
1	DECIMAL		99	RAXDUMPPRIORLOWEST	
1	DECIMAL		99	RAXDUMPPRIORDEFAULT	

Len	Type	Value	Name	Description
			Comment	
			Deleted forcing to doubleword boundary to enable inclusion of constants at end of RAX	
			End of Comment	
1	HEX	C0	RABRABQN	ID FOR THE RAB QUEUE
4	CHARACTER	RAB	IARRAB_KRABID	Normal Rab ID
4	CHARACTER	RABR	IARRAB_KRASPRABID	Rasp Rab ID
4	CHARACTER	RABC	IARRAB_KCOMMONRABID	Common Rab ID

RAB Cross Reference

Name	Hex Offset	Hex Value
RAB	0	
RABASCB	18	
RABASID	16	
RABASIT	C	40
RABASLTC	48	
RABASPIN	12	08
RABBADPT	C	02
RABBADVP	E	80
RABBFXSV	F4	
RABBQPTR	4	
RABCCQF	BC	
RABCCQL	C0	
RABCFEQ	88	
RABCPQF	CC	
RABCPQL	D0	
RABC0DCT	10	
RABC0DFL	12	
RABDAB	DC	
RABDBLDF	E0	
RABDCQF	B4	
RABDCQL	B8	
RABDMXEX	E4	
RABDMXSZ	E8	
RABDRIM	E	20
RABECBNP	12	20
RABFCQF	7C	
RABFCQL	80	
RABFCUR	50	
RABFFOEQ	84	
RABFLAGSABN	4C	
RABFLAGSABN1	4C	
RABFLAW	C	01
RABFLGS1	C	
RABFLGS2	D	
RABFLGS3	E	
RABFLGS4	F	
RABFOENF	12	10
RABFQPTR	0	
RABHVQCV	F	20
RABID	8	
RABIDRAB	8	
RABIDTYP	B	
RABLDPQF	64	
RABLDPQL	68	
RABLOCK	1C	
RABLPPGT	24	
RABLSCON	12	40
RABLSQAO	D	04
RABLSWAP	E	08
RABLVAB	134	
RABMCOQF	5C	
RABMCOQL	60	
RABMUSV	114	
RABNCQF	C4	
RABNCQL	C8	
RABNLSQA	C	08
RABNOTRS	C	80
RABNPGT	C	04
RABNPQF	6C	
RABNPQL	70	
RABPAGDS	D	20
RABPCQF	D4	
RABPCQL	D8	
RABPINCT	FC	
RABRAXD	178	
RABREALSW	F	08
RABREQSW	D	10
RABRMPND	E	10
RABRPH	74	
RABRVRHI	B0	
RABRVRID	A0	
RABRVRLO	AC	
RABRVRPL	D	01
RABRVRQF	A4	
RABRVRQL	A8	
RABSAEQF	EC	
RABSAEQL	F0	
RABSCMEVAC	4C	80
RABSCMEVACIOFCB@	8C	
RABSCMEVACNOTCOMPLETE	4C	20
RABSCMEVACSRBSCHEDULED	4C	40
RABSENDPAGETOAUX	F	04
RABSFT	34	
RABSFTEL	3C	
RABSFTSZ	38	
RABSGT	20	
RABSIBQF	16C	
RABSIBQL	170	
RABSLT	40	
RABSLTQ	44	
RABSPEQF	10C	
RABSPEQL	110	
RABSRTH	108	
RABSSIPF	E	04
RABSTATICFRAMEQHEADERAREAADDR	98	
RABSTKN	100	
RABSTKN1	104	
RABSTKN2	100	
RABSTLPE	12	04
RABSTPIN	12	80
RABSWASM	F	40
RABSWEST	E	40
RABSWEXC	F	10
RABSWFCB	30	
RABSWFXC	2C	

Name	Hex Offset	Hex Value
RABSWIOC	E	02
RABSWOUT	C	10
RABSWPR	C	20
RABTOKEN	94	
RABTOPPFTE	13C	
RABTOPVSA	148	
RABTRMIP	D	02
RABTUSV	118	
RABUDDCV	E	01
RABVATC	13	02
RABVATD	13	20
RABVDXC	13	01
RABVDXD	13	10
RABVFLGA	13	
RABVFLGB	14	
RABVIOHI	12C	
RABVIOID	11C	
RABVIOLO	128	
RABVIOPL	F	80
RABVIOQF	120	
RABVIOQL	124	
RABVPXD	14	80
RABVRPTD	D	08
RABVTTC	13	08
RABVTTD	13	80
RABVTTXC	13	04
RABVTTXD	13	40
RABV64PRIVMOTKN	150	
RABXPPGT	28	
RAB1LPU	D	40
RAB2LPU	D	80
RAX_HIGH_VIRT_GETSTOR	2C6	80
RAX_IAXUA_BDFQSTOLEN	2C3	40
RAX_IAXUA_PFTSTOLEN	2C3	08
RAX_IAXUA_RABSTOLEN	2C3	04
RAX_IAXUA_RSFQ1STOLEN	2C3	80
RAX_IAXUA_RSFQ2STOLEN	2C3	20
RAX_IAXUA_RVTESTOLEN	2C3	01
RAX_IAXUA_SBFQSTOLEN	2C3	02
RAX_IAXUA_VRSTOLEN	2C3	10
RAX_IAXUD_PAGESTOLEN	2C5	80
RAX_IAXUD_SCANPSTOLEN	2C5	20
RAX_IAXUD_SCANSSTOLEN	2C5	10
RAX_IAXUD_SWAPSTOLEN	2C5	40
RAX_IAXUE_IAXDF	2C1	02
RAX_IAXUE_IAXIX	2C1	01
RAX_IAXUE_IAXKL	2C2	80
RAX_IAXUE_IAXPB	2C2	40
RAX_IAXUE_IAXPE	2C2	20
RAX_IAXUE_IAXPP	2C1	04
RAX_IAXUE_IAXPZ	2C2	10
RAX_IAXUE_IAXUO	2C1	08
RAX_IAXUE_IAXUR	2C2	08
RAX_IAXUE_IAXVZ	2C2	04
RAX_IAXUE_IAXV1	2C2	02
RAX_IAXUE_UNKNOWN	2C2	01
RAX_IAXUO_GLOBALSTOLEN	2C1	40
RAX_IAXUO_HIGHSTOLEN	2C1	80
RAX_IAXYG_ANYSSTOLEN	2C5	01
RAX_IAXYG_AREASSTOLEN	2C5	02
RAX_IAXYG_PAGESTOLEN	2C5	08
RAX_IAXYG_SWAPSTOLEN	2C5	04
RAX_IAXYT_IAXCD	2C4	80
RAX_IAXYT_IAXFH	2C4	40
RAX_IAXYT_IAXFP	2C4	20
RAX_IAXYT_IAXFV	2C4	10
RAX_IAXYT_IAXFY	2C4	08
RAX_IAXYT_IAXVO	2C4	04
RAX_IAXYT_IAXXS	2C4	02
RAX_IAXYT_UNKNOWN	2C4	01
RAXABVFX	1F4	
RAXAGED	208	
RAXASOWNSNONCRITICALDS	2C0	20
RAXBELFX	1C8	
RAXBFQFX	200	
RAXBLPEA	17C	40
RAXCRITICALBITS	2C1	
RAXCRITICALBITS2	2C2	
RAXCRITICALBITS3	2C3	
RAXCRITICALBITS4	2C4	
RAXCRITICALBITS5	2C5	
RAXCRITICALPAGESSTOLEN	2C0	40
RAXCRITICALPAGING	2C0	80
RAXCSTAR	1B0	
RAXCSTNO	1F0	
RAXCSWRD	17C	
RAXCSWRD2	264	
RAXDAVQL	17C	08
RAXDBFRM	19C	
RAXDREFR	1FC	
RAXDRM	18C	
RAXDRMIP	190	
RAXDSHWM	198	
RAXEND	3B0	
RAXESCT	180	
RAXESSW	17C	80
RAXFBV	1B4	
RAXFBV1	1B4	
RAXFBV2	1B8	
RAXFBV3	1BC	

Name	Hex Offset	Hex Value
RAXFBV4	1C0	
RAXFFSRBTS	238	
RAXFLGS1	17C	
RAXFLGS2	2C0	
RAXFLGS3	2C6	
RAXFMCT	1A4	
RAXFXABVSTL	268	
RAXFXSTL	1AA	
RAXFXTOTSTL	26C	
RAXHRECT	1E4	
RAXHSPCT	1AC	
RAXHVAUXSCM	318	
RAXHVAUXSLOTS	2A0	
RAXHVCOMMONBYTES	280	
RAXHVCOMMONHWMBYTES	288	
RAXHVCOMMONNMOMB	290	
RAXHVDATFMCT	260	
RAXHVGAUX	2B0	
RAXHVGAUXSLOTS	2B0	
RAXHVGPAGESINREAL	2A8	
RAXHVPAGESINREAL	298	
RAXHVSHRPAGEVALIDATIONS	258	
RAXHVSHRPAGEVALIDATIONS31	25C	
RAXHWRDA	1A8	
RAXID	178	
RAXLARGECOMMONMEMORYOBJECTS	2F4	
RAXLARGECOMMONPAGES	2F8	
RAXLARGEFRAMEAUTH	17C	04
RAXLARGEMEMORYOBJECTS	270	
RAXLARGEMEMORYOBJECTS31	274	
RAXLARGEPAGESBACKEDINREAL	278	
RAXLARGEPAGESBACKEDINREAL31	27C	
RAXLSQA	1F8	
RAXLVABYTES	218	
RAXLVGBYTES	228	
RAXLVHBYTES	220	
RAXLVMEMLIM	210	
RAXLVMEMLIMS	230	
RAXLVNMOMB	234	
RAXLVSHRBYTES	240	
RAXLVSHRGBYTES	248	
RAXLVSHRNMOMB	250	
RAXNMMSS	2E8	
RAXNONCRITICALDSSTOLEN	2C0	10
RAXOVBLK	1C4	
RAXPAGEABLESHORTAGEPHASE1	264	40
RAXPLFRM	2C8	
RAXPLHWM	2CC	
RAXPLRID	2E0	
RAXPLSID	2D8	
RAXPLXRM	2F0	
RAXPMMSS	2D0	
RAXPPTNAME	2B8	

Name	Hex Offset	Hex Value
RAXQDFRM	204	
RAXQDFRMSAVED	20C	
RAXQUOT	184	
RAXREALSWAPCANCEL	264	80
RAXREALSWAPDDP	265	40
RAXREALSWAPINTER	265	80
RAXREALSWAPPREF	265	20
RAXREALSWAP16MSHRT	265	10
RAXREALSWAP2GSHRT	265	08
RAXRSV1	17E	
RAXRSV4	2C7	
RAXRSV5	300	
RAXRSV51	320	
RAXRSV6	358	
RAXSORFL	17C	10
RAXSPGPI	1EC	
RAXSPSNG	1DC	
RAXSPVLC	1D8	
RAXSSCRE	17C	20
RAXSVINR	1D4	
RAXSWAPFLAGS	264	
RAXSWAPREASON	265	
RAXSWSM	188	
RAXSWSS	1CC	
RAXTOTFX	1E0	
RAXTOTPIDASD	328	
RAXTOTPISCM	330	
RAXTOTPI1MSCM	348	
RAXTOTPODASD	338	
RAXTOTPOSCM	340	
RAXTOTPO1MSCM	350	
RAXTOTSV	1D0	
RAXTRIMSTATUS	17D	
RAXUKDSS	194	
RAXVIOCT	1A0	
RAXVIORC	1E8	
RAXV64B	210	
RAXV64C	240	
RAX2GMEMORYOBJECTS	3A0	
RAX2GMEMORYOBJECTS31	3A4	
RAX2GPAGESBACKEDINREAL	3A8	
RAX2GPAGESBACKEDINREAL31	3AC	

RAX Information

RAX Programming Interface information

Programming Interface information

RAX

ONLY the following fields are part of the programming interface information:

- RAXDBFRM
- RAXDRM
- RAXDRMIP
- RAXESCT
- RAXFMCT
- RAXHRECT
- RAXHVSHRPAGEVALIDATIONS
- RAXLARGECOMMONMEMORYOBJECTS
- RAXLARGECOMMONPAGES
- RAXLARGEMEMORYOBJECTS
- RAXLARGEPAGESBACKEDINREAL
- RAXLVABYTES
- RAXLVGBYTES
- RAXLVHBYTES
- RAXLVMEMLIM
- RAXLVMEMLIMS
- RAXLVNMOMB
- RAXLVSHRBYTES
- RAXLVSHRGBYTES
- RAXLVSHRNMOMB
- RAXOVBLK
- RAXPLFRM
- RAXPLXRM
- RAXSPGPI
- RAXSPVLC
- RAXSVINR
- RAXSWSM
- RAXTOTFX
- RAXTOTSV
- RAXVIOCT
- RAXVIORC

End of Programming Interface information

RAX Heading Information

Common Name: RSM ADDRESS SPACE BLOCK EXTENSION
Macro ID: IARRAX
DSECT Name: RAX
Owning Component: REAL STORAGE MANAGER (SC1CR)
Eye-Catcher ID: RAX
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: YES
Subpool: 245
Key: 0
Residency: MUST BE ABOVE 16 MEG VIRTUAL
Size: See assembled listing
Created by: RSM ADDRESS SPACE CREATE
Pointed to by: RCERAX FIELD OF THE RCE DATA AREA
ASCBRSME FIELD OF THE ASCB DATA AREA
Serialization: RSM ADDRESS SPACE LEVEL LOCKS
Function: CONTAINS RSM ADDRESS SPACE-RELATED CONTROL VALUES AND COUNTERS.

RAX Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RAX	
0	(0)	CHARACTER	1	RAXID (4)	RAX CONTROL BLOCK ID
4	(4)	CHARACTER	4	RAXCSWRD (0)	RAX COMPARE AND SWAP WORD
4	(4)	BITSTRING	1	RAXFLGS1	FLAG BYTE 1
		1...		RAXESSW	"X'80'" SWAP THIS ADDRESS SPACE TO EXTENDED STORE - SET BY SRM (ESA MODE ONLY. NOT USED FOR ESAME)
		.1..		RAXBLPEA	"X'40'" BLOCK PAGING FROM EXPANDED STORAGE IS ACTIVE. SET BY RSM (ESA MODE ONLY. NOT USED FOR ESAME)
		..1.		RAXSSCRE	"X'20'" SRM WANTS RSM SWAP OUT PROCESSING TO CALL SECONDARY WORKING SET CREATE
		...1		RAXSORFL	"X'10'" SWAP OUT FAILED DUE TO LACK OF RESOURCES
		.... 1...		RAXDAVQL	"X'08'" SRM WANTS RSM TO DEFER 1ST REFERENCE PAGE FAULTS IN THIS SPACE DURING AVQLOW
		.... .1..		RAXLARGEFRAMEAUTH	"X'04'" RACF authorized for large pages set by IEFSMFIE
5	(5)	CHARACTER	1	RAXTRIMSTATUS	Trim Status Indicator
5	(5)	X'0'	0	RAXNOTRIM	"0" Trimming not in progress
5	(5)	X'1'	0	RAXTRIMPHASE1	"1" Trimming phase 1 is in progress
5	(5)	X'2'	0	RAXTRIMPHASE1DONE	"2" Trimming phase 1 is complete
5	(5)	X'3'	0	RAXTRIMPHASE2	"3" Trimming phase 2 is in progress
6	(6)	BITSTRING	2	RAXRSV1	RESERVED
8	(8)	SIGNED	4	RAXESCT	NUMBER OF PAGES ON EXTENDED STORAGE (ESA MODE ONLY. NOT USED FOR ESAME)

Comment

THIS FIELD APPLIES TO THE COMMON RAX

End of Comment

12	(C)	SIGNED	4	RAXQUOT	STORAGE ISOLATION QUOTA OF MAIN STORAGE FRAMES. FOR ESA MODE, IT ALSO INCLUDES EXTENDED STORAGE E-FRAMES.

Comment

THIS FIELD APPLIES TO THE COMMON RAX

End of Comment

16	(10)	SIGNED	4	RAXSWSM	NUMBER OF MIGRATED SECONDARY WORKING SET PAGES.
20	(14)	SIGNED	4	RAXDRM	NUMBER OF DREF PAGES THAT HAVE BEEN MIGRATED OR ARE IN THE PROCESS OF BEING MIGRATED
24	(18)	SIGNED	4	RAXDRMIP	NUMBER OF DREF PAGES WITH MIGRATION IN PROGRESS
28	(1C)	SIGNED	4	RAXUKDSS	NUMBER OF BLOCKS (4K BYTES) OF USER KEY DATA SPACE IN EXISTENCE FOR THIS ADDRESS SPACE.
32	(20)	SIGNED	4	RAXDSHWM	HIGH WATER MARK (IN MEGABYTES) OF USER KEY DATA SPACE CREATED FOR THIS ADDRESS SPACE. THIS FIELD IS PROVIDED SMF AND MAY BE RESET ONLY BY THE SMF COMPONENT

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
36	(24)	SIGNED	4	RAXDBFRM	NUMBER OF DOUBLE FRAME PAIRS CURRENTLY IN USE BY THIS ADDRESS SPACE
40	(28)	SIGNED	4	RAXVIOCT	NUMBER OF VIO DATA SET PAGES ON EXPANDED STORAGE (ESA MODE ONLY. NOT USED FOR ESAME)
44	(2C)	SIGNED	4	RAXFMCT	NUMBER OF FRAMES CURRENTLY IN USE BY THIS ADDRESS SPACE. It does not include 2G frames.
				Comment	
				THIS FIELD APPLIES TO THE COMMON RAX	
				End of Comment	
48	(30)	SIGNED	2	RAXHWRDA	RESERVED
50	(32)	SIGNED	2	RAXFXSTL	NUMBER OF FIXED PAGES IN THIS LOGICALLY SWAPPED ADDRESS SPACE THAT WERE BACKED BY REAL STORAGE BELOW 16 MEGABYTES (FOR ESA MODE THE PAGES ARE CURRENTLY STOLEN TO EXPANDED)
52	(34)	SIGNED	4	RAXHSPCT	NUMBER OF HIPERSPACE PAGES CURRENTLY ON EXPANDED STORAGE FOR THIS ADDRESS SPACE (THIS COUNT IS ALSO INCLUDED IN RAXESCT) (ESA MODE ONLY. NOT USED FOR ESAME)
56	(38)	SIGNED	4	RAXCSTAR	WORKING SET MANAGEMENT CENTRAL STORAGE TARGET NUMBER OF FRAMES
60	(3C)	CHARACTER	16	RAXFBV (0)	STRUCTURE NAME FOR RAXFBV# FIELDS
				Comment	
				THIS AND THE FOLLOWING 4 FIELDS ARE APPLICABLE TO THE COMMON RAX	
				End of Comment	
60	(3C)	SIGNED	4	RAXFBV1	NUMBER OF FRAMES IN UIC INTERVAL 1 AS SET BY SRM VIA THE RCEFRV# FIELDS.
64	(40)	SIGNED	4	RAXFBV2	NUMBER OF FRAMES IN UIC INTERVAL 2 AS SET BY SRM VIA THE RCEFRV# FIELDS.
68	(44)	SIGNED	4	RAXFBV3	NUMBER OF FRAMES IN UIC INTERVAL 3 AS SET BY SRM VIA THE RCEFRV# FIELDS.
72	(48)	SIGNED	4	RAXFBV4	NUMBER OF FRAMES IN UIC INTERVAL 4 AS SET BY SRM VIA THE RCEFRV# FIELDS.
76	(4C)	SIGNED	4	RAXOVBLK	NUMBER OF IMPLICITLY OVERBLOCKED FRAMES
80	(50)	SIGNED	4	RAXBELFX	NUMBER OF PAGES IN THIS ADDR SPACE EXPLICITLY FIXED AND CURRENTLY BACKED WITH REAL STORAGE BELOW 16 MEGABYTES
84	(54)	SIGNED	4	RAXSWSS	COUNT OF SECONDARY WORKING SET PAGES
88	(58)	SIGNED	4	RAXTOTSV	TOTAL NUMBER OF SHARED PAGE VIEWS IN THIS ADDRESS SPACE
92	(5C)	SIGNED	4	RAXSVINR	TOTAL NUMBER OF SHARED PAGES IN CENTRAL STORAGE THAT ARE VALID IN THIS ADDRESS SPACE
96	(60)	SIGNED	4	RAXSPVLC	CONSTANTLY INCREASING COUNT OF SHARED PAGE VALIDATIONS IN THIS ADDRESS SPACE
100	(64)	SIGNED	4	RAXSPSNG	NUMBER OF SHARED PAGE SINGLETONS IN THIS ADDRESS SPACE
104	(68)	SIGNED	4	RAXTOTFX	TOTAL NUMBER OF FIXED PAGES IN THIS ADDRESS SPACE (DOES NOT INCLUDE SHARED PAGES OR 2G PAGES)
108	(6C)	SIGNED	4	RAXHRECT	NUMBER OF HIPERSPACE PAGES ON REAL (ESAME ONLY)
112	(70)	SIGNED	4	RAXVIORC	NUMBER OF VIO DATASET PAGES IN THE VIO REAL CACHE (ESAME ONLY)
116	(74)	SIGNED	4	RAXSPGPI	TOTAL NUMBER OF SHARED PAGES BROUGHT IN FROM AUXILIARY STORAGE BY THIS ADDRESS SPACE
120	(78)	SIGNED	4	RAXCSTNO	NUMBER OF FRAMES USED TO BACK CASTOUT=NO HIPERSPACE PAGES IN THIS ADDRESS SPACE (ESAME ONLY)
124	(7C)	SIGNED	4	RAXABVFX	NUMBER OF PAGES IN THIS ADDRESS SPACE FIXED AND CURRENTLY BACKED WITH REAL STORAGE BETWEEN 16M AND 2G
128	(80)	SIGNED	4	RAXLSQA	NUMBER OF FIXED LSQA PAGES BACKED IN REAL STORAGE
132	(84)	SIGNED	4	RAXDREFR	NUMBER OF LSQA DREF PAGES AND DATA SPACE DREF PAGES IN REAL STORAGE
136	(88)	SIGNED	4	RAXBFQFX	NUMBER OF PAGES ON A FIXED QUEUE AND BACKED BELOW 16M IN REAL
140	(8C)	SIGNED	4	RAXQDFRM	NUMBER OF QUAD GROUPS CURRENTLY IN USE BY THE ADDRESS SPACE (ESAME ONLY)
144	(90)	SIGNED	4	RAXAGED	Number of frames that have been aged
148	(94)	SIGNED	4	RAXQDFRMSAVED	Value of RAXQDFRM saved during swap
152	(98)	CHARACTER	40	RAXV64B (0)	Counts for 64Bit Virtual Support
152	(98)	CHARACTER	8	RAXLVMEMLIM	Address Space Memory limit in MB
160	(A0)	CHARACTER	8	RAXLVABYTES	Number of bytes allocated from large virtual memory in memory objects
168	(A8)	CHARACTER	8	RAXLVHBYTES	Number of bytes hidden with large virtual memory objects
176	(B0)	CHARACTER	8	RAXLVGBYTES	high water mark for number of usable bytes within large virtual memory objects
184	(B8)	CHARACTER	1	RAXLVMEMLIMS	Source of Address Space memory limit

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
184	(B8)	X'1'	0	RAXLVSMF	"1"
184	(B8)	X'2'	0	RAXLVJCL	"2"
184	(B8)	X'3'	0	RAXLVREG0	"3"
184	(B8)	X'4'	0	RAXLVUSI	"4"
184	(B8)	X'5'	0	RAXLVOMVS	"5"
184	(B8)	X'6'	0	RAXLVSETR	"6"
184	(B8)	X'7'	0	RAXLVSPW	"7"
184	(B8)	X'8'	0	RAXLVSETO	"8"
184	(B8)	X'9'	0	RAXLVAUTH	"9"
184	(B8)	X'A'	0	RAXLVURG	"10"
184	(B8)	X'FF'	0	RAXLVBAD	"255"
185	(B9)	CHARACTER	3		Reserved for future use
188	(BC)	CHARACTER	4	RAXLVNMOMB	Number of memory objects allocated
192	(C0)	CHARACTER	8	RAXFFSRBTS	Time when FF-SRB was last rescheduled
200	(C8)	CHARACTER	32	RAXV64C (0)	Counts for 64Bit Virtual Support
200	(C8)	CHARACTER	8	RAXLVSHRBYTES	Number of shared bytes allocated from high virtual memory
208	(D0)	CHARACTER	8	RAXLVSHRGBYTES	high water mark for number of shared bytes within large virtual memory objects
216	(D8)	CHARACTER	8	RAXLVSHRNMOMB	Number of shared memory objects allocated
224	(E0)	CHARACTER	8	RAXHVSHRPAGEVALIDATIONS (0)	Number of page validations for high virtual shared
224	(E0)	CHARACTER	4		Reserved
228	(E4)	CHARACTER	4	RAXHVSHRPAGEVALIDATIONS31	Number of page validations for high virtual shared
232	(E8)	CHARACTER	4	RAXHVDATFMCT	Number of frames used for high virtual DAT structure (common RAB only).
236	(EC)	CHARACTER	4	RAXCSWRD2 (0)	RAX COMPARE AND SWAP WORD
236	(EC)	BITSTRING	1	RAXSWAPFLAGS	
		1...		RAXREALSWAPCANCEL	"X'80'"
		.1..		RAXPAGEABLESHORTAGEPHASE1	"X'40'"
237	(ED)	BITSTRING	1	RAXSWAPREASON	
		1...		RAXREALSWAPINTER	"X'80'"
		.1..		RAXREALSWAPDDP	"X'40'"
		..1.		RAXREALSWAPPREF	"X'20'"
		...1		RAXREALSWAP16MSHRT	"X'10'"
		.... 1...		RAXREALSWAP2GSHRT	"X'08'"
238	(EE)	CHARACTER	2		Reserved
240	(F0)	SIGNED	4	RAXFXABVSTL	NUMBER OF FIXED PAGES IN THIS LOGICALLY SWAPPED ADDR SPACE THAT WERE BACKED BY REAL BETWEEN 16M and 2G
244	(F4)	SIGNED	4	RAXFXTOTSTL	NUMBER OF FIXED PAGES IN THIS LOGICALLY SWAPPED ADDR SPACE THAT WERE BACKED BY REAL BELOW 16M BETWEEN 16M and 2G DS CL16 Add back some reserved space when shipping Large Page support
248	(F8)	DBL WORD	8	RAXLARGEMEMORYOBJECTS (0)	Number of Large Memory Objects allocated by this address space
248	(F8)	SIGNED	4		
252	(FC)	SIGNED	4	RAXLARGEMEMORYOBJECTS31	
256	(100)	DBL WORD	8	RAXLARGEPAGESBACKEDINREAL (0)	Number of Large Pages (1MB pages) backed in real storage owned by this address space
256	(100)	SIGNED	4		
260	(104)	SIGNED	4	RAXLARGEPAGESBACKEDINREAL31	
264	(108)	DBL WORD	8	RAXHVCOMMONBYTES	Amount of 64-Bit Common allocated with this ASID as the owner
272	(110)	DBL WORD	8	RAXHVCOMMONHWMBYTES	High Water mark for the amount of 64-bit common bytes allocated with this ASID as the owner
280	(118)	DBL WORD	8	RAXHVCOMMONNMOMB	Number of 64-Bit common memory objects allocated with this ASID as the owner
288	(120)	DBL WORD	8	RAXHVPAGESINREAL	Number of real storage frames used to back 64-bit private storage. It does not include 2G frames.
296	(128)	DBL WORD	8	RAXHVAUXSLOTS	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Number of aux slots used to back 64-bit private storage Includes only dasd storage
304	(130)	DBL WORD	8	RAXHVGPAGESINREAL	
					High water mark for the number of real storage frames used to back 64-bit private storage
312	(138)	DBL WORD	8	RAXHVGAUXSLOTS (0)	
					High water mark
312	(138)	DBL WORD	8	RAXHVGAUX	High water mark for the number of aux slots and SCM blocks used to back 64-bit private storage
320	(140)	CHARACTER	8	RAXPPTNAME	Program name associated with this address space
328	(148)	BITSTRING	1	RAXFLGS2	FLAG BYTE 2
		1...		RAXCRITICALPAGING	
					"X'80'" Pages are not allowed to be stolen from this address space (set by IEFPPT and IEFSD101)
		.1..		RAXCRITICALPAGESSTOLEN	
					"X'40'" Pages were stolen from this address space while it was marked critical
		..1.		RAXASOWNSNONCRITICALDS	
					"X'20'" At least one non-critical dataspace has been created by the address space
		...1		RAXNONCRITICALDSSTOLEN	
					"X'10'" Pages were stolen from a non-critical dataspace owned by this critical address space
329	(149)	BITSTRING	1	RAXCRITICALBITS	
					Critical Paging Bits
		1...		RAX_IAXUO_HIGHSTOLEN	
					"X'80'" Bit indicating that critical pages stolen in IAXUO high steal processing
		.1..		RAX_IAXUO_GLOBALSTOLEN	
					"X'40'" Bit indicating that critical pages stolen in IAXUO global steal processing
		.... 1...		RAX_IAXUE_IAXUO	
					"X'08'" Bit indicating that critical pages stolen in IAXUE processing (IAXUO call)
		.... .1..		RAX_IAXUE_IAXPP	
					"X'04'" Bit indicating that critical pages stolen in IAXUE processing (IAXPP call)
		.... ..1.		RAX_IAXUE_IAXDF	
					"X'02'" Bit indicating that critical pages stolen in IAXUE processing (IAXDF call)
		1		RAX_IAXUE_IAXIX	
					"X'01'" Bit indicating that critical pages stolen in IAXUE processing (IAXIX call)
330	(14A)	BITSTRING	1	RAXCRITICALBITS2	
					Critical Paging Bits
		1...		RAX_IAXUE_IAXKL	
					"X'80'" Bit indicating that critical pages stolen in IAXUE processing (IAXKL call)
		.1..		RAX_IAXUE_IAXPB	
					"X'40'" Bit indicating that critical pages stolen in IAXUE processing (IAXPB call)
		..1.		RAX_IAXUE_IAXPE	
					"X'20'" Bit indicating that critical pages stolen in IAXUE processing (IAXPE call)
		...1		RAX_IAXUE_IAXPZ	
					"X'10'" Bit indicating that critical pages stolen in IAXUE processing (IAXPZ call)
		.... 1...		RAX_IAXUE_IAXUR	
					"X'08'" Bit indicating that critical pages stolen in IAXUE processing (IAXUR call)
		.... .1..		RAX_IAXUE_IAXVZ	
					"X'04'" Bit indicating that critical pages stolen in IAXUE processing (IAXVZ call)
		.... ..1.		RAX_IAXUE_IAXV1	
					"X'02'" Bit indicating that critical pages stolen in IAXUE processing (IAXV1 call)
		1		RAX_IAXUE_UNKNOWN	
					"X'01'" Bit indicating that critical pages stolen in IAXUE processing (unknown)
331	(14B)	BITSTRING	1	RAXCRITICALBITS3	
					Critical Paging Bits
		1...		RAX_IAXUA_RSFQ1STOLEN	
					"X'80'" Bit indicating that critical pages stolen in IAXUA processing RSFQ1
		.1..		RAX_IAXUA_BDFQSTOLEN	
					"X'40'" Bit indicating that critical pages stolen in IAXUA processing BDFQ
		..1.		RAX_IAXUA_RSFQ2STOLEN	
					"X'20'" Bit indicating that critical pages stolen in IAXUA processing RSFQ2
		...1		RAX_IAXUA_VRSTOLEN	
					"X'10'" Bit indicating that critical pages stolen in IAXUA processing V=R
		.... 1...		RAX_IAXUA_PFTSTOLEN	
					"X'08'" Bit indicating that critical pages stolen in IAXUA processing PFTSCAN
		.... .1..		RAX_IAXUA_RABSTOLEN	
					"X'04'" Bit indicating that critical pages stolen in IAXUA processing RABSCAN
		.... ..1.		RAX_IAXUA_SBFQSTOLEN	
					"X'02'" Bit indicating that critical pages stolen in IAXUA processing SBFQ
		1		RAX_IAXUA_RVTESTOLEN	
					"X'01'" Bit indicating that critical pages stolen in IAXUA processing RVTE
332	(14C)	BITSTRING	1	RAXCRITICALBITS4	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Critical Paging Bits
		1...		RAX_IAXYT_IAXCD	
					"X'80'" Bit indicating that critical pages stolen in IAXYT processing (IAXCD call)
		.1..		RAX_IAXYT_IAXFH	
					"X'40'" Bit indicating that critical pages stolen in IAXYT processing (IAXFH call)
		..1.		RAX_IAXYT_IAXFP	
					"X'20'" Bit indicating that critical pages stolen in IAXYT processing (IAXFP call)
		...1		RAX_IAXYT_IAXFV	
					"X'10'" Bit indicating that critical pages stolen in IAXYT processing (IAXFV call)
		.... 1...		RAX_IAXYT_IAXFY	
					"X'08'" Bit indicating that critical pages stolen in IAXYT processing (IAXFY call)
		.... .1..		RAX_IAXYT_IAXVO	
					"X'04'" Bit indicating that critical pages stolen in IAXYT processing (IAXVO call)
		.... ..1.		RAX_IAXYT_IAXXS	
					"X'02'" Bit indicating that critical pages stolen in IAXYT processing (IAXXS call)
		1		RAX_IAXYT_UNKNOWN	
					"X'01'" Bit indicating that critical pages stolen in IAXYT processing (unknown)
333	(14D)	BITSTRING	1	RAXCRITICALBITS5	
					Critical Paging Bits
		1...		RAX_IAXUD_PAGESTOLEN	
					"X'80'" Bit indicating that critical pages stolen in IAXUD page processing
		.1..		RAX_IAXUD_SWAPSTOLEN	
					"X'40'" Bit indicating that critical pages stolen in IAXUD swap processing
		..1.		RAX_IAXUD_SCANPSTOLEN	
					"X'20'" Bit indicating that critical pages stolen in IAXUD scan page processing
		...1		RAX_IAXUD_SCANSSTOLEN	
					"X'10'" Bit indicating that critical pages stolen in IAXUD scan swap processing
		.... 1...		RAX_IAXYG_PAGESTOLEN	
					"X'08'" Bit indicating that critical pages stolen in IAXYG page processing
		.... .1..		RAX_IAXYG_SWAPSTOLEN	
					"X'04'" Bit indicating that critical pages stolen in IAXYG swap processing
		.... ..1.		RAX_IAXYG_AREASSTOLEN	
					"X'02'" Bit indicating that critical pages stolen in IAXYG area scan processing
		1		RAX_IAXYG_ANYSSTOLEN	
					"X'01'" Bit indicating that critical pages stolen in IAXYG any scan processing
334	(14E)	BITSTRING	1	RAXFLGS3	FLAG BYTE 3
		1...		RAX_HIGH_VIRT_GETSTOR	
					"X'80'" Bit indicating that high virtual getstor is issued and storage is obtained
335	(14F)	CHARACTER	1	RAXRSV4	Reserved
336	(150)	SIGNED	4	RAXPLFRM	Number of pageable large frame groups currently used by this address space
340	(154)	SIGNED	4	RAXPLHWM	High Water Mark for the number of pageable large frame groups used by this address space
344	(158)	DBL WORD	8	RAXPMMSS	Number of failed attempts to back storage with pageable large frames by this address space (pref)
352	(160)	DBL WORD	8	RAXPLSID	Number of system-initiated demotions from pageable large frames groups to 4k page frames for this address space
360	(168)	DBL WORD	8	RAXPLRID	Number of request-initiated demotions from pageable large frames groups to 4k page frames for this address space
368	(170)	DBL WORD	8	RAXNMMSS	Number of failed attempts to back storage with pageable large frames by this address space (non-pref)
376	(178)	SIGNED	4	RAXPLXRM	Number of pageable large frame groups currently fixed by this address space
380	(17C)	SIGNED	4	RAXLARGECOMMONMEMORYOBJECTS	
					Number of common large memory objects owned by this address space. Serialized by C/S.
384	(180)	DBL WORD	8	RAXLARGECOMMONPAGES	
					Number of common large pages owned by this address space. Serialized by CSG.
392	(188)	CHARACTER	24	RAXRSV5	Reserved
416	(1A0)	DBL WORD	8	RAXHVAUXSCM	Number of SCM blockids used to back 64 bit private storage. Serialized by the RSMAD lock
424	(1A8)	DBL WORD	8	RAXRSV51	Unused
432	(1B0)	DBL WORD	8	RAXTOTPIDASD	Total page-ins from DASD for pages in this address space. Excludes SWAP-IN, VIO, AND HIPERSPACE PAGE-INS. Serialized by the RSMAD lock
440	(1B8)	DBL WORD	8	RAXTOTPISCM	Total page-ins from SCM for pages in this address space. Excludes SWAP-IN, VIO, AND HIPERSPACE PAGE-INS. Serialized by the RSMAD lock.
448	(1C0)	DBL WORD	8	RAXTOTPODASD	Total page-outs to DASD. Excludes SWAP-OUT, VIO PAGE-OUT, VIO MOVEOUT and HIPERSPACE PAGES. Serialized by the RSMAD lock.
456	(1C8)	DBL WORD	8	RAXTOTPOSCM	Total page-outs to SCM. Excludes SWAP-OUT, VIO PAGE-OUT, VIO MOVEOUT and HIPERSPACE PAGES. Serialized by the RSMAD lock.
464	(1D0)	DBL WORD	8	RAXTOTPI1MSCM	
					Total page-ins from SCM for 1M pages in this address space. Excludes SWAP-IN, VIO, AND HIPERSPACE PAGE-INS. Serialized by the RSMAD lock.
472	(1D8)	DBL WORD	8	RAXTOTPO1MSCM	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
					Total 1M page-outs to SCM for 1M pages. Excludes SWAP-OUT, VIO PAGE-OUT, VIO MOVEOUT and HIPERSPACE PAGES. Serialized by the RSMAD lock.
480	(1E0)	CHARACTER	72	RAXRSV6	Reserved for HBB7780
552	(228)	DBL WORD	8	RAX2GMEMORYOBJECTS (0)	
					Number of 2G Memory Objects allocated by this address space
552	(228)	SIGNED	4		
556	(22C)	SIGNED	4	RAX2GMEMORYOBJECTS31	
560	(230)	DBL WORD	8	RAX2GPAGESBACKEDINREAL (0)	
					Number of 2G pages backed in real storage owned by this address space
560	(230)	SIGNED	4		
564	(234)	SIGNED	4	RAX2GPAGESBACKEDINREAL31	
568	(238)	DBL WORD	8	RAXEND (0)	End of RAX

Comment

RAX constants for MOMB DUMP Priorities used by C and Java

End of Comment

568	(238)	X'1'	0	RAXDUMPPRIORHIGHEST "1"
568	(238)	X'5'	0	RAXDUMPPRIORCSTACK "5"
568	(238)	X'F'	0	RAXDUMPPRIORCHEAP "15"
568	(238)	X'F'	0	RAXDUMPPRIORJAVASTACK "15"
568	(238)	X'14'	0	RAXDUMPPRIORJAVASHAREDCLASSDATA "20"
568	(238)	X'1E'	0	RAXDUMPPRIORJAVAHEAP "30"
568	(238)	X'32'	0	RAXDUMPPRIORJAVAAOTJITEDCODE "50"
568	(238)	X'63'	0	RAXDUMPPRIORLOWEST "99"
568	(238)	X'63'	0	RAXDUMPPRIORDEFAULT "99"

RAX Cross Reference

Name	Hex Offset	Hex Value
RAX	0	
RAX_HIGH_VIRT_GETSTOR	14E	80
RAX_IAXUA_BDFQSTOLEN	14B	40
RAX_IAXUA_PFTSTOLEN	14B	8
RAX_IAXUA_RABSTOLEN	14B	4
RAX_IAXUA_RSFQ1STOLEN	14B	80
RAX_IAXUA_RSFQ2STOLEN	14B	20
RAX_IAXUA_RVTESTOLEN	14B	1
RAX_IAXUA_SBFQSTOLEN	14B	2
RAX_IAXUA_VRSTOLEN	14B	10
RAX_IAXUD_PAGESTOLEN	14D	80
RAX_IAXUD_SCANPSTOLEN	14D	20
RAX_IAXUD_SCANSSTOLEN	14D	10
RAX_IAXUD_SWAPSTOLEN	14D	40
RAX_IAXUE_IAXDF	149	2
RAX_IAXUE_IAXIX	149	1
RAX_IAXUE_IAXKL	14A	80
RAX_IAXUE_IAXPB	14A	40
RAX_IAXUE_IAXPE	14A	20
RAX_IAXUE_IAXPP	149	4
RAX_IAXUE_IAXPZ	14A	10
RAX_IAXUE_IAXUO	149	8
RAX_IAXUE_IAXUR	14A	8
RAX_IAXUE_IAXVZ	14A	4
RAX_IAXUE_IAXV1	14A	2
RAX_IAXUE_UNKNOWN	14A	1
RAX_IAXUO_GLOBALSTOLEN	149	40
RAX_IAXUO_HIGHSTOLEN	149	80
RAX_IAXYG_ANYSSTOLEN	14D	1
RAX_IAXYG_AREASSTOLEN		

RAX Cross Reference

Name	Hex Offset	Hex Value
	14D	2
RAX_IAXYG_PAGESTOLEN		
	14D	8
RAX_IAXYG_SWAPSTOLEN		
	14D	4
RAX_IAXYT_IAXCD		
	14C	80
RAX_IAXYT_IAXFH		
	14C	40
RAX_IAXYT_IAXFP		
	14C	20
RAX_IAXYT_IAXFV		
	14C	10
RAX_IAXYT_IAXFY		
	14C	8
RAX_IAXYT_IAXVO		
	14C	4
RAX_IAXYT_IAXXS		
	14C	2
RAX_IAXYT_UNKNOWN		
	14C	1
RAXABVFX	7C	
RAXAGED	90	
RAXASOWNSNONCRITICALDS		
	148	20
RAXBELFX	50	
RAXBFQFX	88	
RAXBLPEA	4	40
RAXCRITICALBITS		
	149	
RAXCRITICALBITS2		
	14A	
RAXCRITICALBITS3		
	14B	
RAXCRITICALBITS4		
	14C	
RAXCRITICALBITS5		
	14D	
RAXCRITICALPAGESSTOLEN		
	148	40
RAXCRITICALPAGING		
	148	80
RAXCSTAR	38	
RAXCSTNO	78	
RAXCSWRD	4	
RAXCSWRD2	EC	
RAXDAVQL	4	8
RAXDBFRM	24	
RAXDREFR	84	
RAXDRM	14	
RAXDRMIP	18	
RAXDSHWM	20	
RAXDUMPPRIORCHEAP		
	238	F
RAXDUMPPRIORCSTACK		
	238	5
RAXDUMPPRIORDEFAULT		
	238	63
RAXDUMPPRIORHIGHEST		
	238	1
RAXDUMPPRIORJAVAAOTJITEDCODE		
	238	32
RAXDUMPPRIORJAVAHEAP		
	238	1E
RAXDUMPPRIORJAVASHAREDCLASSDATA		
	238	14
RAXDUMPPRIORJAVASTACK		
	238	F
RAXDUMPPRIORLOWEST		
	238	63
RAXEND	238	
RAXESCT	8	
RAXESSW	4	80
RAXFBV	3C	
RAXFBV1	3C	
RAXFBV2	40	
RAXFBV3	44	
RAXFBV4	48	
RAXFFSRBTS	C0	
RAXFLGS1	4	
RAXFLGS2	148	
RAXFLGS3	14E	
RAXFMCT	2C	
RAXFXABVSTL	F0	
RAXFXSTL	32	
RAXFXTOTSTL	F4	
RAXHRECT	6C	
RAXHSPCT	34	
RAXHVAUXSCM	1A0	
RAXHVAUXSLOTS		
	128	
RAXHVCOMMONBYTES		
	108	
RAXHVCOMMONHWMBYTES		
	110	
RAXHVCOMMONNMOMB		
	118	
RAXHVDATFMCT	E8	
RAXHVGAUX	138	
RAXHVGAUXSLOTS		
	138	
RAXHVGPAGESINREAL		
	130	
RAXHVPAGESINREAL		
	120	
RAXHVSHRPAGEVALIDATIONS		
	E0	
RAXHVSHRPAGEVALIDATIONS31		
	E4	
RAXHWRDA	30	
RAXID	0	
RAXLARGECOMMONMEMORYOBJECTS		
	17C	
RAXLARGECOMMONPAGES		
	180	
RAXLARGEFRAMEAUTH		
	4	4
RAXLARGEMEMORYOBJECTS		
	F8	
RAXLARGEMEMORYOBJECTS31		
	FC	
RAXLARGEPAGESBACKEDINREAL		
	100	
RAXLARGEPAGESBACKEDINREAL31		
	104	
RAXLSQA	80	
RAXLVABYTES	A0	
RAXLVAUTH	B8	9
RAXLVBAD	B8	FF
RAXLVGBYTES	B0	
RAXLVHBYTES	A8	
RAXLVJCL	B8	2
RAXLVMEMLIM	98	
RAXLVMEMLIMS	B8	
RAXLVNMOMB	BC	
RAXLVOMVS	B8	5
RAXLVREG0	B8	3
RAXLVSETO	B8	8
RAXLVSETR	B8	6
RAXLVSHRBYTES		
	C8	
RAXLVSHRGBYTES		
	D0	
RAXLVSHRNMOMB		
	D8	
RAXLVSMF	B8	1
RAXLVSPW	B8	7
RAXLVURG	B8	A

Name	Hex Offset	Hex Value
RAXLVUSI	B8	4
RAXNMMSS	170	
RAXNONCRITICALDSSTOLEN	148	10
RAXNOTRIM	5	0
RAXOVBLK	4C	
RAXPAGEABLESHORTAGEPHASE1	EC	40
RAXPLFRM	150	
RAXPLHWM	154	
RAXPLRID	168	
RAXPLSID	160	
RAXPLXRM	178	
RAXPMMSS	158	
RAXPPTNAME	140	
RAXQDFRM	8C	
RAXQDFRMSAVED	94	
RAXQUOT	C	
RAXREALSWAPCANCEL	EC	80
RAXREALSWAPDDP	ED	40
RAXREALSWAPINTER	ED	80
RAXREALSWAPPREF	ED	20
RAXREALSWAP16MSHRT	ED	10
RAXREALSWAP2GSHRT	ED	8
RAXRSV1	6	
RAXRSV4	14F	
RAXRSV5	188	
RAXRSV51	1A8	
RAXRSV6	1E0	
RAXSORFL	4	10
RAXSPGPI	74	
RAXSPSNG	64	
RAXSPVLC	60	
RAXSSCRE	4	20
RAXSVINR	5C	
RAXSWAPFLAGS	EC	
RAXSWAPREASON	ED	
RAXSWSM	10	
RAXSWSS	54	
RAXTOTFX	68	
RAXTOTPIDASD	1B0	
RAXTOTPISCM	1B8	
RAXTOTPI1MSCM	1D0	
RAXTOTPODASD	1C0	
RAXTOTPOSCM	1C8	
RAXTOTPO1MSCM	1D8	
RAXTOTSV	58	
RAXTRIMPHASE1	5	1
RAXTRIMPHASE1DONE	5	2
RAXTRIMPHASE2	5	3
RAXTRIMSTATUS	5	
RAXUKDSS	1C	
RAXVIOCT	28	
RAXVIORC	70	
RAXV64B	98	
RAXV64C	C8	
RAX2GMEMORYOBJECTS	228	
RAX2GMEMORYOBJECTS31	22C	
RAX2GPAGESBACKEDINREAL	230	
RAX2GPAGESBACKEDINREAL31	234	

RB Information

RB Programming Interface information

Programming Interface information

RB

End of Programming Interface information

RB Heading Information

Common Name: REQUEST BLOCKS
Macro ID: IHARB
DSECT Name: RBPRFX (DSECT card precedes prefix). RBBASIC should be used for USING for basic section.
Owning Component: Task Management (SC1CL)
Eye-Catcher ID: NONE
Storage Attributes: Subpool: For IRBs, subpool 253. For PRBs, SVRBs, and SIRBs, subpool 255
Key: 0
Residency: Below 16M
Size: For PRBs: 136 bytes.
For TIRBs: 136 bytes.
For SIRBs: 200 bytes.
For SVRBs: 240 bytes.
For IRBs: 128 bytes plus the length of optional fields.
Created by: For IRBs: CIRB (Create IRB) macro.
For PRBs: SYSGEN, address space initialization, ATTACH, LINK, SYNCH, and XCTL.
For SIRBs: SYSGEN, address space initialization.
For SVRBs: SVC first level interruption handler.
Pointed to by: TCBRBP field of the TCB data area
CDRRBP field of the CDE data area (associated RB)
EVNTRBP field of the EVNT data area (waiting RB)
PCBRB field of the PCB data area (associated RB)
RBLINK field of the RB data area (previous RB)
TAXEIRB field of the TAXE data area (associated RB)
TIQEIRB field of the TAXE data area (IRB to be scheduled)
Serialization: If the task is running, from the point of view of a program running under that task, the chain is serialized. If the task is not running and the local lock is held, the RB chain will not change. To ensure the task will not be dispatched, the task must be nondispatchable.
Function: Part of the RB is mapped by IHARB and part is mapped by IKJRB.
Maps out the following Request Blocks:
- IRB (Interrupt Request Block), which is not the same as an Interruption Response Block. See the IRB data area description.
- PRB (Program Request Block)
- SIRB (System Interrupt Request Block)
- SVRB (SuperVisor Request Block for SVC routines)
- TIRB (Task Interrupt Request Block)

The RB control block contains information needed by the supervisor concerning programs and routines, including save areas for all general registers, extended registers, a save area for SVC routines, and additional data needed for control.

RB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
-64	(-40)	STRUCTURE	0	RBPRFX	, - RBSECPTR-64
-64	(-40)	DBL WORD	8	(8)	- PREFIX IS SYSTEM DEPENDENT
-64	(-40)	X'40'	0	RBBASIC	"*" - START OF BASIC SECTION OF RB
0	(0)	CHARACTER	8	RBEXRTNM (0)	- EIGHT-CHARACTER NAME OF ERROR EXIT ROUTINE (SIRB)
0	(0)	BITSTRING	1	RBTMFLD	- INDICATORS FOR TIMER ROUTINES. WHEN THERE ARE NO TIMER ROUTINES, THIS FIELD IS ZERO. (IRB)
		1...		RBTMQUE	"BIT0" - TIMER ELEMENT NOT ON QUEUE
		.1..		RBTMTOD	"BIT1" - LOCAL TIME-OF-DAY OPTION IS USED
		..1.		RBRSV005	"BIT2,,C'X'" - RESERVED
		...1		RBWLIM	"BIT3" - WAIT LIMIT EXCEEDED MDC001
		.... 1...		RBTMCMP	"BIT4" - INTERVAL HAS EXPIRED
		.... .1..		RBTMIND2	"BIT5" - EXIT SPECIFIED WITH TASK OR REAL REQUEST
		11		RBTMIND3	"BIT6+BIT7" - TYPE OF REQUEST
		....		RBTREQ	"X'00'" - TASK REQUEST
		1		RBWREQ	"BIT7" - WAIT REQUEST
		11		RBRREQ	"BIT6+BIT7" - REAL REQUEST
1	(1)	BITSTRING	7		- LAST 7 BYTES OF RBEXRTNM
8	(8)	SIGNED	2		- SYSTEM-DEPENDENT FIELD
10	(A)	BITSTRING	2	RBSTAB (0)	- STATUS AND ATTRIBUTE BITS (ALL RB'S)
10	(A)	BITSTRING	2	XSTAB (0)	- SAME AS RBSTAB
10	(A)	BITSTRING	1	RBSTAB1 (0)	- FIRST BYTE OF STATUS AND ATTRIBUTE BITS
10	(A)	BITSTRING	1	XSTAB1	- SAME AS RBSTAB1

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
				Comment	
		BITS 0-4 ARE SYSTEM-DEPENDENT BITS			
				End of Comment	
		.... .1..		RBFTCKPT	"BIT5" - A CHECKPOINT MAY BE TAKEN IN A USER EXIT FROM THIS SVC ROUTINE (SVRB-BOTH)
		.... .1..		XRBCKPT	"BIT5" - SAME AS RBFTCKPT
				Comment	
		BITS 6-7 ARE SYSTEM-DEPENDENT BITS			
				End of Comment	
11	(B)	BITSTRING	1	RBSTAB2 (0)	- SECOND BYTE OF STATUS AND ATTRIBUTE BITS
11	(B)	BITSTRING	1	XSTAB2	- SAME AS RBSTAB2
		1...		RBTCBNXT	"BIT0" - RBLINK FIELD POINTS TO TCB (ALL RB'S)
		1...		XRBTCBP	"BIT0" - SAME AS RBTCBNXT
		.1..		RBFACTV	"BIT1" - IRB OR SIRB IS QUEUED TO TCB - PROGRAM IS ACTIVE
		.1..		XRBACTV	"BIT1" - ACTIVE PROGRAM (ALL RB'S EXCEPT LPRB AND LRB FOR OS/VS1) (MDC300)
				Comment	
		BITS 2-5 ARE SYSTEM-DEPENDENT BITS			
				End of Comment	
		.... ..1.		RBFDYN	"BIT6" - RB STORAGE CAN BE FREED AT EXIT
		.... ..1.		XRBFRRB	"BIT6" - SAME AS RBFDYN
		1		RBECBWT	"BIT7" - IF ZERO, WAIT FOR A SINGLE EVENT OR ALL OF A NUMBER OF EVENTS --- IF ONE, WAIT FOR A NUMBER OF EVENTS THAT IS LESS THAN THE TOTAL NUMBER OF EVENTS WAITING
		1		XRBWAIT	"BIT7" - SAME AS RBECBWT
12	(C)	ADDRESS	4		- SYSTEM-DEPENDENT FIELD
16	(10)	CHARACTER	8	RBOPSW (0)	- USER'S OLD PSW. THIS OFFSET FIXED BY ARCHITECTURE. (ALL RB'S EXCEPT FRB) (MDC306)
16	(10)	CHARACTER	8	XRBPSW (0)	- SAME AS RBOPSW
16	(10)	BITSTRING	1	RBOPSWB1	- OLD PSW BYTE 1 (MDC309)
		.1..		RBOPER	"X'40'" - PER BIT IN RBOPSWB1 (MDC310)
17	(11)	BITSTRING	1	RBOPSWB2	- OLD PSW BYTE 2 (MDC304)
		1		RBOPSWPS	"X'01'" - PROBLEM STATE BIT IN OLD PSW (MDC305)
18	(12)	CHARACTER	1		- OLD PSW BYTE 3
19	(13)	BITSTRING	1	RBOPSWB4	- OLD PSW BYTE 4
		1		RBOPSW64	"X'01'" AMODE 64
20	(14)	ADDRESS	4	RBOPSWA	- OLD PSW BYTES 5-8 (ADDRESS)
		1...		RBOPSWM	"X'80'" - ADDRESSING MODE OF OLD PSW
		1...		RBOPSW31	"X'80'" - ADDRESSING MODE OF OLD PSW
24	(18)	ADDRESS	4		- SYSTEM-DEPENDENT FIELD
28	(1C)	ADDRESS	4	RBLINK (0)	- SAME AS RBLINKB BELOW. THIS OFFSET FIXED BY ARCHITECTURE. (MDC307)
28	(1C)	ADDRESS	4	XRBLNK (0)	- SAME AS RBLINKB BELOW
28	(1C)	SIGNED	1	RBWCF (0)	- NUMBER OF REQUESTS WAITING (WAIT COUNT) (ALL RB'S FOR OS/VS2)
28	(1C)	SIGNED	1	XRBWT	- SAME AS RBWCF (ALL RB'S EXCEPT LPRB AND LRB FOR OS/VS1) (MDC301)
29	(1D)	ADDRESS	3	RBLINKB (0)	- ADDRESS OF PREVIOUS RB, OR ADDRESS OF TCB WHEN THIS IS FIRST RB ON THE QUEUE (ALL RB'S FOR OS/VS2)
29	(1D)	ADDRESS	3	XRBLNKA	- SAME AS RBLINKB (ALL RB'S EXCEPT LPRB AND LRB FOR OS/VS1) (MDC302)
32	(20)	CHARACTER	64	RBGRSAVE (0)	- GENERAL REGISTER SAVE AREA. THIS OFFSET FIXED BY ARCHITECTURE. (SVRB-BOTH, IRB, TIRB FOR OS/VS2) (MDC308)
32	(20)	CHARACTER	64	XRBREG (0)	- SAME AS RBGRSAVE (IRB, SIRB, SVRB FOR OS/VS1)
32	(20)	SIGNED	4	RBGRS0 (0)	- SAVE AREA FOR GENERAL REGISTER 0
32	(20)	SIGNED	4	XRBREG0	- SAME AS RBGRS0
36	(24)	SIGNED	4	RBGRS1 (0)	- SAVE AREA FOR GENERAL REGISTER 1
36	(24)	SIGNED	4	XRBREG1	- SAME AS RBGRS1
40	(28)	SIGNED	4	RBGRS2 (0)	- SAVE AREA FOR GENERAL REGISTER 2
40	(28)	SIGNED	4	XRBREG2	- SAME AS RBGRS2
44	(2C)	SIGNED	4	RBGRS3 (0)	- SAVE AREA FOR GENERAL REGISTER 3
44	(2C)	SIGNED	4	XRBREG3	- SAME AS RBGRS3
48	(30)	SIGNED	4	RBGRS4 (0)	- SAVE AREA FOR GENERAL REGISTER 4
48	(30)	SIGNED	4	XRBREG4	- SAME AS RBGRS4
52	(34)	SIGNED	4	RBGRS5 (0)	- SAVE AREA FOR GENERAL REGISTER 5

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
52	(34)	SIGNED	4	XRBREG5	- SAME AS RBGRS5
56	(38)	SIGNED	4	RBGRS6 (0)	- SAVE AREA FOR GENERAL REGISTER 6
56	(38)	SIGNED	4	XRBREG6	- SAME AS RBGRS6
60	(3C)	SIGNED	4	RBGRS7 (0)	- SAVE AREA FOR GENERAL REGISTER 7
60	(3C)	SIGNED	4	XRBREG7	- SAME AS RBGRS7
64	(40)	SIGNED	4	RBGRS8 (0)	- SAVE AREA FOR GENERAL REGISTER 8
64	(40)	SIGNED	4	XRBREG8	- SAME AS RBGRS8
68	(44)	SIGNED	4	RBGRS9 (0)	- SAVE AREA FOR GENERAL REGISTER 9
68	(44)	SIGNED	4	XRBREG9	- SAME AS RBGRS9
72	(48)	SIGNED	4	RBGRS10 (0)	- SAVE AREA FOR GENERAL REGISTER 10
72	(48)	SIGNED	4	XRBREG10	- SAME AS RBGRS10
76	(4C)	SIGNED	4	RBGRS11 (0)	- SAVE AREA FOR GENERAL REGISTER 11
76	(4C)	SIGNED	4	XRBREG11	- SAME AS RBGRS11
80	(50)	SIGNED	4	RBGRS12 (0)	- SAVE AREA FOR GENERAL REGISTER 12
80	(50)	SIGNED	4	XRBREG12	- SAME AS RBGRS12
84	(54)	SIGNED	4	RBGRS13 (0)	- SAVE AREA FOR GENERAL REGISTER 13
84	(54)	SIGNED	4	XRBREG13	- SAME AS RBGRS13
88	(58)	SIGNED	4	RBGRS14 (0)	- SAVE AREA FOR GENERAL REGISTER 14
88	(58)	SIGNED	4	XRBREG14	- SAME AS RBGRS14
92	(5C)	SIGNED	4	RBGRS15 (0)	- SAVE AREA FOR GENERAL REGISTER 15
92	(5C)	SIGNED	4	XRBREG15	- SAME AS RBGRS15
96	(60)	DBL WORD	8	(0)	
96	(60)	CHARACTER	48	RBEXSAVE (0)	- EXTENDED SAVE AREA FOR SVC ROUTINES (SVRB-BOTH) (OS/VS2)
96	(60)	DBL WORD	8	XRBESA (10)	- SVRB - EXTENDED SAVE AREA OF UP TO TEN DOUBLEWORDS REQUESTED FOR SVC ROUTINE (OS/VS1) (MDC303)

Comment

```
%RBL1 : ;
  START OF SPECIFICATIONS
 01 MACRO NAME: IKJRB
 01 DESCRIPTIVE NAME: OS/VS2 REQUEST BLOCK
 02 ACRONYM: RB
 01 COPYRIGHT =
      LICENSED MATERIALS - PROPERTY OF IBM
      5694-A01 COPYRIGHT IBM CORP. 1977, 2009
 01 STATUS = HBB7760
 01 EXTERNAL CLASSIFICATION:
 02 GUPI: FIELDS
          RBABANA
          RBABANR
          RBABANS
          RBFEPARM
          RBFLAGS2
          RBGRSAVE
          RBNEXAV
          RBOPSW
          RBPPSAV1
 02 PSPI: BASE
 01 END OF EXTERNAL CLASSIFICATION:
 01 DSECT NAME:
      RBPRFX (DSECT card precedes prefix). RBBASIC should be
      used for USING for basic section
 01 COMPONENT: Task Manager (SC1CL)
 01 EYE-CATCHER: NONE
 01 STORAGE ATTRIBUTES:
 02 SUBPOOL:
               For IRBs, subpool 253.
               For PRBs, SVRBs, and SIRBs, subpool 255
 02 KEY: 0
 02 RESIDENCY: Below 16M
 01 SIZE:
          For PRBs: 136 bytes.
          For SIRBs: 200 bytes.
          For SVRBs: 240 bytes.
```

```
                For IRBs: 128 bytes plus the length of optional fields.
  01 CREATED BY:
                For IRBs: CIRB (Create IRB) macro.
                For PRBs: SYSGEN, address space initialization,
                ATTACH, LINK, SYNCH, and XCTL.
                For SIRBs: SYSGEN, address space initialization.
                For SVRBs: SVC first level interruption handler.
  01 POINTED TO BY:
       TCBRBP field of the TCB data area
       CDRRBP field of the CDE data area (associated RB)
       EVNTRBP field of the EVNT data area (waiting RB)
       PCBRB field of the PCB data area (associated RB)
       RBLINK field of the RB data area (previous RB)
       TAXEIRB field of the TAXE data area (associated RB)
       TIQEIRB field of the TAXE data area (IRB to be scheduled)
  01 SERIALIZATION:
      LOCAL lock, active (RB or TCB), non-dispatchable TCB, etc.
  01 FUNCTION = PROVIDE DATA MAPPING OF THE RB.
       SVRB - SUPERVISOR REQUEST BLOCK FOR TRANSIENT SVC ROUTINES
       SVRB - SUPERVISOR REQUEST BLOCK FOR RESIDENT SVC ROUTINES
       IRB - INTERRUPTION REQUEST BLOCK
       SIRB - SYSTEM INTERRUPT REQUEST BLOCK
       PRB - PROGRAM REQUEST BLOCK
       TIRB - TASK INTERRUPTION REQUEST BLOCK
  01 METHOD OF ACCESS
       THIS MACRO IS INVOKED BY IHARB WHICH MAPS THE FIELDS
       THAT ARE COMMON TO OS/VS1 AND OS/VS2.
       IF THIS MACRO IS INVOKED DIRECTLY IN BAL, IT WILL INVOKE
       IHARB TO MAP THE COMMON FIELDS.
       BAL LISTING - SPECIFY LIST=YES ON MACRO CALL.
       PL/AS LISTING - SPECIFY %IHALIST='YES' BEFORE %INCLUDE.
                  VIA ATTRIBUTE - SPECIFY %IHAVIA='YES' BEFORE
                                  %INCLUDE.
                                  DECLARE RBALET.
  01 COMPONENT = SC1CL (TASK MANAGEMENT)
  01 DISTRIBUTION LIBRARY = AMODGEN
   END OF SPECIFICATIONS
 %GOTO RBL2;
```

End of Comment

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
-64	(-40)	DBL WORD	8	(0)	
-64	(-40)	X'0'	0	RBPREFIX	"*" - RBSECPTR-64
-64	(-40)	ADDRESS	4	RBRSV012	- RESERVED
-60	(-3C)	ADDRESS	4	RBRSV013	- RESERVED
-56	(-38)	SIGNED	2	RBRSV014	- RESERVED
-54	(-36)	BITSTRING	1	RBRSV015	- RESERVED
-53	(-35)	BITSTRING	1	RBRSV016	- RESERVED
-52	(-34)	BITSTRING	1	RBRSV017	- RESERVED
-51	(-33)	BITSTRING	1	RBRSV018	- RESERVED
-50	(-32)	BITSTRING	1	RBRSV019	- RESERVED
		1...		RBRSV020	"X'80',,C'X'" - RESERVED
		.1..		RBRSV021	"X'40',,C'X'" - RESERVED
		..1.		RBRSV022	"X'20',,C'X'" - RESERVED
		...1		RBRSV023	"X'10',,C'X'" - RESERVED
		.... 1...		RBRSV024	"X'08',,C'X'" - RESERVED
		.... .1..		RBRSV025	"X'04',,C'X'" - RESERVED
		.... ..1.		RBRSV026	"X'02',,C'X'" - RESERVED
		1		RBRSV027	"X'01',,C'X'" - RESERVED
-49	(-31)	BITSTRING	1	RBRSV028	- RESERVED
		1...		RBRSV029	"X'80',,C'X'" - RESERVED
		.1..		RBRSV030	"X'40',,C'X'" - RESERVED
		..1.		RBRSV031	"X'20',,C'X'" - RESERVED
		...1		RBRSV032	"X'10',,C'X'" - RESERVED
		.... 1...		RBRSV033	"X'08',,C'X'" - RESERVED
		.... .1..		RBRSV034	"X'04',,C'X'" - RESERVED
		.... ..1.		RBRSV035	"X'02',,C'X'" - RESERVED
		1		RBRSV036	"X'01',,C'X'" - RESERVED
-48	(-30)	ADDRESS	4	RBRSV037	- RESERVED
-44	(-2C)	ADDRESS	4	RBRSV038	- RESERVED
-40	(-28)	SIGNED	2	RBRSV039	- RESERVED
-38	(-26)	BITSTRING	1	RBRSV040	- RESERVED
-37	(-25)	BITSTRING	1	RBRSV041	- RESERVED
		1...		RBRSV042	"X'80',,C'X'" - RESERVED
		.1..		RBRSV043	"X'40',,C'X'" - RESERVED

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		..1.		RBRSV044	"X'20',,C'X'" - RESERVED
		...1		RBRSV045	"X'10',,C'X'" - RESERVED
		.... 1...		RBRSV046	"X'08',,C'X'" - RESERVED
		.... .1..		RBRSV047	"X'04',,C'X'" - RESERVED
		.... ..1.		RBRSV048	"X'02',,C'X'" - RESERVED
		1		RBRSV049	"X'01',,C'X'" - RESERVED
-36	(-24)	ADDRESS	4	RBRSV050	- RESERVED
-32	(-20)	DBL WORD	8	RBPRFXST (0)	- START OF ASSIGNED FIELDS IN RB PREFIX
-32	(-20)	ADDRESS	4	RBXSB	- ADDRESS OF EXTENDED STATUS BLOCK (XSB). SERIALIZATION - TCBACTIV. OWNERSHIP - SUPERVISOR. (MDC347)
-28	(-1C)	SIGNED	2	RBRSV052	- RESERVED
-26	(-1A)	BITSTRING	1	RBKEYSTA	- THE KEY AND STATE OF THE IRB ROUTINE SPECIFIED IN RBEPA. NOTE: THIS BYTE IS COPIED INTO RBOPSW BYTE 1 BY STAGE 3
		1111		RBKEY	"X'F0'" THE KEY OF THE IRB ROUTINE
		.... 11..		RBCIRB	"X'0C'" THIS IRB WAS CREATED BY CIRB
		1		RBSTATE	"X'01'" PROBLEM STATE IRB ROUTINE INDICATOR
-25	(-19)	BITSTRING	1	RBFLAGS2	- Second flag byte. This field is an interface only for bits RBABANA, RBABANS, RBABANR
		1...		RBXWAITA	"BIT0" - AMODE OF WAITER WHO HAS ENTERED EXPLICIT WAIT (1 => 31-BIT MODE)
		.1..		RBRSV056	"X'40',,C'X'" - RESERVED
		..1.		RBRSV057	"X'20',,C'X'" - RESERVED
		...1		RBRSV058	"X'10',,C'X'" - RESERVED
		.... 1...		RBRSV059	"X'08',,C'X'" - RESERVED
		.... .1..		RBABANR	"X'04',,C'X'" - This bit is provided for use by abend analysis products. When on, if an ESTAE-type recovery routine retries to this RB, the system will turn off bits RBABANA and RBABANS after doing any abdump processing, so that the bits will be off when the retry occurs. RBABANR is to be set/reset by the abend analysis product. product.
		.... ..1.		RBABANA	"X'02',,C'X'" - This bit is provided for use by abend analysis products. Abend analysis is currently active for this RB. RBABANA is to be set/reset by the abend analysis product.
		1		RBABANS	"X'01',,C'X'" - This bit is provided for use by abend analysis products. Initial abend analysis has been started for this RB RBABANS is to be set/reset by the abend analysis product
-24	(-18)	DBL WORD	8	(0)	
-24	(-18)	CHARACTER	16	RBRTOPSW (0)	- PROGRAM STATUS INFORMATION STORED AT TIME OF INTERRUPT CAUSING ENTRY INTO THE RTM MDC013
-24	(-18)	CHARACTER	8	RBRTPSW1	- FIRST DOUBLE WORD OF PSW - SYSTEM AND PROGRAM MASKS, KEY CONDITION CODE AND INSTRUCTION COUNTER MDC014
-16	(-10)	CHARACTER	8	RBRTPSW2 (0)	- SECOND DOUBLE WORD OF PSW MDC015
-16	(-10)	CHARACTER	4	RBRTICIL (0)	- ILC AND INTERRUPT CODE MDC016
-16	(-10)	BITSTRING	1	RBRSV160	- RESERVED - SET TO ZERO IN LOW CORE BY HARDWARE MDC017
-15	(-F)	SIGNED	1	RBRTILC	- INSTRUCTION LENGTH COUNTER - NUMBER OF BYTES IN INSTRUCTION CAUSING INTERRUPT MDC018
-14	(-E)	SIGNED	2	RBRTINCD	- INTERRUPT CODE MDC019
-12	(-C)	ADDRESS	4	RBRTRAN	- VIRTUAL ADDRESS CAUSING TRANSLATION EXCEPTION IF PROGRAM INTERRUPT 16, 17 OR 18. OTHERWISE, NOT USED. MDC020
-8	(-8)	BITSTRING	1	RBFLAGS1	- FLAG BYTE
		1...		RBSLOCK	"BIT0" - INDICATES THAT THIS RB IS NONDISPATCHABLE UNTIL THE SUPERVISOR LOCK (CVTSYLK) IS RESET (ALL RB'S)
		.1..		RBXWAIT	"BIT1" - INDICATES THAT THE PROGRAM OPERATING UNDER THIS RB HAS ISSUED AN EXPLICIT (SVC) WAIT (ALL RB'S)
		..1.		RBABEND	"BIT2" - ABEND SVRB (SVRB-BOTH)
		...1		RBXWPRM	"BIT3" - WAIT POST RESOURCE MANAGER REQUEST (MDC341)
		.... 1...		RBASIR	"BIT4" - ASIR IS RUNNING UNDER THIS RB ICB444
		.... .1..		RBLONGWT	"BIT5" - LONG WAIT ISSUED UNDER THIS RB MDC009
		.... ..1.		RBSCB	"BIT6" - SET BY SVC 60 TO INDICATE RB HAS AN ASSOCIATED ESTAE OR STAE EXIT MDC004
		1		RBSSSYN	"BIT7" - SYNCHRONIZED STATUS STOP PENDING FOR THIS RB MDC011
-7	(-7)	BITSTRING	1	RBFLAGS3	- FLAG BYTE. SERIALIZATION:LOCAL LOCK
		1...		RBWTECB	"X'80'" - WAIT WAS ISSUED WITH AN ECB PROVIDED
-6	(-6)	SIGNED	2	RBXWAITI	- EXPLICIT WAIT INDEX (MDC342)
-4	(-4)	SIGNED	4	RBWLIC (0)	- FULLWORD LABEL TO BE USED AS THE KEYFIELD NAME TO REPRESENT THE FIELDS WITHIN THIS WORD.
-4	(-4)	SIGNED	1	RBWCSA	- NUMBER OF REQUESTS WAITING AT TIME OF TERMINATION (WAIT COUNT SAVE AREA) (ALL RB'S)
-3	(-3)	CHARACTER	3	RBINTCDA (0)	- INTERRUPT CODE (ALL RB'S)
-3	(-3)	CHARACTER	1	RBINLNTH	- INSTRUCTION LENGTH CODE - 4 HIGH-ORDER BITS MUST BE ZERO. THIS OFFSET FIXED BY ARCHITECTURE. (ALL RB'S) (MDC343)
-2	(-2)	CHARACTER	2	RBINTCOD	- INTERRUPT CODE. THIS OFFSET FIXED BY ARCHITECTURE. (ALL RB'S) (MDC344)
0	(0)	CHARACTER	1	RBPRFXND (0)	- END OF RB PREFIX
0	(0)	DBL WORD	8	(0)	-

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	X'40'	0	RBSECT	"*" - RBSECPTR - THIS IS THE START OF THE BASIC SECTION OF THE RB
0	(0)	ADDRESS	4	RBPPSAV (0)	- ADDRESS OF PROBLEM PROGRAM REGISTER SAVE AREA (IRB)
0	(0)	BITSTRING	1		- RBTMFLD
1	(1)	ADDRESS	3	RBPPSAV1	- ADDRESS OF PROBLEM PROGRAM REGISTER SAVE AREA (IRB)
4	(4)	CHARACTER	4	RBABOPSW	- AFTER EXECUTION OF TRANSIENT AREA HANDLER ROUTINE - FOUR LOW-ORDER BYTES OF NAME OF REQUESTED ROUTINE (SVRB-TRANS) MDC012
8	(8)	SIGNED	2	RBSIZE	- SIZE OF THIS RB IN DOUBLEWORDS (ALL RB'S)
10	(A)	BITSTRING	2	(0)	- RBSTAB
10	(A)	BITSTRING	1		- RBSTAB1
		111.		RBFTP	"BIT0+BIT1+BIT2" TYPE OF RB
		....		RBFTPRB	"X'00'" - PRB
		.11.		RBFTTIRB	"BIT1+BIT2" - TIRB ICB417
		.1..		RBFTIRB	"BIT1" - IRB
		1...		RBFTSIRB	"BIT0" - SIRB
		11..		RBFTSVRB	"BIT0+BIT1" - SVRB
		...1		RBTRSVRB	"BIT3" - IF RBTRSVRB=0 AND RBCDE=0, THEN TYPE 2 SVC IN NUCLEUS. IF RBTRSVRB=0 AND RBCDE1 NOT 0, THEN SECOND OR SUBSEQUENT LOAD OF TYPE 4 SVC IN FIXED OR MODIFIED LPA (RBCDE1 = ADDRESS OF CDE). IF RBTRSVRB=1 AND RBCDE1=0, THEN TYPE 3 OR FIRST LOAD OF TYPE 4 SVC IN PAGED, FIXED OR MODIFIED LPA. IF RBTRSVRB=1 AND RBCDE1 NOT 0, THEN SECOND OR SUBSEQUENT LOAD OF TYPE 4 SVC IN PAGED LPA (RBCDE1 = ADDRESS OF LPDE).
		...1		RBFNSVRB	"BIT3" - ALIAS FOR RBTRSVRB
		.... 1...		RBWAITP	"BIT4" - INDICATES THAT AN ECB IS POINTING AT THE RB. ICB416
				Comment	
				RBFTCKPT EQU BIT5 - SEE COMMON SECTION	
				End of Comment	
		.... ..1.		RBATNXIT	"BIT6" - THIS IRB IS AN ATTENTION IRB ICB444
		1		RBPMSVRB	"BIT7" - THIS IS A PROGRAM MANAGER SVRB - VALID ONLY ON LINK, LOAD, XCTL OR ATTACH (MDC305)
11	(B)	BITSTRING	1		- RBSTAB2
				Comment	
				RBTCBNXT EQU BIT0 - SEE COMMON SECTION RBFACTV EQU BIT1 - SEE COMMON SECTION	
				End of Comment	
		..1.		RBATTN	"BIT2" - EXITING PROGRAM IS AN ATTENTION EXIT (IRB)
		...1		RBETXR	"BIT3" - IRB IS FOR AN ETXR EXIT ROUTINE
		...1		RBUSIQE	"BIT3" - SAME AS RBETXR ICB444
		.... 11..		RBIQETP	"BIT4+BIT5" -
		....		RBRQENR	"X'00'" - REQUEST QUEUE ELEMENT IS NOT TO BE RETURNED
		.... .1..		RBIRBAER	"BIT5" - IRB HAS QUEUE ELEMENTS FOR ASYNCHRONOUSLY EXECUTED ROUTINES THAT ARE RQE'S
		.... 1...		RBIQENR	"BIT4" - IQE IS NOT TO BE RETURNED AT EXIT
		.... 11..		RBIRBAIQ	"BIT4+BIT5" - IRB HAS QUEUE ELEMENTS FOR ASYNCHRONOUSLY EXECUTED ROUTINES THAT ARE IQE'S
				Comment	
				RBFDYN EQU BIT6 - SEE COMMON SECTION RBECBWT EQU BIT7 - SEE COMMON SECTION	
				End of Comment	
12	(C)	ADDRESS	4	RBEP (0)	- ENTRY POINT ADDRESS OF ASYNCHRONOUSLY EXECUTED ROUTINE (IRB, SIRB)
		1...		RBEPM	"X'80'" - ADDRESSING MODE OF ROUTINE, IF RBEPPD IS ON
12	(C)	ADDRESS	4	RBEPA (0)	- SAME AS RBEP
12	(C)	BITSTRING	3		- FIRST 3 BYTES OF EP ADDRESS
15	(F)	BITSTRING	1	RBEPLBYT	BITS 0-6 = BITS 24-30 OF EP ADDR BIT 7 = EP ADDR BIT 31 = FLAG
		1		RBEPPD	"X'01'" - BIT 31 INDICATES RBEP IS POINTER-DEFINED (BIT 0 IS AMODE)
16	(10)	CHARACTER	8		- RBOPSW
24	(18)	ADDRESS	4	RBPGMQ (0)	- SAME AS RBPGMQ1 BELOW
24	(18)	BITSTRING	1		- ZERO
25	(19)	ADDRESS	3	RBPGMQ1	- ADDRESS OF RB INDICATING A REQUEST TO USE SAME SERIALLY REUSABLE PROGRAM (SVRB-RES, PRB)
28	(1C)	ADDRESS	4	(0)	- RBLINK
28	(1C)	SIGNED	1	(0)	- RBWCF

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
28	(1C)	SIGNED	1	RBSCF	- RB SUSPENDED COUNT (MDC339)
29	(1D)	ADDRESS	3		- RBLINKB
32	(20)	CHARACTER	64		- RBGRSAVE
96	(60)	SIGNED	4	IRBEND (0)	- END OF IRB UNLESS OPTIONAL FIELDS RBNEXAV AND RBIQEWRK ARE PRESENT
96	(60)	CHARACTER	48	(0)	- RBEXSAVE
96	(60)	ADDRESS	4	RBRSV135	- RESERVED
100	(64)	SIGNED	2	RBRSV136	- RESERVED
102	(66)	BITSTRING	1	RBRSV137	- RESERVED
103	(67)	BITSTRING	1	RBRSV138	- RESERVED
		1...		RBRSV139	"X'80',,C'X'" - RESERVED
		.1..		RBRSV140	"X'40',,C'X'" - RESERVED
		..1.		RBRSV141	"X'20',,C'X'" - RESERVED
		...1		RBRSV142	"X'10',,C'X'" - RESERVED
		.... 1...		RBRSV143	"X'08',,C'X'" - RESERVED
		.... .1..		RBRSV144	"X'04',,C'X'" - RESERVED
		.... ..1.		RBRSV145	"X'02',,C'X'" - RESERVED
		1		RBRSV146	"X'01',,C'X'" - RESERVED
104	(68)	SIGNED	4	PRBEND (0)	- END OF PRB
104	(68)	SIGNED	4	TIRBEND (0)	- END OF TIRB
104	(68)	CHARACTER	40		- LAST 40 BYTES OF RBEXSAVE
144	(90)	CHARACTER	24	RBSCBB (0)	- AREA CONTAINING STAE CONTROL BLOCK (SCB) (SVRB ONLY) (MDC347)
144	(90)	ADDRESS	4	RBSCHAIN	- POINTER TO NEXT SCB ON CHAIN (MDC306)
148	(94)	ADDRESS	4	RBSEXIT	- POINTER TO USER WRITTEN EXIT ROUTINE (MDC307)
152	(98)	ADDRESS	4	RBSPARM (0)	- ADDRESS OF PARAMETER LIST FOR STA EXIT (MDC308)
152	(98)	BITSTRING	1	RBSFLGS1	- FIRST FLAG BYTE (MDC309)
		1...		RBSSTAI	"BIT0" - STAI SCB (MDC310)
		.1..		RBSSTAR	"BIT1" - STAR SCB. SCB IF FOR STAE IF NEITHER RBSSTAI NOR RBSSTAR BIT IS SET ON. (MDC311)
		..1.		RBSDUMMY	"BIT2" - DUMMY SCB (WILL NOT BE SCHEDULED) (MDC312)
		...1		RBSESTAE	"BIT3" - ESTAE INDICATOR (MDC313)
		.... 1...		RBRSV162	"BIT4" - RESERVED
		.... .1..		RBSASYNC	"BIT5" - ALLOW ASYNCHRONOUS INTERRUPTS (MDC314)
		11		RBSIOPRC	"BIT6+BIT7" - I/O PROCESSING OPTION. BOTH BITS OFF MEANS QUIESCE I/O. BOTH BITS ON IS NOT DEFINED. (MDC315)
		.... ..1.		RBSNOIOP	"BIT6" - BYPASS I/O INTERVENTION (MDC316)
		1		RBSHALT	"BIT7" - HALT I/O (MDC317)
153	(99)	ADDRESS	3	RBSPARMA	- ADDRESS OF PARAMETER LIST FOR STA EXIT (MDC318)
156	(9C)	ADDRESS	4	RBSOWNR (0)	- TCB/RB ADDRESS CONTROLLING THIS SCB (MDC319)
156	(9C)	BITSTRING	1	RBSFLGS2	- SECOND FLAG BYTE (MDC320)
		1...		RBSAMODE	"BIT0" - USER IS IN 31 BIT ADDRESSING MODE
		.1..		RBSXCTL2	"BIT1" - RETAIN THIS SCB ACROSS XCTL (MDC321)
		..1.		RBRSV164	"BIT2" - RESERVED
		...1		RBSINUSE	"BIT3" - THIS SCB IN USE (MDC322)
		.... 1...		RBRSV165	"BIT4" - RESERVED
		.... .1..		RBRSV166	"BIT5" - RESERVED
		.... ..1.		RBSKEY0	"BIT6" - USER IN KEY 0 (MDC323)
		1		RBSSUPER	"BIT7" - USER IN SUPERVISOR MODE (MDC324)
157	(9D)	ADDRESS	3	RBSOWNRA	- RB ADDRESS IF STAE/STAR, TCB ADDRESS IF STAI (MDC325)
160	(A0)	SIGNED	4	RBSDATA (0)	- FLAGS AND DATA FIELD (MDC326)
160	(A0)	BITSTRING	1	RBSFLG3	- OPTION FLAGS (MDC327)
		1...		RBRSV167	"BIT0" - RESERVED
		.1..		RBSTERMI	"BIT1" - AUTHORIZED FOR TERM PROCESSING (MDC328)
		..1.		RBSRECRD	"BIT2" - ERROR RECORD TO BE WRITTEN TO THE LOGREC DATA SET (MDC329)
		...1		RBSCNCEL	"BIT3" - SCB IS LOGICALLY CANCELED (MDC330)
		.... 1...		RBSPRNTR	"BIT4" - SCB IS PREVIOUSLY ENTERED (MDC331)
		.... .1..		RBSBRNTR	"BIT5" - BRANCH ENTERED SVC 60 (MDC332)
		.... ..1.		RBSTERMO	"BIT6" - TERM PROCESSING ONLY (MDC333)
		1		RBRSV168	"BIT7" - RESERVED
161	(A1)	CHARACTER	1	RBSPKEY	- PROGRAM KEY (MDC334)
162	(A2)	CHARACTER	1	RBSID	- SCB IDENTIFIER (MDC335)
163	(A3)	BITSTRING	1	RBRSV169	- RESERVED (MDC336)
164	(A4)	ADDRESS	4	RBSXPTR	- POINTER TO SCB EXTENSION (SCBX) (MDC347)
168	(A8)	SIGNED	4	SIRBEND (0)	- END OF SIRB MDC021-MDC022
168	(A8)	SIGNED	4	RBFEPARM (6)	- PARAMETER AREA FOR ROUTINES THAT USE FESTAE AND DEFAULT TO USE THIS AREA (I.E., DO NOT CODE PARAM=) (MDC337)
192	(C0)	CHARACTER	16	RBSCBX (0)	- AREA CONTAINING STAE CONTROL BLOCK EXTENSION(SCBX)(SVRB ONLY)
192	(C0)	CHARACTER	12		- FIRST 12 BYTES OF RBSCBX
204	(CC)	ADDRESS	4	RBSXPARM	- 31-BIT PARAMETER LIST ADDRESS
208	(D0)	SIGNED	4	SVRBEND (0)	- END OF SVRB (BOTH) (MDC338)
12	(C)	ADDRESS	4	RBCDE (0)	- SAME AS RBCDE1 BELOW

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
12	(C)	BITSTRING	1	RBCDFLGS	- CONTROL FLAGS
		1...		RBNOCELL	"BIT0" - EXIT SHOULD FREEMAIN THIS SVRB RATHER THAN FREECELL MDC008
		.1..		RBRSV009	"BIT1,,C'X'" - RESERVED
		..1.		RBCDATCH	"BIT2" - CONTENTS SUPERVISION HAS BEEN ENTERED VIA ATTACH ICB444
		...1		RBCDSAVE	"BIT3" - EXIT WILL LOAD REGISTERS FROM PRB ON RETURN FROM SYNCH TO ROUTINE (MDC345)
		.... 1...		RBCDNODE	"BIT4" - NO DE SAVE AREA REQUIRED ICB444
		.... .1..		RBCDSYNC	"BIT5" - SYNCH MACRO INSTRUCTION REQUESTED
		.... ..1.		RBCDXCTL	"BIT6" - XCTL MACRO INSTRUCTION REQUESTED
		1		RBCDLOAD	"BIT7" - LOAD MACRO INSTRUCTION REQUESTED
13	(D)	ADDRESS	3	RBCDE1	- ADDRESS OF CDE, ADDRESS OF LPDE OR ZERO (SEE COMMENTS FOR BIT RBTRSVRB)
24	(18)	ADDRESS	4	RBSQE (0)	- SAME AS RBSQEA BELOW
24	(18)	SIGNED	1		- RBUSE - CONTAINS ZEROS
25	(19)	ADDRESS	3	RBSQEA	- CHAIN OF SUPERVISOR QUEUE ELEMENTS (SQE'S) WHICH REPRESENT ASYNCHRONOUS SUPERVISOR SERVICE REQUESTS RELATED TO TCB UNDER WHICH TIRB IS PRESENTLY OPERATING (TIRB)
24	(18)	ADDRESS	4	RBIQE (0)	- LIST ORIGIN FOR IQE (IRB)
24	(18)	SIGNED	1	RBUSE	- USE COUNT USED BY ATTACH (IRB)
25	(19)	ADDRESS	3	RBIQE1	- LIST ORIGIN FOR IQE (IRB)
24	(18)	SIGNED	4	RBIQE2 (0)	-
24	(18)	SIGNED	4	RBIQEA	- LIST ORIGIN FOR RQE (IRB WITH 4-BYTE LINK FIELD SEGMENT, SIRB) MDC006
96	(60)	ADDRESS	4	RBNEXAV	- ADDRESS OF NEXT AVAILABLE IQE (IRB)
100	(64)	SIGNED	4	RBIQEWRK	- IQE WORK SPACE, VARIABLE LENGTH, MAXIMUM SIZE IS 2036 BYTES (IRB)
96	(60)	CHARACTER	64	RBSIRBWA	- SIRB WORK AREA MDC022
160	(A0)	ADDRESS	4	RBRSV161	- RESERVED - RBRSV148 FOLLOWS THIS FIELD
160	(A0)	X'40'	0	SIRBWALN	"64" - LENGTH OF RBSIRBWA MDC023
160	(A0)	X'40'	0	RBPRFXLN	"RBPRFXND-RBPREFIX" - TOTAL PREFIX LENGTH INCLUDING AREA RESERVED FOR FUTURE EXPANSION
160	(A0)	X'20'	0	RBPRFLNA	"RBPRFXND-RBPRFXST" - ASSIGNED PREFIX LENGTH
160	(A0)	X'88'	0	PRBLEN	"PRBEND-RBPRFXST" - REAL PRB LENGTH FOR GETMAIN
160	(A0)	X'C8'	0	SIRBLEN	"SIRBEND-RBPRFXST" - REAL SIRB LENGTH FOR GETMAIN
160	(A0)	X'88'	0	TIRBLEN	"TIRBEND-RBPRFXST" - REAL TIRB LENGTH FOR GETMAIN
160	(A0)	X'80'	0	IRBLEN	"IRBEND-RBPRFXST" - REAL IRB LENGTH FOR GETMAIN UNLESS OPTIONAL FIELDS ARE ALSO PRESENT
160	(A0)	X'F0'	0	SVRBLEN	"SVRBEND-RBPRFXST" - REAL SVRB LENGTH FOR GETMAIN

RB Cross Reference

Name	Hex Offset	Hex Value
IRBEND	60	
IRBLEN	A0	80
PRBEND	68	
PRBLEN	A0	88
RBABANA	-19	2
RBABANR	-19	4
RBABANS	-19	1
RBABEND	-8	20
RBABOPSW	4	
RBASIR	-8	8
RBATNXIT	A	2
RBATTN	B	20
RBBASIC	-40	40
RBCDATCH	C	20
RBCDE	C	
RBCDE1	D	
RBCDFLGS	C	
RBCDLOAD	C	1
RBCDNODE	C	8
RBCDSAVE	C	10
RBCDSYNC	C	4
RBCDXCTL	C	2
RBCIRB	-1A	C
RBECBWT	B	1
RBEP	C	
RBEPA	C	
RBEPLBYT	F	
RBEPM	C	80
RBEPPD	F	1
RBETXR	B	10
RBEXRTNM	0	
RBEXSAVE	60	
RBFACTV	B	40
RBFDYN	B	2
RBFEPARM	A8	
RBFLAGS1	-8	
RBFLAGS2	-19	
RBFLAGS3	-7	
RBFNSVRB	A	10
RBFTCKPT	A	4
RBFTIRB	A	40
RBFTP	A	E0
RBFTPRB	A	0
RBFTSIRB	A	80
RBFTSVRB	A	C0
RBFTTIRB	A	60
RBGRSAVE	20	
RBGRS0	20	
RBGRS1	24	
RBGRS10	48	
RBGRS11	4C	
RBGRS12	50	
RBGRS13	54	
RBGRS14	58	
RBGRS15	5C	
RBGRS2	28	

Name	Hex Offset	Hex Value
RBGRS3	2C	
RBGRS4	30	
RBGRS5	34	
RBGRS6	38	
RBGRS7	3C	
RBGRS8	40	
RBGRS9	44	
RBINLNTH	-3	
RBINTCDA	-3	
RBINTCOD	-2	
RBIQE	18	
RBIQEA	18	
RBIQENR	B	8
RBIQETP	B	C
RBIQEWRK	64	
RBIQE1	19	
RBIQE2	18	
RBIRBAER	B	4
RBIRBAIQ	B	C
RBKEY	-1A	F0
RBKEYSTA	-1A	
RBLINK	1C	
RBLINKB	1D	
RBLONGWT	-8	4
RBNEXAV	60	
RBNOCELL	C	80
RBOPER	10	40
RBOPSW	10	
RBOPSWA	14	
RBOPSWB1	10	
RBOPSWB2	11	
RBOPSWB4	13	
RBOPSWM	14	80
RBOPSWPS	11	1
RBOPSW31	14	80
RBOPSW64	13	1
RBPGMQ	18	
RBPGMQ1	19	
RBPMSVRB	A	1
RBPPSAV	0	
RBPPSAV1	1	
RBPREFIX	-40	0
RBPRFLNA	A0	20
RBPRFX	-40	
RBPRFXLN	A0	40
RBPRFXND	0	
RBPRFXST	-20	
RBRQENR	B	0
RBRREQ	0	3
RBRSV005	0	20
RBRSV009	C	40
RBRSV012	-40	
RBRSV013	-3C	
RBRSV014	-38	
RBRSV015	-36	
RBRSV016	-35	
RBRSV017	-34	
RBRSV018	-33	
RBRSV019	-32	
RBRSV020	-32	80
RBRSV021	-32	40
RBRSV022	-32	20
RBRSV023	-32	10
RBRSV024	-32	8
RBRSV025	-32	4
RBRSV026	-32	2
RBRSV027	-32	1
RBRSV028	-31	
RBRSV029	-31	80
RBRSV030	-31	40
RBRSV031	-31	20
RBRSV032	-31	10
RBRSV033	-31	8
RBRSV034	-31	4
RBRSV035	-31	2
RBRSV036	-31	1
RBRSV037	-30	
RBRSV038	-2C	
RBRSV039	-28	
RBRSV040	-26	
RBRSV041	-25	
RBRSV042	-25	80
RBRSV043	-25	40
RBRSV044	-25	20
RBRSV045	-25	10
RBRSV046	-25	8
RBRSV047	-25	4
RBRSV048	-25	2
RBRSV049	-25	1
RBRSV050	-24	
RBRSV052	-1C	
RBRSV056	-19	40
RBRSV057	-19	20
RBRSV058	-19	10
RBRSV059	-19	8
RBRSV135	60	
RBRSV136	64	
RBRSV137	66	
RBRSV138	67	
RBRSV139	67	80
RBRSV140	67	40
RBRSV141	67	20
RBRSV142	67	10
RBRSV143	67	8
RBRSV144	67	4
RBRSV145	67	2
RBRSV146	67	1
RBRSV160	-10	
RBRSV161	A0	
RBRSV162	98	8
RBRSV164	9C	20
RBRSV165	9C	8
RBRSV166	9C	4
RBRSV167	A0	80
RBRSV168	A0	1
RBRSV169	A3	
RBRTICIL	-10	
RBRTILC	-F	
RBRTINCD	-E	
RBRTOPSW	-18	
RBRTPSW1	-18	
RBRTPSW2	-10	
RBRTRAN	-C	
RBSAMODE	9C	80
RBSASYNC	98	4
RBSBRNTR	A0	4
RBSCB	-8	2
RBSCBB	90	
RBSCBX	C0	
RBSCF	1C	
RBSCHAIN	90	
RBSCNCEL	A0	10
RBSDATA	A0	
RBSDUMMY	98	20
RBSECT	0	40
RBSESTAE	98	10
RBSEXIT	94	
RBSFLGS1	98	
RBSFLGS2	9C	
RBSFLG3	A0	
RBSHALT	98	1
RBSID	A2	
RBSINUSE	9C	10
RBSIOPRC	98	3
RBSIRBWA	60	
RBSIZE	8	
RBSKEY0	9C	2
RBSLOCK	-8	80

Name	Hex Offset	Hex Value
RBSNOIOP	98	2
RBSOWNR	9C	
RBSOWNRA	9D	
RBSPARM	98	
RBSPARMA	99	
RBSPKEY	A1	
RBSPRNTR	A0	8
RBSQE	18	
RBSQEA	19	
RBSRECRD	A0	20
RBSSSYN	-8	1
RBSSTAI	98	80
RBSSTAR	98	40
RBSSUPER	9C	1
RBSTAB	A	
RBSTAB1	A	
RBSTAB2	B	
RBSTATE	-1A	1
RBSTERMI	A0	40
RBSTERMO	A0	2
RBSXCTL2	9C	40
RBSXPARM	CC	
RBSXPTR	A4	
RBTCBNXT	B	80
RBTMCMP	0	8
RBTMFLD	0	
RBTMIND2	0	4
RBTMIND3	0	3
RBTMQUE	0	80
RBTMTOD	0	40
RBTREQ	0	0
RBTRSVRB	A	10
RBUSE	18	
RBUSIQE	B	10
RBWAITP	A	8
RBWCF	1C	
RBWCSA	-4	
RBWLIC	-4	
RBWLIM	0	10
RBWREQ	0	1
RBWTECB	-7	80
RBXSB	-20	
RBXWAIT	-8	40
RBXWAITA	-19	80
RBXWAITI	-6	
RBXWPRM	-8	10
SIRBEND	A8	
SIRBLEN	A0	C8
SIRBWALN	A0	40
SVRBEND	D0	
SVRBLEN	A0	F0
TIRBEND	68	
TIRBLEN	A0	88
XRBACTV	B	40
XRBCKPT	A	4
XRBESA	60	
XRBFRRB	B	2
XRBLNK	1C	
XRBLNKA	1D	
XRBPSW	10	
XRBREG	20	
XRBREG0	20	
XRBREG1	24	
XRBREG10	48	
XRBREG11	4C	
XRBREG12	50	
XRBREG13	54	
XRBREG14	58	
XRBREG15	5C	
XRBREG2	28	
XRBREG3	2C	
XRBREG4	30	
XRBREG5	34	
XRBREG6	38	
XRBREG7	3C	
XRBREG8	40	
XRBREG9	44	
XRBTCBP	B	80
XRBWAIT	B	1
XRBWT	1C	
XSTAB	A	
XSTAB1	A	
XSTAB2	B	

RBCB Information

RBCB Heading Information

Common Name: Recovery Termination Management Recording Buffers Control Block
Macro ID: RTMRBCB
DSECT Name: None
Owning Component: Recovery Termination Manager (SCRTM)
Storage Attributes: Subpool: 239
Key: 0
Size: 100 bytes
Created by: IEAVNPA6 at NIP time.
Pointed to by: CVTRBCB field of the CVT
Serialization: Individual fields serialized by CS instructions.
Function: The RTMRBCB maps the central control block of the Recording Facility.

RBCB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	100	RBCB	
0	(0)	CHARACTER	4	RBCBRBCB	Acronym field of RBCB
4	(4)	SIGNED	4	RBCBECB	Recording ECB WAITed on by record task, POSTed by record request routine
8	(8)	CHARACTER	4	RBCBFLGS	Information flags.
8	(8)	BITSTRING	1	RBCBFLG1	First flag byte.
		1...		RBCBRPER	Recording permanent error
		.1..		RBCBSIU	On/SRB in use, off/SRB not in use.
		..11 1111		*	Reserved
9	(9)	CHARACTER	3	*	Reserved
12	(C)	SIGNED	4	RBCBLCNT	Count of lost records
16	(10)	CHARACTER	16	RBCBBDAT	Buffer ptrs and lengths
16	(10)	ADDRESS	4	RBCBLRCB	Address of LOGREC buffer
20	(14)	SIGNED	4	RBCBLLEN	Length of LOGREC buffer
24	(18)	ADDRESS	4	RBCBWRCB	Address of WTO buffer
28	(1C)	SIGNED	4	RBCBWLEN	Length of WTO buffer
32	(20)	CHARACTER	24	RBCBPDAT	Maximum and current allocation of buffer partitions
32	(20)	SIGNED	4	RBCBHMAX	Hardware maximum
36	(24)	SIGNED	4	RBCBHSIZ	Hardware current
40	(28)	SIGNED	4	RBCBCMAX	SYMREC maximum
44	(2C)	SIGNED	4	RBCBCSIZ	SYMREC current
48	(30)	SIGNED	4	RBCBSMAX	Software maximum
52	(34)	SIGNED	4	RBCBSSIZ	Software current
56	(38)	CHARACTER	44	RBCBSRB	SRB used to POST the Recording task

RBCB Cross Reference

Name	Hex Offset	Hex Value
RBCB	0	
RBCBBDAT	10	
RBCBCMAX	28	
RBCBCSIZ	2C	
RBCBECB	4	
RBCBFLGS	8	
RBCBFLG1	8	
RBCBHMAX	20	
RBCBHSIZ	24	
RBCBLCNT	C	
RBCBLLEN	14	
RBCBLRCB	10	
RBCBPDAT	20	
RBCBRBCB	0	
RBCBRPER	8	80
RBCBSIU	8	40
RBCBSMAX	30	
RBCBSRB	38	
RBCBSSIZ	34	
RBCBWLEN	1C	
RBCBWRCB	18	

RCB Information

RCB Heading Information

Common Name: RTM Recording Control Buffer
Macro ID: RTMRCB
DSECT Name: RCB
Owning Component: Recovery Termination Manager (SCRTM)
Storage Attributes: Subpool: 239
Key: 0
Size: 96 bytes
Created by: IEAVNPA6 at NIP time.
Pointed to by: RBCBLRCB field of the RBCB (LOGREC buffer)
RBCBWRCB field of the RBCB (WTO buffer)
Serialization: Individual fields serialized by CS instructions.
Function: The first 96 bytes is the Control portion of the buffer which maintains the current status of buffer usage. The remaining area is the actual buffer space.

RCB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	RCB	Record Control Block
0	(0)	CHARACTER	96	RCBCNTL	Control portion of buffer
0	(0)	CHARACTER	4	RCBRCB	Acronym field for RCB
4	(4)	ADDRESS	4	RCBBUFB	Beginning of buffer area
8	(8)	ADDRESS	4	RCBBUFE	End of buffer area
12	(C)	SIGNED	4	RCBTLNG	Total buffer length
16	(10)	CHARACTER	8	RCBCDS	Must be double word origin for CDS instruction.
16	(10)	ADDRESS	4	RCBFREE	Next free area in buffer
20	(14)	SIGNED	4	RCBFLNG	Length of free area
24	(18)	SIGNED	4	RCBACNT	Active count
28	(1C)	BITSTRING	4	RCBFLGS	Word of flags
28	(1C)	CHARACTER	1	RCBBFLG	Buffer flags
		1...		RCBRTER	Temporary error
		.1..		RCBRERT	IEAVTRER temp error
		..11 1111		*	Reserved
29	(1D)	CHARACTER	3	*	Reserved
32	(20)	CHARACTER	64	*	Reserved
96	(60)	CHARACTER	*	RCBBUFRS	Buffer area for records

RCB Cross Reference

Name	Hex Offset	Hex Value
RCB	0	
RCBACNT	18	
RCBBFLG	1C	
RCBBUFB	4	
RCBBUFE	8	
RCBBUFRS	60	
RCBCDS	10	
RCBCNTL	0	
RCBFLGS	1C	
RCBFLNG	14	
RCBFREE	10	
RCBRCB	0	
RCBRERT	1C	40
RCBRTER	1C	80
RCBTLNG	C	

RCBE Information

RCBE Heading Information

Common Name: RTM Record Control Buffer Entry
Macro ID: RTMRCBE
DSECT Name: RCBENTRY
Owning Component: Recovery Termination Manager (SCRTM)
Eye-Catcher ID: None
Storage Attributes: Subpool: 239 and 250
Key: 0
Size: Variable
Created by: IEAVTRER when a request for recording is made via the internal RECORD macro.
Pointed to by: Indirectly via control information in the RTMRCB.
Serialization: Compare and Swap on fields in the RCB.
Function: The RTMRCBE maps each entry in the RTMRCB buffer. It is built largely by IEAVTRER and contains information that is to be recorded as requested via the internal RECORD macro. IEAVTRET copies this information to a private buffer before writing it to LOGREC or via a WTO request. This mapping is also used by IEAVTREM at memory termination to determine if there are any incomplete entries that must be freed.

RCBE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	RCBENTRY	Map an entry in RTMRCB
0	(0)	CHARACTER	16	RCBECNTL	Header information
0	(0)	CHARACTER	8	RCBECTL1	First double word of header
0	(0)	UNSIGNED	2	RCBELEN	Entry length of data to be written. This does not include the errorid length for nonbuffered entries or the length of the timestamp for WTO entries
2	(2)	BITSTRING	2	RCBEFLGS	Flags describing this entry
2	(2)	BITSTRING	1	RCBEFLG1	First byte of flags
		1...		RCBELREC	On, LOGREC destined entry
		.1..		RCBEWTO	On, WTO type entry
		..11 1111		*	(reserved)
3	(3)	BITSTRING	1	RCBEFLG2	Second byte of flags
		1...		RCBECOPY	On, entry was copied from sqa rcb to work buffer
		.1..		RCBEWRAP	On, entry wraps RCB
		..1.		RCBEFRES	On, free nonbuffered entry
		...1		RCBERIV	On, indicates entry is invalid
		.... 1...		RCBENBFR	On, nonbuffered entry
		.... .1..		RCBEPOST	On, POSTing required
		.... ..1.		RCBEERFG	On, errorid appended - Applies to nonbuffered entries only
		1		RCBERDY	On, entry is ready
4	(4)	CHARACTER	4	RCBEIDS	Word used in STCTL inst
4	(4)	UNSIGNED	2	RCBEHASI	ASID of home address space
6	(6)	UNSIGNED	2	RCBEPASI	ASID of primary address space
8	(8)	CHARACTER	8	RCBECTL2	Second dword of header
8	(8)	ADDRESS	4	RCBEECB	ECB to be POSTed
12	(C)	CHARACTER	1	RCBESUBP	Subpool number for frestor
13	(D)	UNSIGNED	1	RCBERTYP	LOGREC record type
14	(E)	UNSIGNED	2	RCBEASID	ASID for POSTing
16	(10)	CHARACTER	*	RCBEDATA	Start of data

RCBE Cross Reference

Name	Hex Offset	Hex Value
RCBEASID	E	
RCBECNTL	0	
RCBECOPY	3	80
RCBECTL1	0	
RCBECTL2	8	
RCBEDATA	10	
RCBEECB	8	
RCBEERFG	3	02
RCBEFLGS	2	
RCBEFLG1	2	
RCBEFLG2	3	
RCBEFRES	3	20
RCBEHASI	4	
RCBEIDS	4	
RCBELEN	0	
RCBELREC	2	80
RCBENBFR	3	08
RCBENTRY	0	
RCBEPASI	6	
RCBEPOST	3	04
RCBERDY	3	01
RCBERIV	3	10
RCBERTYP	D	
RCBESUBP	C	
RCBEWRAP	3	40
RCBEWTO	2	40

RCE Information

RCE Programming Interface information

Programming Interface information

RCE

ONLY the following fields are part of the programming interface information:

- RCEABVFX
- RCEAEC
- RCEAFC
- RCEAFCLO
- RCEAFCOK
- RCEBELFX
- RCEBELPL
- RCEBELSF
- RCEBLPIA
- RCEBLPIE
- RCEBLSTA
- RCEBLSTE
- RCEBPPIA
- RCEBPPIE
- RCEBPSTA
- RCEBPSTE
- RCECOMBI
- RCECOMPI
- RCECOMPI1M
- RCECOMPO
- RCECOMPO1M
- RCECOMRC
- RCEDBFRM
- RCEDFC
- RCEDFRS
- RCEDRIPS
- RCEDRIRS
- RCEESINU
- RCEESPI
- RCEESPL
- RCEESREA
- RCEESSPI
- RCEESSPO
- RCEESST
- RCEESWRT
- RCEFIXB1
- RCEFIXB2
- RCEHSPEM
- RCEHSPER
- RCEHSPEW
- RCEHSPPI
- RCEHSPPO
- RCEHSPRR
- RCEHSPRW
- RCEHVCOMMONAUXSCM
- RCEHVCOMMONOBJECTSFIXED1M
- RCEHVCOMMONPAGES
- RCEHVCOMMONPAGESFIXED1M
- RCEHVSHRAUXSCM
- RCEHVSHRAUXSLOTS
- RCEHVSHRINREAL
- RCEHVSHRPAGEINS
- RCEHVSHRPAGEOUTS
- RCEHVSHRPAGES
- RCEINCLUDE1MAFC
- RCELARGEALLOCATEDPL
- RCELARGEPAGESBACKEDINREAL
- RCELARGEUSEDPL
- RCELARGEUSED4K
- RCELFAVAILGROUPS
- RCELPABI
- RCELPAPI
- RCELPARC
- RCELSIRS
- RCELVSHRGBYTES
- RCELVSHRNMOMB
- RCELVSHRPAGES
- RCEMIGAI
- RCEMVBEL
- RCENMAFC
- RCENONRECONLFASIZE
- RCENONRECONLFAUSED
- RCENWSF
- RCEOA44207APPLIED
- RCEPAGEABLELARGE
- RCEPAGMV
- RCEPBAFC
- RCEPLFRM
- RCEPLTOTAL
- RCEPMAFC
- RCEPOOL
- RCERAX
- RCERECONLFASIZE
- RCERECONLFAUSED
- RCERET
- RCERSQA
- RCESGAUX
- RCESGAUXSCM
- RCESGINE
- RCESGINR
- RCESPFR
- RCESPGPI
- RCESPGPO
- RCESTLTI
- RCESUBSPACEV64
- RCESWPPI
- RCESWPPO
- RCETOTFX
- RCETOTPI
- RCETOTPIDASD

- RCETOTPISCM
- RCETOTPI1M
- RCETOTPI1MSCM
- RCETOTPO
- RCETOTPODASD
- RCETOTPOSCM
- RCETOTPO1M
- RCETOTPO1MSCM
- RCETOTRC
- RCETOTSF
- RCETOTSG
- RCEUNOWNEDCOMMONLARGEOBJECTS
- RCEUNOWNEDCOMMONLARGEPAGES
- RCEUSE2GTO32GAREAOK
- RCEVIOME
- RCEVIOMG
- RCEVIOMR
- RCEVIOPI
- RCEVIOPO
- RCEVIORE
- RCEVIORR
- RCEVIORU
- RCEV64CommonGuard
- RCEV64COUNTPAGES
- RCEWSDNE
- RCE2GNONRECONLFASIZE
- RCE2GNONRECONLFAUSED

End of Programming Interface information

RCE Heading Information

Common Name: RSM Control and Enumeration Area
Macro ID: IARRCE
DSECT Name: RCE
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: RCE
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: Yes
Subpool: Extended Nucleus
Key: 0
Data Space: No
Residency: Above 16 megabytes virtual
Size: RCELEN bytes
Created by: IARMR
Pointed to by: CVTRCEP field of the CVT data area
Serialization: Field dependent
Function: The RCE contains system wide counts and control information used by RSM, and other components that interface with RSM, such as SRM, VSM, Etc.

RCE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RCE	RCE
0	(0)	CHARACTER	1	RCEID (4)	RCE CONTROL BLOCK ID
4	(4)	SIGNED	4	RCEPOOL	NUMBER OF FRAMES CURRENTLY AVAILABLE TO SYSTEM. EXCLUDED ARE FRAMES BACKING PERM STORAGE, FRAMES OFFLINE, BAD FRAMES AND 2G FRAMES
8	(8)	SIGNED	4	RCEBELPL	THE SAME AS RCEPOOL EXCEPT THAT ONLY FRAMES BELOW 16M REAL ARE COUNTED.
12	(C)	SIGNED	4	RCEMAXFX	FIXED FRAME THRESHOLD. SRM IS NOTIFIED WHEN THE TOTAL NUMBER OF FIXED FRAMES BELOW 16M REAL INCREASES TO THIS VALUE.
16	(10)	SIGNED	4	RCEDEFFX	PAGE FIXES ARE DEFERRED IF THE NUMBER OF AVAILABLE FRAMES IS EQUAL TO OR LESS THAN THIS VALUE in ESA mode. In z/Architecture mode, all suspendable frame requests are deferred if the number of available frames is less than or equal to this value
20	(14)	SIGNED	2	RCERPBEX	MINIMUM NUMBER OF RPBS WHICH SHOULD BE BUILT WHENEVER THE RPB POOL IS EXPANDED.
22	(16)	SIGNED	1	RCEHRTPP	High Real Threshold Percentage (Preferred). This value dictates the percentage of preferred high real frames that should be available
23	(17)	SIGNED	1	RCEHRTPN	High Real Threshold Percentage (Non-Preferred). This value dictates the percentage of non-preferred high real frames that should be available
24	(18)	SIGNED	4	RCEAFCLO	AFQ LOW THRESHOLD. SRM IS NOTIFIED IF THE NUMBER OF AVAILABLE FRAMES FALLS BELOW THIS VALUE.
28	(1C)	SIGNED	4	RCEAFCOK	AFQ SATISFACTORY THRESHOLD. SRM IS NOTIFIED ONCE THE NUMBER OF AVAILABLE FRAMES INCREASES TO THIS VALUE.
32	(20)	SIGNED	4	RCERSQA	NUMBER OF TIMES A RESERVED SQA QUEUE FRAME WAS USED TO BACK AN SQA PAGE.
36	(24)	SIGNED	4	RCEDFRS	NUMBER OF TIMES A DEFERRED REQUEST HAS BEEN SATISFIED.
40	(28)	SIGNED	4	RCEPRKPR	AVAILABLE FRAME THRESHOLD ABOVE WHICH PAGE RELEASE WILL KEEP RELEASED PAGES BACKED WITH REAL
44	(2C)	SIGNED	4	RCESPFR	NUMBER OF FRAMES MADE AVAILABLE BY SWAP-OUT WITHOUT REQUIRING I/O.
48	(30)	SIGNED	4	RCEVIORU	NUMBER OF TIMES A VIO DATA SET PAGE WAS REUSED.
52	(34)	SIGNED	4	RCETOTRC	TOTAL NUMBER OF TIMES A PAGE WAS RECLAIMED FROM AN AFQ. This field will always be 0 as of JBB4422
56	(38)	SIGNED	4	RCECOMRC	NUMBER OF COMMON AREA PAGES WHICH HAVE BEEN RECLAIMED FROM AN AFQ. This field will always be 0 as of JBB4422
60	(3C)	SIGNED	4	RCELPARC	NUMBER OF PLPA AND PLPA DIRECTORY PAGES RECLAIMED FROM AN AFQ. This field will always be 0 as of JBB4422
64	(40)	SIGNED	4	RCEPBAFL	PREFERRED BELOW AVAILABLE FRAME COUNT THRESHOLD. USED BY GETFRAME WHEN STEALING
68	(44)	SIGNED	4	RCETOTPI	TOTAL NUMBER OF PAGES PAGED-IN EXCLUDING SWAP-IN, VIO, AND HIPERSPACE PAGE-INS. (INCLUDES THE RCETOTPI1M VALUE)
72	(48)	SIGNED	4	RCECOMPI	NUMBER OF COMMON AREA PAGES PAGED-IN. (INCLUDES THE RCECOMPI1M VALUE)
76	(4C)	SIGNED	4	RCELPAPI	NUMBER OF PLPA AND PLPA DIRECTORY PAGES PAGED-IN.
80	(50)	SIGNED	4	RCESWPPI	TOTAL NUMBER OF PAGES REQUIRING I/O TO SWAP-IN.
84	(54)	SIGNED	4	RCEVIOPI	TOTAL NUMBER OF VIO PAGES PAGED-IN EXCLUDING SWAP-IN.
88	(58)	SIGNED	4	RCETOTPO	TOTAL NUMBER OF PAGES PAGED-OUT EXCLUDING SWAP-OUT, VIO PAGE-OUT, VIO MOVEOUT AND HIPERSPACE PAGES (INCLUDES THE RCETOTPO1M VALUE)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
92	(5C)	SIGNED	4	RCECOMPO	NUMBER OF COMMON AREA PAGES PAGED-OUT (INCLUDES THE RCECOMPO1M VALUE)
96	(60)	SIGNED	4	RCESWPPO	TOTAL NUMBER OF PAGES REQUIRING I/O TO SWAP-OUT, EXCLUDING MIGRATION SWAPS
100	(64)	SIGNED	4	RCEVIOPO	TOTAL NUMBER OF VIO PAGES (EXCLUDES SWAP-OUT) MOVED-OUT OR PAGED-OUT.
104	(68)	CHARACTER	4	RCEWLM (0)	Work Load Manager related fields
104	(68)	SIGNED	1	RCEESTTS	Expanded STorage Time Stamp - set by SRM (ESA Mode Only, Not used for ESAME)
105	(69)	SIGNED	1	RCEESTB1	Expanded STorage UIC delimiter value 1 Set by SRM (ESA Mode Only, Not used for ESAME)
106	(6A)	SIGNED	1	RCEESTB2	Expanded STorage UIC delimiter value 2 Set by SRM (ESA Mode Only, Not used for ESAME)
107	(6B)	SIGNED	1	RCEESTB3	Expanded STorage UIC delimiter value 3 Set by SRM (ESA Mode Only, Not used for ESAME)
108	(6C)	SIGNED	4	RCEVIOME	NUMBER OF VIO DATA SET PAGES MOVED OUT TO EXPNANDED STORAGE (ESA Mode Only, Not used for ESAME)
112	(70)	SIGNED	4	RCEVIORE	NUMBER OF VIO DATA SET PAGES READ FROM EXPNANDED STORAGE (ESA Mode Only, Not used for ESAME)
116	(74)	SIGNED	4	RCEVIOMG	NUMBER OF VIO DATA SET PAGES MIGRATED FROM EXPANDED TO AUXILIARY STORAGE (ESA Mode Only, Not used for ESAME)
120	(78)	SIGNED	4	RCETOTFX	TOTAL NUMBER OF PAGES CURRENTLY FIXED. IT IS THE SUM OF PAGE-FIXED, LSQA, SQA (EXCL RSVD SQA), AND V=R ALLOCATED PAGES. It also includes the number of large pages that are backed in the system. IT DOES NOT INCLUDE STEALABLE FIXED PAGES OF LOGICALLY SWAPPED ADDRESS SPACES. It does not include 2G pages. THIS COUNT IS SERIALIZED BY C/S.
124	(7C)	SIGNED	4	RCEBELFX	THE SAME AS RCETOTFX EXCEPT THAT ONLY PAGES BACKED BELOW 16M REAL ARE COUNTED. THIS COUNT IS SERIALIZED BY C/S.
128	(80)	SIGNED	4	RCERAX	ADDRESS OF THE COMMON RAX
132	(84)	SIGNED	4	RCEPBAFC	TOTAL NUMBER OF FRAMES CURRENTLY ON THE PREFERRED BELOW AVAILABLE FRAME QUEUE.
136	(88)	SIGNED	4	RCEAFC	TOTAL NUMBER OF FRAMES CURRENTLY ON ALL AVAILABLE FRAME QUEUES.
140	(8C)	SIGNED	4	RCEDFC	NUMBER OF FRAME PAIRS ON DBL FRAME QUEUES PLUS ANY PAIRS SELECTED TO BE ADDED PLUS ANY PAIRS CURRENTLY IN USE
144	(90)	SIGNED	4	RCEPAGMV	NUMBER OF TIMES A PAGE WAS MOVED FROM ONE FRAME TO ANOTHER.
148	(94)	SIGNED	4	RCEAEC	TOTAL NUMBER OF EXTENDED STORAGE E-FRAMES CURRENTLY ON THE AVAILABLE ESTE QUEUE EXCLUDING THOSE RESERVED FOR PREF STEAL. (ESA Mode Only, Not used for ESAME)
152	(98)	SIGNED	4	RCEAECLO	ESTE LOW THRESHOLD. SRM IS NOTIFIED IF THE NUMBER OF AVAILABLE ESTES FALLS BELOW THIS VALUE. (ESA Mode Only, Not used for ESAME)
156	(9C)	SIGNED	4	RCEAECOK	ESTE SATISFACTORY THRESHOLD. (ESA Mode Only, Not used for ESAME)
160	(A0)	SIGNED	4	RCEESPL	THE TOTAL NUMBER OF EXTENDED STORAGE E-FRAMES CURRENTLY AVAILABLE TO THE SYSTEM. EXTENDED STORAGE E-FRAMES EXCLUDED ARE THOSE OFFLINE, AND BAD EXTENDED STORAGE E-FRAMES ONCE THEY ARE MARKED OFFLINE. (ESA Mode Only, Not used for ESAME)
164	(A4)	SIGNED	4	RCEESINU	THE NUMBER OF IN USE EXTENDED STORAGE E-FRAMES. (ESA Mode Only, Not used for ESAME)
168	(A8)	SIGNED	4	RCEESWRT	THE NUMBER OF PAGES WRITTEN OUT TO EXTENDED STORAGE. (ESA Mode Only, Not used for ESAME)
172	(AC)	SIGNED	4	RCEESREA	THE NUMBER OF PAGES READ FROM EXTENDED STORAGE. (ESA Mode Only, Not used for ESAME)
176	(B0)	SIGNED	4	RCEGROUP	THE MAXIMUM NUMBER OF FRAMES THAT MAY BE USED FOR MIGRATION.
180	(B4)	SIGNED	4	RCECOMBI	Common Blocked Page in Count
184	(B8)	SIGNED	4	RCEMVBEL	NUMBER OF PAGES MOVED TO SATISFY BELOW REQUESTS
188	(BC)	SIGNED	4	RCEFRQM	MINIMUM NUMBER OF RPBS ON THE FRQ DURING THE LAST SAMPLING PERIOD
192	(C0)	SIGNED	4	RCEWRAPS	A TOKEN REPRESENTING THE LATEST PASS (OF THE LRU PHASE OF EXTENDED STORE MIGRATION) THROUGH THE EST. (ESA Mode Only, Not used for ESAME)
196	(C4)	SIGNED	4	RCENWSP	TOTAL NUMBER OF CHANGED NON-WORKING SET PAGES AND SECONDARY WORKING SET PAGES READY FOR MIGRATION (ESA Mode Only, Not used for ESAME)
200	(C8)	SIGNED	4	RCENWSS	TOTAL NUMBER OF CHANGED NON-WORKING SET PAGES AND SECONDARY WORKING SET PAGES WHICH HAVE STARTED MIGRATION. (ESA Mode Only, Not used for ESAME)
204	(CC)	SIGNED	4	RCENWSF	TOTAL NUMBER OF CHANGED NON-WORKING SET PAGES AND SECONDARY WORKING SET PAGES WHICH HAVE COMPLETED MIGRATION. THIS COUNT IS SERIALIZED BY C/S. (ESA Mode Only, Not used for ESAME)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
208	(D0)	SIGNED	4	RCEWSPRP	NUMBER OF PRIMARY WORKING SET PAGES READY FOR MIGRATION. THIS COUNT IS SERIALIZED BY C/S. (ESA Mode Only, Not used for ESAME)
212	(D4)	SIGNED	4	RCEWSDNE	NUMBER OF PRIMARY WORKING SET PAGES WHICH HAVE COMPLETED MIGRATION. THIS COUNT IS SERIALIZED BY C/S. (ESA Mode Only, Not used for ESAME)
216	(D8)	SIGNED	4	RCELPABI	PLPA Blocked Page in Count
220	(DC)	SIGNED	4	RCEDRIPS	NUMBER OF DREF PAGES IN PROCESSOR STORAGE.
224	(E0)	SIGNED	4	RCEFRQC	NUMBER OF RPBS ON THE FRQ
228	(E4)	SIGNED	4	RCEDBFRM	NUMBER OF DOUBLE FRAME PAIRS CURRENTLY IN USE BY THE SYSTEM
232	(E8)	SIGNED	4	RCEDRIRS	NUMBER OF DREF PAGES IN REAL
236	(EC)	SIGNED	4	RCELSIRS	NUMBER OF LSQA PAGES IN REAL
240	(F0)	SIGNED	4	RCERET	TARGET NUMBER OF EXPANDED STORAGE E-FRAMES TO BE RESERVED FOR PREF STEAL (ESA Mode Only, Not used for ESAME)
244	(F4)	SIGNED	4	RCEMIGAI	NUMBER OF FRAMES THAT MIGRATION DID NOT HAVE TO DO I/O FOR (ESA Mode Only, Not used for ESAME)
248	(F8)	SIGNED	4	RCEWSACT	NUMBER OF WORK/SAVE AREAS ON THE AVAILABLE WSA QUEUE. NOT SERIALIZED
252	(FC)	SIGNED	4	RCEWSAM	MINIMUM NUMBER OF WSAS ON THE AVAILABLE WSA QUEUE DURING THE LAST SAMPLING PERIOD
256	(100)	SIGNED	4	RCEHSPEW	TOTAL NUMBER OF HIPERSPACE PAGES WRITTEN TO EXPANDED STORAGE (ESA Mode Only, Not used for ESAME)
260	(104)	SIGNED	4	RCEHSPER	TOTAL NUMBER OF HIPERSPACE PAGES READ FROM EXPANDED STORAGE (ESA Mode Only, Not used for ESAME)
264	(108)	SIGNED	4	RCEHSPEM	TOTAL NUMBER OF HIPERSPACE PAGES MIGRATED FROM EXPANDED STORAGE (ESA Mode Only, Not used for ESAME)
268	(10C)	SIGNED	4	RCEHSPPO	TOTAL NUMBER OF HIPERSPACE PAGES PAGED OUT TO AUXILIARY STORAGE
272	(110)	SIGNED	4	RCEHSPPI	TOTAL NUMBER OF HIPERSPACE PAGES PAGED-IN FROM AUXILIARY STORAGE
276	(114)	SIGNED	4	RCESTABL	TOTAL NUMBER OF FIXED PAGES IN LOGICALLY SWAPPED ADDRESS SPACES ELIGIBLE TO BE STOLEN
280	(118)	SIGNED	4	RCEFXSTL	NUMBER OF FIXED PAGES IN LOGICALLY SWAPPED ADDRESS SPACES THAT WERE BACKED BY REAL BELOW 16 MEGABYTES (For ESA Mode, the frames are currently stolen to Expanded)

Comment

FOR THE FOLLOWING FIELDS, THE IMPLICIT BOUNDS OF UIC RANGE ARE 0 AND 255. SRM SETS THE OTHER 3 INTERMEDIATE VALUES.

End of Comment

284	(11C)	SIGNED	2	RCEFRV1	FRAME UIC RANGE VALUE 1 SET BY SRM
286	(11E)	SIGNED	2	RCEFRV2	FRAME UIC RANGE VALUE 2 SET BY SRM
288	(120)	SIGNED	2	RCEFRV3	FRAME UIC RANGE VALUE 3 SET BY SRM
290	(122)	SIGNED	2	RCEFRV4	RESERVED for RSM (set to 256)
292	(124)	SIGNED	4	RCEBPPIE	NUMBER OF BLOCKED PAGES PAGED IN FROM EXPANDED STORAGE (ESA Mode Only, Not used for ESAME)
296	(128)	SIGNED	4	RCEBPPIA	NUMBER OF BLOCKED PAGES PAGED IN FROM AUXILIARY STORAGE
300	(12C)	SIGNED	4	RCEBPSTE	NUMBER OF BLOCKED PAGES STOLEN TO EXPANDED STORAGE (ESA Mode Only, Not used for ESAME)
304	(130)	SIGNED	4	RCEBPSTA	NUMBER OF BLOCKED PAGES STOLEN TO AUXILIARY STORAGE
308	(134)	SIGNED	4	RCEBLPIE	NUMBER OF BLOCKS OF PAGES PAGED IN FROM EXPANDED STORAGE (ESA Mode Only, Not used for ESAME)
312	(138)	SIGNED	4	RCEBLPIA	NUMBER OF BLOCKS OF PAGES PAGED IN FROM AUXILIARY STORAGE
316	(13C)	SIGNED	4	RCEBLSTE	NUMBER OF BLOCKS OF PAGES STOLEN TO EXPANDED STORAGE (ESA Mode Only, Not used for ESAME)
320	(140)	SIGNED	4	RCEBLSTA	NUMBER OF BLOCKS OF PAGES STOLEN TO AUXILIARY STORAGE
324	(144)	SIGNED	4	RCEESPI	NUMBER OF PAGES FAULTED IN FROM EXPANDED (ESA Mode Only, Not used for ESAME)
328	(148)	SIGNED	4	RCEESST	NUMBER OF PAGES STOLEN OUT TO EXPANDED (ESA Mode Only, Not used for ESAME)
332	(14C)	SIGNED	4	RCEFLAGS (0)	FLAGS
332	(14C)	CHARACTER	1	RCEFLAGS1	FLAGS
		1...		RCEFIXAB	"X'80'" SRM SAYS GO TO EXTRA EFFORTS TO PREVENT PAGE FIXED PAGES THAT CAN GO ANYWHERE FROM BEING FIXED IN BELOW STORAGE
		.1..		RCESSINH	"X'40'" SRM has inhibitted self steal
		..1.		RCENORCF	"X'20'" Indicates there is no reconfigurable storage on this system. This flag is set at IPL time and never changed.
		...1		RCERCFEX	"X'10'" Indicates that reconfigurable storage exists. This flag is set at NIP and is never changed.
		.... 1...		RCEPIOOK	"X'08'" Indicates that SRM finds there is no backup of paging requests in ASM. (Average requests outstanding < 1/8 of all PCCWs.)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... .1..		RCEFAUXS	"X'04'" Indicates that there is a aux storage shortage and that I/O complete should free the current slot if all conditions are met never changes
		.... ..1.		RCEUSE2GTO32GAREAOK	"X'02'" Indicates that the Use2gTo32gArea parameter for the IARV64 service is supported
		1		RCEUKCR	"X'01'" Indicates that there are user key CADS requestors
333	(14D)	CHARACTER	1	RCEFLAGS2	FLAGS
		1...		RCERASPINITIALIZED	"X'80'" Rsm address space has initialized
		.1..		RCESCMBLOCKMANAGERENABLED	"X'40'"
		..1.		RCESCMBLOCKMANAGERDAMAGED	"X'20'"
		...1		RCESTORAGESTATUSGOODATIPL	"X'10'"
334	(14E)	CHARACTER	1	RCEFLAGS3	FLAGS3
		1...		RCESUBSPACEV64	"X'80'" 64-bit Subspaces are supported
		.1..		RCEV64COUNTPAGES	"X'40'" IARV64 CountPages request supported
		..1.		RCEFEAT3ENAB	"X'20'" RSM feature3 enabled - SCM
		...1		RCEPAGEABLELARGE	"X'10'" Pageable Large supported
		.... 1...		RCEFEAT5ENAB	"X'08'" Feature5 enabled
		.... .1..		RCESOFFENAB	"X'04'" SCM Offline enabled
		.... ..1.		RCEINCLUDE1MAFC	"X'02'" Feature to include the fixed 1M pages in the available frame count (RCEAFC) enabled
		1		RCEV64COMMONGUARD	"X'01'" IARV64 Changeguard for common area supported
335	(14F)	CHARACTER	1	RCEFLAGS4	FLAGS
		1...		RCEOA44207APPLIED	"X'80'" Indicates APAR OA44207 is applied on this system
336	(150)	DBL WORD	8	RCEPRTDW (0)	Structure name for PRA header
336	(150)	SIGNED	4	RCEPRTBL	Beginning of PRA
340	(154)	SIGNED	4	RCEPRCUR	Pointer to available slot in PRA
344	(158)	SIGNED	4	RCEKRE	Reserved -- do not use
348	(15C)	SIGNED	2	RCEFIXB1	COUNT OF THE TIMES FRAMES WERE FIXED BELOW WHEN THEY COULD HAVE BEEN FIXED ABOVE AND RCEFIXAB WAS OFF
350	(15E)	SIGNED	2	RCEFIXB2	COUNT OF THE TIMES FRAMES WERE FIXED BELOW WHEN THEY COULD HAVE BEEN FIXED ABOVE AND RCEFIXAB WAS ON
352	(160)	DBL WORD	8	RCESTLTI	STEAL TIMER - ELAPSED TIME SPENT IN PREF STEAL IN CPU TIMER UNITS
360	(168)	SIGNED	4	RCETOTSG	Total number of shared page groups in the system including shared segments
364	(16C)	SIGNED	4	RCESGINR	Total number groups in central storage including shared segment pages
368	(170)	SIGNED	4	RCESGINE	Total number of shared page groups in expanded storage including shared segment pages (ESA Mode Only, Not used for ESAME)
372	(174)	SIGNED	4	RCESGAUX	Total number of auxiliary slots in use for shared page groups. Includes only dasd storage.
376	(178)	SIGNED	4	RCETOTSF	Total number of shared page groups fixed in the system including shared segments pages
380	(17C)	SIGNED	4	RCEBELSF	Total number of shared page groups fixed in the system below 16 meg real including shared segments pages
384	(180)	SIGNED	4	RCESPQUO	Storage isolation quota for central and expanded storage in use for shared page groups
388	(184)	SIGNED	4	RCESPGPI	Number of page-ins from auxiliary storage for shared page groups including shared segments pages
392	(188)	SIGNED	4	RCESPGPO	Number of page-outs to auxiliary storage for shared page groups including shared segments pages
396	(18C)	SIGNED	4	RCEESSPI	Number of page-ins from expanded storage for shared page groups including shared segments pages (ESA Mode Only, Not used for ESAME)
400	(190)	SIGNED	4	RCEESSPO	Number of page-outs to expanded storage for shared page groups including shared segments pages (ESA Mode Only, Not used for ESAME)
404	(194)	CHARACTER	16	RCEFBV (0)	STRUCTURE NAME FOR RCEFBV# FIELDS

Comment

THIS AND THE FOLLOWING 4 FIELDS ARE APPLICABLE TO PAGEABLE SHARED FRAMES

End of Comment

404	(194)	SIGNED	4	RCEFBV1	NUMBER OF FRAMES IN UIC INTERVAL 1 AS SET BY SRM VIA THE RCEFRV# FIELDS.

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
408	(198)	SIGNED	4	RCEFBV2	NUMBER OF FRAMES IN UIC INTERVAL 2 AS SET BY SRM VIA THE RCEFRV# FIELDS.
412	(19C)	SIGNED	4	RCEFBV3	NUMBER OF FRAMES IN UIC INTERVAL 3 AS SET BY SRM VIA THE RCEFRV# FIELDS.
416	(1A0)	SIGNED	4	RCEFBV4	NUMBER OF FRAMES IN UIC INTERVAL 4 AS SET BY SRM VIA THE RCEFRV# FIELDS.
420	(1A4)	SIGNED	4	RCETOTSM	Total number of shared pages that are in shared segments
424	(1A8)	SIGNED	4	RCEPAAFC	Available above the line preferred frame count
428	(1AC)	SIGNED	4	RCEPFCOK	Preferred frame shortage threshold
432	(1B0)	SIGNED	4	RCEVIOMR	NUMBER OF VIO DATA SET PAGES MOVED TO THE VIO REAL CACHE
436	(1B4)	SIGNED	4	RCEVIORR	NUMBER OF VIO DATA SET PAGES READ FROM THE VIO REAL CACHE
440	(1B8)	SIGNED	4	RCECSARE	NUMBER OF CSA PAGES BACKED IN REAL STORAGE
444	(1BC)	SIGNED	4	RCELPARE	NUMBER OF PLPA/MLPA PAGES BACKED IN REAL STORAGE
448	(1C0)	SIGNED	4	RCELPAFX	NUMBER OF PLPA/MLPA PAGES THAT ARE PAGE FIXED
452	(1C4)	SIGNED	4	RCESQAFX	NUMBER OF SQA/FIXED-CSA PAGES IN REAL STORAGE
456	(1C8)	SIGNED	4	RCEDREFR	NUMBER OF SQA DREF PAGES IN REAL STORAGE
460	(1CC)	SIGNED	4	RCEPHAFC	Number of available preferred HIGH frames (above 2GB)
464	(1D0)	SIGNED	4	RCEQDAFC	Number of available quad frame groups
468	(1D4)	SIGNED	1	RCEQFAIL	Number of consecutive calls to Quad Frame Steal IARYGFRM that failed to obtain a group
469	(1D5)	SIGNED	1	RCEDEFQF	Default number of Quad Frame groups to be obtained by Quad Frame steal
470	(1D6)	CHARACTER	2	RCERSV2	Reserved
472	(1D8)	SIGNED	4	RCENBAFC	TOTAL NUMBER OF FRAMES CURRENTLY ON THE NONPREFERRED BELOW AVAILABLE FRAME QUEUE.
476	(1DC)	SIGNED	4	RCENAAFC	Available nonpreferred above frame count
480	(1E0)	SIGNED	4	RCENHAFC	Number of available nonpreferred HIGH frames (above 2GB)
484	(1E4)	SIGNED	4	RCEQSAFC	Number of available quad single frames
488	(1E8)	SIGNED	4	RCEABVFX	Number of pages fixed between 16M and 2G
492	(1EC)	SIGNED	4	RCEQDFRM	Number of quad groups that are in-use by the system
496	(1F0)	SIGNED	4	RCEQDTHR	Threshhold number of quad frames available needed in order to satisfy non-pref requests from the quad area
500	(1F4)	SIGNED	4	RCEABVPL	Same as RCEPOOL, but only includes frames between 16M and 2G
504	(1F8)	SIGNED	4	RCESTECB	ECB posted by IARM8MSI, waited on by reconfiguration (IEECB927)
		1...		RCESTORAGEINITECBW	"X'80'" ECB Waiting bit
		.1..		RCESTORAGEINITECBP	"X'40'" ECB Posted bit
508	(1FC)	SIGNED	4	RCEHSPRW	TOTAL NUMBER OF HIPERSPACE PAGES WRITTEN TO REAL STORAGE
512	(200)	SIGNED	4	RCEHSPRR	TOTAL NUMBER OF HIPERSPACE PAGES READ FROM REAL STORAGE
516	(204)	SIGNED	4	RCEPFTAL	Alet for the PFT CADS - 0 for ESA
520	(208)	CHARACTER	8	RCEQDSZ	Initial Quad Area Size
528	(210)	SIGNED	4	RCEPRMCT	Count of the number of non-nucleus frames comprising permanent storage
532	(214)	SIGNED	2	RCEBELOWLOW	Low on below 16Meg real
534	(216)	SIGNED	2	RCEBELOWOK	Ok on below 16Meg real
536	(218)	SIGNED	2	RCEABOVELOW	Low on 16M-2G real
538	(21A)	SIGNED	2	RCEABOVEOK	Ok on 16M-2G real
540	(21C)	CHARACTER	4	RCEFLAGSABN (0)	Flags serialized by CS
540	(21C)	CHARACTER	1	RCEFLGS5	
		1...		RCESCMEVACINPROGRESS	"X'80'"
541	(21D)	CHARACTER	1	RCEFLGS6	
542	(21E)	CHARACTER	1	RCEFLGS7	
543	(21F)	CHARACTER	1	RCEFLGS8	
544	(220)	DBL WORD	8	RCELVSHRSTRT (0)	Lowest Virtual address of the high virtual shared area (system default is 2**41
544	(220)	DBL WORD	8	RCELVLPRLIM	Lowest Virtual address of the high virtual shared area (system default is 2**41
552	(228)	DBL WORD	8	RCELVHPRSTRT (0)	Lowest virtual address of High Virtual High Private storage (system default is 2**49)
552	(228)	DBL WORD	8	RCELVSHRLIM	Lowest virtual address of High Virtual High Private storage (system default is 2**49)
560	(230)	SIGNED	4	RCEMINHVFRM	Min number of PFTEs on High Virtual frame section
564	(234)	SIGNED	4	RCEMAXHVFRM	Max number of PFTEs on High Virtual frame section
568	(238)	SIGNED	4	RCESTBRK	Paging system is overloaded when the number of in progress I/O requests equals this threshold
572	(23C)	SIGNED	2	RCECADSUSED	Number of CADS ASTES in use
574	(23E)	SIGNED	2	RCECADSHW	MAX CADS ASTES in use during this IPL
576	(240)	SIGNED	2	RCECADSLOST	Number of lost CADS ASTES (not on the free queue, but not in use nor defective)
578	(242)	CHARACTER	6	RCERSV1	Reserved (for HBB7705)
584	(248)	DBL WORD	8	RCELVSHRPAGES (0)	Number of high virtual shared memory pages allocated for the entire system. This count includes hidden pages. (64-bit value)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
584	(248)	SIGNED	4		
588	(24C)	SIGNED	4	RCELVSHRPAGES31	Number of high virtual shared memory pages allocated for the entire system. This count includes hidden pages. (31-bit value)
592	(250)	CHARACTER	8	RCELVSHRGBYTES	high water mark for number of shared bytes within large virtual memory objects
600	(258)	CHARACTER	4	RCELVSHRNMOMB	Number of shared memory objects allocated
604	(25C)	SIGNED	4	RCEGETFRAMEDEFERTHRESHOLD	Suspend suspendable getframe requests when the number of available frames is less than this
608	(260)	DBL WORD	8	RCEHVSHRINREAL (0)	Number of high virtual shared pages backed in real storage (4K pages) (64-bit value)
608	(260)	SIGNED	4		
612	(264)	SIGNED	4	RCEHVSHRINREAL31	Number of high virtual shared pages backed in real storage (4K pages) (31-bit value)
616	(268)	DBL WORD	8	RCEHVSHRAUXSLOTS (0)	Number of auxiliary slots used for high virtual shared storage (64-bit value). Includes only dasd storage.
616	(268)	SIGNED	4		
620	(26C)	SIGNED	4	RCEHVSHRAUXSLOTS31	Number of auxiliary slots used for high virtual shared storage (31-bit value). Includes only dasd storage.
624	(270)	DBL WORD	8	RCEHVSHRPAGEINS (0)	Number of high virtual shared pages paged in from auxiliary storage (64-bit value)
624	(270)	SIGNED	4		
628	(274)	SIGNED	4	RCEHVSHRPAGEINS31	Number of high virtual shared pages paged in from auxiliary storage (31-bit value)
632	(278)	DBL WORD	8	RCEHVSHRPAGEOUTS (0)	Number of high virtual shared pages paged out to auxiliary storage (64-bit value)
632	(278)	SIGNED	4		
636	(27C)	SIGNED	4	RCEHVSHRPAGEOUTS31	Number of high virtual shared pages paged out to auxiliary storage (31-bit value)
640	(280)	SIGNED	4	RCENUMOFGETMAINREQUESTS	Total number of getmain requests that have been issued during the life of the system
644	(284)	SIGNED	4	RCEPGSBACKEDONGTMNREQS	Total number of pages backed during getmain requests that have been issued during the life of the system
648	(288)	SIGNED	4	RCENUMOFFIXREQUESTS	Total number of fix requests that have been issued during the life of the system for storage (address space only) below two gigabytes
652	(28C)	SIGNED	4	RCENUMFRAMESFX	Total number of frames that were requested to be fixed during the life of the system for storage (address space only) below two gigabytes
656	(290)	SIGNED	4	RCE1STREFFAULTS	Total number of first reference faults taken during the life of the system
660	(294)	SIGNED	4	RCENON1STREFFAULTS	Total number of non-first reference faults taken during the life of the system
664	(298)	SIGNED	4	RCERSV4	Reserved
668	(29C)	SIGNED	4	RCESTTOT	Total number of fixed pages in logically swapped address spaces eligible to be stolen that are below 16M or between 16M and 2G
672	(2A0)	SIGNED	4	RCEMAXFRAMESCPUQ	Max number of frames on the CPU related frame queues
676	(2A4)	SIGNED	4	RCEFQSPLITLIM	Number of frames on a frame queue before it is to be split
680	(2A8)	DBL WORD	8	RCEPRESTOLENOK (0)	Pre-stolen satisfactory threshold. Link-edited into the nucleus with a value of zero. SRM is responsible for updating this field. This field is only read by RSM and read and written to by SRM.
680	(2A8)	SIGNED	4		
684	(2AC)	SIGNED	4	RCEPRESTOLENOK31	
688	(2B0)	DBL WORD	8	RCEPRESTOLENLOW (0)	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Pre-stolen low threshold. Link-edited into the nucleus with a value of zero. SRM is responsible for updating this field. This field is only read by RSM and written to by SRM. to by SRM.
688	(2B0)	SIGNED	4		
692	(2B4)	SIGNED	4	RCEPRESTOLENLOW31	
696	(2B8)	DBL WORD	8	RCENUMBEROFPRESTOLENFRAMES (0)	
					Number of frames that are currently prestolen. Link-edited into the nucleus with a value of zero. RSM is responsible for updating this field. SRM just reads this field.
696	(2B8)	SIGNED	4		
700	(2BC)	SIGNED	4	RCENUMBEROFPRESTOLENFRAMES31	
704	(2C0)	DBL WORD	8	RCENUMOFFRAVAILABLEBYSWAP (0)	
					Number of frames that will be made available by swap. Link-edited into the nucleus with a value of zero. SRM is responsible for updating this field. RSM just reads this field.
704	(2C0)	SIGNED	4		
708	(2C4)	SIGNED	4	RCENUMOFFRAVAILABLEBYSWAP31	
712	(2C8)	DBL WORD	8	RCEMAXFRAMESTOEXAMINE (0)	
					Number of frames to examine in Global Steal before enabling
712	(2C8)	SIGNED	4		
716	(2CC)	SIGNED	4	RCEMAXFRAMESTOEXAMINE31	
720	(2D0)	DBL WORD	8	RCEPERCENTSTOLEN (0)	
					Percent of frames we expect to have stolen in Global Steal after examining RCEMaxFramesToExamine
720	(2D0)	SIGNED	4		
724	(2D4)	SIGNED	4	RCEPERCENTSTOLEN31	
728	(2D8)	SIGNED	4	RCEGLRUSEGHDRPTR	
					Pointer to the Global LRU Segment Header
732	(2DC)	SIGNED	4	RCEFXABVSTL	Number of fixed pages in logically swapped address spaces that were backed by real frames between 16M and 2G
736	(2E0)	SIGNED	4	RCEFXTOTSTL	Number of fixed pages in logically swapped address spaces that were backed by real frames below 16M or between 16M and 2G
740	(2E4)	SIGNED	4	RCESTABV	Total number of fixed pages in logically swapped address spaces eligible to be stolen that are between 16M and 2G
744	(2E8)	DBL WORD	8	RCERSV5 (0)	Reserved for HBB7720
744	(2E8)	DBL WORD	8	RCELARGEMEMORYOBJECTS (0)	
					Number of Large Memory Objects allocated in the system
744	(2E8)	SIGNED	4		
748	(2EC)	SIGNED	4	RCELARGEMEMORYOBJECTS31	
752	(2F0)	DBL WORD	8	RCELARGEPAGESBACKEDINREAL (0)	
					Number of Large Pages (1MB pages) backed in real storage
752	(2F0)	SIGNED	4		
756	(2F4)	SIGNED	4	RCELARGEPAGESBACKEDINREAL31	
760	(2F8)	DBL WORD	8	RCERECONLFASIZE (0)	
					Size of the Reconfigurable Large Frame Area in Megabytes
760	(2F8)	SIGNED	4		
764	(2FC)	SIGNED	4	RCERECONLFASIZE31	
768	(300)	DBL WORD	8	RCENONRECONLFASIZE (0)	
					Size of the Non-Reconfigurable Large Frame Area in Megabytes
768	(300)	SIGNED	4		
772	(304)	SIGNED	4	RCENONRECONLFASIZE31	
776	(308)	DBL WORD	8	RCERECONLFAUSED (0)	
					Number of 1MB frames in the Reconfigurable Large Frame Area that are allocated
776	(308)	SIGNED	4		
780	(30C)	SIGNED	4	RCERECONLFAUSED31	
784	(310)	DBL WORD	8	RCENONRECONLFAUSED (0)	
					Number of 1MB frames in the Non-Reconfigurable Large Frame Area that are allocated
784	(310)	SIGNED	4		
788	(314)	SIGNED	4	RCENONRECONLFAUSED31	
792	(318)	SIGNED	4	RCELFTHR	Threshhold number of available large frames needed in order to satisfy non-pref requests from the large frame area
796	(31C)	SIGNED	4	RCELFAVAILGROUPS	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dec)	Description
					Count of available large frames groups
800	(320)	SIGNED	4	RCELSAFC	Count of available single large frames
804	(324)	SIGNED	4	RCELARGEUSED1MHWM	
					High-Water mark of the number of large pages allocated on behalf of large page requests
808	(328)	SIGNED	4	RCELARGEUSED4KHWM	
					High-Water mark of the number of large pages allocated on behalf of 4K page requests
812	(32C)	CHARACTER	60	RCERSV6	RESERVED FOR HBB7740
872	(368)	DBL WORD	8	RCEHVCOMMONSTRT	
					Lowest Virtual address for the high virtual common area
880	(370)	DBL WORD	8	RCEHVCOMMONEND	
					Highest Virtual address for the high virtual common area
888	(378)	DBL WORD	8	RCEHVCOMMONPAGES	
				(0)	
					Number of high virtual common memory pages allocated for the entire system. This count includes hidden pages.
888	(378)	SIGNED	4		
892	(37C)	SIGNED	4	RCEHVCOMMONPAGES31	
					Number of 64-bit common memory pages allocated for the entire system. This count includes hidden pages.
896	(380)	DBL WORD	8	RCEHVCOMMONHWMBYTES	
					High Water Mark for number of 64-Bit common bytes allocated in the entire system
904	(388)	DBL WORD	8	RCEHVCOMMONNMOMB	
					Number of 64-Bit common memory objects currently allocated
912	(390)	DBL WORD	8	RCEHVCOMMONINREAL	
				(0)	
					Number of 64-Bit common memory pages backed in real
912	(390)	SIGNED	4		
916	(394)	SIGNED	4	RCEHVCOMMONINREAL31	
					Number of 64-Bit common memory pages backed in real
920	(398)	DBL WORD	8	RCEHVCOMMONAUXSLOTS	
				(0)	
					Number of 64-Bit common memory pages backed in aux storage. Includes only dasd storage.
920	(398)	SIGNED	4		
924	(39C)	SIGNED	4	RCEHVCOMMONAUXSLOTS31	
					Number of 64-Bit common memory pages backed in aux storage. Includes only dasd storage.
928	(3A0)	DBL WORD	8	RCEHVCOMMONPAGESFIXED	
				(0)	
					Number of 64-Bit common memory pages that are fixed in real
928	(3A0)	SIGNED	4		
932	(3A4)	SIGNED	4	RCEHVCOMMONPAGESFIXED31	
					Number of 64-Bit common memory pages that are fixed in real
936	(3A8)	DBL WORD	8	RCEHVCOMMONPAGESDREF	
				(0)	
					Number of 64-Bit common DREF pages in real
936	(3A8)	SIGNED	4		
940	(3AC)	SIGNED	4	RCEHVCOMMONPAGESDREF31	
					Number of 64-Bit common DREF pages in real
944	(3B0)	SIGNED	4	RCEENABLEINCRSHARED	
					Enable Increment used in IAXXR during online processing for shared CPs
948	(3B4)	SIGNED	4	RCEENABLEINCRDEDICATED	
					Enable Increment used in IAXXR during online processing for dedicated CPs
952	(3B8)	CHARACTER	8	RCEPLSZ	Initial pageable large area size
960	(3C0)	DBL WORD	8	RCEPMMSS	Number of failed attempts to back storage with pageable large frames (pref)
968	(3C8)	DBL WORD	8	RCEPLSID	Number of system-initiated demotions from pageable large frame groups to 4k page frames
976	(3D0)	DBL WORD	8	RCEPLRID	Number of request-initiated demotions from pageable large frame groups to 4k page frames
984	(3D8)	SIGNED	4	RCEPMAFC	Number of available pageable large frame groups (pref)
988	(3DC)	SIGNED	4	RCEPLHWM	High water mark for the number of pageable large frame groups used by the system
992	(3E0)	SIGNED	4	RCEPSAFC	Number of available pageable large single frames (pref)
996	(3E4)	SIGNED	4	RCERSV3	Reserved for HBB7750
1000	(3E8)	DBL WORD	8	RCEPLFAILEDSINCEPLARGESTEAL	
					Last count of total number of failed attempts to back storage with pageable large frames (RCEPMMSS+RCENMMSS) since Pageable Large Steal called
1008	(3F0)	SIGNED	4	RCEPLFRM	Number of pageable large frame groups in-use by the system
1012	(3F4)	SIGNED	4	RCENSAFC	Number of available pageable large single frames (non-pref)
1016	(3F8)	CHARACTER	16	RCERSV7	Reserved for HBB7750
1032	(408)	SIGNED	4	RCELARGEUSED4K	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Number of fixed large frames used to satisfy 4K frame requests. Serialized by CS.
1036	(40C)	SIGNED	4	RCEUNOWNEDCOMMONLARGEOBJECTS	
					Number of common large memory objects whose owner is no longer active. Serialized by RSMGL lock.
1040	(410)	DBL WORD	8	RCEUNOWNEDCOMMONLARGEPAGES	
					Number of common large pages whose owner is no longer active. Serialized by RSMGL lock.
1048	(418)	SIGNED	4	RCEHVCOMMONOBJECTSFIXED1M	
					Number of 64-Bit common large memory objects allocated in the system. Serialized by C/S.
1052	(41C)	SIGNED	4	RCELARGEUSEDPL	
					Number of fixed large frame used to satisfy pageable large frame requests
1056	(420)	SIGNED	4	RCEPLXRM	Number of pageable large frame groups that are fixed
1060	(424)	SIGNED	4	RCENMAFC	Number of available pageable large frame groups (non-pref)
1064	(428)	DBL WORD	8	RCENMMSS	Number of failed attempts to back storage with pageable large frames (non-pref)
1072	(430)	DBL WORD	8	RCESGAUXSCM	
1080	(438)	DBL WORD	8	RCEHVSHRAUXSCM	
1088	(440)	DBL WORD	8	RCEHVCOMMONAUXSCM	
1096	(448)	DBL WORD	8	RCETOTPIDASD	Total page-ins from DASD. Excludes SWAP-IN, VIO, AND HIPERSPACE PAGE-INS. Serialized by CSG
1104	(450)	DBL WORD	8	RCETOTPISCM	
1112	(458)	DBL WORD	8	RCETOTPODASD	Total page-outs to DASD. Excludes SWAP-OUT, VIO PAGE-OUT, VIO MOVEOUT and HIPERSPACE PAGES. Serialized by CSG
1120	(460)	DBL WORD	8	RCETOTPOSCM	
1128	(468)	DBL WORD	8	RCEHVSHRPAGES1M	
					Number of 64-bit shared memory 1M pages that are backed in real
1136	(470)	DBL WORD	8	RCEHVSHRPAGESFIXED	
					Number of 64-bit shared memory pages that are fixed in real
1144	(478)	DBL WORD	8	RCEHVSHRPAGESFIXED1M	
					Number of 64-bit shared memory 1M pages that are fixed in real
1152	(480)	DBL WORD	8	RCEHVCOMMONPAGES1M	
					Number of 64-bit common memory 1M pages that are backed in real
1160	(488)	DBL WORD	8	RCEHVCOMMONPAGESFIXED1M	
					Number of 64-Bit common memory 1M pages that are fixed in real
1168	(490)	DBL WORD	8	RCETOTPO1M	Total page-outs of 1Meg pages. Excludes Swap-Out, vio page-out vio moveout and hiperspace pages.
1176	(498)	DBL WORD	8	RCETOTPI1M	Total page-ins of 1Meg pages. Excludes Swap-Out, vio page-out vio moveout and hiperspace pages.
1184	(4A0)	DBL WORD	8	RCETOTPO1MSCM	
1192	(4A8)	DBL WORD	8	RCETOTPI1MSCM	
1200	(4B0)	DBL WORD	8	RCECOMPO1M	Number of 64-bit common memory 1M pages paged-out
1208	(4B8)	DBL WORD	8	RCECOMPI1M	Number of 64-bit common memory 1M pages paged-in
1216	(4C0)	CHARACTER	28	RCERSV8	Reserved for HBB7780
1244	(4DC)	SIGNED	4	RCELARGEALLOCATEDPL	
					Number of Fixed Large Pages allocated as Pageable Large Pages - different from RCELargeUsedPL which is the number of fixed large pages currently used as pageable large pages. If they were to be demoted, they would not be counted in RCELargeUsedPL. Demoted pageable large pages are included in this field. Serialized by CS.
1248	(4E0)	SIGNED	4	RCEPLTOTAL	Total Number of Pageable Large Pages
1252	(4E4)	SIGNED	4	RCELARGEUSEDPLHWM	
					High-Water mark of the number of large pages allocated on behalf of pageable large requests
1256	(4E8)	DBL WORD	8	RCE2GMEMORYOBJECTS	
				(0)	
					Number of 2G Memory Objects allocated in the system
1256	(4E8)	SIGNED	4		
1260	(4EC)	SIGNED	4	RCE2GMEMORYOBJECTS31	
1264	(4F0)	DBL WORD	8	RCE2GPAGESBACKEDINREAL	
				(0)	
					Number of 2G Pages backed in real storage
1264	(4F0)	SIGNED	4		
1268	(4F4)	SIGNED	4	RCE2GPAGESBACKEDINREAL31	
1272	(4F8)	DBL WORD	8	RCE2GNONRECONLFASIZE	
				(0)	
					Size of the Non-Reconfigurable 2G Frame Area in 2G units
1272	(4F8)	SIGNED	4		
1276	(4FC)	SIGNED	4	RCE2GNONRECONLFASIZE31	
1280	(500)	DBL WORD	8	RCE2GNONRECONLFAUSED	
				(0)	
					Number of 2G frames in the Non-Reconfigurable 2G Frame Area that are allocated
1280	(500)	SIGNED	4		

RCE Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
1284	(504)	SIGNED	4	RCE2GNONRECONLFAUSED31	
1288	(508)	SIGNED	4	RCE2GHWM	High water mark for the number of 2G frame groups used by the system
1292	(50C)	CHARACTER	116	RCERSV9	Reserved for JBB778H
1408	(580)	DBL WORD	8	RCEEND (0)	End of RCE (keep multiple of 8 bytes)
1408	(580)	X'580'	0	RCELEN	"RCEEND-RCE" Length of the RCE

RCE Cross Reference

Name	Hex Offset	Hex Value
RCE	0	
RCEABOVELOW	218	
RCEABOVEOK	21A	
RCEABVFX	1E8	
RCEABVPL	1F4	
RCEAEC	94	
RCEAECLO	98	
RCEAECOK	9C	
RCEAFC	88	
RCEAFCLO	18	
RCEAFCOK	1C	
RCEBELFX	7C	
RCEBELOWLOW	214	
RCEBELOWOK	216	
RCEBELPL	8	
RCEBELSF	17C	
RCEBLPIA	138	
RCEBLPIE	134	
RCEBLSTA	140	
RCEBLSTE	13C	
RCEBPPIA	128	
RCEBPPIE	124	
RCEBPSTA	130	
RCEBPSTE	12C	
RCECADSHW	23E	
RCECADSLOST	240	
RCECADSUSED	23C	
RCECOMBI	B4	
RCECOMPI	48	
RCECOMPI1M	4B8	
RCECOMPO	5C	
RCECOMPO1M	4B0	
RCECOMRC	38	
RCECSARE	1B8	
RCEDBFRM	E4	
RCEDEFFX	10	
RCEDEFQF	1D5	
RCEDFC	8C	
RCEDFRS	24	
RCEDREFR	1C8	
RCEDRIPS	DC	
RCEDRIRS	E8	
RCEENABLEINCRDEDICATED	3B4	
RCEENABLEINCRSHARED	3B0	
RCEEND	580	
RCEESINU	A4	
RCEESPI	144	
RCEESPL	A0	
RCEESREA	AC	
RCEESSPI	18C	
RCEESSPO	190	
RCEESST	148	
RCEESTB1	69	
RCEESTB2	6A	
RCEESTB3	6B	
RCEESTTS	68	
RCEESWRT	A8	
RCEFAUXS	14C	4
RCEFBV	194	
RCEFBV1	194	
RCEFBV2	198	
RCEFBV3	19C	
RCEFBV4	1A0	
RCEFEAT3ENAB	14E	20
RCEFEAT5ENAB	14E	8
RCEFIXAB	14C	80
RCEFIXB1	15C	
RCEFIXB2	15E	
RCEFLAGS	14C	
RCEFLAGSABN	21C	
RCEFLAGS1	14C	
RCEFLAGS2	14D	
RCEFLAGS3	14E	
RCEFLAGS4	14F	
RCEFLGS5	21C	
RCEFLGS6	21D	
RCEFLGS7	21E	
RCEFLGS8	21F	
RCEFQSPLITLIM	2A4	
RCEFRQC	E0	
RCEFRQM	BC	
RCEFRV1	11C	
RCEFRV2	11E	
RCEFRV3	120	
RCEFRV4	122	
RCEFXABVSTL	2DC	
RCEFXSTL	118	
RCEFXTOTSTL	2E0	
RCEGETFRAMEDEFERTHRESHOLD	25C	
RCEGLRUSEGHDRPTR	2D8	
RCEGROUP	B0	
RCEHRTPN	17	
RCEHRTPP	16	
RCEHSPEM	108	
RCEHSPER	104	
RCEHSPEW	100	
RCEHSPPI	110	
RCEHSPPO	10C	
RCEHSPRR	200	
RCEHSPRW	1FC	
RCEHVCOMMONAUXSCM	440	
RCEHVCOMMONAUXSLOTS	398	
RCEHVCOMMONAUXSLOTS31	39C	
RCEHVCOMMONEND	370	
RCEHVCOMMONHWMBYTES	380	
RCEHVCOMMONINREAL	390	
RCEHVCOMMONINREAL31	394	
RCEHVCOMMONNMOMB	388	
RCEHVCOMMONOBJECTSFIXED1M	418	
RCEHVCOMMONPAGES	378	
RCEHVCOMMONPAGESDREF		

Name	Hex Offset	Hex Value
	3A8	
RCEHVCOMMONPAGESDREF31		
	3AC	
RCEHVCOMMONPAGESFIXED		
	3A0	
RCEHVCOMMONPAGESFIXED1M		
	488	
RCEHVCOMMONPAGESFIXED31		
	3A4	
RCEHVCOMMONPAGES1M		
	480	
RCEHVCOMMONPAGES31		
	37C	
RCEHVCOMMONSTRT		
	368	
RCEHVSHRAUXSCM		
	438	
RCEHVSHRAUXSLOTS		
	268	
RCEHVSHRAUXSLOTS31		
	26C	
RCEHVSHRINREAL		
	260	
RCEHVSHRINREAL31		
	264	
RCEHVSHRPAGEINS		
	270	
RCEHVSHRPAGEINS31		
	274	
RCEHVSHRPAGEOUTS		
	278	
RCEHVSHRPAGEOUTS31		
	27C	
RCEHVSHRPAGESFIXED		
	470	
RCEHVSHRPAGESFIXED1M		
	478	
RCEHVSHRPAGES1M		
	468	
RCEID	0	
RCEINCLUDE1MAFC		
	14E	2
RCEKRE	158	
RCELARGEALLOCATEDPL		
	4DC	
RCELARGEMEMORYOBJECTS		
	2E8	
RCELARGEMEMORYOBJECTS31		
	2EC	
RCELARGEPAGESBACKEDINREAL		
	2F0	
RCELARGEPAGESBACKEDINREAL31		
	2F4	
RCELARGEUSEDPL		
	41C	
RCELARGEUSEDPLHWM		
	4E4	
RCELARGEUSED1MHWM		
	324	
RCELARGEUSED4K		
	408	
RCELARGEUSED4KHWM		
	328	
RCELEN	580	580
RCELFAVAILGROUPS		
	31C	
RCELFTHR	318	
RCELPABI	D8	
RCELPAFX	1C0	
RCELPAPI	4C	
RCELPARC	3C	
RCELPARE	1BC	
RCELSAFC	320	
RCELSIRS	EC	

Name	Hex Offset	Hex Value
RCELVHPRSTRT	228	
RCELVLPRLIM	220	
RCELVSHRGBYTES		
	250	
RCELVSHRLIM	228	
RCELVSHRNMOMB		
	258	
RCELVSHRPAGES		
	248	
RCELVSHRPAGES31		
	24C	
RCELVSHRSTRT	220	
RCEMAXFRAMESCPUQ		
	2A0	
RCEMAXFRAMESTOEXAMINE		
	2C8	
RCEMAXFRAMESTOEXAMINE31		
	2CC	
RCEMAXFX	C	
RCEMAXHVFRM	234	
RCEMIGAI	F4	
RCEMINHVFRM	230	
RCEMVBEL	B8	
RCENAAFC	1DC	
RCENBAFC	1D8	
RCENHAFC	1E0	
RCENMAFC	424	
RCENMMSS	428	
RCENONRECONLFASIZE		
	300	
RCENONRECONLFASIZE31		
	304	
RCENONRECONLFAUSED		
	310	
RCENONRECONLFAUSED31		
	314	
RCENON1STREFFAULTS		
	294	
RCENORCF	14C	20
RCENSAFC	3F4	
RCENUMBEROFPRESTOLENFRAMES		
	2B8	
RCENUMBEROFPRESTOLENFRAMES31		
	2BC	
RCENUMFRAMESFX		
	28C	
RCENUMOFFIXREQUESTS		
	288	
RCENUMOFFRAVAILABLEBYSWAP		
	2C0	
RCENUMOFFRAVAILABLEBYSWAP31		
	2C4	
RCENUMOFGETMAINREQUESTS		
	280	
RCENWSF	CC	
RCENWSP	C4	
RCENWSS	C8	
RCEOA44207APPLIED		
	14F	80
RCEPAAFC	1A8	
RCEPAGEABLELARGE		
	14E	10
RCEPAGMV	90	
RCEPBAFC	84	
RCEPBAFL	40	
RCEPERCENTSTOLEN		
	2D0	
RCEPERCENTSTOLEN31		
	2D4	
RCEPFCOK	1AC	
RCEPFTAL	204	
RCEPGSBACKEDONGTMNREQS		
	284	
RCEPHAFC	1CC	

Name	Hex Offset	Hex Value
RCEPIOOK	14C	8
RCEPLFAILEDSINCEPLARGESTEAL	3E8	
RCEPLFRM	3F0	
RCEPLHWM	3DC	
RCEPLRID	3D0	
RCEPLSID	3C8	
RCEPLSZ	3B8	
RCEPLTOTAL	4E0	
RCEPLXRM	420	
RCEPMAFC	3D8	
RCEPMMSS	3C0	
RCEPOOL	4	
RCEPRCUR	154	
RCEPRESTOLENLOW	2B0	
RCEPRESTOLENLOW31	2B4	
RCEPRESTOLENOK	2A8	
RCEPRESTOLENOK31	2AC	
RCEPRKPR	28	
RCEPRMCT	210	
RCEPRTBL	150	
RCEPRTDW	150	
RCEPSAFC	3E0	
RCEQDAFC	1D0	
RCEQDFRM	1EC	
RCEQDSZ	208	
RCEQDTHR	1F0	
RCEQFAIL	1D4	
RCEQSAFC	1E4	
RCERASPINITIALIZED	14D	80
RCERAX	80	
RCERCFEX	14C	10
RCERECONLFASIZE	2F8	
RCERECONLFASIZE31	2FC	
RCERECONLFAUSED	308	
RCERECONLFAUSED31	30C	
RCERET	F0	
RCERPBEX	14	
RCERSQA	20	
RCERSV1	242	
RCERSV2	1D6	
RCERSV3	3E4	
RCERSV4	298	
RCERSV5	2E8	
RCERSV6	32C	
RCERSV7	3F8	
RCERSV8	4C0	
RCERSV9	50C	
RCESCMBLOCKMANAGERDAMAGED	14D	20
RCESCMBLOCKMANAGERENABLED	14D	40
RCESCMEVACINPROGRESS	21C	80
RCESGAUX	174	
RCESGAUXSCM	430	
RCESGINE	170	
RCESGINR	16C	
RCESOFFENAB	14E	4
RCESPFR	2C	
RCESPGPI	184	
RCESPGPO	188	
RCESPQUO	180	
RCESQAFX	1C4	
RCESSINH	14C	40
RCESTABL	114	
RCESTABV	2E4	
RCESTBRK	238	
RCESTECB	1F8	
RCESTLTI	160	
RCESTORAGEINITECBP	1F8	40
RCESTORAGEINITECBW	1F8	80
RCESTORAGESTATUSGOODATIPL	14D	10
RCESTTOT	29C	
RCESUBSPACEV64	14E	80
RCESWPPI	50	
RCESWPPO	60	
RCETOTFX	78	
RCETOTPI	44	
RCETOTPIDASD	448	
RCETOTPISCM	450	
RCETOTPI1M	498	
RCETOTPI1MSCM	4A8	
RCETOTPO	58	
RCETOTPODASD	458	
RCETOTPOSCM	460	
RCETOTPO1M	490	
RCETOTPO1MSCM	4A0	
RCETOTRC	34	
RCETOTSF	178	
RCETOTSG	168	
RCETOTSM	1A4	
RCEUKCR	14C	1
RCEUNOWNEDCOMMONLARGEOBJECTS	40C	
RCEUNOWNEDCOMMONLARGEPAGES	410	
RCEUSE2GTO32GAREAOK	14C	2
RCEVIOME	6C	
RCEVIOMG	74	
RCEVIOMR	1B0	
RCEVIOPI	54	
RCEVIOPO	64	
RCEVIORE	70	
RCEVIORR	1B4	
RCEVIORU	30	
RCEV64COMMONGUARD	14E	1
RCEV64COUNTPAGES	14E	40
RCEWLM	68	
RCEWRAPS	C0	
RCEWSACT	F8	
RCEWSAM	FC	
RCEWSDNE	D4	
RCEWSPRP	D0	
RCE1STREFFAULTS	290	
RCE2GHWM	508	
RCE2GMEMORYOBJECTS	4E8	
RCE2GMEMORYOBJECTS31	4EC	
RCE2GNONRECONLFASIZE	4F8	
RCE2GNONRECONLFASIZE31	4FC	
RCE2GNONRECONLFAUSED	500	
RCE2GNONRECONLFAUSED31	504	
RCE2GPAGESBACKEDINREAL		

Name	Hex Offset	Hex Value
	4F0	
RCE2GPAGESBACKEDINREAL31		
	4F4	

RCT Information

RCT Heading Information

Common Name: System Resource Manager Resource Control Table
Macro ID: IRARCT
DSECT Name: RCT (unless DSECT=NO is coded)
Owning Component: System Resource Manager (SC1CX)
Eye-Catcher ID: RCT
Offset: 0
Length: CHAR(4)
Storage Attributes: Subpool: Nucleus
Key: 0
Residency: Nucleus (above 16M line)
Size: 272 bytes @LPOWSMC
Created by: Assembled into nucleus module IRARMCNS
Pointed to by: RMCTRCT field of the RMCT data area
Serialization: SRM lock
Function: Contains constants and statistics used by the system resource manager's resource monitor routine

RCT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	272	RCT	RESOURCE CONTROL TABLE
0	(0)	CHARACTER	4	RCTRCT	ACRONYM IN EBCDIC -RCT-
				Comment	
				RESOURCE CONTROL CONSTANTS	
				End of Comment	
4	(4)	SIGNED	2	RCCUICTL	UIC THRESHOLD LOW
6	(6)	SIGNED	2	RCCUICTH	UIC HIGH THRESHOLD
8	(8)	SIGNED	2	RCCCPUTL	CPU LOW THRESHOLD SCALED BY 16
10	(A)	SIGNED	2	RCCCPUTH	CPU HIGH THRESHOLD SCALED BY 16
12	(C)	SIGNED	2	RCCPTRTL	PAGING RATE LOW THRESHOLD
14	(E)	SIGNED	2	RCCPTRTH	PAGING RATE HIGH THRESHOLD
16	(10)	SIGNED	2	RCCSRSF	SWAP RATE SCALING FACTOR SCALED BY 100
18	(12)	SIGNED	2	RCCILEV	In Long enough recommendation value threshold for select and swapout action to occur
20	(14)	UNSIGNED	2	RCCOVBMP	Percentage used to determine whether overblocking is occuring (percentage of uneeded frames brought in from aux and expanded as part of a block)
22	(16)	UNSIGNED	2	RCVDASAV	RM2 interval average number of address spaces delayed for CPU, scaled by 16
24	(18)	UNSIGNED	4	RCVDASAC	Accumulated samples of CcvDasCt for current RM2 interval
28	(1C)	UNSIGNED	4	RCTIMGWU	Workload Units available to MVS image when not running as VM guest. If running as VM guest, capacity available to VM. Only calculated on machines that support the STSI instruction.
32	(20)	UNSIGNED	4	RCTCECWU	Workload Units capacity of CEC. Only calculated on machines that support the STSI intruction.
36	(24)	SIGNED	2	RCCRUAM	MULTIPLIER FOR OLD READY USER AVG
38	(26)	SIGNED	2	RCCRUCM	MULTIPLIER FOR ACCUM READY USER AVG
40	(28)	SIGNED	2	RCCWSRM	MULTIPLIER FOR OLD WEIGHTED SVCE RATE
42	(2A)	SIGNED	2	RCCSRCM	MULTIPLIER FOR ACCUMULATED SERVICE RATE
44	(2C)	SIGNED	2	RCCDCITL	CONTENTION INDEX THRESHOLD FOR EXCHANGE
46	(2E)	SIGNED	2	RCCETOLD	MULT FOR OLD E.T. AVG
48	(30)	SIGNED	2	RCCETCUR	MULT FOR NEW E.T. AVG
50	(32)	SIGNED	2	RCCRSVF3	RESERVED
				Comment	
				RESOURCE CONTROL VARIABLES	
				End of Comment	
52	(34)	SIGNED	2	RCVCTMC	SAMPLE INTERVALS COUNT
54	(36)	SIGNED	2	RCVUICA	UIC AVERAGE
56	(38)	SIGNED	2	RCVCPUA	CPU USAGE AVERAGE
58	(3A)	SIGNED	2	RCVAVQC	AVQ LOW COUNT
60	(3C)	SIGNED	2	RCVNSQLA_4K	NONSWAP ASM QUEUE LENGTH AVERAGE 4K-frame based, i.e. counts the number of real storage frames involved in the I/O requests
62	(3E)	SIGNED	2	RCVPINSC	policy interval sample count, wlm mode only

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
64	(40)	UNSIGNED	2	RCVCPUAA	total processor usage average
66	(42)	BITSTRING	2	RCTFLAG1	Flag area
		1...		RCTFLAG1_LACS_RCU	If on, the RCTLACS value is provided for systems running in LPAR mode or for systems running as a VM guest (if VM is running in LPAR mode). The value does no longer include CPU wait time
66	(42)	BITSTRING	1	*	Reserved
68	(44)	UNSIGNED	4	RCVCPUAC	total processor usage accumulator
72	(48)	SIGNED	4	RCVUICC	UIC ACCUMULATOR
76	(4C)	SIGNED	4	RCVCPUC	CPU USAGE ACCUMULATOR
80	(50)	SIGNED	4	RCVAVQP	AVQ LOW COUNT SAVE AREA
84	(54)	SIGNED	4	RCVMQFP	Maximum number of quad frame groups used by RSM.
88	(58)	UNSIGNED	4	RCVBSWCT	Base Swap Count value for the accumulated number of pages swapped in/out from auxiliary/ expanded.
92	(5C)	SIGNED	4	RCVBPTCT	BASE PAGE FAULT COUNT
96	(60)	UNSIGNED	4	RCVBPUCT	Base Paging and Moving count for accumulated number of pages paged/moved to and from aux/ expanded.
100	(64)	SIGNED	4	RCVBPPCT	BASE TOTAL PAGE COUNT
104	(68)	SIGNED	4	RCVBPTTM	BASE PAGE FAULT TIME
108	(6C)	UNSIGNED	4	RCVIFAC	IFA usage accumulator
112	(70)	SIGNED	4	RCVTAPAD	LAST ALLOCATED TAPE
116	(74)	SIGNED	2	RCVGMTRM	GLOBAL COUNT OF TERMWAITS DETECTED BY MS6
118	(76)	UNSIGNED	2	RCVIFAA	IFA usage average
		Comment			
		EXTENDED REAL CONSTANTS			
		End of Comment			
120	(78)	SIGNED	2	RCCFXTTL	% All of real low MPL threshold
122	(7A)	SIGNED	2	RCCFXTTH	% All of real high MPL threshold
124	(7C)	SIGNED	2	RCCFXETL	% Below the line low MPL threshold
126	(7E)	SIGNED	2	RCCFXETH	% Below the line high MPL threshold
		Comment			
		EXTENDED REAL VARIABLES			
		End of Comment			
128	(80)	SIGNED	2	RCVFXIOP	AVG % OF TOTAL FRAMES THAT ARE FIXED OR IN I/O
130	(82)	SIGNED	2	RCVMFXA	AVG % OF TOTAL FRAMES BELOW 16MEG THAT ARE FIXED
132	(84)	SIGNED	4	RCVFXCA	BELOW 16M FIXED FRAME COUNT AVERAGE
136	(88)	SIGNED	4	RCVFXCC	BELOW 16M FIXED FRAME COUNT ACCUMULATOR
140	(8C)	SIGNED	4	RCVBSWIC	Base swap count value for the accumulated number of pages swapped in from auxiliary
144	(90)	SIGNED	4	RCVASMQN	NONSWAP ASM QUEUE ACCUMULATOR I/O based, i.e., each page I/O counts as one no matter what size the page has
148	(94)	SIGNED	2	RCVNSQLA	NONSWAP ASM QUEUE LENGTH AVERAGE I/O based, i.e., each page I/O counts as one no matter what size the page has
150	(96)	SIGNED	2	RCVSWPTM	SWAP PAGE DELAY TIME (MILLISECS)
152	(98)	SIGNED	4	RCVASMQS	SWAP ASM QUEUE ACCUMULATOR
156	(9C)	UNSIGNED	4	RCVSWRQC	BASE SWAP PAGE COMPLETE COUNT
160	(A0)	SIGNED	4	RCVTFXCA	TOTAL FIXED FRAME COUNT AVERAGE
164	(A4)	SIGNED	4	RCVASMQN_4K	NONSWAP ASM QUEUE ACCUMULATOR 4K-frame based, i.e. counts the number of real storage frames involved in the I/O requests
168	(A8)	SIGNED	4	RCVMDFP	MAX # OF DOUBLE FRAME PAIRS USED
172	(AC)	SIGNED	4	RCCRM2OR	VALUE OF THE RM2 INVOCATION INTERVAL CALCULATED USING CPU ADJUSTMENT FACTOR (BEFORE LIMITS APPLIED)
176	(B0)	SIGNED	4	RCCMS6OR	VALUE OF THE MS6 INVOCATION INTERVAL CALCULATED USING CPU ADJUSTMENT FACTOR (BEFORE LIMITS APPLIED)
180	(B4)	SIGNED	4	RCCWM2OR	VALUE OF THE WM2 EVALUATION THRESHOLD CALCULATED USING CPU ADJUSTMENT FACTOR (BEFORE LIMITS APPLIED)
184	(B8)	UNSIGNED	4	RCVSRBS	Accumulated Workload Management SRB Service for entire system. It is accumulated by WM1 and reset and used by RM3
188	(BC)	UNSIGNED	4	RCVTCBS	Accumulated Workload Management TCB Service for entire system. It is accumulated by WM1 and reset and used by RM3
192	(C0)	SIGNED	4	RCVCMPIB	Base for rcecompi (policy interval)
196	(C4)	UNSIGNED	4	RCTLACS	Long-term average CPU service used by this logical partition, in millions of service units per hour. If this value is above the partition's defined capacity, the partition will be capped. It is calculated using the physical CPU adjustment factor (RCTPCPUA) so it may not match other measures of service which are based on the logical CPU adjustment factor. It is available if the hardware supports LPAR cluster

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
200	(C8)	SIGNED	4	RCVPTR	Paging rate - fullword
204	(CC)	SIGNED	4	RCVSWRT	Swapin rate - fullword
208	(D0)	SIGNED	4	RCVPAGRT	Total paging rate - fullword
212	(D4)	SIGNED	4	RCTPCPUA	Physical CPU adjustment factor (i.e. adjustment factor for converting CPU time to equivalent service in basic-mode with all processors online).
216	(D8)	SIGNED	4	RCVAFQA	Available frame avg.
220	(DC)	UNSIGNED	4	RCVSUPC	SUP usage accumulator
224	(E0)	UNSIGNED	2	RCVSUPA	SUP usage average
226	(E2)	UNSIGNED	2	RCTRSVF3	reserved
228	(E4)	SIGNED	4	RCVF2GCA	Between 16M and 2G fixed frame count average
232	(E8)	CHARACTER	8	RCVF2GCC	Between 16M and 2G fixed frame count accumulator
240	(F0)	CHARACTER	8	RCVTFXCC	Total fixed frame count accumulator
248	(F8)	CHARACTER	8	RCVAFQC	Available frame count acumulator
256	(100)	SIGNED	4	RCTPCPUA_ACTUAL	Physical CPU adjustment factor (i.e. adjustment factor for converting CPU time to equivalent service in basic-mode with all processors online). Based on Model Capacity Rating
260	(104)	SIGNED	4	RCTPCPUA_NOMINAL	Physical CPU adjustment factor (i.e. adjustment factor for converting CPU time to equivalent service in basic-mode with all processors online). Based on Nominal Model Capacity Rating
264	(108)	SIGNED	4	RCTPCPUA_SCALING_FACTOR	scaling factor for RCTPCPUA_actual and RCTPCPUA_nominal
268	(10C)	SIGNED	4	*	reserved
272	(110)	CHARACTER	0	RCTEND	END OF RCT End of this block

RCT Cross Reference

Name	Hex Offset	Hex Value
RCCCPUTH	A	
RCCCPUTL	8	
RCCDCITL	2C	
RCCETCUR	30	
RCCETOLD	2E	
RCCFXETH	7E	
RCCFXETL	7C	
RCCFXTTH	7A	
RCCFXTTL	78	
RCCILEV	12	
RCCMS6OR	B0	
RCCOVBMP	14	
RCCPTRTH	E	
RCCPTRTL	C	
RCCRM2OR	AC	
RCCRSVF3	32	
RCCRUAM	24	
RCCRUCM	26	
RCCSRCM	2A	
RCCSRSF	10	
RCCUICTH	6	
RCCUICTL	4	
RCCWM2OR	B4	
RCCWSRM	28	
RCT	0	
RCTCECWU	20	
RCTEND	110	
RCTFLAG1	42	
RCTFLAG1_LACS_RCU	42	80
RCTIMGWU	1C	
RCTLACS	C4	
RCTPCPUA	D4	
RCTPCPUA_ACTUAL	100	
RCTPCPUA_NOMINAL	104	
RCTPCPUA_SCALING_FACTOR	108	
RCTRCT	0	
RCTRSVF3	E2	
RCVAFQA	D8	
RCVAFQC	F8	

Name	Hex Offset	Hex Value
RCVASMQN	90	
RCVASMQN_4K	A4	
RCVASMQS	98	
RCVAVQC	3A	
RCVAVQP	50	
RCVBPPCT	64	
RCVBPTCT	5C	
RCVBPTTM	68	
RCVBPUCT	60	
RCVBSWCT	58	
RCVBSWIC	8C	
RCVCMPIB	C0	
RCVCPUA	38	
RCVCPUAA	40	
RCVCPUAC	44	
RCVCPUC	4C	
RCVCTMC	34	
RCVDASAC	18	
RCVDASAV	16	
RCVFXCA	84	
RCVFXCC	88	
RCVFXIOP	80	
RCVF2GCA	E4	
RCVF2GCC	E8	
RCVGMTRM	74	
RCVIFAA	76	
RCVIFAC	6C	
RCVMDFP	A8	
RCVMFXA	82	
RCVMQFP	54	
RCVNSQLA	94	
RCVNSQLA_4K	3C	
RCVPAGRT	D0	
RCVPINSC	3E	
RCVPTR	C8	
RCVSRBS	B8	
RCVSUPA	E0	
RCVSUPC	DC	
RCVSWPTM	96	
RCVSWRQC	9C	
RCVSWRT	CC	
RCVTAPAD	70	
RCVTCBS	BC	

RCT Cross Reference

Name	Hex Offset	Hex Value
RCVTFXCA	A0	
RCVTFXCC	F0	
RCVUICA	36	
RCVUICC	48	

RCTD Information

RCTD Heading Information

Common Name: Region Control Task Data Area
Macro ID: IEARCTD
DSECT Name: RCTD
Owning Component: Region Control Task (SC1CU)
Eye-Catcher ID: None
Storage Attributes: Virtual Storage: Yes
Subpool: 255
Key: 0
Size: 496 Bytes
Created by: IEAVEMIN
Pointed to by: ASXBRCTD field of the ASXB data area.
Serialization: None
Function: This area is used by RCT to store information relevant to its processing.

RCTD Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	RCTD	RCT DATA AREA
0	(0)	CHARACTER	0	RCTDBEGN	BEGINING OF RCTD
0	(0)	UNSIGNED	4	RCTDISAV	INTERNAL SAVE AREA
				(4294967314:562128232)	
72	(48)	ADDRESS	4	RCTDTCBD	ADDRESS OF DUMP TCB
76	(4C)	ADDRESS	4	RCTDTCBS	ADDRESS OF STC TCB
80	(50)	CHARACTER	72	RCTDWORK	WORK AREA
80	(50)	CHARACTER	72	RCTDLMAC	LIST FORM MACROS
80	(50)	BITSTRING	72	RCTDCLRL	CLEAR WORK AREA
80	(50)	BITSTRING	8	RCTDTIME	CURRENT TIME
80	(50)	UNSIGNED	4	RCTDTMLH	LEFT HALF USED FOR CALCULATIONS
84	(54)	UNSIGNED	4	*	RESERVED
88	(58)	CHARACTER	32	RCTDPRG	PURGE PARAMETER LIST
152	(98)	BITSTRING	2	RCTDFLG1	FLAGS
		1...		RCTDCLAS	ENHANCED CLIST ATTENTION EXIT SUPPORT
152	(98)	BITSTRING	1	*	RESERVED
154	(9A)	UNSIGNED	2	RCTDCLST	COUNT OF CLIST ATTN STMT'S WITHIN NESTED CLISTS
156	(9C)	CHARACTER	8	RCTDECBS	ECB LIST
156	(9C)	ADDRESS	4	RCTDTPTR	POINTER TO RCTDTECB
160	(A0)	ADDRESS	4	RCTDWPTR	POINTER TO ASCBECB
		1...		RCTDECBE	LAST ECB INDICATOR END OF ECB LIST
164	(A4)	ADDRESS	4	RCTDTECB	RCT TERMINATION ECB
		1...		*	UNUSED
		.1..		RCTDPOST	TERMINATION ECB'S POST BIT
164	(A4)	BITSTRING	3	*	UNUSED
168	(A8)	ADDRESS	4	RCTDRET@	AREA FOR SAVING INIT/TERM CALLER'S RETURN ADDRESS
172	(AC)	ADDRESS	4	RCTDTAXE	ADDRESS OF TAXE QUEUE
176	(B0)	ADDRESS	4	RCTDPIRL	ADDRESS OF PURGE I/O REQ
176	(B0)	CHARACTER	1	*	RESERVED
177	(B1)	ADDRESS	3	RCTDBASE	ANCHOR FOR PURGE I/O REQUESTS
180	(B4)	ADDRESS	4	RCTDRC	SAVE AREA FOR INVALID RETURN CODES
184	(B8)	ADDRESS	4	RCTDRTRY	RECURSION INDICATOR
188	(BC)	ADDRESS	4	RCTDRTY@	POTENTIAL RETRY ADDRESS
192	(C0)	UNSIGNED	4	RCTDQSRC	QSCECMP return code OR ASCBQECB post code
192	(C0)	CHARACTER	3	*	Unused portion
195	(C3)	UNSIGNED	1	RCTDQSCD	Return Code/Post Code portion of RCTDQSRC
196	(C4)	UNSIGNED	4	RCTRSCM	REALSWAP completion input
196	(C4)	CHARACTER	2	*	Unused portion
198	(C6)	UNSIGNED	1	RCTRSCMT	Swap type
199	(C7)	UNSIGNED	1	RCTRSCMC	Realswap success/failure indicator. 0 = Successful, 4 = Unsuccessful.
200	(C8)	UNSIGNED	4	RCTDUSRD	?USERRDY SET RC
204	(CC)	UNSIGNED	4	RCTDPSSH	PageableStorageShortage 0 = Do not call IARSAEXC to continue processing PShortageRestoreRequest 4 = Continue processing PShortageRestoreRequest
208	(D0)	CHARACTER	8	RCTDRES6	RESERVED

Comment

RECOVERY FOOTPRINTS

End of Comment

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
216	(D8)	BITSTRING	4	RCTDRCTR	RECOVERY FOOTPRINTS
216	(D8)	BITSTRING	1	RCTDMOID	MODULE IDENTIFIER
		1...		RCTDINIT	INITIALIZATION
		.1..		RCTDCOMN	COMMON PROCESSING
		..1.		RCTDQUIS	QUIESCE
		...1		RCTDREST	RESTORE
		.... 1...		RCTDATTN	ATTENTION EXIT
		.... .1..		RCTDTERM	TERMINATION
		.... ..1.		RCTDDUMP	DUMP REQUESTED BY RCT
		1		*	RESERVED
217	(D9)	BITSTRING	3	RCTDFLGS	RECOVERY FLAGS
				Comment	
				WHEN RCTDINIT MODULE FLAG IS ON	
				End of Comment	
		1...		RCTDATTD	DUMP TASK BEING ATTACHED
		.1..		RCTDRES3	RESERVED FOR FUTURE USE
		..1.		RCTDATTS	STC BEING ATTACHED
		...1		RCTDBRCP	BRANCHING TO COMMON PROC
217	(D9)	BITSTRING	2	*	RESERVED
				Comment	
				RCT INTERNAL ACTION FLAGS	
				End of Comment	
220	(DC)	BITSTRING	4	RCTDINTF	RCT INTERNAL ACTION FLAGS
220	(DC)	BITSTRING	1	RCTDRCTF	RCT'S CROSS COMMUNICATIONS RECOVERY FLAGS
		1...		RCTDRSBO	QUIESCE BACKOUT INVOKED
		.1..		RCTDSUBN	RCT'S SUBTASKS STOPPED
		..1.		RCTDPRGR	RCT OWNS PURGE RESOURCE
		...1		RCTDSRBN	SRB'S STOPPED
		.... 1...		RCTDDLCK	QUIESCE HAS DISPATCHER LOCK
		.... .111		*	RESERVED
221	(DD)	BITSTRING	3	RCTDRES5	RESERVED
224	(E0)	CHARACTER	12	RCTDRES1	RESERVED WAS CHAR(16)
236	(EC)	ADDRESS	4	RCTDPLST	ADDRESS OF PROTECTED
240	(F0)	CHARACTER	*	*	
240	(F0)	CHARACTER	40	RCTDSLST	AREA TO CONTAIN LIST FORM OF SDUMP MACRO
240	(F0)	BITSTRING	40	RCTDSCLR	CLEAR LIST AREA
				Comment	
				PLIST	
				End of Comment	
240	(F0)	CHARACTER	*	RCTDBIND	Workarea for BB

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
217	(D9)	STRUCTURE	3	*	
		1...		RCTDWAIT	PREPARING TO ISSUE WAIT
		.1..		RCTDLOOP	LOOKING FOR WORK REQUESTS
		..1.		RCTDBR2T	BRANCHING TO TERMINATION
		...1		RCTDBR2Q	BRANCHING TO QUIESCE
		.... 1...		RCTDBR2R	BRANCHING TO RESTORE
		.... .1..		RCTDBR2A	BRANCHING TO ATTENTION EXIT
217	(D9)	BITSTRING	2	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
217	(D9)	STRUCTURE	3	*	
		1...		RCTDRES4	RESERVED FOR FUTURE USE
		.1..		RCTDDETS	STC BEING DETACHED
		..1.		RCTDDETD	DUMP BEING DETACHED
		...1		RCTDCANE	CANCEL ESTAE
217	(D9)	BITSTRING	2	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
217	(D9)	STRUCTURE	3	*	
		1...		RCTDAFPE	ATTENTION SCHEDULING BEGUN
		.1..		RCTDAFPC	ATTENTION SCHEDULING ENDED
		..1.		RCTDPMSG	THE ATTENTION ERROR MESSAGE SHOULD BE ISSUED
		...1		RCTDIGAT	IGNORE ATTENTION
217	(D9)	BITSTRING	2	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
217	(D9)	STRUCTURE	3	*	

Comment

BYTE 1

End of Comment

		1...	RCTDENQ	ENQ INVOKED
		.1..	RCTDOLL1	SETLOCK OBTAIN INVOKED THE 1ST TIME FOR LOCAL LOCK
		..1.	RCTDRLL1	SETLOCK RELEASE INVOKED THE 1ST TIME FOR LOCAL LOCK
		...1	RCTDPSUB	STATUS INVOKED TO STOP SUBTASKS
		.... 1...	RCTDSY12	SYSEVENT 12 INVOKED
		.... .1..	RCTDSV16	SVC 16 INVOKED
		.... ..1.	RCTDDEQ	DEQ INVOKED
		1	RCTDPSRB	STATUS INVOKED TO STOP SRBS

Comment

BYTE 2

End of Comment

218	(DA)	1...	RCTDSY13	SYSEVENT 13 INVOKED
		.1..	RCTDSSUB	STATUS INVOKED TO START SUBTASKS
		..1.	RCTDSSRB	STATUS INVOKED TO START SRB
		...1	RCTDSWOT	SWAP-OUT INVOKED
		.... 1...	RCTDQWAI	WAIT INVOKED
		.... .1..	RCTDSLFL	SETLOCK FAILED
		.... ..1.	RCTDPRGF	PURGE FAILED
		1	RCTDSYBC	SRM PROCESSING COMPLETE FOR BRANCH ENTRY

Comment

BYTE 3

End of Comment

219	(DB)	1...	RCTDSWPF	SWAP-OUT RETURN CODE NON ZERO
		.1..	RCTDOLL2	SETLOCK OBTAIN INVOKED THE 2ND TIME FOR THE LOCAL LOCK
		..1.	RCTDRLL2	SETLOCK RELEASE INVOKED THE 2ND TIME FOR THE LOCAL LOCK
		...1	RCTDOGL1	SETLOCK OBTAIN INVOKED THE 1ST TIME FOR THE GLOBAL DISPATCHER LOCK
		.... 1...	RCTDRGL1	SETLOCK RELEASE INVOKED THE 1ST TIME FOR THE GLOBAL DISPATCHER LOCK
		.... .1..	RCTDQABD	QUIESCE HAS SCHEDULED AN 078 ABEND
		.... ..1.	RCTDSWPR	In-real swap invoked
		1	RCTDSWPC	Swap processing is complete

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
217	(D9)	STRUCTURE	3	*	

Comment

BYTE 1

End of Comment

		1...	RCTDRS17	SVC 17 BEING INVOKED
		.1..	RCTDOBLK	SETLOCK OBTAIN INVOKED
		..1.	RCTDSTAT	STATUS BEING INVOKED
		...1	RCTDRLLK	SETLOCK RELEASE INVOKED
		.... 1...	RCTDSY19	SYSEVENT 19 INVOKED

RCTD Cross Reference

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... .1..		RCTDSY18	SYSEVENT 18 INVOKED
		.... ..1.		RCTDFAIL	SETLOCK FAILED
		1		RCTDRSLW	RESTORE FOUND ADDRESS SPACE IN LONG WAIT
		Comment			
		BYTE 2			
		End of Comment			
218	(DA)	1...		RCTDRIOC	I/O PROCESSING COMPLETE
		.1..		RCTDWTLB	WAIT LIMIT PROCESSING BEGINNING
		..1.		RCTDSTAC	STATUS PROCESSING COMPLETE
		...1		RCTDRLWC	LONG WAIT PROC COMPLETE
		.... 1...		RCTDRLWB	LONG WAIT PROC BEGINNING
		.... .111		*	RESERVED
		Comment			
		BYTE 3			
		End of Comment			
219	(DB)	1...		*	RESERVED
		.1..		RCTDWTLC	WAIT LIMIT PROCESSIMG COMPLETE
		..1.		RCTDOPTC	SRM PROC COMPLETE
		...1 1111		*	RESERVED

RCTD Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
RCTD	0		RCTDPLST	EC	
RCTDAFPC	D9	40	RCTDPMSG	D9	20
RCTDAFPE	D9	80	RCTDPOST	A4	40
RCTDATTD	D9	80	RCTDPRG	58	
RCTDATTN	D8	08	RCTDPRGF	DA	02
RCTDATTS	D9	20	RCTDPRGR	DC	20
RCTDBASE	B1		RCTDPSRB	D9	01
RCTDBEGN	0		RCTDPSSH	CC	
RCTDBIND	F0		RCTDPSUB	D9	10
RCTDBRCP	D9	10	RCTDQABD	DB	04
RCTDBR2A	D9	04	RCTDQSCD	C3	
RCTDBR2Q	D9	10	RCTDQSRC	C0	
RCTDBR2R	D9	08	RCTDQUIS	D8	20
RCTDBR2T	D9	20	RCTDQWAI	DA	08
RCTDCANE	D9	10	RCTDRC	B4	
RCTDCLAS	98	80	RCTDRCTF	DC	
RCTDCLRL	50		RCTDRCTR	D8	
RCTDCLST	9A		RCTDREST	D8	10
RCTDCOMN	D8	40	RCTDRES1	E0	
RCTDDEQ	D9	02	RCTDRES3	D9	40
RCTDDETD	D9	20	RCTDRES4	D9	80
RCTDDETS	D9	40	RCTDRES5	DD	
RCTDDLCK	DC	08	RCTDRES6	D0	
RCTDDUMP	D8	02	RCTDRET@	A8	
RCTDECBE	A0	80	RCTDRGL1	DB	08
RCTDECBS	9C		RCTDRIOC	DA	80
RCTDENQ	D9	80	RCTDRLLK	D9	10
RCTDFAIL	D9	02	RCTDRLL1	D9	20
RCTDFLGS	D9		RCTDRLL2	DB	20
RCTDFLG1	98		RCTDRLWB	DA	08
RCTDIGAT	D9	10	RCTDRLWC	DA	10
RCTDINIT	D8	80	RCTDRSBO	DC	80
RCTDINTF	DC		RCTDRSLW	D9	01
RCTDISAV	0		RCTDRS17	D9	80
RCTDLMAC	50		RCTDRTRY	B8	
RCTDLOOP	D9	40	RCTDRTY@	BC	
RCTDMOID	D8		RCTDSCLR	F0	
RCTDOBLK	D9	40	RCTDSLFL	DA	04
RCTDOGL1	DB	10	RCTDSLST	F0	
RCTDOLL1	D9	40	RCTDSRBN	DC	10
RCTDOLL2	DB	40	RCTDSSRB	DA	20
RCTDOPTC	DB	20	RCTDSSUB	DA	40
RCTDPIRL	B0		RCTDSTAC	DA	20

Name	Hex Offset	Hex Value
RCTDSTAT	D9	20
RCTDSUBN	DC	40
RCTDSV16	D9	04
RCTDSWOT	DA	10
RCTDSWPC	DB	01
RCTDSWPF	DB	80
RCTDSWPR	DB	02
RCTDSYBC	DA	01
RCTDSY12	D9	08
RCTDSY13	DA	80
RCTDSY18	D9	04
RCTDSY19	D9	08
RCTDTAXE	AC	
RCTDTCBD	48	
RCTDTCBS	4C	
RCTDTECB	A4	
RCTDTERM	D8	04
RCTDTIME	50	
RCTDTMLH	50	
RCTDTPTR	9C	
RCTDUSRD	C8	
RCTDWAIT	D9	80
RCTDWORK	50	
RCTDWPTR	A0	
RCTDWTLB	DA	40
RCTDWTLC	DB	40
RCTRSCM	C4	
RCTRSCMC	C7	
RCTRSCMT	C6	

RCWK Information

RCWK Heading Information

Common Name: VSM RECOVERY WORK AREA
Macro ID: IGVRCWK
DSECT Name: RCWK
Owning Component: Virtual Storage Manager (SC1CH)
Eye-Catcher ID: NONE
Storage Attributes: Subpool: 245, 255
Key: 0
Residency: Above 16M line
Size: RCWK -- X'44' bytes
Created by: IGVRVSM
Pointed to by: VSWKRCWK
Serialization: NONE
Function: CONTAINS INFORMATION RELATED TO VSM RECOVERY

RCWK Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	44	RCWK	VSM RECOVERY WORK AREA
0	(0)	ADDRESS	4	RCWKADDR	ADDRESS OF AREA TO BE VERIFIED
4	(4)	CHARACTER	1	RCWKFLG1	STORAGE FLAGS
		1...		RCWKTYPE	0 => STORAGE IS SQA 1 => STORAGE IS LSQA
		.1..		RCWKCELL	0 => CHECK CELLPOOL 1 => DON'T CHECK CELLPOOL
		..1.		RCWKCERR	0 => NO CELLPOOL ERRORS 1 => CELLPOOL ERRORS
		...1		RCWKRET	0 => RETRY TO IGVVSMRT 1 => RETRY TO CALLER OF IGVVSMRT
		.... 1...		RCWKPERC	1 => FORCE PERCOLATION 0 => PERCOLATION NOT FORCED
		.... .1..		RCWKABND	1 => ABEND 704,705,70A,778 0 => NOT ONE OF THE ABOVE
		.... ..1.		RCWKBACK	1 => DO BACKOUT PROCESSING 0 => DO NOT DO BACKOUT
		1		*	RESERVED
5	(5)	CHARACTER	1	RCWKFLG2	QUEUE FLAGS
		1...		RCWKFOR	1 => QUEUE IS CIRCULAR IN THE FORWARD DIRECTION
		.1..		RCWKBAC	1 => QUEUE IS CIRCULAR IN THE BACKWARDS DIRECTION
		..11 1111		*	RESERVED
6	(6)	CHARACTER	1	RCWKPFLG	VSWK PROCESSING FLAGS
		1...		RCWKRFIX	0 => DON'T RELEASE VSMFIX LOCK 1 => RELEASE VSMFIX LOCK
		.1..		RCWKENT	0 => BRANCH ENTRY 1 => SVC ENTRY
		..1.		RCWKGLBL	0 => NOT GLOBAL BRANCH ENTRY 1 => GLOBAL BRANCH ENTRY
		...1		RCWKRPAG	1 => DON'T RELEASE VSMPAG LOCK 0 => RELEASE VSMPAG LOCK
		.... 1...		RCWKSTAT	0 => CALLER IS IN SUPERVISOR STATE 1 => CALLER IS IN PROBLEM PROGRAM STATE
		.... .1..		RCWKLST	0 => THIS IS NOT A LIST REQUEST 1 => THIS IS A LIST REQUEST
		.... ..1.		RCWKRCUR	0 => THIS IS NOT A RECOVERY RECURSION 1 => THIS IS A RECOVERY RECURSION
		1		RCWKFSP	0 => THIS IS NOT SUBPOOL FREEMAIN 1 => THIS IS A SUBPOOL FREEMAIN
7	(7)	CHARACTER	3	RCWKABD	EBCDIC ABEND CODE
10	(A)	CHARACTER	2	*	Need for fullword align
12	(C)	UNSIGNED	4	RCWKLENG	LENGTH OF AREA TO BE VERIFIED
16	(10)	ADDRESS	4	RCWKHEAD	ADDRESS OF THE QUEUE HEADER
20	(14)	ADDRESS	4	RCWKTRAL	ADDRESS OF THE QUEUE TRAILER
24	(18)	ADDRESS	4	RCWKMADR	ADDRESS OF ABENDING MODULE
28	(1C)	CHARACTER	8	RCWKEPID	ENTRY POINT MODULE NAME
36	(24)	SIGNED	2	RCWKNEXT	OFFSET IN A QUEUE ELEMENT TO THE NEXT POINTER
38	(26)	SIGNED	2	RCWKPREV	OFFSET IN A QUEUE ELEMENT TO THE PREVIOUS POINTER
40	(28)	SIGNED	4	RCWKVRAP	ADDRESS OF THE NEXT AVAILABLE AREA IN THE VRA

RCWK Constants

Len	Type	Value	Name	Description
0	BIT	0	RCWKSQA	TYPE IS SQA
0	BIT	1	RCWKLSQA	TYPE IS LSQA

Comment

RCVRASQA 200 - AREA IS NOT IN SQA OR SQA CELLPOOL AS REQUESTED
4 BYTES-ADDRESS OF AREA REQUESTED
2 BYTES-LENGTH OF AREA

End of Comment

1	DECIMAL	200	RCVRASQA

Comment

RCVRALSQ 201 - AREA IS NOT IN LSQA OR LSQA CELLPOOL AS REQUESTED
4 BYTES-ADDRESS OF AREA REQUESTED
2 BYTES-LENGTH OF AREA

End of Comment

1	DECIMAL	201	RCVRALSQ

Comment

RCVRAPOS 202 - SIZE OF AREA DESCRIBED BY A VSM CONTROL BLOCK IS
ZERO OR GREATER THAN '7FFFFFFF'X
4 BYTES-CONTROL BLOCK ID
4 BYTES-ADDR OF BLOCK PREVIOUS TO BLOCK WITH ERROR
4 BYTES-ADDR OF BLOCK WITH ERROR

End of Comment

1	DECIMAL	202	RCVRAPOS

Comment

RCVRASIZ 203 - SIZE OF AREA DESCRIBED BY A VSM CONTROL BLOCK IS
NOT A PROPER MULTIPLE
4 BYTES-CONTROL BLOCK ID
4 BYTES-ADDR OF BLOCK PREVIOUS TO BLOCK WITH ERROR
4 BYTES-ADDR OF BLOCK WITH ERROR

End of Comment

1	DECIMAL	203	RCVRASIZ

Comment

RCVRABDY 204 - AREA DESCRIBED BY A VSM CONTROL BLOCK IS NOT ON
PROPER BOUNDARY
4 BYTES-CONTROL BLOCK ID
4 BYTES-ADDR OF BLOCK PREVIOUS TO BLOCK WITH ERROR
4 BYTES-ADDR OF BLOCK WITH ERROR

End of Comment

1	DECIMAL	204	RCVRABDY

Comment

RCVRAID 206 - INVALID CONTROL BLOCK ID
4 BYTES-EXPECTED CONTROL BLOCK ID
4 BYTES-ADDR OF BLOCK WITH ERROR

End of Comment

1	DECIMAL	206	RCVRAID

Comment

RCVRAFCL 207 - COUNT OF FREE CELLS IN CELLPOOL IS NOT CORRECT
4 BYTES-ADDRESS OF CELLPOOL ANCHORS (VSWKCELA)
4 BYTES-ACTUAL NUMBER OF CELLS COUNTED BY RECOVERY
4 BYTES-EXPECTED NUMBER OF CELLS (VSMCPCNT)

End of Comment

1	DECIMAL	207	RCVRAFCL

Len	Type	Value	Name	Description
			Comment	
			RCVRAADF 208 - DFE ON THE ADDRESS QUEUE IS NOT ON THE SIZE QUEUE. IF NO OTHER ERRORS ARE FOUND WITH THIS DFE THEN THE DFE IS ENQUEUED ON THE SIZE QUEUE. ELSE IT IS DEQUEUED FROM THE ADDRESS QUEUE. 4 BYTES-ADDR OF PREVIOUS DFE ON THE ADDRESS QUEUE 4 BYTES-ADDR OF PREVIOUS DFE ON THE SIZE QUEUE (THIS DATA MAY BE INVALID) 4 BYTES-ADDR OF DFE IN ERROR	
			End of Comment	
1	DECIMAL	208	RCVRAADF	
			Comment	
			RCVRASDF 209 - DFE ON THE SIZE QUEUE IS NOT ON THE ADDRESS QUEUE. IF NO OTHER ERRORS ARE FOUND WITH THIS DFE THEN THE DFE IS ENQUEUED ON THE ADDRESS QUEUE. ELSE IT IS DEQUEUED FROM THE SIZE QUEUE. 4 BYTES-ADDR OF PREVIOUS DFE ON THE ADDRESS QUEUE (THIS DATA MAY BE INVALID) 4 BYTES-ADDR OF PREVIOUS DFE ON THE SIZE QUEUE 4 BYTES-ADDR OF DFE IN ERROR	
			End of Comment	
1	DECIMAL	209	RCVRASDF	
			Comment	
			RCVRAORD 210 - SUBPOOL ID'S AND KEYS ARE NOT IN ORDER IN THE SPQE QUEUE 4 BYTES-CONTROL BLOCK ID 4 BYTES-ADDR OF PREVIOUS BLOCK PROCESSED ON QUEUE 4 BYTES-ADDR OF CURRENT BLOCK BEING PROCESSED	
			End of Comment	
1	DECIMAL	210	RCVRAORD	
			Comment	
			RCVRAWA4 211 - VSWK STACK POINTER IS NOT WITHIN THE STACK AREA 4 BYTES-ADDRESS OF VSWK	
			End of Comment	
1	DECIMAL	211	RCVRAWA4	
			Comment	
			RCVRADBL 212 - DOUBLY TREADED ELEMENT OR ELEMENTS DEQUEUED 4 BYTES-CASE NUMBER 4 BYTES-ADDR1 4 BYTES-ADDR2 FOR EACH CASE, ADDR1 AND ADDR2 ARE AS FOLLOWS: CASE NUMBER = 1 A BACKWARD POINTER IS INCORRECT. (IN PROCESS OF ENQUEUE) ADDR1 = THE ADDRESS OF THE ELEMENT IN THE FORWARD DIRECTION THAT DOES NOT HAVE A VALID PREVIOUS POINTER. ADDR2 = THE ADDRESS OF THE ELEMENT IN THE BACKWARD DIRECTION THAT DOES NOT HAVE A VALID NEXT POINTER. CASE NUMBER = 2 A FORWARD POINTER IS INCORRECT (IN PROCESS OF DEQUEUE) ADDR1 = THE ADDRESS OF THE ELEMENT IN THE FORWARD DIRECTION THAT DOES NOT HAVE A VALID PREVIOUS POINTER.	

Len	Type	Value	Name	Description
		ADDR2 = THE ADDRESS OF THE ELEMENT IN THE BACKWARD DIRECTION THAT DOES NOT HAVE A VALID NEXT POINTER.		
		CASE NUMBER = 3 UNEXPECTED ERROR DETECTED		
		ADDR1 = THE ADDRESS OF THE ELEMENT IN THE FORWARD DIRECTION THAT DOES NOT HAVE A VALID PREVIOUS POINTER.		
		ADDR2 = THE ADDRESS OF THE ELEMENT IN THE BACKWARD DIRECTION THAT DOES NOT HAVE A VALID NEXT POINTER.		
		CASE NUMBER = 4 AN INVALID PREVIOUS POINTER - NO ERROR FOUND IN THE BACKWARD DIRECTION. QUEUE NON-CIRCULAR IN BACKWARD DIRECTION		
		ADDR1 = THE ADDRESS OF THE ELEMENT IN THE FORWARD DIRECTION THAT DOES NOT HAVE A VALID PREVIOUS POINTER.		
		ADDR2 = THE ADDRESS OF THE ELEMENT THAT IS THE BACKWARD TRAILER.		
		CASE NUMBER = 5 THE TRAILER APPEARS TO HAVE AN INVALID PREVIOUS POINTER (EQUIVALENT TO CASE 2)		
		ADDR1 = THE ADDRESS OF THE LAST VALID ELEMENT IN THE FORWARD DIRECTION		
		ADDR2 = THE ADDRESS OF THE BACKWARD TRAILER		
		CASE NUMBER = 6 THE TRAILER HAS AN INVALID PREVIOUS POINTER (EQUIVALENT TO CASE 1)		
		ADDR1 = THE ADDRESS OF THE LAST VALID ELEMENT IN THE FORWARD DIRECTION		
		ADDR2 = THE ADDRESS OF THE BACKWARD TRAILER		
		CASE NUMBER = 7 INVALID BACKWARD POINTER AND THERE IS NO TRAILER IN THE BACKWARD DIRECTION (EQUIVALENT TO CASE 1)		
		ADDR1 = THE ADDRESS OF THE ELEMENT IN THE FORWARD THAT DOES NOT HAVE A VALID PREVIOUS POINTER		
		ADDR2 = THE ADDRESS OF THE LAST VALID ELEMENT IN THE FORWARD DIRECTION		
		CASE NUMBER = 8 INVALID FORWARD POINTER AND THERE IS NO TRAILER IN THE BACKWARD DIRECTION EQUIVALENT TO CASE 2		
		ADDR1 = THE ADDRESS OF THE ELEMENT THAT HAS AN INVALID BACKWARD POINTER		
		ADDR2 = THE ADDRESS OF THE ELEMENT THAT POINTS TO ADDRESS ONE		
		CASE NUMBER = 9 UNEXPECTED ERROR DETECTED AND THERE IS NO TRAILER IN THE BACKWARD DIRECTION EQUIVALENT TO CASE 3- UNEXPECTED CASE		
		ADDR1 = THE ADDRESS OF THE ELEMENT IN THE FORWARD DIRECTION THAT DOES NOT HAVE A VALID PREVIOUS POINTER.		
		ADDR2 = THE ADDRESS OF THE ELEMENT THAT POINTS TO ADDRESS ONE		
		CASE NUMBER = 10 2ND ADDRESS BACK IS INVALID AND THERE'S NO TRAILER IN THE BACKWARD DIRECTION		
		ADDR1 = THE ADDRESS OF THE ELEMENT THAT HAS AN INVALID BACKWARD POINTER		
		ADDR2 = THE ADDRESS OF THE ELEMENT THAT POINTS TO ADDRESS ONE		
		CASE NUMBER = 11 FORWARD IS INVALID AND THERE IS NO TRAILER IN THE BACKWARD DIRECTION		
		ADDR1 = THE ADDRESS OF THE INVALID FORWARD ELEMENT		
		ADDR2 = THE ADDRESS OF THE ELEMENT THAT POINTS TO ADDRESS ONE		
		CASE NUMBER =12 NON CIRCULAR IN THE FORWARD DIRECTION AND FORWARD IS THE HEADER ADDRESS		
		ADDR1 = THE ADDRESS OF THE HEADER		
		ADDR2 = THE ADDRESS OF THE ELEMENT THAT POINTS TO THE HEADER		
		End of Comment		
1	DECIMAL	212	RCVRADBL	

Len	Type	Value	Name	Description
			Comment	
			RCVRASNG 213 - SINGLY THREADED QUEUE IS CIRCULAR 4 BYTES-ADDR OF PREVIOUS BLOCK PROCESSED ON QUEUE (QUEUE WAS TERMINATED HERE) 4 BYTES-ADDR OF CURRENT BLOCK BEING PROCESSED	
			End of Comment	
1	DECIMAL	213	RCVRASNG	
			Comment	
			RCVRAFQE 214 - FQE OR FBQE IS NOT IN THE BOUNDS OF ITS DQE OR RD. THE FQE IS DEQUEUED. 4 BYTES-CONTROL BLOCK ID 4 BYTES-ADDR OF PREVIOUS BLOCK PROCESSED ON QUEUE 4 BYTES-ADDR OF ELEMENT WITH ERROR	
			End of Comment	
1	DECIMAL	214	RCVRAFQE	
			Comment	
			RCVRAWA0 215 - POINTER TO THE VSWK IS ZERO. IF THE 4 BYTES RECORDED ARE ZERO THEN THE ADDRESS OF THE VSWK IN THE FRR PARAMETER LIST IS ZERO. THIS IS AN ERROR. IF THE 4 BYTES ARE NOT ZERO THEN IT IS THE ADDRESS OF THE VSWK IN USE AT THE TIME OF THE ERROR. THIS IS RECORDED FOR INFORMATION ONLY. 4 BYTES-ADDRESS OF VSWK	
			End of Comment	
1	DECIMAL	215	RCVRAWA0	
			Comment	
			RCVRASCK 216 - AREA BEING VALIDATED IS IN STORAGE CHECK AREA 4 BYTES-CONTROL BLOCK ID IF KNOWN OR BLANKS 4 BYTES-ADDRESS OF AREA BEING VALIDATED 4 BYTES-LENGTH OF AREA BEING VALIDATED	
			End of Comment	
1	DECIMAL	216	RCVRASCK	
			Comment	
			RCVRAOVL 217 - OVERLAP DETECTED IN CONTROL BLOCK 4 BYTES-CONTROL BLOCK ID 4 BYTES-ADDRESS OF AREA 4 BYTES-LENGTH OF AREA 4 BYTES-ADDRESS OF CONTROL BLOCK	
			End of Comment	
1	DECIMAL	217	RCVRAOVL	
			Comment	
			RCVRAVWA 218 - RECORD AS MUCH OF VSWK AS POSSIBLE X BYTES-VSWK	
			End of Comment	
1	DECIMAL	218	RCVRAVWA	
			Comment	
			RCVRAWA1 219 - A GLOBAL VSWK IS NOT ADDRESSED BY THE GDA 4 BYTES-ADDRESS OF VSWK	
			End of Comment	
1	DECIMAL	219	RCVRAWA1	

Len	Type	Value	Name	Description
			Comment	
			RCVRAOOO 220 - ADDRESSES OF AREAS DESCRIBED BY CONSECUTIVE ELEMENTS OVERLAP OR ARE NOT IN ASCENDING ORDER. THE CONSECUTIVE ELEMENTS IN ERROR ARE DEQUEUED. 4 BYTES-CONTROL BLOCK ID 4 BYTES-ADDR OF PREVIOUS BLOCK PROCESSED ON QUEUE 4 BYTES-ADDR OF FIRST ELEMENT WITH ERROR 4 BYTES-ADDR OF SECOND ELEMENT WITH ERROR	
			End of Comment	
1	DECIMAL	220	RCVRAOOO	
			Comment	
			RCVRASPQ 221 - AN SPQE WAS FOUND THAT WAS NEITHER SHARED NOR OWNED. THE SPQE IS MARKED OWNED. 4 BYTES-ADDR OF SPQE WITH ERROR	
			End of Comment	
1	DECIMAL	221	RCVRASPQ	
			Comment	
			RCVRAWA2 222 - A LOCAL VSWK IS NOT ADDRESSED BY THE LDA 4 BYTES-ADDRESS OF VSWK	
			End of Comment	
1	DECIMAL	222	RCVRAWA2	
			Comment	
			RCVRAWA3 223 - INVALID REQUEST TYPE IN THE VSWK. REQUEST TYPE MUST BE CSA,SQA,LSQA OR PVT. 8 BYTES-VSWKSPTT	
			End of Comment	
1	DECIMAL	223	RCVRAWA3	
			Comment	
			RCVRAPVT 224 - UNEXPECTED RETURN CODE FROM IGVRSRCH 4 BYTES-CONTROL BLOCK ID 4 BYTES-ADDRESS OF AREA 4 BYTES-LENGTH OF AREA 4 BYTES-RETURN CODE FROM IGVRSRCH	
			End of Comment	
1	DECIMAL	224	RCVRAPVT	
			Comment	
			RCVRAADO 225 - A DFE IS FOUND WHICH IS OUT OF ADDRESS ORDER. THE DFE AND THE PREVIOUS DFE ON THE ADDRESS QUEUE ARE DEQUEUED FROM THE ADDRESS AND SIZE QUEUES. 4 BYTES-ADDR OF PREVIOUS DFE THAT REMAINS ON THE ADDRESS QUEUE 4 BYTES-PREVIOUS DFE THAT IS DEQUEUED 4 BYTES-DFE THAT IS DEQUEUED	
			End of Comment	
1	DECIMAL	225	RCVRAADO	
			Comment	
			RCVRAADD 226 - A DUMMY DFE IS FOUND WHICH IS OUT OF ADDRESS ORDER. THE DUMMY DFE IS ENQUEUED ON THE FRONT OF THE ADDRESS QUEUE. 4 BYTES-ADDR OF PREVIOUS DFE ON THE ADDRESS QUEUE WHERE THE DUMMY DFE WAS DEQUEUED 4 BYTES-ADDR OF DUMMY DFE	
			End of Comment	

Len	Type	Value	Name	Description
1	DECIMAL	226	RCVRAADD	

Comment

RCVRASZO 227 - A DFE IS FOUND WHICH IS OUT OF SIZE ORDER.
THE DFE AND THE PREVIOUS DFE ON THE SIZE QUEUE
ARE DEQUEUED FROM THE ADDRESS AND SIZE QUEUES.
4 BYTES-ADDR OF PREVIOUS DFE THAT REMAINS ON THE
SIZE QUEUE
4 BYTES-PREVIOUS DFE THAT IS DEQUEUED
4 BYTES-DFE THAT IS DEQUEUED

End of Comment

1	DECIMAL	227	RCVRASZO	

Comment

RCVRADUM 228 - AN SQA DUMMY DFE HAS BEEN OVERLAID. THE DUMMY IS
ENQUEUED IN THE PROPER POSITION ON THE ADDRESS
AND SIZE QUEUES.
4 BYTES-ADDR OF PREVIOUS DFE ON THE SIZE QUEUE
WHERE THE DUMMY DFE WAS DEQUEUED.
(THIS DATA MAY BE INVALID)
4 BYTES-ADDR OF DUMMY DFE

End of Comment

1	DECIMAL	228	RCVRADUM	

Comment

RCVRADCT 229 - THE LSQA DUMMY DFE COUNT IS IN ERROR. IF POSSIBLE,
THE NEEDED NUMBER OF DUMMIES ARE OBTAINED AND
ENQUEUED IN THE PROPER POSITIONS ON THE ADDRESS
AND SIZE QUEUES. IF THERE ARE NO CELLS AVAILABLE
THEN THE ADDRESS SPACE IS TERMINATED.
4 BYTES-ADDR OF DFE QUEUE ANCHOR
4 BYTES-ACTUAL DUMMY DFE COUNT
4 BYTES-EXPECTED DFE COUNT

End of Comment

1	DECIMAL	229	RCVRADCT	

Comment

RCVRABDF 230 - A DFE THAT IS ON THE SIZE QUEUE BUT NOT ON THE
ADDRESS QUEUE OVERLAPS SPACE ALREADY ON THE
ADDRESS QUEUE. THE DFE IS DEQUEUED FROM THE SIZE
QUEUE.
4 BYTES-ADDR PREVIOUS DFE THAT REMAINS ON THE
SIZE QUEUE
4 BYTES-ADDR DFE IN ERROR

End of Comment

1	DECIMAL	230	RCVRABDF	

Comment

RCVRAAQT 231 - A DFE IS IN ERROR BECAUSE SPACE DESCRIBED BY THE
DFE IS NOT PART OF L/SQA. THIS ERROR COULD BE
CAUSED BY:
1) DFEAREA IS ZERO OR NEGATIVE
2) DFESIZE IS ZERO OR NEGATIVE
3) THE AREA DESCRIBED BY DFEAREA AND DFESIZE IS
NOT DESCRIBED BY AQAT ALLOCATION BITS (SOME OF
THE ALLOCATION BITS ARE OFF)
THE DFE IS DEQUEUED FROM THE ADDRESS AND SIZE
QUEUES.
4 BYTES-ADDR OF DFE IN ERROR
4 BYTES-DFEAREA
4 BYTES-DFESIZE

End of Comment

1	DECIMAL	231	RCVRAAQT	

RCWK Cross Reference

Len	Type	Value	Name	Description
		Comment		
		RCVRADAD 232 - A DFE IS IN ERROR BECAUSE DFEAREA IS NOT ON A DOUBLEWORD BOUNDARY. THE DFE IS DEQUEUED FROM THE ADDRESS AND SIZE QUEUES. 4 BYTES-ADDR OF DFE IN ERROR 4 BYTES-DFEAREA 4 BYTES-DFESIZE		
		End of Comment		
1	DECIMAL	232	RCVRADAD	
		Comment		
		RCVRADSZ 233 - A DFE IS IN ERROR BECAUSE DFESIZE IS NOT A DOUBLEWORD MULTIPLE. THE DFE IS DEQUEUED FROM THE ADDRESS AND SIZE QUEUES. 4 BYTES-ADDR OF DFE IN ERROR 4 BYTES-DFEAREA 4 BYTES-DFESIZE		
		End of Comment		
1	DECIMAL	233	RCVRADSZ	
		Comment		
		RCVRADML 234 - AN SQA DUMMY DFE IS IN ERROR. ONE OF THE FOLLOWING HAS OCCURRED: 1) NO DUMMY DFES WHERE FOUND ON THE SIZE QUEUE. 2) THE LAST DUMMY FOUND ON THE SIZE QUEUE WAS ALSO ANCHORED IN THE SQAT. THE LAST DUMMY ON THE SIZE QUEUE SHOULD NOT BE ANCHORED IN THE SQAT. 4 BYTES-NUMBER OF DUMMY DFES FOUND ON THE SIZE QUEUE 4 BYTES-ADDR OF LAST DUMMY FOUND ON THE SIZE QUEUE (THIS DATA WILL BE INVALID IF THE NUMBER OF DFES FOUND IS ZERO)		
		End of Comment		
1	DECIMAL	234	RCVRADML	
		Comment		
		RCVRAEXT 235 - COUNT OF CELLPOOL EXTENTS IS NOT CORRECT 4 BYTES-ADDRESS OF CELLPOOL ANCHORS (VSWKCELA) 4 BYTES-ACTUAL NUMBER OF EXTENTS COUNTED BY RECOVERY 4 BYTES-EXPECTED NUMBER OF EXTENTS (VSWKPNUM)		
		End of Comment		
1	DECIMAL	235	RCVRAEXT	

RCWK Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
RCWK	0		RCWKLENG	C	
RCWKABD	7		RCWKLST	6	04
RCWKABND	4	04	RCWKMADR	18	
RCWKADDR	0		RCWKNEXT	24	
RCWKBAC	5	40	RCWKPERC	4	08
RCWKBACK	4	02	RCWKPFLG	6	
RCWKCELL	4	40	RCWKPREV	26	
RCWKCERR	4	20	RCWKRCUR	6	02
RCWKENT	6	40	RCWKRET	4	10
RCWKEPID	1C		RCWKRFIX	6	80
RCWKFLG1	4		RCWKRPAG	6	10
RCWKFLG2	5		RCWKSTAT	6	08
RCWKFOR	5	80	RCWKTRAL	14	
RCWKFSP	6	01	RCWKTYPE	4	80
RCWKGLBL	6	20	RCWKVRAP	28	
RCWKHEAD	10				

RD Information

RD Heading Information

Common Name: VSM REGION DESCRIPTOR
Macro ID: IHARD
DSECT Name: RD
Owning Component: Virtual Storage Manager (SC1CH)
Eye-Catcher ID: None
Storage Attributes: Subpool: 245 or 255
Key: 0
Residency: Above 16M line
Size: 16 BYTES
Created by: IEAIPL04, IEAVNP08, IGVGCAS
Pointed to by: TCBRD, TCBERD
Serialization: VSMFIX lock for global subpools
LOCAL lock for private area subpools
Function: DESCRIBES THE CSA REGION, SYSTEM REGION, V=V REGION OR V=R REGION SPACE.

RD Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	RD	REGION DESCRIPTOR
0	(0)	ADDRESS	4	RDFBQEF	ADDRESS OF FIRST FBQE ON THE REGION'S FBQE QUEUE
4	(4)	ADDRESS	4	RDFBQEL	ADDRESS OF LAST FBQE ON THE REGION'S FBQE QUEUE
8	(8)	ADDRESS	4	RDSTART	LOWEST ADDRESS IN THE REGION
12	(C)	UNSIGNED	4	RDSIZE	SIZE OF THE REGION

RDCM Information

RDCM Heading Information

Common Name: RESIDENT DISPLAY CONTROL MODULE MAPPING MACRO
Macro ID: IEERDCM
DSECT Name: DCMTSRT
Owning Component: Console (SC1C4)
Eye-Catcher ID: 'RDCM'
Offset: '14'x
Length: 4
Storage Attributes: Subpool: 229 or 239 for HMCS consoles
Key: 0
Residency: 31-bit storage
Size: 76 BYTES PLUS 40 BYTES FOR EACH SACB.
Created by: IEECVFTW (1 PER ACTIVE DISPLAY CONSOLE)
Pointed to by: UCMXB FIELD OF THE UCME DATA AREA
DCMTRDCM FIELD OF THE TDCM DATA AREA
Serialization: LOCAL AND CMS LOCKS
Function: THIS MACRO MAPS THE RESIDENT DISPLAY CONTROL MODULE (RDCM).

RDCM Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0		

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	DCMTSRT	DCMTSPTR
0	(0)	ADDRESS	4	DCMADTRN	POINTER TO PAGEABLE DCM
4	(4)	ADDRESS	1	DCMRVERN	CONTROL BLOCK VERSION
4	(4)	X'1'	0	DCMRSP21	"1" OS/VS2 HBB2102
4	(4)	X'2'	0	DCMRSP22	"2" OS/VS2 JBB2220
4	(4)	X'3'	0	DCMRSP41	"3" MVS/ESA HBB4410
4	(4)	X'4'	0	DCMRSP42	"4" MVS/ESA HBB4420
4	(4)	X'4'	0	DCMVERSN	"DCMRSP42" CURRENT VERSION LEVEL
5	(5)	BITSTRING	1	DCMRFLGS	FLAGS
		1...		DCMRDFPK	"X'80'" DEFAULT PFKS ARE IN USE
		.1..		DCMPFKWK	"X'40'" PFK'S ARE OPERATIONAL
		...1		DCMDOM	"X'10'" DOM MUST BE TRIED
6	(6)	SIGNED	2		Reserved
8	(8)	ADDRESS	4	DCMADKP	ADDRESS OF ROUTED K COMMAND PARAMETER LIST
12	(C)	CHARACTER	1	DCMTOPAR	TOP DISPLAY AREA DEFINED
13	(D)	CHARACTER	1	DCMTOPDS	TOP DISPLAY ON SCREEN
14	(E)	BITSTRING	1	DCMPREVM	CONSOLE'S PREVIOUS OPERATING MODE AND MAP LIKE UCMDISP1 IN UCME
		.... 1...		DCMPREFC	"X'08'" PREVIOUS MODE WAS FC
		.... .1..		DCMPREMS	"X'04'" PREVIOUS MODE WAS MS
		.... ..1.		DCMPRESD	"X'02'" PREVIOUS MODE WAS SD
15	(F)	BITSTRING	1	DCMDEVTY	DEVICE TYPE FLAGS
		1...		DCMTY60	"X'80'" USABLE FOR SD
		..1.		DCMBCOLR	"X'20'" USABLE FOR BASE COLOR
		...1		DCMECOLR	"X'10'" USABLE FOR EXTENDED COLOR
		.... 1...		DCMSPRPQ	"X'08'" DEVICE SUPPORTS READ PARTITION QUERY FUNCTION
		.... .1..		DCMEXTDS	"X'04'" DEVICE SUPPORTS EXTENDED DATA STREAM I/O AND 14 BIT ADDRESSES ON OUTBOUND DATA
		1		DCMIMGLT	"X'01'" DEVICE HAS AN IMAGE LIMIT
16	(10)	ADDRESS	4	DCMADSDS	POINTER TO FIRST SDS SUPPORT AREA
20	(14)	CHARACTER	4	DCMRCBID	CONTROL BLOCK ID - 'RDCM'
24	(18)	ADDRESS	4	DCMWLAST	PT CON Q ENTRY LAST OUT (O-O-L)
28	(1C)	SIGNED	2	DCMRMSAL	NUMBER LINES IN MSG AREA
30	(1E)	SIGNED	2	DCMDOMKY	CONSOLE DOM ELEMENT MC XM5812
32	(20)	SIGNED	4	DCMCBTIM	Time that console went into BUSY and CLOSE PENDING state
36	(24)	ADDRESS	4	DCMADPFK	POINTER TO RESIDENT PFK AREA
40	(28)	SIGNED	2	DCMINTVL	INTERVAL FOR THIS DCM
42	(2A)	SIGNED	2	DCMTMCTR	TIME COUNTER FOR THIS DCM
44	(2C)	BITSTRING	1	DCMR2FLG	TIMER FLAGS
		1...		DCMRXSFL	"X'80'" FULL SCREEN FLAG
		.1..		DCMRXUNV	"X'40'" Unviewable message displayed

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		..1.		DCMRXTMR	"X'20'" TIMER FLAG
		...1		DCMRXRLL	"X'10'" READY TO ROLL
		.... 1...		DCMRXDEL	"X'08'" PENDING DELETE REQUEST
		.... ..1.		DCMRXTIM	"X'02'" TIMER ELAPSED FOR THIS DISPLAY
45	(2D)	BITSTRING	1	DCMR3FLG	MISC FLAGS
		.1..		DCMKVIP	"X'40'" ENTRY FOR K VARY COMMAND
		..1.		DCMCLPR	"X'20'" CLOSE IN PROCESS
		...1		DCMRXSCN	"X'10'" ASY ERROR MESSAGE ON SCREEN
		.... .1..		DCMRXHMT	"X'04'" FULL SCREEN SIMULATED MC YM4102
		.... ..1.		DCMOPEN	"X'02'" IF ON, THE CONSOLE IS BEING OPENED AND INITIALIZE SACBS. NOTE. THIS BIT IS SET BY IEECVFTU TO INDICATE THAT CONSOLE IS BEING OPENED AND IEECVFTG USED THE BIT FOR INITIALIZATION OF SACBS
		1		DCMIFVLD	"X'01'" IF ON, RDCM/TDCM INFORMATION ARE VALIDATED
46	(2E)	SIGNED	2	DCMPFKSZ	LENGTH OF PFK BUFFER
48	(30)	ADDRESS	4	DCMRQDEL	DELETE REQUEST BUFFER
52	(34)	ADDRESS	4	DCMRQDEL_PART2	DELETE REQUEST BUFFER
56	(38)	SIGNED	4	DCMMSGSV	LINE COUNT SAVE AREA FOR IEECVFTP
60	(3C)	ADDRESS	4	DCMPFKBF	PFK BUFFER ADDRESS
64	(40)	ADDRESS	4	DCMDTPTR	Pointer to the DISPLAY/TRACK/STOPTR request attributes
68	(44)	SIGNED	4	DCMLEN	Length of TDCM
68	(44)	X'48'	0	DCMSIZE	"*-DCMTSRT" LENGTH OF RDCM

Comment

SCREEN AREA CONTROL BLOCK (SACB)

End of Comment

68	(44)	X'48'	0	DCMACB	"*" SACB
72	(48)	ADDRESS	4	DCMACBNX	POINTER TO NEXT SACB
76	(4C)	CHARACTER	1	DCMAID	AREA ID
77	(4D)	BITSTRING	1	DCMASACB	SACB FLAGS
		1...		DCMAUSE	"X'80'" AREA PRESENTLY DEFINED MB Y02958

Comment

IF DCMAUSE IS OFF, AN AREA HAS BEEN FREED E.G. K A,NONE

End of Comment

		.1..		DCMAGM	"X'40'" GETMAINED SACB
78	(4E)	SIGNED	2	DCMALN	LENGTH OF AREA
80	(50)	SIGNED	1	DCMATOP	TOP ROW OF AREA
80	(50)	X'51'	0	DCMACLR	"*" REINITIALIZED PORTION MB Y02958
81	(51)	SIGNED	1	DCMAROW	ROW TO BE WRITTEN NEXT
82	(52)	SIGNED	2	DCMAFR	FRAME ON SCREEN
84	(54)	ADDRESS	4	DCMAMJWQ	POINTER TO CON Q ENTRY FOR MAJOR
88	(58)	ADDRESS	4	DCMAMIN	POINTER TO MINOR WQE
92	(5C)	SIGNED	4	DCMATIME	TIME CONTROL LINE WAS WRITTEN
96	(60)	SIGNED	1	DCMANLAB	NUMBER OF LABEL LINES FOUND
97	(61)	BITSTRING	1	DCMARES1	RESERVED
98	(62)	BITSTRING	1	DCMAFLG1	AREA FLAGS1
		.1..		DCMADISP	"X'40'" DISPLAY IN AREA
		..1.		DCMADEND	"X'20'" END OF DISPLAY ON SCREEN
		...1		DCMAFRPR	"X'10'" FRAMING IN PROGRESS
		.... 1...		DCMAFULL	"X'08'" FRAME FULL
		.... .1..		DCMABL	"X'04'" BLANKING TO BE DONE
		.... ..1.		DCMAELLF	"X'02'" EMBEDDED LABEL LINE FOUND
		1		DCMADLF	"X'01'" DATA LINE FOUND WHILE WRITING DISPLAY
99	(63)	BITSTRING	1	DCMAFLG2	AREA FLAGS 2
		1...		DCMALMIN	"X'80'" SAVED POINTER TO LAST MINOR OUTPUT
		.1..		DCMAWCON	"X'40'" WRITE CONTROL LINE
		...1		DCMAMJFR	"X'10'" MAJOR WQE HAS BEEN FOUND
100	(64)	BITSTRING	4	DCMRSV01	Reserved - Was DCMATFLG and contained DCMADFLG, DCMADD and DCMAHOLD
104	(68)	SIGNED	4	DCMRSV02	Reserved - Was DCMATECB
108	(6C)	SIGNED	4	DCMRSV04	Reserved - Was DCMAASCB
108	(6C)	X'70'	0	DCMACBND	"*" SACB END
108	(6C)	X'1F'	0	DCMCLRLN	"*-DCMACLR" LENGTH FOR REINITIALIZING MB Y02958
112	(70)	CHARACTER	4	DCMACBID	ACRONYM = SACB
116	(74)	CHARACTER	4	DCMRSV03	RESERVED
116	(74)	X'30'	0	DCMACBSZ	"*-DCMACB" SACB SIZE

RDCM Cross Reference

Name	Hex Offset	Hex Value
DCMABL	62	4
DCMACB	44	48
DCMACBID	70	
DCMACBND	6C	70
DCMACBNX	48	
DCMACBSZ	74	30
DCMACLR	50	51
DCMADEND	62	20
DCMADISP	62	40
DCMADKP	8	
DCMADLF	62	1
DCMADPFK	24	
DCMADSDS	10	
DCMADTRN	0	
DCMAELLF	62	2
DCMAFLG1	62	
DCMAFLG2	63	
DCMAFR	52	
DCMAFRPR	62	10
DCMAFULL	62	8
DCMAGM	4D	40
DCMAID	4C	
DCMALMIN	63	80
DCMALN	4E	
DCMAMIN	58	
DCMAMJFR	63	10
DCMAMJWQ	54	
DCMANLAB	60	
DCMARES1	61	
DCMAROW	51	
DCMASACB	4D	
DCMATIME	5C	
DCMATOP	50	
DCMAUSE	4D	80
DCMAWCON	63	40
DCMBCOLR	F	20
DCMCBTIM	20	
DCMCLPR	2D	20
DCMCLRLN	6C	1F
DCMDEVTY	F	
DCMDOM	5	10
DCMDOMKY	1E	
DCMDTPTR	40	
DCMECOLR	F	10
DCMEXTDS	F	4
DCMIFVLD	2D	1
DCMIMGLT	F	1
DCMINTVL	28	
DCMKVIP	2D	40
DCMLEN	44	
DCMMSGSV	38	
DCMOPEN	2D	2
DCMPFKBF	3C	
DCMPFKSZ	2E	
DCMPFKWK	5	40
DCMPREFC	E	8
DCMPREMS	E	4
DCMPRESD	E	2
DCMPREVM	E	
DCMRCBID	14	
DCMRDFPK	5	80
DCMRFLGS	5	
DCMRMSAL	1C	
DCMRQDEL	30	
DCMRQDEL_PART2	34	
DCMRSP21	4	1
DCMRSP22	4	2
DCMRSP41	4	3
DCMRSP42	4	4
DCMRSV01	64	
DCMRSV02	68	
DCMRSV03	74	
DCMRSV04	6C	
DCMRVERN	4	
DCMRXDEL	2C	8
DCMRXHMT	2D	4
DCMRXRLL	2C	10
DCMRXSCN	2D	10
DCMRXSFL	2C	80
DCMRXTIM	2C	2
DCMRXTMR	2C	20
DCMRXUNV	2C	40
DCMR2FLG	2C	
DCMR3FLG	2D	
DCMSIZE	44	48
DCMSPRPQ	F	8
DCMTMCTR	2A	
DCMTOPAR	C	
DCMTOPDS	D	
DCMTSRT	0	
DCMTY60	F	80
DCMVERSN	4	4
DCMWLAST	18	

RESPA information

RESPA Programming Interface information

Programming Interface information

RESPA

End of Programming Interface information

RESPA Heading Information

Common Name: Response Area
Macro ID: IAZRESPA
DSECT Name: IAZRESPA
Owning Component: JES2 (SC141)
Eye-Catcher ID: RESP
Offset: 0
Length: 4

RESPA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	IAZRESPA	
0	(0)	X'0'	0	RESPA	"IAZRESPA" ALTERNATE DSECT NAME
0	(0)	CHARACTER	4	RESPID	RESPONSE AREA ID
4	(4)	SIGNED	4	RESPLEN	LENGTH OF RESPONSE AREA
8	(8)	ADDRESS	1	RESPFL1	FLAG BYTE (DEVICE STATUS)
		1...		RESP1DIN	"B'10000000'" DEVICE IS INACTIVE
8	(8)	X'80'	0	RESPDIN	"RESP1DIN" (ALTERNATE FLAG NAME)
		.1..		RESP1DSP	"B'01000000'" DEVICE IS STOPPED
9	(9)	ADDRESS	1	RESPFL2	FLAG BYTE (PROCESSING STATUS)
		1...		RESP2EOD	"B'10000000'" EOD REACHED ON FWD SYNCH
		.1..		RESP2NDS	"B'01000000'" NO DS AT OOP DETECTED
		..1.		RESP2ETE	"B'00100000'" ENVIRONMENTAL TYPE ERROR - USED ONLY ON START FSA ORDER RESPONSE
10	(A)	ADDRESS	2		RESERVED
12	(C)	SIGNED	4	RESPRETC	RETURN CODE OF REQUESTED FUNCTION - 00 = SUCCESSFUL COMPLETION - >0 = UNSUCCESSFUL COMPLETION
16	(10)	SIGNED	4		RESERVED
20	(14)	SIGNED	2	RESPCPYC	COPY NUMBER OF DATA SET AT OOP
22	(16)	SIGNED	2		RESERVED
24	(18)	SIGNED	4	RESPPGEC	PAGE NUMBER OF DATA SET AT OOP
28	(1C)	SIGNED	4	RESPLREC	LOGICAL REC NUM AT OOP (APPROX)
32	(20)	CHARACTER	12	RESPOOPI	IDENTIFIER OF DATA SET AT OOP
44	(2C)	ADDRESS	4	RESPEXTN	RESERVED POINTER
48	(30)	SIGNED	4	(3)	RESERVED
60	(3C)	SIGNED	4	(0)	BOUNDARY ALIGNMENT
60	(3C)	X'3C'	0	RESPSIZ	"*-RESPA" RESPONSE AREA SIZE

RESPA Cross Reference

Name	**Hex Offset**	**Hex Value**
IAZRESPA	0	
RESPA	0	0
RESPCPYC	14	
RESPDIN	8	80
RESPEXTN	2C	
RESPFL1	8	
RESPFL2	9	
RESPID	0	
RESPLEN	4	
RESPLREC	1C	
RESPOOPI	20	
RESPPGEC	18	
RESPRETC	C	
RESPSIZ	3C	3C
RESP1DIN	8	80
RESP1DSP	8	40
RESP2EOD	9	80
RESP2ETE	9	20
RESP2NDS	9	40

RGR Information

RGR Heading Information

Common Name: VSM Region Request Element
Macro ID: IHARGR
DSECT Name: RGR
Owning Component: Virtual Storage Manager (SC1CH)
Eye-Catcher ID: None
Storage Attributes: Subpool: 245
Key: 0
Residency: Above 16M line
Size: 24 bytes
Created by: IGVGRRGN
Pointed to by: GDARGR, RGRNEXT
Serialization: VSMFIX lock
Function: Describes a request waiting for a V=R region.

RGR Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	24	RGR	REGION REQUEST ELEMENT
0	(0)	CHARACTER	4	RGRID	CONTROL BLOCK IDENTIFIER
4	(4)	ADDRESS	4	RGRNEXT	ADDRESS OF NEXT RGR OR ZERO
8	(8)	ADDRESS	4	RGRASCB	ASCB ADDRESS OF REQUESTING INITIATOR
12	(C)	ADDRESS	4	RGRECB	ECB ADDRESS OF REQUESTING INITIATOR
16	(10)	SIGNED	4	RGRSIZE	SIZE OF REGION REQUESTED
20	(14)	ADDRESS	4	RGRSTART	START ADDRESS OF SPECIFIC REGION REQUESTED OR ZERO

RIB Information

RIB Programming Interface information

Programming Interface information

RIB

End of Programming Interface information

RIB Heading Information

Common Name: RESOURCE INFORMATION BLOCK RESOURCE INFORMATION BLOCK EXTENT
Macro ID: ISGRIB
DSECT Name: RIB RIBE
Owning Component: Global Resource Serialization (SCSDS)
Eye-Catcher ID: None
Storage Attributes: Subpool: 229 WHILE IN GRS PRIVATE AREA
Key: 0 WHILE IN GRS PRIVATE AREA
Residency: Above 16M while in GRS Private Area
Size: RIB - 40 BYTES FOR THE FIXED SECTION AND
N BYTES FOR THE VARIABLE SECTION
(WHERE N IS A MULTIPLE OF FOUR
IN THE RANGE OF 4 TO 256)
RIBE - 48 BYTES
Created by: THE GLOBAL RESOURCE SERIALIZATION (GRS) QUEUE SCANNING MODULE (ISGQSC) BUILDS THE RIBS AND RIBES IN THE GRS PRIVATE AREA BEFORE MOVING THEM INTO THE AREA PROVIDED BY THE CALLER OF THE GQSCAN MACRO.
Pointed to by: POINTER IS MAINTAINED BY USER
Serialization: NO SERIALIZATION REQUIRED
Function: CONTAINS INFORMATION DESCRIBING A RESOURCE AND ANY REQUESTORS OF THAT RESOURCE. THE RESOURCE INFORMATION BLOCK (RIB) DESCRIBES A GIVEN RESOURCE AND THE RESOURCE INFORMATION BLOCK EXTENT (RIBE) DESCRIBES EACH OWNER OR EACH WAITER FOR THAT RESOURCE. THE VARIABLE SECTION OF THE RIB (RIBVAR) IS LOCATED IMMEDIATELY AFTER THE RIB.
For ISGECA requests only, the RIB can be used as follows:
1) Long Waiter List
There will be up to 'count' resources reflected in the list.
Each resource will have a single RIB (reflecting the resource in contention), one RIBE for the top blocker of the resource, and one RIBE for the long waiter of the resource. RIBTOD will be set to the time of day that the long waiter ENQueued on the resource. Other important fields:
RIBNTO - total number of owners of the resource
RIBNTWE - total number of exclusive waiters of the resource
RIBNTWS - total number of shared waiters of the resource
RIBTRIBE and RIBNRIBE - will be set to two
Each of the RIBEs will be set to the appropriate information obtained from the QEL/QXB representing the resource request
2) Long Blocker List
There will be up to 'count' resources reflected in the list.
Each resource will have a single RIB (reflecting the resource in contention) and one RIBE for the top blocker of the resource. RIBTOD will be set to the time of day that the long blocker began blocking the resource (not when it ENQueued on the resource).
RIBNTO - total number of owners of the resource
RIBNTWE - total number of exclusive waiters of the resource
RIBNTWS - total number of shared waiters of the resource
RIBTRIBE and RIBNRIBE - will be set to one
The RIBE will be set to the appropriate information obtained from the QEL/QXB representing the resource request

RIB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RIB	RESOURCE INFORMATION BLOCK - FIXED SECTION
0	(0)	SIGNED	4	(0)	
0	(0)	SIGNED	4	RIBTOD (2)	FOR ISGECA REQUESTS ONLY, THE TIME OF DAY A RESOURCE REQUEST BEGAN WAITING
0	(0)	ADDRESS	4		RESERVED
4	(4)	ADDRESS	4	RIBCHAIN	POINTER TO NEXT RIB - USED BY ISGDSORT TO ALPHABETICALLY SORT RIBS BY RESOURCE NAME (QNAME AMD RNAME)
8	(8)	SIGNED	4	RIBNTO	NUMBER OF TASKS OWNING RESOURCE
12	(C)	SIGNED	4	RIBNTWE (0)	NUMBER OF TASKS WAITING FOR EXCLUSIVE CONTROL OF RESOURCE
12	(C)	SIGNED	4	RIBQSCANRC	FOR ISGECA REQUESTS ONLY, IF RIBNTO EQUALS -1 THEN THIS FIELD CONTAINS THE FAILING QSCAN RETURN CODE
16	(10)	SIGNED	4	RIBNTWS (0)	NUMBER OF TASKS WAITING FOR SHARED CONTROL OF RESOURCE
16	(10)	SIGNED	4	RIBQSCANRSN	FOR ISGECA REQUESTS ONLY, IF RIBNTO EQUALS -1 THEN THIS FIELD CONTAINS THE FAILING QSCAN REASON CODE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
20	(14)	SIGNED	4	RIBTRIBE	TOTAL NUMBER OF RESOURCE INFORMATION BLOCK EXTENTS ASSOCIATED WITH THIS RIB
24	(18)	SIGNED	4	RIBNRIBE	NUMBER OF RESOURCE INFORMATION BLOCK EXTENTS RETURNED WITH THIS RIB IN THE USER SPECIFIED AREA
28	(1C)	SIGNED	2	RIBVLEN	LENGTH OF THE VARIABLE SECTION OF THE RIB (MULTIPLE OF FOUR)
30	(1E)	BITSTRING	1	RIBSCOPE	SCOPE OF REQUEST FLAGS
		1...		RIBSYS	"X'80'" SYSTEM SCOPE (1 = SYSTEM, 0 = NONSYSTEM)
		.1..		RIBSYSS	"X'40'" SYSTEMS SCOPE (1 = SYSTEMS, 0 = NONSYSTEMS)
		..1.		RIBSTEP	"X'20'" STEP SCOPE (1 = STEP, 0 = NONSTEP)
		...1		RIBGLBL	"X'10'" GLOBAL RESOURCE INDICATOR (1 = GLOBAL, 0 = LOCAL)
		.... 1...		RIBBIT1	"X'08'" RESERVED
		.... .1..		RIBBIT2	"X'04'" RESERVED
		.... ..1.		RIBBIT3	"X'02'" RESERVED
		1		RIBBIT4	"X'01'" RESERVED
31	(1F)	BITSTRING	1	RIBRNMLN	RNAME LENGTH
32	(20)	CHARACTER	8	RIBQNAME	QNAME - MAJOR NAME OF RESOURCE
40	(28)	SIGNED	4	RIBEND (0)	END OF RIB FIXED SECTION

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RIBVAR	RESOURCE INFORMATION BLOCK - VARIABLE SECTION
0	(0)	SIGNED	4	(0)	
0	(0)	BITSTRING	1	RIBRNAME (0)	RNAME - MINOR NAME OF RESOURCE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RIBE	RESOURCE INFORMATION BLOCK EXTENT
0	(0)	SIGNED	4	(0)	
0	(0)	CHARACTER	8	RIBEJBNM	JOBNAME OF REQUESTOR
8	(8)	CHARACTER	8	RIBESYSN	SYSTEM NAME OF REQUESTOR
16	(10)	ADDRESS	4	RIBETCB	TCB ADDRESS OF REQUESTOR
20	(14)	ADDRESS	4	RIBEECB	ECB ADDRESS WHEN RIBEECBF IS ONE
20	(14)	ADDRESS	4	RIBESVRB	SVRB ADDRESS WHEN RIBEECBF IS ZERO
24	(18)	ADDRESS	4	RIBEUCB	If RIBE is for a RESERVE request from this system, against a 3-digit-device-number device, this field contains the UCB Address, otherwise, it is an arithmetic zero
28	(1C)	SIGNED	2	RIBEASID	ADDRESS SPACE ID OF REQUESTOR
30	(1E)	BITSTRING	1	RIBERFLG	FLAGS PERTAINING TO THE REQUEST
		1...		RIBETYPE	"X'80'" REQUEST TYPE (0 = EXCLUSIVE, 1 = SHARED)
		.1..		RIBEMC	"X'40'" MUST COMPLETE (MC) REQUEST (1 = MC, 0 = NOT MC)
		..1.		RIBERESV	"X'20'" RESERVE REQUEST (1 = RESERVE, 0 = NOT RESERVE)
		...1		RIBERESC	"X'10'" RESERVE REQUEST CONVERTED TO GLOBAL ENQ (1 = CONVERTED, 0 = NOT CONVERTED)
		.... 1...		RIBEAUTH	"X'08'" AUTHORIZED CALLER (1 = AUTHORIZED, 0 = UNAUTHORIZED)
		.... ..1.		RIBESIDV	"X'02'" RIBESAID VALIDITY FLAG (1 = RIBESAID VALID, 0 = RIBESAID NOT VALID).
		1		RIBEMATC	"X'01'" This is a matching task (MASID/MTCB) request. RIBEMASI and RIBEMTCB contain the ASID and TCB of the matching task.
31	(1F)	BITSTRING	1	RIBELFLG	FLAGS PERTAINING TO A LIST REQUEST
		1...		RIBEPOST	"X'80'" The requester has been informed that the request has completed. The ECB has been posted or the requester's suspended TCB RB has been posted. Note that in Ring mode, when the request originated on another system and SYNCHRES was enabled on the requesting system at the time of the request, the requester may be waiting for synchronous reserve processing to complete.
		.1..		RIBEECBF	"X'40'" ECB REQUEST (1 = ECB, 0 = NOT ECB)
		..1.		RIBELRS1	"X'20'" RESERVED
		...1		RIBELRS2	"X'10'" RESERVED
		.... 1...		RIBELRS3	"X'08'" RESERVED
		.... .1..		RIBELRS4	"X'04'" RESERVED
		.... ..1.		RIBELRS5	"X'02'" RESERVED
		1		RIBELRS6	"X'01'" RESERVED
32	(20)	BITSTRING	1	RIBESFLG	STATUS FLAGS
		1...		RIBESTAT	"X'80'" REQUEST STATUS (0 = WAITING FOR RESOURCE, 1 = OWNS RESOURCE)
		.1..		RIBEMATO	"X'40'" Matching task use indicator (0 = Requestor is not using the resource as the result of a MASID/MTCB request 1 = Requestor is using the resource as the result of a MASID/MTCB request)
		..1.		RIBESRS2	"X'20'" RESERVED
		...1		RIBESRS3	"X'10'" RESERVED
		.... 1...		RIBESRS4	"X'08'" RESERVED

RIB Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.... .1..		RIBESRS5	"X'04'" RESERVED
		.... ..1.		RIBESRS6	"X'02'" RESERVED
		1		RIBESRS7	"X'01'" RESERVED
33	(21)	CHARACTER	1	RIBERS01	RESERVED
34	(22)	SIGNED	2	RIBESAID	ASID of task for which a service PROVIDER performed this ENQ/RESERVE request. If RIBESIDV is set, RIBEASID is the ASID of the service PROVIDER and RIBESAID is the ASID of the service REQUESTOR. If RIBESAID is zero, the service REQUESTOR'S ASID is not available.
36	(24)	CHARACTER	4	RIBEDEVN	If RIBE is for a RESERVE request from this system, this field contains the EBCDIC device number of the device that was the target of the RESERVE, otherwise it is an arithmetic zero
40	(28)	ADDRESS	4	RIBEMTCB	Matching task TCB value (MTCB) specified by the requestor. Valid only when RIBEMATC is set.
44	(2C)	SIGNED	2	RIBEMASI	Matching task TCB value (MASID) specified by the requestor. Valid only when RIBEMATC is set.
46	(2E)	SIGNED	2	RIBERSVD	Reserved
48	(30)	SIGNED	4	RIBEEND (0)	END OF RIBE

RIB Cross Reference

Name	Hex Offset	Hex Value
RIB	0	
RIBBIT1	1E	8
RIBBIT2	1E	4
RIBBIT3	1E	2
RIBBIT4	1E	1
RIBCHAIN	4	
RIBE	0	
RIBEASID	1C	
RIBEAUTH	1E	8
RIBEDEVN	24	
RIBEECB	14	
RIBEECBF	1F	40
RIBEEND	30	
RIBEJBNM	0	
RIBELFLG	1F	
RIBELRS1	1F	20
RIBELRS2	1F	10
RIBELRS3	1F	8
RIBELRS4	1F	4
RIBELRS5	1F	2
RIBELRS6	1F	1
RIBEMASI	2C	
RIBEMATC	1E	1
RIBEMATO	20	40
RIBEMC	1E	40
RIBEMTCB	28	
RIBEND	28	
RIBEPOST	1F	80
RIBERESC	1E	10
RIBERESV	1E	20
RIBERFLG	1E	
RIBERSVD	2E	
RIBERS01	21	
RIBESAID	22	
RIBESFLG	20	
RIBESIDV	1E	2
RIBESRS2	20	20
RIBESRS3	20	10
RIBESRS4	20	8
RIBESRS5	20	4
RIBESRS6	20	2
RIBESRS7	20	1
RIBESTAT	20	80
RIBESVRB	14	
RIBESYSN	8	
RIBETCB	10	
RIBETYPE	1E	80
RIBEUCB	18	
RIBGLBL	1E	10
RIBNRIBE	18	
RIBNTO	8	
RIBNTWE	C	
RIBNTWS	10	
RIBQNAME	20	
RIBQSCANRC	C	
RIBQSCANRSN	10	
RIBRNAME	0	
RIBRNMLN	1F	
RIBSCOPE	1E	
RIBSTEP	1E	20
RIBSYS	1E	80
RIBSYSS	1E	40
RIBTOD	0	
RIBTRIBE	14	
RIBVAR	0	
RIBVLEN	1C	

RIT Information

RIT Heading Information

Common Name: RSM Internal Table
Macro ID: IARRIT
DSECT Name: RIT
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: RIT
Offset: 0
Length: 4
Storage Attributes: Virtual Storage: Yes
Subpool: Extended Nucleus
Key: 0
Data Space: No
Residency: Above 16 megabytes virtual
Size: See assembled listing
Created by: IARMR
Pointed to by: PVTRIT field of the PVT data area
Serialization: Field dependent
Function: Information used internally by RSM

RIT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	1992	RIT	
0	(0)	CHARACTER	4	RITID	RIT CONTROL BLOCK ID
4	(4)	BITSTRING	1	RITFLGS1	FLAG BYTE 1
		1...		RITDODMP	SDUMP SHOULD BE TAKEN REGARDLESS OF THE CURRENT RSM ENVIRONMENT. THIS BIT CAN ONLY BE SET ON MANUALLY. IF SO SET, IT MAY CAUSE RSM QUEUES TO BE DESTROYED.
		.1..		RITSRMNT	SRM HAS BEEN NOTIFIED OF AN AVQLOW SITUATION
		..1.		RITNZDC	TOTAL OF RSFQ DEFICIT COUNTS IS NON-ZERO
		...1		RITSPA	IF ON, ASYNCHRONOUS PAGING FACILITY, (AKA SPA) IS INSTALLED ON ALL PROCESSORS.
		.... 1...		RITDMPOK	SDUMP MAY BE TAKEN EVEN WHEN THE RSM LOCK IS HELD EXCLUSIVELY
		.... .1..		RITRSUPR	THE RSU PARAMETER HAS BEEN PROCESSED
		.... ..1.		RITTRACE	RSM TRACING IS ACTIVE
		1		RITMASX	MULTIPLE ADDRESS SPACE EXTENSIONS ARE INSTALLED ON ALL PROCESSORS
5	(5)	BITSTRING	1	RITFLGS2	FLAG BYTE 2
		1...		RITDPQNQ	A PCB THAT GENERAL DEFER NEEDS TO PROCESS HAS BEEN ENQUEUED TO THE DPQ SINCE THE LAST CHECK
		.1..		RITDEFXX	THE DPQ NEEDS AN ANYTYPE/ANYWHERE FRAME.
		..1.		RITDEFPX	THE DPQ NEEDS A PREFERRED/ANYWHERE FRAME.
		...1		RITDEFXB	THE DPQ NEEDS AN ANYTYPE/BELOW FRAME.
		.... 1...		RITDEFPB	THE DPQ NEEDS A PREFERRED/BELOW FRAME.
		.... .1..		RITCSP	THE CSP INSTRUCTION IS INSTALLED
		11		RITTRSCP	SCOPE OF CURRENT TRACE INVOCATION
		.... ..1.		RITTRCOL	TRACE DATA IS TO BE COLLECTED
		1		RITTRJMP	JUMP TRACING FOR EVENTS IS ACTIVE
6	(6)	BITSTRING	1	*	RESERVED
		1...		RITIAVQL	IGNORE AVQLOW DEFERS
		.1..		RITDEFXA	The DPQ needs an above frame
		..1.		RITDEFPA	The DPQ needs a pref above frame
		...1		RITDFRIO	Defer I/O
		.... 1...		RITLSVERIFICATIONREQ	Recovery verification or reverification of the large fixed single AFQ is necessary. Turned on when a THISPFTE request is honored during recovery split processing - Serialized by RSM global lock
		.... .1..		RITPSVERIFICATIONREQ	Recovery verification or reverification of the pageable large single AFQ is necessary. Turned on when a THISPFTE request is honored during recovery split processing - Serialized by RSM global lock
		.... ..1.		RITPHVERIFICATIONREQ	Recovery verification or reverification of the PH AFQ is necessary. Turned on when a THISPFTE request is honored during recovery split processing - Serialized by RSM global lock
		1		RITNHVERIFICATIONREQ	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Recovery verification or reverification of the NH AFQ is necessary. Turned on when a THISPFTE request is honored during recovery split processing - Serialized by RSM global lock
7	(7)	BITSTRING	1	*	Flags
		1...		RITOFFDT	For a storage reconfig command an offline-do-it request has come in
		.1..		RITDEFXH	The DPQ needs a high frame
		..1.		RITDEFPH	The DPQ needs a pref high frame
		...1		RITHIGHSTEALCURSORNPSET	
					IAXUO has already tried to set the HighSteal cursor for non-pref
		.... 1...		RITLPV2	Bit indicating the Large Page Service (IAXV2) active
		.... .1..		RITSCMASMREGISTERED	
					ASM has successfully registerd to the SCM block manager
		.... ..1.		RITSCM4KINITSPACENOTAVAIL	
					ASM's 4K register to the block manager failed to get the specified initial space. For diag.
		1		RITSCM1MINITSPACENOTAVAIL	
					ASM's 1M register to the block manager failed to get the specified initial space. For diag.
8	(8)	UNSIGNED	4	RITCSWRD	FIELDS SERIALIZED BY C/S
8	(8)	BITSTRING	1	RITFLGS3	FLAG BYTE 3
		1...		RITDBSCH	DOUBLE FRAME STEAL ROUTINE HAS BEEN SCHEDULED
		.1..		RITGDSCH	GENERAL DEFER PROCESSOR HAS BEEN SCHEDULED
		..1.		RITQDSCH	QUAD FRAME STEAL ROUTINE HAS BEEN SCHEDULED
		...1		RITFFDIE	THE FREE FRAME SRB ROUTINE HAS BEEN DEFERRED SCHEDULED VIA SETDIE
		.... 1...		RITFFSCH	THE FREE FRAME SRB ROUTINE HAS BEEN SCHEDULED
		.... .1..		RITFFINT	A FRAME HAS BEEN NEWLY INTERCEPTED
		.... ..1.		RITMGSCH	THE MIGRATION SRB ROUTINE HAS BEEN SCHEDULED
		.... ..1.		RITXCHUPSCH	Indicates the exchange up processor has been scheduled
		1		RITMGPND	MIGRATION SHOULD EXECUTE AGAIN BEFORE EXITING
9	(9)	BITSTRING	1	RITFLGS4	FLAG BYTE 4
		1...		RITCNSTR	MIGRATION MUST ISSUE THE MIGRATION RELIEF SYSEVENT
		.1..		RITRBSCH	The available frame count SRB routine has been scheduled
		..1.		RITGSSCH	The Global Steal SRB routine has been scheduled
		...1		RITGSPND	The Global Steal routine should execute again before exiting
		.... 1...		RITLPSCH	The LargePageAllocator SRB routine has been scheduled
		.... .1..		RITVHSTEAL	VH scheduled highsteal
		.... ..1.		RIT2GSCH	The 2GPageAllocator SRB routine has been scheduled
		1		RITSCMEVACTABLEEXISTS	
					The SCM Evacuation table has been created. See IAXSCMET.
10	(A)	BITSTRING	1	RITCSFLAGS3	More CS flags
		1...		RITRESPFTEAREAREADY	
					The Reserved PFTE Area is ready to be used to back PFTEs
11	(B)	CHARACTER	1	*	RESERVED C/S FIELDS
12	(C)	SIGNED	2	RITC0DCT	REMAINING NUMBER OF TIMES THAT A C0D ERROR OF THE TYPE FLAGGED IN RITC0DFL WILL BE LOGGED
14	(E)	BITSTRING	1	RITC0DFL	ERROR FLAGS
		1...		RITBADAS	AN INVALID ASID WAS FOUND IN A PFTE ASID FIELD
		.1..		RITBADTR	A BAD TRACE RECORD WAS GENERATED
		..11 1111		*	RESERVED
15	(F)	CHARACTER	1	RITTSTFL	Function test flags
		1...		RITBYPASSDIRECTPO	
					Bypass invocation of syseven directpo and use RitUseReal instead to determine whether to use real
		.1..		RITUSEREAL	Use real storage and not aux
		..1.		RITBYPASSLFAREAVICOMFORMULA	
					0 = Use the VICOM formula for the LFAREA when CvtVicom is ON. 1 = Use the normal formula instead of the VICOM formula for the LFAREA. IAXMT uses a less restrictive formula when CvtVicom is ON so that testers can get an LFAREA for large pages with very little real storage. If the real formula needs to be tested, set this bit when prompted for sysparms. This bit has no meaning when CvtVicom is OFF.
		...1 1111		*	
16	(10)	UNSIGNED	1	RITDEFDF	DEFAULT NUMBER OF DOUBLE FRAME PAIRS TO BE OBTAINED BY DOUBLE FRAME STEAL
17	(11)	UNSIGNED	1	RITDFAIL	COUNT OF CONSECUTIVE CALLS TO IARUDFRM WHICH HAVE RESULTED IN NO FRAMES
18	(12)	UNSIGNED	1	RITPADC	DEFICIT COUNT. NUMBER OF PREFERRED ABOVE FRAMES NEEDED FOR THE RSFQ.
19	(13)	UNSIGNED	1	RITPBDC	DEFICIT COUNT. NUMBER OF PREFERRED BELOW FRAMES NEEDED FOR THE RSFQ.
20	(14)	UNSIGNED	4	RITGLLK	LOCK WORD
24	(18)	ADDRESS	4	RITCRAB	ADDRESS OF THE COMMON RAB
28	(1C)	ADDRESS	4	RITRABQF	ADDRESS OF FIRST RAB ON THE RABQ

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
32	(20)	ADDRESS	4	RITRABQL	ADDRESS OF LAST RAB ON THE RABQ
36	(24)	ADDRESS	4	RITLPCQF	Address of the first FCB of the large page control queue
40	(28)	ADDRESS	4	RITLPCQL	Address of the last FCB of the large page control queue
44	(2C)	ADDRESS	4	RITRPLQF	ADDRESS OF THE FIRST RPB POOL ON THE RPB POOL QUEUE
48	(30)	ADDRESS	4	RITRPLQL	ADDRESS OF THE LAST RPB POOL ON THE RPB POOL QUEUE
52	(34)	ADDRESS	4	RITFRQF	ADDRESS OF FIRST RPB ON THE FREE RPB QUEUE
56	(38)	ADDRESS	4	RITFRQL	ADDRESS OF LAST RPB ON THE FREE RPB QUEUE
60	(3C)	ADDRESS	4	RITDPQF	ADDRESS OF FIRST PCB ON THE DEFER PCB QUEUE
64	(40)	ADDRESS	4	RITDPQL	ADDRESS OF LAST PCB ON THE DEFER PCB QUEUE
68	(44)	ADDRESS	4	RITASPQF	ADDRESS OF FIRST PCB ON THE ADDR SP CREATE PCB QUEUE
72	(48)	ADDRESS	4	RITASPQL	ADDRESS OF LAST PCB ON THE ADDR SP CREATE PCB QUEUE
76	(4C)	ADDRESS	8	RITPFTAC	ABOVE FRAME CURSOR
84	(54)	ADDRESS	8	RITEXCHANGEUPCURSOR	Pfte in 16M-2G range to start exchange up processing
92	(5C)	ADDRESS	4	RITVRCQF	ADDRESS OF FIRST FCB ON THE V=R CONTROL QUEUE
96	(60)	ADDRESS	4	RITVRCQL	ADDRESS OF LAST FCB ON THE V=R CONTROL QUEUE
100	(64)	ADDRESS	4	RITASCQF	ADDRESS OF FIRST FCB ON THE ADDR SP CREATE CNTL QUEUE
104	(68)	ADDRESS	4	RITASCQL	ADDRESS OF LAST FCB ON THE ADDR SP CREATE CNTL QUEUE
108	(6C)	ADDRESS	4	RITDFCQF	ADDRESS OF FIRST FCB ON THE DOUBLE FRAME CNTL QUEUE
112	(70)	ADDRESS	4	RITDFCQL	ADDRESS OF LAST FCB ON THE DOUBLE FRAME CNTL QUEUE
116	(74)	ADDRESS	4	RITVFCB	ADDRESS OF VARY FCB
120	(78)	ADDRESS	8	RITEXCHANGEUPCURSORBELOW	Pfte in 0-16M range to start exchange up processing
128	(80)	SIGNED	2	RITNUMQUADGROUPSRESERVED	Number of quad groups have been reserved
130	(82)	BITSTRING	1	RITCRITICALBITS1	
		1...		RIT_IAXUO_HIGHSTOLEN	Bit indicating that critical pages stolen in IAXUO high steal processing
		.1..		RIT_IAXUO_GLOBALSTOLEN	Bit indicating that critical pages stolen in IAXUO global steal processing
		..11		*	Reserved
		.... 1...		RIT_IAXUE_IAXUO	Bit indicating that critical pages stolen in IAXUE processing (IAXUO call)
		.... .1..		RIT_IAXUE_IAXPP	Bit indicating that critical pages stolen in IAXUE processing (IAXPP call)
		.... ..1.		RIT_IAXUE_IAXDF	Bit indicating that critical pages stolen in IAXUE processing (IAXDF call)
		1		RIT_IAXUE_IAXIX	Bit indicating that critical pages stolen in IAXUE processing (IAXIX call)
131	(83)	BITSTRING	1	RITCRITICALBITS2	
		1...		RIT_IAXUE_IAXKL	Bit indicating that critical pages stolen in IAXUE processing (IAXKL call)
		.1..		RIT_IAXUE_IAXPB	Bit indicating that critical pages stolen in IAXUE processing (IAXPB call)
		..1.		RIT_IAXUE_IAXPE	Bit indicating that critical pages stolen in IAXUE processing (IAXPE call)
		...1		RIT_IAXUE_IAXPZ	Bit indicating that critical pages stolen in IAXUE processing (IAXPZ call)
		.... 1...		RIT_IAXUE_IAXUR	Bit indicating that critical pages stolen in IAXUE processing (IAXUR call)
		.... .1..		RIT_IAXUE_IAXVZ	Bit indicating that critical pages stolen in IAXUE processing (IAXVZ call)
		.... ..1.		RIT_IAXUE_IAXV1	Bit indicating that critical pages stolen in IAXUE processing (IAXV1 call)
		1		RIT_IAXUE_UNKNOWN	Bit indicating that critical pages stolen in IAXUE processing (unknown)
132	(84)	BITSTRING	1	RITCRITICALBITS3	
		1...		RIT_IAXUA_RSFQ1STOLEN	Bit indicating that critical pages stolen in IAXUA processing RSFQ1
		.1..		RIT_IAXUA_BDFQSTOLEN	Bit indicating that critical pages stolen in IAXUA processing BDFQ
		..1.		RIT_IAXUA_RSFQ2STOLEN	Bit indicating that critical pages stolen in IAXUA processing RSFQ2
		...1		RIT_IAXUA_VRSTOLEN	Bit indicating that critical pages stolen in IAXUA processing V=R
		.... 1...		RIT_IAXUA_PFTSTOLEN	Bit indicating that critical pages stolen in IAXUA processing PFTSCAN
		.... .1..		RIT_IAXUA_RABSTOLEN	Bit indicating that critical pages stolen in IAXUA processing RABSCAN
		.... ..1.		RIT_IAXUA_SBFQSTOLEN	Bit indicating that critical pages stolen in IAXUA processing SBFQ
		1		RIT_IAXUA_RVTESTOLEN	Bit indicating that critical pages stolen in IAXUA processing RVTE

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
133	(85)	BITSTRING	1	RITCRITICALBITS4	
		1...		RIT_IAXYT_IAXCD	
					Bit indicating that critical pages stolen in IAXYT processing (IAXCD call)
		.1..		RIT_IAXYT_IAXFH	
					Bit indicating that critical pages stolen in IAXYT processing (IAXFH call)
		..1.		RIT_IAXYT_IAXFP	
					Bit indicating that critical pages stolen in IAXYT processing (IAXFP call)
		...1		RIT_IAXYT_IAXFV	
					Bit indicating that critical pages stolen in IAXYT processing (IAXFV call)
		.... 1...		RIT_IAXYT_IAXFY	
					Bit indicating that critical pages stolen in IAXYT processing (IAXFY call)
		.... .1..		RIT_IAXYT_IAXVO	
					Bit indicating that critical pages stolen in IAXYT processing (IAXVO call)
		.... ..1.		RIT_IAXYT_IAXXS	
					Bit indicating that critical pages stolen in IAXYT processing (IAXXS call)
		1		RIT_IAXYT_UNKNOWN	
					Bit indicating that critical pages stolen in IAXYT processing (unknown)
134	(86)	BITSTRING	1	RITCRITICALBITS5	
		1...		RIT_IAXUD_PAGESTOLEN	
					Bit indicating that critical pages stolen in IAXUD page processing
		.1..		RIT_IAXUD_SWAPSTOLEN	
					Bit indicating that critical pages stolen in IAXUD swap processing
		..1.		RIT_IAXUD_SCANPSTOLEN	
					Bit indicating that critical pages stolen in IAXUD scan page processing
		...1		RIT_IAXUD_SCANSSTOLEN	
					Bit indicating that critical pages stolen in IAXUD scan swap processing
		.... 1...		RIT_IAXYG_PAGESTOLEN	
					Bit indicating that critical pages stolen in IAXYG page processing
		.... .1..		RIT_IAXYG_SWAPSTOLEN	
					Bit indicating that critical pages stolen in IAXYG swap processing
		.... ..1.		RIT_IAXYG_AREASSTOLEN	
					Bit indicating that critical pages stolen in IAXYG area scan processing
		1		RIT_IAXYG_ANYSSTOLEN	
					Bit indicating that critical pages stolen in IAXYG any scan processing
135	(87)	BITSTRING	1	RITLONGAFQSPLITBITS	
					All bits in this byte are serialized by the RSM global lock
		1...		RITPHFQSPLITINPROG	
					Recovery split processing for the preferred high frame queue is in process
		.1..		RITNHFQSPLITINPROG	
					Recovery split processing for the non-preferred high frame queue is in process
		..1.		RITNQFQSPLITINPROG	
					Recovery split processing for the quad frame queue is in process
		...1		RITLFFQSPLITINPROG	
					Recovery split processing for the large fixed frame queue is in process
		.... 1...		RITLSFQSPLITINPROG	
					Recovery split processing for the large fixed single frame queue is in process
		.... .1..		RITPMFQSPLITINPROG	
					Recovery split processing for the pageable large frame queue is in process
		.... ..1.		RITPSFQSPLITINPROG	
					Recovery split processing for the pageable large single frame queue is in process
		1		*	Reserved - should only be used for future long AFQs that can be split
136	(88)	ADDRESS	4	RITFCFEQ	ADDRESS OF THE FIRST CFE ON FREE CFE QUEUE
140	(8C)	ADDRESS	8	RITPFT	ADDRESS OF THE PFT
140	(8C)	ADDRESS	8	RITFPFTE	Address of the first PFTE in the page frame table
148	(94)	ADDRESS	8	RITLPFTE	ADDRESS OF THE LAST PFTE IN THE PAGE FRAME TABLE
156	(9C)	ADDRESS	8	RITNPFTE	PFTE WITH THE HIGHEST ADDRESS WHICH MAY BE NON- PERMANENTLY RESIDENT
164	(A4)	ADDRESS	4	RITFPRV	ADDRESS OF FIRST (LOWEST VSA) PRIVATE AREA PAGE POSSIBLE
168	(A8)	ADDRESS	4	RITFCSA	ADDRESS OF FIRST (LOWEST VSA) CSA PAGE POSSIBLE
172	(AC)	ADDRESS	4	RITLCSA	ADDRESS OF FIRST PAGE AFTER LAST CSA PAGE
176	(B0)	ADDRESS	4	RITFQSA	ADDRESS OF FIRST (LOWEST VSA) PLPA/PLPA DIRECTORY PAGE
180	(B4)	ADDRESS	4	RITLQSA	ADDRESS OF FIRST PAGE AFTER LAST PLPA/PLPA DIR. PAGE
184	(B8)	ADDRESS	4	RITFQSAX	ADDRESS OF FIRST (LOWEST VSA) EXTENDED PLPA/PLPA DIRECTORY PAGE
188	(BC)	ADDRESS	4	RITLQSAX	ADDRESS OF FIRST PAGE AFTER LAST EXTENDED PLPA/PLPA DIRECTORY PAGE
192	(C0)	ADDRESS	4	RITFCSAX	ADDRESS OF FIRST (LOWEST VSA) EXTENDED CSA PAGE
196	(C4)	ADDRESS	4	RITFPRVX	ADDRESS OF FIRST (LOWEST VSA) EXTENDED PRIVATE AREA PAGE
200	(C8)	ADDRESS	4	RITCPGT	APPARENT ORIGIN OF THE COMMON AREA PAGE TABLES
204	(CC)	UNSIGNED	4	RITREALSPACEALET	
					Real Space Alet
208	(D0)	UNSIGNED	4	RITAI	Number of bytes in an address increment (in ESA mode)
212	(D4)	UNSIGNED	2	RITSDHVERIFYTOKEN	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Indicator of when the SDH queue was last verified by recovery. Serialized by RSMCM lock
214	(D6)	SIGNED	2	RITSKIP	SKIP FACTOR (HARDWARE STORAGE INTERLEAVE FACTOR). THIS IS EQUAL TO THE NUMBER OF BANDS IN AN AI. THE DEFAULT NUMBER OF BANDS IN AN AI IS ONE.
216	(D8)	ADDRESS	4	RITAIM	ADDRESS OF THE AIM
216	(D8)	ADDRESS	4	RITFAIME	ADDRESS OF FIRST AIME
220	(DC)	ADDRESS	4	RITLAIME	ADDRESS OF LAST AIME
224	(E0)	ADDRESS	4	RITDSTBK	ADDRESS OF THE SDUMP FRAME STEAL BACK EXIT
228	(E4)	SIGNED	4	RITSWPSZ	NUMBER OF SLOTS IN AN ASM SWAP SET
232	(E8)	SIGNED	2	RITREALP	REAL PARM VALUE INITIALLY SPECIFIED - MEANINGFUL ONLY DURING NIP PROCESSING.
234	(EA)	BITSTRING	1	RITFLGS5	FLAG BYTE 5
		1...		RITINSTL	A FEATURE IS INSTALLED ON THIS MACHINE TO PROVIDE RECOVERY OF HARD KEY ERRORS BY MACHINE CHECK
		.1..		RITPAGESCMPARM	PAGESCM parm was specified
		..1.		RITPAGESCMNONE	PAGESCM NONE was specified
		...1		RITPAGESCMALL	PAGESCM ALL was specified or defaulted
		.... 1...		RITPCIEGTAI	IAXMT could not define a PCIE LFAREA because its size is greater than the size of the smallest address increment
		.... .1..		RITREALPARMPROCESSED	REAL parm was processed
		.... ..1.		RITDEFNP	THE DPQ NEEDS A NON-PREF/1MEG FRAME
		1		RITDEFPP	THE DPQ NEEDS A PREFERRED/1MEG FRAME
235	(EB)	BITSTRING	1	RITFLGS6	FLAG BYTE 6
235	(EB)	BITSTRING	1	*	RESERVED
236	(EC)	ADDRESS	8	RITPFTEC	CURSOR FOR GETFRAME
244	(F4)	ADDRESS	4	RITFCAQF	ADDRESS OF FIRST ASTE ON THE FREE COMMON ASTE QUE
248	(F8)	ADDRESS	4	RITFCAQL	ADDRESS OF LAST ASTE ON THE FREE COMMON ASTE QUE
252	(FC)	UNSIGNED	4	RITSHRHC	COUNT OF SHARED SCROLL HIPERSPACE CREATED DURING LIFE OF IPL
256	(100)	UNSIGNED	4	RITPDIPC	RPB POOL DELETION IN PROGRESS COUNT
260	(104)	SIGNED	4	RITDSLN	Length of stack to start of DSPSERV section
264	(108)	ADDRESS	8	RITQDPREFSTEALCURSOR	PFTE Cursor of Last Pref storage stolen for use as Quad Frames
272	(110)	ADDRESS	8	RITQDSTEALCURSOR	PFTE Cursor of last storage stolen for use as Quad Frames
280	(118)	UNSIGNED	4	RITQDPREFSTEALCOUNT	Count the number of times a quad group was created from Pref frames
284	(11C)	UNSIGNED	4	RITPAGESCMVALUE	The PAGESCM value in megabytes
288	(120)	CHARACTER	8	*	Reserved - formerly RITLargeFrameAreaSizeLimit (unused at JBB778H)
296	(128)	SIGNED	8	RITTOTALONLINESTORAGEATIPL	The total amount of online storage at IPL
304	(130)	CHARACTER	8	*	Reserved - formerly RITLFGroupCursor (unused at HBB778H)
312	(138)	UNSIGNED	8	RITV64COMMMOTKN	System Generated token for IARV64 GETCOMMON
320	(140)	CHARACTER	8	RITMASTERSSTEPTR	Save CR1
320	(140)	CHARACTER	4	*	
324	(144)	CHARACTER	4	RITMASTERSSTELOWHALF	Low half of CR1
328	(148)	UNSIGNED	4	RITPHRFC	Number of preferred frames needed above the bar
332	(14C)	UNSIGNED	4	RITNHRFC	Number of non-pref frames needed above the bar
336	(150)	ADDRESS	8	RITHIGHSTEALCURSORP	High Steal cursor (preferred)
344	(158)	ADDRESS	8	RITHIGHSTEALCURSORNP	High Steal cursor (non-pref)
352	(160)	CHARACTER	8	*	Reserved - formerly RITLFGroupYZCursor (unused at JBB778H)
360	(168)	UNSIGNED	8	RITSCM4KINITSPACE	Initial space to allocate for ASM 4K register to the block manager. Zappable for test.
368	(170)	UNSIGNED	8	RITSCM1MINITSPACE	Initial space to allocate for ASM 1M register to the block manager. Zappable for test.
376	(178)	CHARACTER	8	RITSCMEVACDEFRAGTOD	Time the last SCM defragment started
384	(180)	CHARACTER	8	RITSCMEVACDEFRAGENDTOD	Time the last SCM defragment ended
392	(188)	CHARACTER	24	*	RESERVED
416	(1A0)	SIGNED	4	RITPLREFORMCOUNT	Number of pageable large frame groups that are marked to be reformed

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
420	(1A4)	SIGNED	4	RITLFREFORMCOUNT	Number of fixed large frame groups that are marked to be reformed
424	(1A8)	SIGNED	4	RITSINGLESTHRESHOLD	Target threshold of frames to maintain for any large singles queue
428	(1AC)	ADDRESS	8	RITPFTHC	CURSOR FOR GETFRAME
436	(1B4)	SIGNED	4	RITSPLN	LENGTH OF STACK TO START OF THE SPECIAL STACK SECTION
440	(1B8)	SIGNED	4	RITRCLN	LENGTH OF STACK TO THE START OF RECOVERY STACK SECTION
444	(1BC)	SIGNED	4	RITRSLN	LENGTH OF STACK TO THE START OF THE REAL STG BUF STACK SECTION
448	(1C0)	SIGNED	4	RITRRLN	LENGTH OF STACK TO THE START OF THE REAL STORAGE BUFER RECOVERY STACK SECTION
452	(1C4)	SIGNED	4	RITMCLN	LENGTH OF STACK TO THE START OF THE MACHINE CHECK STACK SECTION
456	(1C8)	SIGNED	4	RITSSLN	LENGTH OF STACK TO THE START OF THE SPECIAL SRM STACK SECTION
460	(1CC)	CHARACTER	44	RITSRBDB	SRB TO SCHEDULE THE DOUBLE FRAME STEAL ROUTINE
460	(1CC)	UNSIGNED	4	* (4294967307:562114560)	
504	(1F8)	CHARACTER	44	RITSRBGD	SRB TO SCHEDULE THE GENERAL DEFER FRAME PROCESSOR
504	(1F8)	UNSIGNED	4	* (4294967307:562114560)	
548	(224)	CHARACTER	44	RITSRBLP	SRB TO SCHEDULE THE LARGE FRAME ALLOCATION ROUTINE
548	(224)	UNSIGNED	4	* (4294967307:562114560)	
592	(250)	CHARACTER	44	RITSRBFF	SRB TO SCHEDULE THE FREE FRAME SRB ROUTINE
592	(250)	UNSIGNED	4	* (4294967307:562114560)	
636	(27C)	SIGNED	4	RITTERMASYNCCOUNT	Count of Asynchronous processing associated w/ memterm. Serialized w/ CS
640	(280)	CHARACTER	128	RITTQEFF	FREE FRAME SRB ROUTINE TQE
640	(280)	UNSIGNED	4	* (4294967328:562114560)	
768	(300)	CHARACTER	44	RITSRBMG	SRB TO SCHEDULE THE EXTENDED STORAGE MIGRATION SRB ROUTINE (ESA Only, nOt used for ESAME)
768	(300)	CHARACTER	44	RITSRBXCHUP	SRB to schedule exchange up processor (z/Architecture only)
768	(300)	UNSIGNED	4	* (4294967307:562114560)	
812	(32C)	ADDRESS	4	RITAEQF	ADDRESS OF FIRST ESTE ON THE AVAILABLE EXTENDED STORAGE ESTE QUEUE (AEQ) (ESA Only, nOt used for ESAME)
816	(330)	ADDRESS	4	RITAEQL	ADDRESS OF LAST ESTE ON THE AVAILABLE EXTENDED STORAGE ESTE QUEUE (AEQ) (ESA Only, nOt used for ESAME)
820	(334)	ADDRESS	4	RITEST	ADDRESS OF THE EST (ESA Only, nOt used for ESAME)
820	(334)	ADDRESS	4	RITFESTE	ADDRESS OF THE FIRST ESTE IN THE EXTENDED STORAGE TABLE (EST) (ESA Only, nOt used for ESAME)
824	(338)	ADDRESS	4	RITLESTE	ADDRESS OF THE LAST ESTE IN THE EXTENDED STORAGE TABLE (EST) (ESA Only, nOt used for ESAME)
828	(33C)	ADDRESS	4	RITEIM	ADDRESS OF EXTENDED STORAGE INCREMENT MAP (EIM) (ESA Only, nOt used for ESAME)
828	(33C)	ADDRESS	4	RITFEIME	ADDRESS OF THE FIRST EIME (ESA Only, nOt used for ESAME)
832	(340)	ADDRESS	4	RITLEIME	ADDRESS OF THE LAST EIME (ESA Only, nOt used for ESAME)
836	(344)	SIGNED	4	RITESI	NUMBER OF EXTENDED STORAGE E-FRAMES IS AN INCREMENT (ESA Only, nOt used for ESAME)
840	(348)	ADDRESS	4	RITESFCB	ADDRESS OF THE EXTENDED STORAGE RECONFIGURATION FCB (ESA Only, nOt used for ESAME)
844	(34C)	ADDRESS	4	RITVFEQF	ADDRESS OF THE FIRST ESTE ON THE VIRTUAL FETCH DATA SET ESTE QUEUE (ESA Only, nOt used for ESAME)
848	(350)	ADDRESS	4	RITVFEQL	ADDRESS OF THE LAST ESTE ON THE VIRTUAL FETCH DATA SET ESTE QUEUE (ESA Only, nOt used for ESAME)
852	(354)	ADDRESS	4	RITVFL	ADDRESS OF THE VIRTUAL FETCH LIST (ESA Only, nOt used for ESAME)
856	(358)	CHARACTER	4	RITMPEID	MPE POOL ID
860	(35C)	ADDRESS	4	RITRCMLO	FIRST ESTE (LOWEST VSA) IN THE RANGE OF ESTES TO BE CONFIGURED OFFLINE (ESA Only, nOt used for ESAME)
864	(360)	ADDRESS	4	RITRCMHI	LAST ESTE (HIGHEST VSA) IN THE RANGE OF ESTES TO BE CONFIGURED OFFLINE (ESA Only, nOt used for ESAME)
868	(364)	SIGNED	4	RITBURST	PREFERRED NUMBER OF AIAS TO BE SENT TO ASM BY MIGRATION AT ANY ONE TIME.
872	(368)	ADDRESS	4	RITCURSR	MIGRATION CURSOR. POINTS TO THE MOST RECENT ESTE VISITED. (ESA Only, nOt used for ESAME)
876	(36C)	SIGNED	4	RITTOTLN	TOTAL LENGTH OF THE RSM STACK (IN BYTES)
880	(370)	SIGNED	4	RITDFLN	LENGTH OF STACK TO THE START OF THE DISABLED FAULT STACK SECTION
884	(374)	SIGNED	4	RITDRLN	LENGTH OF STACK TO START OF DISABLED FAULT RECOVERY STACK SECTION

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
888	(378)	SIGNED	4	RITCNLN	LENGTH OF STACK TO THE START OF THE DATA SPACE CONVERT STACK SECTION
892	(37C)	SIGNED	4	RITNMLN	LENGTH OF STACK TO THE START OF THE NORMAL STACK SECTION
896	(380)	ADDRESS	4	RITRRAB	ADDRESS OF RASP RAB
900	(384)	ADDRESS	4	RITRLA	ADDRESS OF THE RLA
904	(388)	ADDRESS	4	RITRDH	ADDRESS OF THE RDH
908	(38C)	UNSIGNED	4	RITDBLDF	SYSTEM DEFAULT FOR DEFAULT NUMBER OF BLOCKS ON DSPSERV CREATE.
912	(390)	UNSIGNED	4	RITDMXEX	SYSTEM DEFAULT FOR MAXIMUM NO. OF USER KEY DATA SPACES FOR AN ADDRESS SPACE.
916	(394)	UNSIGNED	4	RITDMXSZ	SYSTEM DEFAULT FOR MAXIMUM NO. OF MEGABYTES OF USER KEY DATA SPACES FOR AN ADDRESS SPACE.
920	(398)	SIGNED	4	RITDSPOR	VIRTUAL STOARGE ORIGIN FOR ALL USER DATA SPACES
924	(39C)	BITSTRING	1	RITEDATFLAGS	DAT flags
		1...		RITENABLEENHANCEDDAT	DAT features are supported by RSM
		.1..		RITEDATINSTALLED	DAT features are installed on this machine
		..11 1111		*	Reserved
925	(39D)	BITSTRING	3	*	Reserved
928	(3A0)	ADDRESS	4	RITTRCB	ADDRESS OF THE TRCB
932	(3A4)	UNSIGNED	2	RITTRNUM	TRACE DATA SPACE NUMBER
934	(3A6)	UNSIGNED	2	*	RESERVED
936	(3A8)	SIGNED	4	RITHILN	LENGTH OF STACK TO THE START OF THE HIPERSPACE SERVICES STACK SECTION
940	(3AC)	SIGNED	4	RITWSATH	WSA POOL THRESHOLD NUMBER
944	(3B0)	CHARACTER	8	RITWSADW	CDS DOUBLE WORD FOR WSA POOL MANAGEMENT
944	(3B0)	ADDRESS	4	RITAWSAQ	POINTER TO THE FIRST WSA ON THE AVAILABLE WSA QUEUE
948	(3B4)	UNSIGNED	4	RITAWSAS	QUEUEING SEQUENCE NUMBER FOR THE AVAILABLE WSA QUEUE
952	(3B8)	SIGNED	4	RITIOLN	LENGTH OF STACK TO THE START OF THE GENERAL I/O COMP STACK SECTION
956	(3BC)	ADDRESS	4	RITIPLEP	TEMPORARY POINTER TO EST STORAGE. USED DURING IPL NIP
960	(3C0)	UNSIGNED	4	RITWSAQC	AVAILABLE WSA QUEUE CONTROL COUNTER
964	(3C4)	ADDRESS	4	RITRCUR	PREF STEAL RAB Q CURSOR
968	(3C8)	ADDRESS	4	RITFBTBL	ADDRESS OF UIC TABLE FOR FRAME BUCKETS. SET BY IARXU
972	(3CC)	ADDRESS	4	RITHWCPO	HIGH WATERMARK OF ONLINE CPS - INITIAL VALUE OF 1
976	(3D0)	ADDRESS	8	RIT2GPFTE	The PFTE address of last above frame
984	(3D8)	CHARACTER	256	RITTRCRI	TRACE FILTERING CRITERIA

Comment

Note that the last byte of both RITTRFUN and RITTREVN is currently not cleared when the trace is turned off. This is ok as long as this entry remains unused.

End of Comment

984	(3D8)	CHARACTER	128	RITTRFUN	TRACE FUNCTION ARRAY
984	(3D8)	CHARACTER	1	RITTRFN1 (4294967424:562176400)	
1112	(458)	CHARACTER	128	RITTREVN	TRACE EVENT ARRAY
1112	(458)	CHARACTER	1	RITTREV1 (4294967424:562114560)	
1240	(4D8)	CHARACTER	12	RITSSFTA	CELL POOL ANCHORS FOR SMALL SIZED SFTS
1240	(4D8)	ADDRESS	4	RITSSFTF	FORWARD QUEUE POINTER
1244	(4DC)	ADDRESS	4	RITSSFTB	BACK QUEUE POINTER
1248	(4E0)	UNSIGNED	4	RITSSFTC	FREE CELL COUNT FOR ENTIRE POOL
1252	(4E4)	CHARACTER	12	RITMSFTA	CELL POOL ANCHORS FOR MEDIUM SIZED SFTS
1252	(4E4)	ADDRESS	4	RITMSFTF	FORWARD QUEUE POINTER
1256	(4E8)	ADDRESS	4	RITMSFTB	BACK QUEUE POINTER
1260	(4EC)	UNSIGNED	4	RITMSFTC	FREE CELL COUNT FOR ENTIRE POOL
1264	(4F0)	CHARACTER	12	RITLSFTA	CELL POOL ANCHORS FOR LARGE SIZED SFTS
1264	(4F0)	ADDRESS	4	RITLSFTF	FORWARD QUEUE POINTER
1268	(4F4)	ADDRESS	4	RITLSFTB	BACK QUEUE POINTER
1272	(4F8)	UNSIGNED	4	RITLSFTC	FREE CELL COUNT FOR ENTIRE POOL
1276	(4FC)	CHARACTER	12	RITXSFTA	CELL POOL ANCHORS FOR EXTRA LARGE SIZED SFTS
1276	(4FC)	ADDRESS	4	RITXSFTF	FORWARD QUEUE POINTER
1280	(500)	ADDRESS	4	RITXSFTB	BACK QUEUE POINTER
1284	(504)	UNSIGNED	4	RITXSFTC	FREE CELL COUNT FOR ENTIRE POOL
1288	(508)	ADDRESS	4	RITFSAQF	ADDRESS OF FIRST ASTE ON THE FREE SUBSPACE ASTE QUEUE
1292	(50C)	ADDRESS	4	RITFSAQL	ADDRESS OF LAST ASTE ON THE FREE SUBSPACE ASTE QUEUE
1296	(510)	ADDRESS	4	RITSDHQF	ADDRESS OF FIRST SDH ON THE SHARED DATA PAGE HEADER QUEUE
1300	(514)	ADDRESS	4	RITSDHQL	ADDRESS OF LAST SDH ON THE SHARED DATA PAGE HEADER QUEUE
1304	(518)	ADDRESS	4	RITSCMEVACFCB@	FCB for SCM evacuation request

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
1308	(51C)	ADDRESS	4	RITSEFQF	ADDRESS OF FIRST ESTE ON THE SHARED EXPANDED FRAME QUEUE (ESA Only, nOt used for ESAME)
1312	(520)	ADDRESS	4	RITSEFQL	ADDRESS OF LAST ESTE ON THE SHARED EXPANDED FRAME QUEUE (ESA Only, nOt used for ESAME)
1316	(524)	CHARACTER	12	RITSDHPA	Cell pool anchors for the SDH pool queue. These 3 words must match the first 3 words of the SOBP mapping.
1316	(524)	ADDRESS	4	RITSDHPF	Address of first SDH-pool on the SDH pool queue
1320	(528)	ADDRESS	4	RITSDHPL	Address of last SDH-pool on the SDH pool queue
1324	(52C)	UNSIGNED	4	RITSDHPC	Free cell count for total of all SDH-pool extents
1328	(530)	CHARACTER	12	RITSPEPA	Cell pool anchors for the SPE pool queue. These 3 words must match the first 3 words of the SOBP mapping.
1328	(530)	ADDRESS	4	RITSPEPF	address of first SPE-pool on the SPE pool queue
1332	(534)	ADDRESS	4	RITSPEPL	address of last SPE-pool on the SPE pool queue
1336	(538)	UNSIGNED	4	RITSPEPC	Free cell count for total of all SPE-pool extents
1340	(53C)	SIGNED	4	RITDMUSV	System default for the maximum number of unauthorized shared views that can be owned by an address space
1344	(540)	ADDRESS	4	RITFIRSTSEGMENTTABLEREAL@FORPFTCADSDAT	The real address of the first segment table for PFT CADS DAT structure
1348	(544)	ADDRESS	4	RITPFTCADSASTE@	Virtual address of the aste for the pft cads
1352	(548)	ADDRESS	4	RITREALSPACEASTE@	Virtual address of the real space aste
1356	(54C)	ADDRESS	4	RITQFCQF	Address of first FCB on the quad frame FCB control queue
1360	(550)	ADDRESS	4	RITQFCQL	Address of last FCB on the quad frame FCB control queue
1364	(554)	CHARACTER	44	RITSRBQD	SRB to schedule the Quad Frame Steal routine
1364	(554)	UNSIGNED	4	* (4294967307:562114560)	
1408	(580)	CHARACTER	44	RITSRBFC	SRB to schedule the AFQ Frame Count routine
1408	(580)	UNSIGNED	4	* (4294967307:562114560)	
1452	(5AC)	UNSIGNED	4	RITHVHID	High Virtual Header Cell Pool ID
1456	(5B0)	CHARACTER	8	RITTQADR	Real Address of the top of the Quad Frame Candidate Area
1464	(5B8)	CHARACTER	8	RITQADR	Beginning address of quad frame candidate area
1472	(5C0)	CHARACTER	8	RITOFFLINEPFTEREAL@	Address of frame containing offline pftes
1480	(5C8)	CHARACTER	8	RITTWICEBAR	Artificial 4G Bar line or the actual top of storage
1488	(5D0)	UNSIGNED	4	RIT2GBAR	Artificial High Bar
1492	(5D4)	CHARACTER	8	RITAIX	Number of bytes in an address Increment (in ESAME mode)
1500	(5DC)	UNSIGNED	4	RITNVAL	Number of frames per address increment
1504	(5E0)	UNSIGNED	4	RITSIBCPID	Cell Pool ID for the SIBS
1508	(5E4)	UNSIGNED	4	RITNUMOFSGTESPOINTEDTOOFFLINEPAGETABLE	Number of SGTEs which point to OfflinePageTable in the PFT CADS DAT structure
1512	(5E8)	CHARACTER	8	RITOFFLINEPAGETABLEREAL@	Real address of the offline page table
1520	(5F0)	UNSIGNED	4	RITNUMOFSEGTABLESFORPFTCADSDAT	Number of segment tables for PFT CADS DAT structure
1524	(5F4)	ADDRESS	4	RITSCMAUXMG@	Address of the SCM AUX manager class
1528	(5F8)	CHARACTER	4	*	Reserved for when/if following ptr is changed to ptr(64)
1532	(5FC)	ADDRESS	4	RITBLOCKMGRSTACK@	RDA/RCA stack for block manager use. Serialized by the ASMGL lock.
1536	(600)	CHARACTER	8	*	Reserved - not used
1544	(608)	CHARACTER	8	RITQUADBUILTUPPERBOUND	The real address of the upper bound of the Quad frame areas which are built in IAXMH
1552	(610)	CHARACTER	8	RITFFSRBTS	Last time FFSRB was deferred
1560	(618)	ADDRESS	8	RITGLOBALSTEALCURSOR	Global Steal cursor
1568	(620)	ADDRESS	8	RITPRESTEALCURSOR	Presteal cursor
1576	(628)	CHARACTER	44	RITSRBGLOBALSTEAL	SRB TO SCHEDULE THE Global Steal processor
1576	(628)	UNSIGNED	4	* (4294967307:562114560)	
1620	(654)	BITSTRING	1	RITCRITICALBITS	
		1...		RITCRITICALPAGESSTOLEN	Bit indicating that pages were stolen from a critical address space while the critical paging function was activated
		.1..		RITCRITCOMMPAGESSTOLEN	Bit indicating that common pages were stolen while the critical paging function was activated
		..1.		RITSTEALCRITICALPAGESH	Bit indicating that pages from critical address spaces should be stolen in a High Steal request

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		RITSTEALCRITICALPAGESG	
					Bit indicating that pages from critical address spaces should be stolen in a Global Steal request
		.... 1...		RITSTEALCRITICALPAGESP	
					Bit indicating that pages from critical address spaces should be stolen in a Pre Steal request
		.... .1..		RITREFFRAMESSKIPPED	Reserved
		11		*	
1621	(655)	BITSTRING	1	RITGSBITS	Global steal flags
		1...		RITGSFEXHAUSTED	
		.1..		RITGSOVERLOAD	
					Reserved
		..1.		RITGSNOMOREPCBS	
					Reserved
		...1		RITGSUNDOREFORM	
					Bit indicating that IAXUO should undo some of the groups marked to be reformed
1622	(656)	BITSTRING	1	RITPLBITS	
		1...		RITPLDOSTEAL	Bit indicating that IAXUO should replenish the PLAREA
		.1..		RITPLCOALESCEDONE	
					Internally used (for unit or function test) to verify that replenishment by coalescing was done
		..1.		RITPLPAGINGDONE	
					Internally used (for unit or function test) to verify that replenishment by paging was done
		...1		RITPLCOALESCEINPROGRESS	
					Indicates that coalescing is in progress (used by IAXUA to prevent 1M singles or groups from being used while coalescing is in progress)
		.... 1...		RITLFINTERCEPTINPROGRESS	
					Indicates that IAXYZ intercept processing is in progress (used by IAXUA to prevent UndoReform from being scheduled while intercept is in progress)
1623	(657)	CHARACTER	1	*	Reserved
1624	(658)	ADDRESS	8	RITPREVIOUSPRESTEALCURSOR	
					Presteal cursor
1632	(660)	ADDRESS	8	RITPREVIOUSGLOBALSTEALCURSOR	
					steal cursor
1640	(668)	CHARACTER	8	RITLARGEFRAMEAREASTARTADDR	
					Beginning address of Large Frame Area
1648	(670)	CHARACTER	8	RITLARGEFRAMEAREAENDADDR	
					End address of Large Frame Area
1656	(678)	ADDRESS	8	RITMOORIGIN	
1664	(680)	ADDRESS	8	RITMASTERSRTEPTR	
1672	(688)	ADDRESS	8	RITTOT64COMMALLOCATIPL	
					Total size of the 64-Bit Common Storage allocated at IPL
1680	(690)	UNSIGNED	8	RITMOSIZEBYTES	
					Size in bytes of IOS HV common memory object
1688	(698)	ADDRESS	8	RITMCUQF	Address of first MOMB on the 64Bit Common Unowned queue
1696	(6A0)	ADDRESS	8	RITMCUQL	Address of last MOMB on the 64Bit Common Unowned queue
1704	(6A8)	CHARACTER	8	RITPLAREASTARTADDR	
					Beginning address of the Pageable Large Frame Area
1712	(6B0)	CHARACTER	8	RITPLAREAENDADDR	
					End address of the Pageable Large Frame Area
1720	(6B8)	ADDRESS	8	RITPLAREACURSOR	
					Address of the PFTE of the next Pageable Large frame group to start searching to satisfy a Pageable Large Frame request
1728	(6C0)	CHARACTER	8	RITPLBUILTUPPERBOUNDATIPL	
					The real address of the upper bound of the Pageable Large Frame areas which are built in IAXMH
1736	(6C8)	UNSIGNED	8	RITUNOWNEDCOUNT	
					Number of unowned MOMbs
1744	(6D0)	CHARACTER	24	*	Reserved
1768	(6E8)	CHARACTER	4	*	Reserved
1772	(6EC)	UNSIGNED	4	RITLFPQHDRMAX	
					Maximum entries for a Large Fixed Preferred queue header
1776	(6F0)	ADDRESS	8	RIT2GGROUPY3CURSOR	
					Address of the Pfte of the next 2G frame group to start searching to satisfy a 2G page request
1784	(6F8)	ADDRESS	4	RITL2CQF	Address of the first FCB of the 2G page control queue
1788	(6FC)	ADDRESS	4	RITL2CQL	Address of the last FCB of the 2G page control queue
1792	(700)	CHARACTER	8	RIT2GFRAMEAREASTARTADDR	
					Beginning address of 2G Frame Area
1800	(708)	CHARACTER	8	RIT2GFRAMEAREAENDADDR	
					End address of 2G Frame Area
1808	(710)	CHARACTER	44	RITSRB2G	SRB to schedule the 2G frame allocation routine

RIT Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
1808	(710)	UNSIGNED	4	*	(4294967307:562114560)
1852	(73C)	UNSIGNED	4	RITSDHQUEUEDEQCOUNT	Monotonically increasing count of number of SDHs removed from the SDH queue (RITSDHQF). Serialized by RSMCM lock
1856	(740)	ADDRESS	8	RITRESPFTEAREAHIGHRSA	Points to the top byte (highest addr) of the Reserved PFTE Area
1864	(748)	ADDRESS	8	RITRESPFTEAREALOWRSA	Points to the bottom (lowest addr) of the Reserved PFTE Area
1872	(750)	ADDRESS	4	RITFIRSTRPAD@	Points to the first RPAD of the queue of RPADs
1876	(754)	ADDRESS	4	RITFIRSTFREERPAD@	Points to the first RPAD of the queue of RPADs that are elible to be freed
1880	(758)	CHARACTER	44	*	Available as of JBB778H
1924	(784)	ADDRESS	4	RITSATDPQF	ADDRESS OF FIRST PCB ON THE Satisfied defer PCB queue - serialized by the RSM lock shared by IAXGD and RSMEXCL by IAXGA
1928	(788)	ADDRESS	4	RITSATDPQL	ADDRESS OF LAST PCB ON THE Satisfied defer PCB queue
1932	(78C)	CHARACTER	4	*	Reserved for HBB7790
1936	(790)	ADDRESS	8	RITPCIERSA	Starting real address for PCIE. Note that in the prior release this field was named RitPcieAddr
1944	(798)	ADDRESS	8	RITPCIEORIGIN	MO Origin for PCIE
1952	(7A0)	UNSIGNED	8	RITPCIESIZE	MO Size in bytes for PCIE
1960	(7A8)	UNSIGNED	8	RITPREFRESERVETHRESHOLD	Amount of online real storage at which point the PrefReservePct will be enabled
1968	(7B0)	UNSIGNED	4	RITPREFRESERVEPCT	Percentage of online real storage to preserve for high pref
1972	(7B4)	CHARACTER	20	*	Reserved for HBB7790
1992	(7C8)	CHARACTER	0	*	KEEP RIT A MULTIPLE OF 8 BYTES

RIT Constants

Len	Type	Value	Name	Description
4	HEX	7FFFFFFF	RITDBSC0	TO TURN RITDBSCH OFF
4	HEX	80000000	RITDBSC1	TO TURN RITDBSCH ON
4	HEX	BFFFFFFF	RITGDSC0	TO TURN RITGDSCH OFF
4	HEX	40000000	RITGDDC1	TO TURN RITGDSCH ON
4	HEX	DFFFFFFF	RITQDSC0	TO TURN RITQDSCH OFF
4	HEX	20000000	RITQDSC1	TO TURN RITQDSCH ON
4	HEX	EFFFFFFF	RITTRIP0	TO TURN RITTRIPR OFF
4	HEX	10000000	RITTRIP1	TO TURN RITTRIPR ON
4	HEX	F7FFFFFF	RITFFSC0	TO TURN RITFFSCH OFF
4	HEX	08000000	RITFFSC1	TO TURN RITFFSCH ON
4	HEX	FBFFFFFF	RITFFIN0	TO TURN RITFFINT OFF
4	HEX	04000000	RITFFIN1	TO TURN RITFFINT ON
4	HEX	FDFFFFFF	RITMGSC0	TO TURN RITMGSCH OFF
4	HEX	02000000	RITMGSC1	TO TURN RITMGSCH ON
4	HEX	FEFFFFFF	RITMGPN0	TO TURN RITMGPND OFF
4	HEX	01000000	RITMGPN1	TO TURN RITMGPND ON
4	HEX	FF7FFFFF	RITCNST0	TO TURN RITCNSTR OFF
4	HEX	00800000	RITCNST1	TO TURN RITCNSTR ON
4	HEX	FFF7FFFF	RITLPSC0	TO TURN RITLPSCH OFF
4	HEX	00080000	RITLPSC1	TO TURN RITLPSCH ON
4	HEX	FFFDFFFF	RIT2GSC0	TO TURN RIT2GSCH OFF
4	HEX	00020000	RIT2GSC1	TO TURN RIT2GSCH ON
8	NUMB HEX	0000000FFFFFFFFF	RITPFTAREAEND	The upper bound of the PFT area in PFT CADS
8	NUMB HEX	0000001080000000	RITSTATICFRAMEQHDRSORIGIN	The beginning address of the static frame queue headers in PFT CADS
8	NUMB HEX	00000010FFFFFFFF	RITSTATICFRAMEQHDRSEND	The upper bound of the static frame queue headers area
8	NUMB HEX	0000001180000000	RITDYNAMICFRAMEQHDRSORIGIN	The beginning address of the dynamic frame queue headers cell pool in PFT CADS
8	NUMB HEX	0000001200000000	RITDYNFQHDRSCELLSTORAGEORIGIN	The beginning address of the dynamic frame queue headers cell storage area in PFT CADS
8	NUMB HEX	0000001FFFFFFFFF	RITDYNAMICFRAMEQHDRSENDADDR	The upper bound of the dynamic frame queue headers cell pool
8	NUMB HEX	0000002080000000	RITBLOCKMGRAREAORIGIN	The beginning address of the block manager main object in PFT CADS

Len	Type	Value	Name	Description
8	NUMB HEX	000000213FFFFFFF	RITBLOCKMGRAREAEND	The upper bound of the block manager storage area
8	NUMB HEX	0000002160000000	RITBLOCKIDPOOLAREAORIGIN	The beginning address of the ASM SCM block ID pools in PFT CADS (see ILRSCMMG)
8	NUMB HEX	000000216FFFFFFF	RITBLOCKIDPOOLAREAEND	The upper bound of the ASM SCM block ID pools in PFT CADS (see ILRSCMMG)
8	NUMB HEX	0000002170000000	RITSCMEVACTABLEORIGIN	The beginning address of the RSM SCM Evacuation Table in PFT CADS (see IAXSCMET)
8	NUMB HEX	0000002170001FFF	RITSCMEVACTABLEEND	The upper bound
8	NUMB HEX	0000002180000000	RITFFVECTORTABLEORIGIN	The beginning address of the Free Frame Vector table in PFT CADS (see IAXFFVT)
8	NUMB HEX	0000002180000FFF	RITFFVECTORTABLEEND	The ending address of the Free Frame Vector table in PFT CADS (see IAXFFVT)

RIT Cross Reference

Name	Hex Offset	Hex Value
RIT	0	
RIT_IAXUA_BDFQSTOLEN	84	40
RIT_IAXUA_PFTSTOLEN	84	08
RIT_IAXUA_RABSTOLEN	84	04
RIT_IAXUA_RSFQ1STOLEN	84	80
RIT_IAXUA_RSFQ2STOLEN	84	20
RIT_IAXUA_RVTESTOLEN	84	01
RIT_IAXUA_SBFQSTOLEN	84	02
RIT_IAXUA_VRSTOLEN	84	10
RIT_IAXUD_PAGESTOLEN	86	80
RIT_IAXUD_SCANPSTOLEN	86	20
RIT_IAXUD_SCANSSTOLEN	86	10
RIT_IAXUD_SWAPSTOLEN	86	40
RIT_IAXUE_IAXDF	82	02
RIT_IAXUE_IAXIX	82	01
RIT_IAXUE_IAXKL	83	80
RIT_IAXUE_IAXPB	83	40
RIT_IAXUE_IAXPE	83	20
RIT_IAXUE_IAXPP	82	04
RIT_IAXUE_IAXPZ	83	10
RIT_IAXUE_IAXUO	82	08
RIT_IAXUE_IAXUR	83	08
RIT_IAXUE_IAXVZ	83	04
RIT_IAXUE_IAXV1	83	02
RIT_IAXUE_UNKNOWN	83	01
RIT_IAXUO_GLOBALSTOLEN	82	40
RIT_IAXUO_HIGHSTOLEN	82	80
RIT_IAXYG_ANYSSTOLEN	86	01
RIT_IAXYG_AREASSTOLEN	86	02
RIT_IAXYG_PAGESTOLEN	86	08
RIT_IAXYG_SWAPSTOLEN	86	04
RIT_IAXYT_IAXCD	85	80
RIT_IAXYT_IAXFH	85	40
RIT_IAXYT_IAXFP	85	20
RIT_IAXYT_IAXFV	85	10
RIT_IAXYT_IAXFY	85	08
RIT_IAXYT_IAXVO	85	04
RIT_IAXYT_IAXXS	85	02
RIT_IAXYT_UNKNOWN	85	01
RITAEQF	32C	
RITAEQL	330	
RITAI	D0	
RITAIM	D8	
RITAIX	5D4	
RITASCQF	64	
RITASCQL	68	
RITASPQF	44	
RITASPQL	48	
RITAWSAQ	3B0	
RITAWSAS	3B4	
RITBADAS	E	80
RITBADTR	E	40
RITBLOCKMGRSTACK@	5FC	
RITBURST	364	
RITBYPASSDIRECTPO	F	80
RITBYPASSLFAREAVICOMFORMULA	F	20
RITCNLN	378	
RITCNSTR	9	80
RITCPGT	C8	
RITCRAB	18	
RITCRITCOMMPAGESSTOLEN		

Name	Hex Offset	Hex Value
	654	40
RITCRITICALBITS		
	654	
RITCRITICALBITS1		
	82	
RITCRITICALBITS2		
	83	
RITCRITICALBITS3		
	84	
RITCRITICALBITS4		
	85	
RITCRITICALBITS5		
	86	
RITCRITICALPAGESSTOLEN		
	654	80
RITCSFLAGS3	A	
RITCSP	5	04
RITCSWRD	8	
RITCURSR	368	
RITC0DCT	C	
RITC0DFL	E	
RITDBLDF	38C	
RITDBSCH	8	80
RITDEFDF	10	
RITDEFNP	EA	02
RITDEFPA	6	20
RITDEFPB	5	08
RITDEFPH	7	20
RITDEFPP	EA	01
RITDEFPX	5	20
RITDEFXA	6	40
RITDEFXB	5	10
RITDEFXH	7	40
RITDEFXX	5	40
RITDFAIL	11	
RITDFCQF	6C	
RITDFCQL	70	
RITDFLN	370	
RITDFRIO	6	10
RITDMPOK	4	08
RITDMUSV	53C	
RITDMXEX	390	
RITDMXSZ	394	
RITDODMP	4	80
RITDPQF	3C	
RITDPQL	40	
RITDPQNQ	5	80
RITDRLN	374	
RITDSLN	104	
RITDSPOR	398	
RITDSTBK	E0	
RITEDATFLAGS	39C	
RITEDATINSTALLED		
	39C	40
RITEIM	33C	
RITENABLEENHANCEDDAT		
	39C	80
RITESFCB	348	
RITESI	344	
RITEST	334	
RITEXCHANGEUPCURSOR		
	54	
RITEXCHANGEUPCURSORBELOW		
	78	
RITFAIME	D8	
RITFBTBL	3C8	
RITFCAQF	F4	
RITFCAQL	F8	
RITFCFEQ	88	
RITFCSA	A8	
RITFCSAX	C0	
RITFEIME	33C	
RITFESTE	334	
RITFFDIE	8	10
RITFFINT	8	04
RITFFSCH	8	08
RITFFSRBTS	610	
RITFIRSTFREERPAD@		
	754	
RITFIRSTRPAD@		
	750	
RITFIRSTSEGMENTTABLEREAL@FORPFTCADSDAT		
	540	
RITFLGS1	4	
RITFLGS2	5	
RITFLGS3	8	
RITFLGS4	9	
RITFLGS5	EA	
RITFLGS6	EB	
RITFPFTE	8C	
RITFPRV	A4	
RITFPRVX	C4	
RITFQSA	B0	
RITFQSAX	B8	
RITFRQF	34	
RITFRQL	38	
RITFSAQF	508	
RITFSAQL	50C	
RITGDSCH	8	40
RITGLLK	14	
RITGLOBALSTEALCURSOR		
	618	
RITGSBITS	655	
RITGSFEXHAUSTED		
	655	80
RITGSNOMOREPCBS		
	655	20
RITGSOVERLOAD		
	655	40
RITGSPND	9	10
RITGSSCH	9	20
RITGSUNDOREFORM		
	655	10
RITHIGHSTEALCURSORNP		
	158	
RITHIGHSTEALCURSORNPSET		
	7	10
RITHIGHSTEALCURSORP		
	150	
RITHILN	3A8	
RITHVHID	5AC	
RITHWCPO	3CC	
RITIAVQL	6	80
RITID	0	
RITINSTL	EA	80
RITIOLN	3B8	
RITIPLEP	3BC	
RITLAIME	DC	
RITLARGEFRAMEAREAENDADDR		
	670	
RITLARGEFRAMEAREASTARTADDR		
	668	
RITLCSA	AC	
RITLEIME	340	
RITLESTE	338	
RITLFFQSPLITINPROG		
	87	10
RITLFINTERCEPTINPROGRESS		
	656	08
RITLFPQHDRMAX		
	6EC	
RITLFREFORMCOUNT		
	1A4	
RITLONGAFQSPLITBITS		
	87	
RITLPCQF	24	
RITLPCQL	28	
RITLPFTE	94	

Name	Hex Offset	Hex Value
RITLPSCH	9	08
RITLPV2	7	08
RITLQSA	B4	
RITLQSAX	BC	
RITLSFQSPLITINPROG	87	08
RITLSFTA	4F0	
RITLSFTB	4F4	
RITLSFTC	4F8	
RITLSFTF	4F0	
RITLSVERIFICATIONREQ	6	08
RITL2CQF	6F8	
RITL2CQL	6FC	
RITMASTERSRTEPTR	680	
RITMASTERSSTELOWHALF	144	
RITMASTERSSTEPTR	140	
RITMASX	4	01
RITMCLN	1C4	
RITMCUQF	698	
RITMCUQL	6A0	
RITMGPND	8	01
RITMGSCH	8	02
RITMOORIGIN	678	
RITMOSIZEBYTES	690	
RITMPEID	358	
RITMSFTA	4E4	
RITMSFTB	4E8	
RITMSFTC	4EC	
RITMSFTF	4E4	
RITNHFQSPLITINPROG	87	40
RITNHRFC	14C	
RITNHVERIFICATIONREQ	6	01
RITNMLN	37C	
RITNPFTE	9C	
RITNQFQSPLITINPROG	87	20
RITNUMOFSEGTABLESFORPFTCADSDAT	5F0	
RITNUMOFSGTESPOINTEDTOOFFLINEPAGETABLE	5E4	
RITNUMQUADGROUPSRESERVED	80	
RITNVAL	5DC	
RITNZDC	4	20
RITOFFDT	7	80
RITOFFLINEPAGETABLEREAL@	5E8	
RITOFFLINEPFTEREAL@	5C0	
RITPADC	12	
RITPAGESCMALL	EA	10
RITPAGESCMNONE	EA	20
RITPAGESCMPARM	EA	40
RITPAGESCMVALUE	11C	
RITPBDC	13	
RITPCIEGTAI	EA	08
RITPCIEORIGIN	798	
RITPCIERSA	790	
RITPCIESIZE	7A0	
RITPDIPC	100	
RITPFT	8C	
RITPFTAC	4C	
RITPFTCADSASTE@	544	
RITPFTEC	EC	
RITPFTHC	1AC	
RITPHFQSPLITINPROG	87	80
RITPHRFC	148	
RITPHVERIFICATIONREQ	6	02
RITPLAREACURSOR	6B8	
RITPLAREAENDADDR	6B0	
RITPLAREASTARTADDR	6A8	
RITPLBITS	656	
RITPLBUILTUPPERBOUNDATIPL	6C0	
RITPLCOALESCEDONE	656	40
RITPLCOALESCEINPROGRESS	656	10
RITPLDOSTEAL	656	80
RITPLPAGINGDONE	656	20
RITPLREFORMCOUNT	1A0	
RITPMFQSPLITINPROG	87	04
RITPREFRESERVEPCT	7B0	
RITPREFRESERVETHRESHOLD	7A8	
RITPRESTEALCURSOR	620	
RITPREVIOUSGLOBALSTEALCURSOR	660	
RITPREVIOUSPRESTEALCURSOR	658	
RITPSFQSPLITINPROG	87	02
RITPSVERIFICATIONREQ	6	04
RITQADR	5B8	
RITQDPREFSTEALCOUNT	118	
RITQDPREFSTEALCURSOR	108	
RITQDSCH	8	20
RITQDSTEALCURSOR	110	
RITQFCQF	54C	
RITQFCQL	550	
RITQUADBUILTUPPERBOUND	608	
RITRABQF	1C	
RITRABQL	20	
RITRBSCH	9	40
RITRCLN	1B8	
RITRCMHI	360	
RITRCMLO	35C	
RITRCUR	3C4	
RITRDH	388	
RITREALP	E8	
RITREALPARMPROCESSED	EA	04
RITREALSPACEALET	CC	
RITREALSPACEASTE@	548	
RITREFFRAMESSKIPPED	654	04
RITRESPFTEAREAHIGHRSA	740	

Name	Hex Offset	Hex Value
RITRESPFTEAREALOWRSA	748	
RITRESPFTEAREAREADY	A	80
RITRLA	384	
RITRPLQF	2C	
RITRPLQL	30	
RITRRAB	380	
RITRRLN	1C0	
RITRSLN	1BC	
RITRSUPR	4	04
RITSATDPQF	784	
RITSATDPQL	788	
RITSCMASMREGISTERED	7	04
RITSCMAUXMG@	5F4	
RITSCMEVACDEFRAGENDTOD	180	
RITSCMEVACDEFRAGTOD	178	
RITSCMEVACFCB@	518	
RITSCMEVACTABLEEXISTS	9	01
RITSCM1MINITSPACE	170	
RITSCM1MINITSPACENOTAVAIL	7	01
RITSCM4KINITSPACE	168	
RITSCM4KINITSPACENOTAVAIL	7	02
RITSDHPA	524	
RITSDHPC	52C	
RITSDHPF	524	
RITSDHPL	528	
RITSDHQF	510	
RITSDHQL	514	
RITSDHQUEUEDEQCOUNT	73C	
RITSDHVERIFYTOKEN	D4	
RITSEFQF	51C	
RITSEFQL	520	
RITSHRHC	FC	
RITSIBCPID	5E0	
RITSINGLESTHRESHOLD	1A8	
RITSKIP	D6	
RITSPA	4	10
RITSPEPA	530	
RITSPEPC	538	
RITSPEPF	530	
RITSPEPL	534	
RITSPLN	1B4	
RITSRBDB	1CC	
RITSRBFC	580	
RITSRBFF	250	
RITSRBGD	1F8	
RITSRBGLOBALSTEAL	628	
RITSRBLP	224	
RITSRBMG	300	
RITSRBQD	554	
RITSRBXCHUP	300	
RITSRB2G	710	
RITSRMNT	4	40
RITSSFTA	4D8	
RITSSFTB	4DC	
RITSSFTC	4E0	
RITSSFTF	4D8	
RITSSLN	1C8	
RITSTEALCRITICALPAGESG	654	10
RITSTEALCRITICALPAGESH	654	20
RITSTEALCRITICALPAGESP	654	08
RITSWPSZ	E4	
RITTERMASYNCCOUNT	27C	
RITTOTALONLINESTORAGEATIPL	128	
RITTOTLN	36C	
RITTOT64COMMALLOCATIPL	688	
RITTQADR	5B0	
RITTQEFF	280	
RITTRACE	4	02
RITTRCB	3A0	
RITTRCOL	5	02
RITTRCRI	3D8	
RITTREVN	458	
RITTREV1	458	
RITTRFN1	3D8	
RITTRFUN	3D8	
RITTRJMP	5	01
RITTRNUM	3A4	
RITTRSCP	5	03
RITTSTFL	F	
RITTWICEBAR	5C8	
RITUNOWNEDCOUNT	6C8	
RITUSEREAL	F	40
RITVFCB	74	
RITVFEQF	34C	
RITVFEQL	350	
RITVFL	354	
RITVHSTEAL	9	04
RITVRCQF	5C	
RITVRCQL	60	
RITV64COMMMOTKN	138	
RITWSADW	3B0	
RITWSAQC	3C0	
RITWSATH	3AC	
RITXCHUPSCH	8	02
RITXSFTA	4FC	
RITXSFTB	500	
RITXSFTC	504	
RITXSFTF	4FC	
RIT2GBAR	5D0	
RIT2GFRAMEAREAENDADDR	708	
RIT2GFRAMEAREASTARTADDR	700	
RIT2GGROUPY3CURSOR	6F0	
RIT2GPFTE	3D0	
RIT2GSCH	9	02

RMCA Information

RMCA Heading Information

Common Name: System Resource Manager Control Area
Macro ID: IRARMCA
DSECT Name: RMCA (unless DSECT=NO is coded)
Owning Component: System Resource Manager (SC1CX)
Eye-Catcher ID: RMCA
Offset: 0
Length: CHAR(4)
Storage Attributes: Subpool: Nucleus
Key: 0
Residency: Nucleus (above 16M line)
Size: 368 bytes
Created by: Assembled into nucleus module IRARMCNS
Pointed to by: RMCTRMCA field of the RMCT data area
Serialization: SRM Lock
Function: Provides the storage area containing swap analysis variables used within the system resource manager.

RMCA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	416	RMCA	
0	(0)	CHARACTER	4	RMCANAME	BLOCK IDENTIFICATION - 'RMCA'
4	(4)	SIGNED	2	RMCASWFT	SWAP IN FAIL THRESHOLD
6	(6)	SIGNED	2	RMCAINUS	COUNT OF IN-CORE USERS
8	(8)	SIGNED	2	RMCASFET	SWAP IN FAIL EVALUATION THRESHOLD - INIT BASED ON RMCASFTM
10	(A)	SIGNED	2	RMCASFWC	SWAP IN FAIL WAIT QUEUE COUNT
12	(C)	UNSIGNED	4	RMCATQS	SYSTEM QUIESCE TIME
16	(10)	UNSIGNED	4	RMCATRS	SYSTEM RESTART TIME
20	(14)	UNSIGNED	4	*	reserved (was RMCATOI)
24	(18)	UNSIGNED	4	RMCAREIN	Time of last DMDT reinitialization
28	(1C)	SIGNED	2	RMCATAU	TUNITS TILL SRM ALG EXEC
30	(1E)	SIGNED	2	*	reserved (was RMCATSU)
32	(20)	UNSIGNED	4	RMCASFTM	SWAP IN FAIL EVALUATION TIME - DIVIDE BY CAP INVOCATION INTERVAL TO GET RMCASFET
36	(24)	ADDRESS	4	*	reserved (was RMCAPTRN)
40	(28)	UNSIGNED	4	*	reserved (was RMCANHLD)
44	(2C)	UNSIGNED	4	RMCAFHLD	# OF NED AROUND DUE TO HOLD STATUS
48	(30)	CHARACTER	4	RMCACHP	CHAP LIST FOR SWAP
52	(34)	ADDRESS	4	RMCACHU	USER CHAPPED FOR SWAP
56	(38)	BITSTRING	4	RMCAINV	RTNE INVOCATION WORK AREA
60	(3C)	SIGNED	2	RMCADFCT	COUNT OF NONEXPRESS USERS DEFERRED FOR SWAP IN FAIL
62	(3E)	UNSIGNED	2	RMCAISV	ISV REC. VALUE BOOST
64	(40)	ADDRESS	2	RMCALGPG	DEF LOGON PERF GRP #
66	(42)	ADDRESS	2	RMCABCPG	DEF BATCH PERF GRP #
68	(44)	ADDRESS	4	RMCAMAS	ASCB ADDR FOR MASTER SCHEDULR
72	(48)	UNSIGNED	8	RMCATIMEOFINT	Time of next interrupt
80	(50)	BITSTRING	8	RMCAALGENDTIME	Time when the algorithms finished
88	(58)	CHARACTER	144	*	reserved (was RMCAWKA)
232	(E8)	UNSIGNED	1	RMCANDP	SA FOR NDP
233	(E9)	UNSIGNED	1	RMCATNDP	SA FOR TNDP
234	(EA)	UNSIGNED	1	*	reserved (was RMCANTSG)
235	(EB)	UNSIGNED	1	RMCADSPN	SA FOR DSP STATUS
236	(EC)	SIGNED	2	RMCADFCK	# DEFERRED USERS CHECKED
238	(EE)	SIGNED	2	RMCACIUS	CT OF USERS COMING IN
240	(F0)	SIGNED	2	RMCACSU	Tunits until capping runs
242	(F2)	SIGNED	2	RMCADFFX	Deferred Job on LSW queue count (Pageable Storage Shortage)
244	(F4)	CHARACTER	72	RMCASRC	SWAP OUT REASON CNTS.
244	(F4)	UNSIGNED	4	RMCATOSC	TERMINAL OUTPUT SWAP COUNT
248	(F8)	UNSIGNED	4	RMCATISC	TERMINAL OUTPUT SWAP COUNT
252	(FC)	UNSIGNED	4	RMCALWSC	LONG WAIT SWAP COUNT
256	(100)	UNSIGNED	4	RMCAXSSC	AUT STOR SHORTAGE SWAP COUNT
260	(104)	UNSIGNED	4	RMCARSSC	REAL STOR SHORTAGE SWAP COUNT
264	(108)	UNSIGNED	4	RMCADWSC	DETECTED WAIT SWAP COUNT
268	(10C)	UNSIGNED	4	RMCARSV	reserved for new SRC
272	(110)	UNSIGNED	4	RMCANQSC	CAP ENQ EXCHANGE SWAP COUNT
276	(114)	UNSIGNED	4	RMCAEXSC	CAP EXCHANGE BASED ON RECOMM. VALUE SWAP COUNT

RMCA Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
280	(118)	UNSIGNED	4	RMCAUSSC	CAP UNILATERAL SWAP OUT COUNT
284	(11C)	UNSIGNED	4	RMCATSSC	TRANSITION SWAP COUNT
288	(120)	UNSIGNED	4	RMCAICSC	Improve central storage utilization swap count
292	(124)	UNSIGNED	4	RMCAIPSC	Improve demand page-in rate swap count
296	(128)	UNSIGNED	4	RMCAMRSC	Make room for an out-too-long address space swap count
300	(12C)	UNSIGNED	4	RMCAAWSC	APPC verb service request wait
304	(130)	UNSIGNED	4	RMCAOISC	OpenMVS input wait
308	(134)	UNSIGNED	4	RMCAOOSC	OpenMVS output wait
312	(138)	UNSIGNED	4	RMCAIRSC	REALSWAP count
316	(13C)	SIGNED	2	RMCAPRVU	TUNITS BEFORE LAST TIMER
318	(13E)	SIGNED	2	RMCANXTU	TUNITS TILL NEXT TIMER
320	(140)	CHARACTER	8	RMCACVD	WORK AREA FOR CONVERT TO DECIMAL.
328	(148)	SIGNED	2	RMCADSIN	# DEFERRED USERS BEING SWAPPED IN
330	(14A)	SIGNED	2	RMCAEDCT	# OF EXPRESS USERS DEFERRED FOR SWAP IN FAIL
332	(14C)	SIGNED	2	RMCADFAX	Deferred Job on LSW queue count (Auxiliary Storage Shortage)
334	(14E)	SIGNED	2	RMCARSV7	Reserved
336	(150)	SIGNED	4	RMCARSV1	Reserved
340	(154)	SIGNED	4	RMCARSV2	Reserved
344	(158)	SIGNED	2	RMCASEET	SWAP TO EXT FAIL EVAL THRES
346	(15A)	SIGNED	2	RMCAREDR	The count of suspended users and/or pending REQSWAPs or TRANSWAPs
348	(15C)	ADDRESS	4	RMCASWCT	SWAP COUNT TABLE ADDRESS
352	(160)	SIGNED	4	RMCARSV3	Reserved
356	(164)	UNSIGNED	4	RMCAEXSW	Time of last exchange swap initiation
360	(168)	UNSIGNED	1	*	reserved (was RMCATSG)
361	(169)	UNSIGNED	1	RMCARSV9	RESERVED
362	(16A)	SIGNED	2	RMCASFCT	SYS SWAP IN FAIL COUNT
364	(16C)	ADDRESS	4	*	reserved (was RMCATS1L)
368	(170)	UNSIGNED	4	RMCARSVA	reserved
372	(174)	SIGNED	2	RMCANUMI	number of entries in the express user list
374	(176)	SIGNED	2	RMCACPII	Index into express user swap in list
376	(178)	ADDRESS	4	RMCACPI (4294967306:562114560)	Express user swap in list
416	(1A0)	CHARACTER	0	RMCAEND	END OF RMCA End of this block @ME19625A

RMCA Cross Reference

Name	Hex Offset	Hex Value
RMCA	0	
RMCAALGENDTIME	50	
RMCAAWSC	12C	
RMCABCPG	42	
RMCACHP	30	
RMCACHU	34	
RMCACIUS	EE	
RMCACPI	178	
RMCACPII	176	
RMCACSU	F0	
RMCACVD	140	
RMCADFAX	14C	
RMCADFCK	EC	
RMCADFCT	3C	
RMCADFFX	F2	
RMCADSIN	148	
RMCADSPN	EB	
RMCADWSC	108	
RMCAEDCT	14A	
RMCAEND	1A0	
RMCAEXSC	114	
RMCAEXSW	164	
RMCAFHLD	2C	
RMCAICSC	120	
RMCAINUS	6	
RMCAINV	38	
RMCAIPSC	124	
RMCAIRSC	138	
RMCAISV	3E	
RMCALGPG	40	
RMCALWSC	FC	
RMCAMAS	44	
RMCAMRSC	128	
RMCANAME	0	

Name	Hex Offset	Hex Value
RMCANDP	E8	
RMCANQSC	110	
RMCANUMI	174	
RMCANXTU	13E	
RMCAOISC	130	
RMCAOOSC	134	
RMCAPRVU	13C	
RMCAREDR	15A	
RMCAREIN	18	
RMCARSSC	104	
RMCARSV	10C	
RMCARSVA	170	
RMCARSV1	150	
RMCARSV2	154	
RMCARSV3	160	
RMCARSV7	14E	
RMCARSV9	169	
RMCASEET	158	
RMCASFCT	16A	
RMCASFET	8	
RMCASFTM	20	
RMCASFWC	A	
RMCASRC	F4	
RMCASWCT	15C	
RMCASWFT	4	
RMCATAU	1C	
RMCATIMEOFINT	48	
RMCATISC	F8	
RMCATNDP	E9	
RMCATOSC	F4	
RMCATQS	C	
RMCATRS	10	
RMCATSSC	11C	
RMCAUSSC	118	

Name	Hex Offset	Hex Value
RMCAXSSC	100	

RMCT Information

RMCT Programming Interface information

Programming Interface information

RMCT

ONLY the following fields are part of the programming interface information:

- RMCTCCT
- RMCTCLST
- RMCTCMCT
- RMCTCPMP
- RMCTDMDT
- RMCTDMNC
- RMCTICST
- RMCTICT
- RMCTLSCT
- RMCTMCT
- RMCTMFS
- RMCTRCT
- RMCTRMCA
- RMCTRMEX
- RMCTRMPT
- RMCTTOD
- RMCTTRPC
- RMCTX3

End of Programming Interface information

RMCT Heading Information

Common Name: SYSTEM RESOURCES MANAGER CONTROL TABLE
Macro ID: IRARMCT
DSECT Name: RMCT
Owning Component: System Resources Manager (SC1CX)
Eye-Catcher ID: RMCT
Offset: 0
Length: 4
Storage Attributes: Main Storage: YES
Virtual Storage: YES
Auxiliary Storage: NO
Subpool: NUCLEUS
Key: key 0
Data Space: NO
Residency: (Residence - above 16M)
Size: 1024 bytes @ME08717
Created by: Assembled into nucleus module, IRARMCNS
Pointed to by: CVTOPCTP field of the CVT data area
Serialization: SRM lock
(some fields are compare and swap serialized,
as noted on field descriptions)
Function: THE RMCT SERVES AS THE ORIGIN TO LOCATE SYSTEM RESOURCES MANAGER TABLES AND ENTRY POINTS. THE RMCT LOCATES THE SCHEDULING / ROUTING INFORMATION USED TO INVOKE THE REQUIRED PROCESSING FOR THE VARIOUS SYSTEM RESOURCES MANAGER FUNCTIONS.

RMCT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RMCT	
0	(0)	CHARACTER	4	RMCTNAME	- BLOCK IDENTIFICATION
4	(4)	ADDRESS	4	RMCTCCT	- CPU MANAGEMENT CONTROL TABLE
8	(8)	ADDRESS	4	RMCTICT	- I/O MANAGEMENT CONTROL TABLE
12	(C)	ADDRESS	4	RMCTMCT	- STORAGE MANAGEMENT CONTROL TABLE
16	(10)	ADDRESS	4	RMCTRMPT	- CTL ALGORITHM PARAMETER TABLE
20	(14)	ADDRESS	4	RMCTRMCA	- CTL ALGORITHM CONTROL AREA
24	(18)	ADDRESS	4	RMCTWMST	- ADDR OF WLM SPECIFICATION TABLE
28	(1C)	ADDRESS	4	RMCTSRBC	"V(IRARSRBT)" - TCB/SRB CODE IN INT
32	(20)	ADDRESS	4	RMCTWMCT	- WLM mode control table
36	(24)	ADDRESS	4	RMCTRMPD	- ADDR OF RESOURCES MANAGER PERF DATA
40	(28)	ADDRESS	4	RMCTRMEX	- ROUTINE EXITING VECTOR TABLE
44	(2C)	ADDRESS	4	RMCTRMSB	- SUBROUTINE CALLING VECTOR TABLE
48	(30)	ADDRESS	4	RMCTEPPA	- PRTL ANALYSIS ENTRY TABLE
52	(34)	ADDRESS	4	RMCTEPDT	- USER ACTION ENTRY TABLE
56	(38)	ADDRESS	4	RMCTEPAT	- ALGORITHM ENTRY TABLE
60	(3C)	ADDRESS	4	RMCTLSCT	- LOGICAL SWAP CONTROL TABLE
64	(40)	SIGNED	4	RMCTADJC	- ADJUSTMENT FACTOR FOR CPU RATE
68	(44)	ADDRESS	4	RMCTITT	"V(IRASECHT)" - Addr of the Sysevent Characteristic Table in IRARMEV0
72	(48)	SIGNED	4		- Reserved
76	(4C)	ADDRESS	4	RMCTLTT	lock trace table address
80	(50)	ADDRESS	4	RMCTEPPR	- PROCESS RATE DEPENDENT ENTRY TABLE
84	(54)	ADDRESS	4	RMCTWAST	- ADDR OF WMST FOR SET IPS
88	(58)	ADDRESS	4	RMCT15F	"V(IRARMI10)" Address of 15F abend
92	(5C)	ADDRESS	4	RMCTTMQE	- SCHEDULED RTNE QUEUE HEADER ADDR
96	(60)	SIGNED	4	RMCTAQCT	- ACTION QUEUE MEMBER COUNT
100	(64)	ADDRESS	4	RMCTAQHD	- ACTION QUEUE FORWARD POINTER
104	(68)	ADDRESS	4	RMCTWTQE	- WAIT - QUEUE HEADER BLOCK ADDRESS
108	(6C)	ADDRESS	4	RMCTLSQE	- LOG SWAP WAIT QUEUE HEADER ADDR
112	(70)	ADDRESS	4	RMCTOTQE	- OUT - QUEUE HEADER BLOCK ADDRESS
116	(74)	ADDRESS	4	RMCTINQE	- IN - QUEUE HEADER BLOCK ADDRESS
120	(78)	SIGNED	4	RMCTTBS	- STARTING TIME BASE FOR TIME OF DAY
124	(7C)	SIGNED	4	RMCTTOD	- TIME OF DAY - SYST RSRC MGR INVOKE - SRM internal use RmctxTod
128	(80)	DBL WORD	8	RMCTTOC	- TIME OF CENTURY - 64BIT BINARY NMB - SRM internal use RmctxToc
136	(88)	BITSTRING	1	RMCTALA (4)	- ALGORITHM REQUEST ACCUMULATOR FIELD
140	(8C)	BITSTRING	1	RMCTALR (4)	- IMMEDIATE ALGORITHM REQUEST FIELD
144	(90)	ADDRESS	4	RMCTRQSV	- ADDR OF REQ SRV DATA AREA
148	(94)	BITSTRING	1	RMCTFLGS	- PROCESSING CONTROL FLAGS
		1...		RMCTMFA	"BIT0" - MEASUREMENT FACILITY ACTIVE
		.1..		RMCTCPS1	"BIT1" CAP SWITCH
		..1.		RMCTICS	"BIT2" CONTROL MEMBER DEFINED
		...1		RMCTINIT	"BIT3" - SRM INITIALIZATION WAS PERFORMED
		.... 1...		RMCTREPT	"BIT4" - NON-TSO REPORTING ACTIVE

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... .1..		RMCTSTW	"BIT5" - SET HAS STOPPED WAR COLLECTION
		.... ..1.		RMCTFRSV	"BIT6" - Reserved
		1		RMCTWLM	"BIT7" - WLM mode is always active
149	(95)	BITSTRING	1		- OVERRIDE CONDITION FLAGS
		1...		RMCTREP1	"BIT0" - Reactivation of the in storage policy in progress, first pass
		.1..		RMCTMFS	"BIT1" - RMF ACTIVE,SET RCVD
		..1.		RMCTCNSW	"BIT2" - COUNT NON-SWAPPABLES IN CMPL
		...1		RMCTCLST	"BIT3" - COUNT EACH COMMAND IN A CLIST
		.... 1...		RMCTRTSO	"BIT4" - TSO TRXNAME REPORTING ACTIVE
		.... .1..		RMCTDMTI	"BIT5" - Set to reinitialize the domain table in Swap Analysis (CAP)
		.... ..1.		RMCTOKCP	"BIT6" - This bit is set so that next time Sysevent 24 is issued, CAP will be scheduled.
		1		RMCTREP2	"BIT7" - Reactivation of the in storage policy in progress, 2nd pass
150	(96)	BITSTRING	1	RMCTTAPE	(OPT PARM) TAPE SELECTION:
		1...		RMCTSLTN	"BIT0" - NEXT HIGHEST
		.1..		RMCTSLTR	"BIT1" - RANDOM
		..1.		RMCTSLTL	"BIT2" - LOWEST ADDRESS
		...1		RMCTSLTF	"BIT3" - FIRST DEVICE IN LIST
151	(97)	BITSTRING	1	RMCTFLG2	- PROCESSING FLAGS
		.1..		RMCTOVFL	"BIT1" - OVERFLOW OCCURED
		..1.		RMCTUNTR	"BIT2" - Real time period durations are in effect.
		...1		RMCTSRNG	"BIT3" - Send empty SRRU to systems
		.... 1...		RMCTSQMR	"BIT4" - Schedule queue manager recovery
152	(98)	ADDRESS	4	RMCTTELM	"V(IEATSELM)" - RESOURCES MANAGER TIMING ELEMENT
156	(9C)	SIGNED	4	RMCTCPID	- RES MANAGER CELL POOL ID
160	(A0)	DBL WORD	8	RMCTTOCI	- CLOCK READ AREA - 64BIT BINARY NMB -SRM internal use RmctxToci
168	(A8)	ADDRESS	4	RMCTOUCB	- PREASSEMBLED MODEL OUCB
172	(AC)	ADDRESS	4	RMCTOUXB	- INTERPOSED DUMMY OUXB
176	(B0)	ADDRESS	4	RMCTSRBT	- RESOURCES MANAGER SRB TABLE
180	(B4)	ADDRESS	4	RMCTDMDT	- ADDR OF DOMAIN TABLE
184	(B8)	ADDRESS	4	RMCTDMDE	- ADDR OF LAST DMN TAB ENTRY
188	(BC)	SIGNED	2	RMCTDMNC	- NUMBER OF DOMAINS
190	(BE)	SIGNED	2	RMCTSCSQ	- Speed change sequence no
192	(C0)	SIGNED	4		- Reserved
196	(C4)	SIGNED	4		- Reserved
200	(C8)	SIGNED	4		- Reserved
204	(CC)	SIGNED	4		- Reserved
208	(D0)	SIGNED	4	RMCTCPU	- CPU service coefficient
212	(D4)	SIGNED	4	RMCTMSO	- MSO service coefficient
216	(D8)	SIGNED	4	RMCTIOC	- IOC service coefficient
220	(DC)	ADDRESS	4	RMCTICST	- ICSC TABLE ADDR
224	(E0)	SIGNED	4	RMCTCPMP	- CPU ADJUSTING FACTOR - IF THIS PROCESSOR MODEL HAS RELATED CPUS, THIS ADJUSTMENT FACTOR IS FOR THE FASTER CPU
228	(E4)	ADDRESS	4	RMCTRCT	- ADDRESS OF RCT
232	(E8)	DBL WORD	8	RMCTBRQE	- BASIC REPORTING QUEUE
232	(E8)	X'E8'	0	RMCTBRQH	"RMCTBRQE+0" HEADER
232	(E8)	X'EC'	0	RMCTBRQC	"RMCTBRQE+4" USE COUNT
240	(F0)	DBL WORD	8	RMCTERQE	- EXTENDED REPORTING QUEUE
240	(F0)	X'F0'	0	RMCTERQH	"RMCTERQE+0" HEADER
240	(F0)	X'F4'	0	RMCTERQC	"RMCTERQE+4" USE COUNT
248	(F8)	DBL WORD	8	RMCTUPDQ	- UPDATE QUEUE
248	(F8)	X'F8'	0	RMCTUPQH	"RMCTUPDQ+0" HEADER
248	(F8)	X'FC'	0	RMCTUPQC	"RMCTUPDQ+4" USE COUNT
256	(100)	ADDRESS	4	RMCTTRAD	- XACN RPTING Q 1ST PAGE PTR
260	(104)	SIGNED	4	RMCTTRPC	- XACN RPTING PAGE COUNT
264	(108)	ADDRESS	4	RMCTICSP	- XACN DESCRIPTION TABLE
268	(10C)	ADDRESS	4	RMCTRSPL	- RSPL ADDRESS
272	(110)	BITSTRING	1	RMCTCSB	- RMCT COMPARE/SWAP BIT
		1...		RMCTUTQE	"BIT0" - SRM TQE IS PAST DUE
		.1..		RMCTSIMT	"BIT1" - PASSAGE OF TIME HAS BEEN SIMULATED DUE TO TOD CLOCK FAILURE
		..1.		RMCTNOBQ	"BIT2" - NO BASIC RPTING Q ELEMNTS
		...1		RMCTNOEQ	"BIT3" - NO EXTND RPTING Q ELEMNTS
		.... 1...		RMCTTPP	"BIT4" - SRM TIMER POP PENDING
		.... .1..		RMCTSTGF	"BIT5" - VIO journaling dataset failure
		.... ..1.		RMCTWSMF	"BIT6" - Write SMF 99 records
		1		RMCTSSMF	"BIT7" - SRB to write SMF99 records has been scheduled
273	(111)	BITSTRING	1	RMCTRSVB	- RESERVED
274	(112)	SIGNED	2	RMCTSHDL	- LENGTH OF SRM STACK HEADER (MUST BE A MULTIPLE OF 8 TO INSURE A DWORD BDY FOR EACH STACK FRAME)
276	(114)	ADDRESS	4	RMCTPTCH	"V(IRARMPAT)" - PATCH AREA ADRESS
280	(118)	ADDRESS	4	RMCTCMCT	CHANNEL MEAS CNTL TABLE
284	(11C)	ADDRESS	4	RMCTESCT	EXT STORE CRITERIA TABLE
288	(120)	ADDRESS	4	RMCTEPBG	- EPAT BEGINNING

RMCT Cross Reference

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
292	(124)	SIGNED	4		- Reserved
296	(128)	ADDRESS	4	RMCTPAGP	- SYSTEM PAGING BLOCK POINTER
300	(12C)	SIGNED	4	RMCTELET	Executed long enough threshold for determing if an address space has executed long enough to be analyzed for monitoring. Value is 2 SRM seconds
304	(130)	SIGNED	4	RMCTSRB	- SRB service coefficient
308	(134)	SIGNED	4	RMCTITER	iteration number
312	(138)	SIGNED	4	RMCTSMC	sample monitoring control block pointer
316	(13C)	SIGNED	4	RMCTDFQF	address of first oucb on deferred freemain queue
320	(140)	SIGNED	4	RMCTDFQL	address of last oucb on deferred freemain queue
324	(144)	SIGNED	4	RMCTSMFB	Pointer to SMF99 Buffer
328	(148)	ADDRESS	4	RMCTEPDB	First RMEP on EPDT
332	(14C)	SIGNED	4	RMCTSMFS	Address of last SMF99 SRB
336	(150)	SIGNED	4	RMCTRSDA	Address of RSD List
340	(154)	SIGNED	4	RMCTENCH	System Encb Queue header
344	(158)	SIGNED	4	RMCTENCL	System Encb Queue trailer
348	(15C)	SIGNED	4	RMCTSRRU	Address of Sysplex Router Registered User block
352	(160)	SIGNED	4	RMCTDSFC	Number of times sending sysplex data failed on this system. Moved from WMCT_DM_SEND_FAILED_CNT so it can be referenced in compat mode
356	(164)	SIGNED	4	RMCTGRSS	Address of Generic Resource Selected Systems
360	(168)	SIGNED	4	RMCTLE55	- Last saved for ENF 55
364	(16C)	SIGNED	4	RMCTSWMB	Address of Subsystem Work Measurement Block
368	(170)	SIGNED	4	RMCTRCTS	Timestamp (time of day in 1.048576 second units, the leftmost 32 bits of the clock) of when resource constraints were last detected - may be 0 if no such problem has been seen
372	(174)	SIGNED	4	RMCTIPAT	Internal policy activation timestamp (time of day in 1.048576 second units, the leftmost 32 bits of the clock). This timestamp is set to when reactivation of the in storage policy was initiated (by IRARMER3) + 1 minute elapse time to account for policy activation delays and to allow the system to re- accumulate meaningful statistics. It denotes when it is ok to enable this system for TSO generic resource selection. A value of 0 indicates either there is no rebuild policy action or the elapse time has expired

Comment

When defining any NEW doubleword fields, MAKE SURE that you define the new fields on doubleword boundaries so the assembler does not helpfully skip bytes to properly align the data.

End of Comment

376	(178)	SIGNED	4	RMCTX3	Address of RMCT Extention 3 Mapped by IRARMCTZ
380	(17C)	SIGNED	4	RMCTX2	Address of RMCT Extention 2 Mapped by IRARMCTY
384	(180)	DBL WORD	8	RMCTX1 (0)	Mapped by IRARMCTX
384	(180)	BITSTRING	128	RMCTX1C4	Cache Line 4
512	(200)	BITSTRING	128	RMCTX1C5	Cache Line 5
640	(280)	BITSTRING	128	RMCTX1C6	Cache Line 6
768	(300)	BITSTRING	128	RMCTX1C7	Cache Line 7
896	(380)	BITSTRING	128	RMCTX1C8	Cache Line 8
1024	(400)	DBL WORD	8	RMCTEND (0)	- END OF RMCT
1024	(400)	X'400'	0	RMCTLEN	"RMCTEND-RMCT" - LENGTH OF RMCT

RMCT Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
RMCT	0		RMCTDFQL	140	0
RMCTADJC	40	0	RMCTDMDE	B8	
RMCTALA	88	0	RMCTDMDT	B4	
RMCTALR	8C	0	RMCTDMNC	BC	0
RMCTAQCT	60	0	RMCTDMTI	95	4
RMCTAQHD	64		RMCTDSFC	160	0
RMCTBRQC	E8	EC	RMCTELET	12C	7D0
RMCTBRQE	E8	0	RMCTENCH	154	0
RMCTBRQH	E8	E8	RMCTENCL	158	0
RMCTCCT	4		RMCTEND	400	
RMCTCLST	95	10	RMCTEPAT	38	
RMCTCMCT	118		RMCTEPBG	120	
RMCTCNSW	95	20	RMCTEPDB	148	
RMCTCPID	9C	0	RMCTEPDT	34	
RMCTCPMP	E0	0	RMCTEPPA	30	
RMCTCPS1	94	40	RMCTEPPR	50	
RMCTCPU	D0	0	RMCTERQC	F0	F4
RMCTCSB	110	0	RMCTERQE	F0	0
RMCTDFQF	13C	0	RMCTERQH	F0	F0

Name	Hex Offset	Hex Value
RMCTESCT	11C	
RMCTFLGS	94	1
RMCTFLG2	97	0
RMCTFRSV	94	2
RMCTGRSS	164	0
RMCTICS	94	20
RMCTICSP	108	
RMCTICST	DC	
RMCTICT	8	
RMCTINIT	94	10
RMCTINQE	74	
RMCTIOC	D8	0
RMCTIPAT	174	0
RMCTITER	134	1C
RMCTITT	44	
RMCTLEN	400	400
RMCTLE55	168	0
RMCTLSCT	3C	
RMCTLSQE	6C	
RMCTLTT	4C	
RMCTMCT	C	
RMCTMFA	94	80
RMCTMFS	95	40
RMCTMSO	D4	0
RMCTNAME	0	D9D4C3E3
RMCTNOBQ	110	20
RMCTNOEQ	110	10
RMCTOKCP	95	2
RMCTOTQE	70	
RMCTOUCB	A8	
RMCTOUXB	AC	
RMCTOVFL	97	40
RMCTPAGP	128	
RMCTPTCH	114	
RMCTRCT	E4	
RMCTRCTS	170	0
RMCTREPT	94	8
RMCTREP1	95	80
RMCTREP2	95	1
RMCTRMCA	14	
RMCTRMEX	28	
RMCTRMPD	24	
RMCTRMPT	10	
RMCTRMSB	2C	
RMCTRQSV	90	
RMCTRSDA	150	0
RMCTRSPL	10C	
RMCTRSVB	111	0
RMCTRTSO	95	8
RMCTSCSQ	BE	
RMCTSHDL	112	10
RMCTSIMT	110	40
RMCTSLTF	96	10
RMCTSLTL	96	20
RMCTSLTN	96	80
RMCTSLTR	96	40
RMCTSMC	138	0
RMCTSMFB	144	0
RMCTSMFS	14C	0
RMCTSQMR	97	8
RMCTSRB	130	0
RMCTSRBC	1C	
RMCTSRBT	B0	
RMCTSRNG	97	10
RMCTSRRU	15C	0
RMCTSSMF	110	1
RMCTSTGF	110	4
RMCTSTW	94	4
RMCTSWMB	16C	0
RMCTTAPE	96	0
RMCTTBS	78	0
RMCTTELM	98	
RMCTTMQE	5C	
RMCTTOC	80	0
RMCTTOCI	A0	0
RMCTTOD	7C	0
RMCTTPP	110	8
RMCTTRAD	100	
RMCTTRPC	104	0
RMCTUNTR	97	20
RMCTUPDQ	F8	0
RMCTUPQC	F8	FC
RMCTUPQH	F8	F8
RMCTUTQE	110	80
RMCTWAST	54	
RMCTWLM	94	1
RMCTWMCT	20	
RMCTWMST	18	
RMCTWSMF	110	2
RMCTWTQE	68	
RMCTX1	180	
RMCTX1C4	180	0
RMCTX1C5	200	0
RMCTX1C6	280	0
RMCTX1C7	300	0
RMCTX1C8	380	0
RMCTX2	17C	0
RMCTX3	178	0
RMCT15F	58	

RMEP Information

RMEP Heading Information

Common Name: System Resources Manager Entry Point Block
Macro ID: IRARMEP
DSECT Name: RMEP
Owning Component: System Resources Manager (SC1CX)
Eye-Catcher ID: n/a
Storage Attributes: Main Storage: YES
Virtual Storage: Common
Auxiliary Storage: No
Subpool: Nucleus
Key: 0
Data Space: No
Residency: above 16M line
Size: 3 flavors - 16, 32, or 56 @ME18324
Created by: Assembled into nucleus module, IRARMCNS
Pointed to by: RRPAEPA field of the RRPA data area
Serialization: SRM lock
Function: THE RMEP DESIGNATES A SYSTEM RESOURCES MANAGER PROCESSING ROUTINE WHICH MAY BE INVOKED THROUGH THE CONTROL ALGORITHM. THE RMEP CONTAINS THE ROUTINE ENTRY POINT ADDRESS, DEFINES A BIT MASK TO BE USED TO REQUEST THE ROUTINE, AND OPTIONALLY A RECOVERY EXTENSION AND/OR A PROVISION FOR PERIODIC EXECUTION OF THE ROUTINE. THE RMEP BLOCK CONTAINS FLAGS INDICATING HOW THE DESCRIBED ROUTINE MAY BE INVOKED.

RMEP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	56	RMEP	
		Comment			
		Entry Point Descriptor Block			
		End of Comment			
0	(0)	CHARACTER	16	RMEPEPB	ENTRY POINT BLOCK
0	(0)	ADDRESS	4	RMEPEPA	ENTRY POINT ADDRESS
4	(4)	ADDRESS	4	RMEPERA	ERROR RETRY POINT ADDRESS
8	(8)	BITSTRING	4	RMEPFLG	INVOCATION FLAG MASK
8	(8)	BITSTRING	3	RMEPVFL	RTNE INVOC FLAG FIELD
		11..		*	
		..1.		RMEPCL1	IRARMCL1
		...1 11..		*	
		.... ..1.		RMEPAP1	IRARMAP1
8	(8)	BITSTRING	2	*	
11	(B)	1...		RMEPWM8	IRARMWM8
		.... .1..		RMEPRCR	CRITICAL ALGORITHM INDICATOR
		.... ..1.		RMEPTMD	RTNE INVOKE TIME-DEPENDENT
		1		RMEPACN	RTNE PERFORMS USER LEVL ACTN
12	(C)	ADDRESS	4	RMEPPRV	ADDRESS OF PREV RMEP BLOCK
16	(10)	CHARACTER	0	RMEPEND	END OF BASE RMEP
		Comment			
		Recovery Extension Block			
		End of Comment			
16	(10)	CHARACTER	16	RMEPREC	RECOVERY EXTN
16	(10)	ADDRESS	4	RMEPMID	ADDRESS OF MODULE ID
20	(14)	CHARACTER	3	RMEPLBL	ROUTINE LABEL SUFFIX
23	(17)	CHARACTER	1	RMEPRSV	RESERVED
24	(18)	ADDRESS	4	RMEPFPT	STACK FRAME POINTER
28	(1C)	ADDRESS	4	RMEPCNT	Count for debugging
32	(20)	CHARACTER	0	RMEPRND	END RECOVERY EXTN

RMEP Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
Comment					
Entry Point Scheduling Extension					
End of Comment					
32	(20)	CHARACTER	24	RMEPSCH	SCHEDULING EXTENSN
32	(20)	ADDRESS	4	RMEPFWD	TIME DRIVEN CHAIN FORWRD PTR
36	(24)	ADDRESS	4	RMEPBCK	TIME DRIVEN CHAIN BCKWRD PTR
40	(28)	UNSIGNED	4	RMEPRSV	Reserved
44	(2C)	UNSIGNED	4	RMEPINT	INVOCATION INTERVAL
48	(30)	UNSIGNED	8	RMEPTIME	Time when entry scheduled in STCK format
56	(38)	CHARACTER	0	RMEPSND	END OF SCHD RMEP 1@L1D End of this block

RMEP Cross Reference

Name	**Hex Offset**	**Hex Value**
RMEP	0	
RMEPACN	B	01
RMEPAP1	8	02
RMEPBCK	24	
RMEPCL1	8	20
RMEPCNT	1C	
RMEPEND	10	
RMEPEPA	0	
RMEPEPB	0	
RMEPERA	4	
RMEPFLG	8	
RMEPFPT	18	
RMEPFWD	20	
RMEPINT	2C	
RMEPLBL	14	
RMEPMID	10	
RMEPPRV	C	
RMEPRCR	B	04
RMEPREC	10	
RMEPRND	20	
RMEPRSV	17	
RMEPRSV	28	
RMEPSCH	20	
RMEPSND	38	
RMEPTIME	30	
RMEPTMD	B	02
RMEPVFL	8	
RMEPWM8	B	80

RMEX Information

RMEX Heading Information

Common Name: SYSTEM RESOURCES MANAGER EXTERNAL ENTRY POINT DESCRIPTOR TABLE.
Macro ID: IRARMEX
DSECT Name: RMEX
Owning Component: SYSTEMS RESOURCE MANAGER (SC1CX)
Eye-Catcher ID: NONE
Storage Attributes: Key: 0
Residency: NUCLEUS (ABOVE 16M LINE)
Size: 136 BYTES
Created by: ASSEMBLED INTO NUCLEUS MODULE IRARMCNS
Pointed to by: THE ADDRESS OF THE RMEX IS CONTAINED IN THE -RMCTRMEX- FIELD OF THE SYSTEM RESOURCES MANAGER CONTROL TABLE.
Serialization: SRM LOCK
Function: THE RMEX CONTAINS THE ENTRY POINT DESCRIPTORS OF ALL EXTERNALLY ENTERED BRANCH POINTS (ROUTINES WHICH DO NOT RETURN CONTROL) WITHIN THE SYSTEM RESOURCES MANAGER COMPONENT. THE IRACTLCL MACRO KEYS OFF THE RMEX DISPLACEMENTS TO ROUTE CONTROL TO THE REQUESTED BRANCH (I.E. EXTERNAL ENTRY) POINT.

RMEX Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	136	RMEX	
0	(0)	CHARACTER	32	RMEPBEVT	PERFORM SYSEVENT PROCESS
0	(0)	ADDRESS	4	RMEXEVT	EVT RTNE ENTRY POINT ADDRESS
32	(20)	CHARACTER	32	RMEPBCTL	ROUTE CONTROL WITHIN SRM
32	(20)	ADDRESS	4	RMEXCTL	CTL RTNE ENTRY POINT ADDRESS
64	(40)	CHARACTER	32	RMEPBFIP	PERFORM SYSEVENT PROCESS
64	(40)	ADDRESS	4	RMEXFIP	FIP RTNE ENTRY POINT
96	(60)	ADDRESS	4	RMEXI01	NORM EXIT FROM SRM PROCESSING
100	(64)	ADDRESS	4	RMEXI17	SRM POST ECB ROUTINE
104	(68)	ADDRESS	4	RMEXCET	SRM TIMEREXP PROCESS ENTRY PT
108	(6C)	ADDRESS	4	RMEXI48	SRM SYSEVENT PROCESS ENTRY PT
112	(70)	ADDRESS	4	RMEXRR1	RECOVERY RTNE IF W/O SRM LOCK
116	(74)	ADDRESS	4	RMEXRR2	RECOVERY RTNE IF HAV SRM LOCK
120	(78)	ADDRESS	4	RMEXXPE	RECOVERY RTNE IF XM-POST FAIL
124	(7C)	ADDRESS	4	RMEXSRE	RECOVERY RTNE IF SRM SRB PURG
128	(80)	ADDRESS	4	RMEXPSE	POST SRB PURGE
132	(84)	ADDRESS	4	RMEXPFE	POST FAILURE ROUTINE
136	(88)	CHARACTER	0	RMEXEND	END OF RMEX TABLE END OF THIS BLOCK

RMEX Cross Reference

Name	Hex Offset	Hex Value
RMEPBCTL	20	
RMEPBEVT	0	
RMEPBFIP	40	
RMEX	0	
RMEXCET	68	
RMEXCTL	20	
RMEXEND	88	
RMEXEVT	0	
RMEXFIP	40	
RMEXI01	60	
RMEXI17	64	
RMEXI48	6C	
RMEXPFE	84	
RMEXPSE	80	
RMEXRR1	70	
RMEXRR2	74	
RMEXSRE	7C	
RMEXXPE	78	

RMPL Information

RMPL Programming Interface information

Programming Interface information

RMPL

ONLY the following fields are part of the programming interface information:

- RMPLASCB
- RMPLASID
- RMPLJST
- RMPLTCBA
- RMPLTERM
- RMPLTYPE

End of Programming Interface information

RMPL Heading Information

Common Name: RESOURCE MANAGER PARAMETER LIST
Macro ID: IHARMPL
DSECT Name: RMPL
Owning Component: RECOVERY TERMINATION MANAGER (SCRTM)
Eye-Catcher ID: NONE
Storage Attributes: Subpool: 255
Key: 0
Residency: BELOW THE 16M LINE
Size: 24 BYTES
Created by: IEAVTRT2
Pointed to by: REGISTER 1 UPON ENTRY TO A RESOURCE MANAGER.
THE RMPL CAN ALSO BE FOUND VIA THE RTM2RMPL FIELD OF THE RTM2WA DATA AREA.
Serialization: NONE
Function: THE RESOURCE MANAGER PARAMETER LIST IS THE COMMUNICATION AREA BETWEEN TASK/MEMORY TERMINATION AND THE USER/SYSTEM DEFINED RESOURCE MANAGERS.

RMPL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RMPL	, RMPLPTR
0	(0)	X'80'	0	BIT0	"128"
0	(0)	X'40'	0	BIT1	"64"
0	(0)	X'20'	0	BIT2	"32"
0	(0)	X'10'	0	BIT3	"16"
0	(0)	X'8'	0	BIT4	"8"
0	(0)	X'4'	0	BIT5	"4"
0	(0)	X'2'	0	BIT6	"2"
0	(0)	X'1'	0	BIT7	"1"
0	(0)	BITSTRING	1	RMPLFLG1	FLAGS INDICATING TYPE OF TERMINATION
		1...		RMPLTYPE	"BIT0" ON,INDICATES ABNORMAL TERMINATION OFF,INDICATES NORMAL TERMINATION
		.1..		RMPLTERM	"BIT1" ON,INDICATES MEMORY TERMINATION OFF,INDICATES TASK TERMINATION
		..1.		RMPLRBPP	"BIT2" ON,INDICATES RB RELATED PURGE
		...1		RMPLIPUF	"BIT3" ON,INHIBITING PAGE UNFIXES
		.... 1...		RMPLRCOV	"BIT4" ON, INDICATES TASK RECOVERY PORTION OF RTM2 IS THE CALLER
		.... .1..		RMPLJST	"BIT5" ON, INDICATES THAT TERMINATING TASK IS JOB STEP TCB
		.... ..1.		RMPLWT1M	"BIT6" ON, INDICATES TYPE 1 MSG TABLE ENTRIES ARE TO BE WRITTEN
		1		RMPLMTC	"BIT7" ON, INDICATES CALL TO RESOURCE MANAGER WAS MADE BY THE MEMORY TERMINATION CONTROLLER-MODULE IEAVTMTC
		1		RMPLIOPG	"BIT7" ON WHEN RMPLTERM IS OFF INDICATES TO THE CONTENTS RESOURCE MANAGER THAT I/O HAS BEEN PURGED
		1		RMPLEQPG	"BIT7" ON WHEN RMPLTERM IS OFF INDICATES TO THE GRS RESOURCE MANAGER THAT PENDING ENQS FOR THIS TASK/ASID MUST BE PURGED AS PART OF EARLY RTM2 NON-RETRIABLE ERROR PROCESSING
1	(1)	BITSTRING	1	RMPLFLG2	RESERVED
2	(2)	CHARACTER	2	RMPLASID	ASID ASSOCIATED WITH THE TERMINATING TASK OR MEMORY
4	(4)	ADDRESS	4	RMPLASCB	ADDRESS OF ASCB ASSOCIATED WITH TERMINATING TASK OR MEMORY
8	(8)	ADDRESS	4	RMPLTCBA	ADDRESS OF TERMINATING TCB(ZEROES IF MEMORY TERMINATION)
12	(C)	ADDRESS	4	RMPLRBPA	ADDRESS OF TERMINATING RB
16	(10)	ADDRESS	4	RMPLRMWA	ADDRESS OF RESOURCE MANAGERS WORK AREA
20	(14)	ADDRESS	4	RMPLDCBL	ADDRESS OF DCB LIST USED BY DATA MGR TASK CLOSE ROUTINE FOR RB RELATED PURGES
20	(14)	X'10'	0	RMPLWALN	"16" LENGTH IN FULLWORDS OF RESOURCE MANAGERS WORK AREA

RMPL Cross Reference

Name	Hex Offset	Hex Value
BIT0	0	80
BIT1	0	40
BIT2	0	20
BIT3	0	10
BIT4	0	8
BIT5	0	4
BIT6	0	2
BIT7	0	1
RMPL	0	
RMPLASCB	4	
RMPLASID	2	
RMPLDCBL	14	
RMPLEQPG	0	1
RMPLFLG1	0	
RMPLFLG2	1	
RMPLIOPG	0	1
RMPLIPUF	0	10
RMPLJST	0	4
RMPLMTC	0	1
RMPLRBPA	C	
RMPLRBPP	0	20
RMPLRCOV	0	8
RMPLRMWA	10	
RMPLTCBA	8	
RMPLTERM	0	40
RMPLTYPE	0	80
RMPLWALN	14	10
RMPLWT1M	0	2

RNLE Information

RNLE Programming Interface information

Programming Interface information

RNLE

End of Programming Interface information

RNLE Heading Information

Common Name: RESOURCE NAME LIST ENTRY
Macro ID: ISGRNLE
DSECT Name: RNLE, RNL_HEADER
Owning Component: GLOBAL RESOURCE SERIALIZATION (SCSDS)
Eye-Catcher ID: N/A
Offset: N/A
Length: N/A
Storage Attributes: Subpool: 245
Key: 0
Residency: ABOVE THE 16M LINE
Size: 10 BYTES + VARIABLE LENGTH RNAME
Created by: THE RNLES ARE CREATED BY ISGNRNLP AT SYSTEM INITIALIZATION OR BY ISGCRNLP DURING A DYNAMIC RNL CHANGE.
Pointed to by: THERE ARE THREE LISTS OF RNLES:
GVTSERNL POINTS TO THE FIRST RNLE IN THE SYSTEMS EXCLUSION LIST
GVTSIRNL POINTS TO THE FIRST RNLE IN THE SYSTEM INCLUSION LIST
GVTRCRNL POINTS TO THE FIRST RNLE IN THE RESERVE CONVERSION LIST
Serialization: CHANGES TO THE RNLS ARE SERIALIZED BY THE CMSEQDQ LOCK
Function: DEFINES RESOURCES THAT ARE TO BE INCLUDED OR EXCLUDED FROM GLOBAL RESOURCE SERIALIZATION AND ALSO DEFINES RESERVE RESOURCES THAT ARE TO BE CONVERTED TO GLOBAL ENQS

RNLE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RNLE	RNLE ENTRY
0	(0)	CHARACTER	10	RNLEFXDP (0)	FIXED PORTION OF RNLE
0	(0)	BITSTRING	1	RNLEFLGS	FLAG BYTE
		1...		RNLELAST	"X'80'" WHEN 1, THIS IS A DUMMY RNLE INDICATING THE END OF THE LIST
		.1..		RNLEGENR	"X'40'" WHEN 1, GENERIC ENTRY
		..1.		RNLEXALL	"X'20'" WHEN 1, GRSRNL=EXCLUDE, INDICATED ON A DUMMY RNLE
		...1		RNLEPATT	"X'10'" WHEN 1, PATTERN ENTRY
		.... 1...		RNLEFR04	"X'08'" RESERVED
		.... .1..		RNLEFR03	"X'04'" RESERVED
		.... ..1.		RNLEFR02	"X'02'" RESERVED
		1		RNLEFR01	"X'01'" RESERVED
1	(1)	ADDRESS	1	RNLERNML	LENGTH OF RNAME
2	(2)	CHARACTER	8	RNLEQNME	QNAME
10	(A)	CHARACTER	1	RNLERNME (0)	RNAME (VARIABLE LENGTH)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RNL_HEADDR	RNL HEADER 3
0	(0)	CHARACTER	8	RNLTYPE	TYPE OF RNL
8	(8)	SIGNED	4	RNL_LENGTH	LENGTH OF RNL

RNLE Cross Reference

Name	Hex Offset	Hex Value
RNL_HEADDR	0	
RNL_LENGTH	8	
RNLE	0	
RNLEFLGS	0	
RNLEFR01	0	1
RNLEFR02	0	2
RNLEFR03	0	4
RNLEFR04	0	8
RNLEFXDP	0	
RNLEGENR	0	40
RNLELAST	0	80
RNLEPATT	0	10
RNLEQNME	2	
RNLERNME	A	
RNLERNML	1	
RNLEXALL	0	20
RNLTYPE	0	

RQE Information

RQE Heading Information

Common Name: RQE - EXCP Request Queue Element
Macro ID: IECDRQE
DSECT Name: RQE
Owning Component: Execute Channel Program (SC1C6)
Eye-Catcher ID: None
Storage Attributes: Subpool: SP245
Key: 0
Size: See Assembler Listing
Created by: - IECVEXSM - PAGEHDR, SMBLKHDR, LGXBLOCK, MEDBLOCK, LGABLOCK
Pointed to by: RQENRQE in IECDRQE
IOSUSE in IECDIOSB
RRQFIRST in IECDRRQ
RRQLAST in IECDRRQ
XFRRCRQE in IECDXFRR
XFRRPRQE in IECDXFRR
Serialization: N/A
Function: This DSECT describes the control block used within the EXCP processor to define a single request. It contains all the information necessary to initiate and terminate I/O requests within the EXCP processor.

RQE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RQE	
0	(0)	ADDRESS	4	RQEUCB	Address of the Unit Control Block
4	(4)	ADDRESS	4	RQEIOB	Address of the Input-Output Block
8	(8)	ADDRESS	4	RQEDEB	Address of the Data Extent Block
12	(C)	ADDRESS	4	RQETCB	ADDRESS of the Task Control Block
16	(10)	BITSTRING	16	RQEVIOWK (0)	Area used by VIO as a work area - valid with RQEVAM bit set
16	(10)	ADDRESS	4	RQETCCW	Address of Translation Control block (TCCW) for EXCP virtual and V=R requests, or zero for EXCPVR requests
20	(14)	ADDRESS	4	RQENRQE	Address of the next RQE on Related Request chain (RRQ)
24	(18)	ADDRESS	4	RQERRQ	Address of Related Request Queue
28	(1C)	ADDRESS	4	RQESRB	Address of SRB/IOSB block
32	(20)	ADDRESS	4	RQEIPIB	Address of Purge IPIB
		Comment			
		BIT SETTINGS FOR RQEPRT			
		End of Comment			
36	(24)	BITSTRING	1	RQEPRT	Protect key from SVC old PSW (BITS 0-3) and flags(4-7)
		1111		RQEPKEY	"X'F0'" Protect key - bits 0-3
		.... 1...		RQEPRT4R	"X'08'" BIT4 - reserved
		.... .1..		RQEZHPF	"X'04'" . zHPF channel program
		.... ..1.		RQEK0BYP	"X'02'" SAM-E request
		1		RQEFMT1	"X'01'" Format-1 channel program (copied from IOBEFMT1). Note that for EXCP virtual requests, this represents the format of the input channel program, not the translated channel program, which is always format-1
		Comment			
		BIT SETTINGS FOR RQETYPE			
		End of Comment			
37	(25)	BITSTRING	1	RQETYPE	Request type flags----------
		1...		RQE114	"X'80'" . EXCPVR request
		.1..		RQEVIRT	"X'40'" . Virtual EXCP request
		..1.		RQE1TO1	"X'20'" . Virtual equal real request
		...1		RQEVAM	"X'10'" . VIO RQE request
		.... 1...		RQEEOEE	"X'08'" . End-of-Extent-error, to be purged
		.... .1..		RQEDIE	"X'04'" . EXCP DIE go to PCI appendage
		11		RQERRTYP	"X'03'" . Related request flags.....
		11		RQETYP3	"X'03'" . . Related request type 3

RQE Cross Reference

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... ..1.		RQETYP2	"X'02'" . . Related request type 2
		1		RQETYP1	"X'01'" . . Related request type 1
		Comment			
		BIT SETTINGS FOR RQEFLAG2			
		End of Comment			
38	(26)	BITSTRING	1	RQEFLAG	RQE flag byte----------
		1...		RQERETRY	"X'80'" . Retry requested by appendage
		.1..		RQENOPST	"X'40'" . No post requested
		..1.		RQENOFRE	"X'20'" . Dont free RQE
		...1		RQEFIXST	"X'10'" . Fix process has been started or completed, unfixing required
		.... 1...		RQESTBL	"X'08'" . This request is startable - that is all fixing and translation is done
		.... .1..		RQESRBS	"X'04'" . SRB scheduled for this RQE
		.... ..1.		RQEPURGE	"X'02'" . RQE undergoing purge
		1		RQEFLR01	"X'01'" . Reserved
		Comment			
		BIT SETTINGS FOR RQEFLAG3			
		End of Comment			
39	(27)	BITSTRING	1	RQEFLAG3	RQEFLAG3 flag byte----------
		1...		RQEINIOS	"X'80'" . Request sent to IOS via STARTIO
		.1..		RQEPCEDE	"X'40'" . PCI DIE with CE/DE
		..1.		RQEXDERP	"X'20'" . DASD ERP is caller of extent check routine
		...1		RQESMFCT	"X'10'" . SMFIOCNT macro invoked
		.... 1...		RQEACDCT	"X'08'" . Indicate to accumulate DCTI
		.... .1..		RQEPSDCT	"X'04'" . Pass DCTI count to SAM-E
		.... ..1.		RQELGBAL	"X'02'" . Indicates that above-the-line large block storage is used
		1		RQEXCPS	"X'01'" . CPS exit exists
40	(28)	ADDRESS	4	RQEDCTI	Counter used to accumulate DCTI
44	(2C)	ADDRESS	4	RQEIOBE	Address of the caller's IOB extension or zero
48	(30)	ADDRESS	4	RQEUPSW	Contents of RBOPSW containing the caller's address following the EXCP SVC.
52	(34)	ADDRESS	4	RQERQEX	Address of the RQE extension.
52	(34)	X'38'	0	RQENSASZ	"*-RQE" RQE size without the Save area
56	(38)	BITSTRING	72	RQESAVE	Area passed by IECVEXCP as an 18 word savearea
56	(38)	X'38'	0	RQESAV16	"RQESAVE+0,64" 16 word savearea
56	(38)	X'78'	0	RQESEEKA	"RQESAVE+64,8" The seek address passed to the EOE and SIO appendages
56	(38)	X'80'	0	RQEBL	"*-RQE" RQE block length

RQE Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
RQE	0		RQEPURGE	26	2
RQEACDCT	27	8	RQERETRY	26	80
RQEBL	38	80	RQERQEX	34	
RQEDCTI	28		RQERRQ	18	
RQEDEB	8		RQERRTYP	25	3
RQEDIE	25	4	RQESAVE	38	
RQEEOEE	25	8	RQESAV16	38	38
RQEFIXST	26	10	RQESEEKA	38	78
RQEFLAG	26		RQESMFCT	27	10
RQEFLAG3	27		RQESRB	1C	
RQEFLR01	26	1	RQESRBS	26	4
RQEFMT1	24	1	RQESTBL	26	8
RQEINIOS	27	80	RQETCB	C	
RQEIOB	4		RQETCCW	10	
RQEIOBE	2C		RQETYPE	25	
RQEIPIB	20		RQETYP1	25	1
RQEK0BYP	24	2	RQETYP2	25	2
RQELGBAL	27	2	RQETYP3	25	3
RQENOFRE	26	20	RQEUCB	0	
RQENOPST	26	40	RQEUPSW	30	
RQENRQE	14		RQEVAM	25	10
RQENSASZ	34	38	RQEVIOWK	10	
RQEPCEDE	27	40	RQEVIRT	25	40
RQEPKEY	24	F0	RQEXCPS	27	1
RQEPRT	24		RQEXDERP	27	20
RQEPRT4R	24	8	RQEZHPF	24	4
RQEPSDCT	27	4	RQE1TO1	25	20

Name	Hex Offset	Hex Value
RQE114	25	80

RRPA Information

RRPA Heading Information

Common Name: SYSTEM RESOURCES MANAGER RECOVERY ROUTINE PARAMETER AREA
Macro ID: IRARRPA
DSECT Name: RRPA
Owning Component: System Resources Manager (SC1CX)
Eye-Catcher ID: RRPA
Offset: 0
Length: 4
Storage Attributes: Main Storage: YES
Virtual Storage: Common
Auxiliary Storage: No
Subpool: Nucleus or subpool 245
Key: 0
Data Space: No
Residency: Above 16M line
Size: 88 @ME18197
RRPA -- X'0058' bytes
Created by: Recovery Termination Manager
Pointed to by: Register 3 in SRM
Serialization: Disablement (CPU or SRM locks obtained by SRM)
Function: - THE RRPA IDENTIFIES THE INVOCATION THAT RESULTED IN SYSTEM RESOURCES MANAGER PROCESSING, AND SPECIFIES THE INTERNAL ROUTINE CURRENTLY IN CONTROL. THE RRPA PRESERVES STATUS FOR EXIT FROM THE SYSTEM RESOURCES MANAGER.

RRPA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	88	RRPA	
0	(0)	CHARACTER	4	RRPANAME	BLOCK INDENTIFICATION - 'RRPA'
4	(4)	ADDRESS	4	RRPA_STACKEND	
					Used for stack overflow checks. 7FFFFFFF no-ops checking.
8	(8)	CHARACTER	4	RRPAINC	REGISTER 0 AT ENTRY
8	(8)	BITSTRING	2	RRPAASD	ASID FOR ORIGINAL ENTRY
		1...		RRPAENCLAVEIDENTIFIER	
					High order bit being on in the identifier indicates an enclave ID
8	(8)	BITSTRING	1	*	Identifier
10	(A)	BITSTRING	1	RRPAFLG0	FLAG BYTE
		1...		RRPANSWI	=1 FOR MEMDEL TYPE=NOSWIN
		1...		RRPAINTV	=1 FOR REQPGDAT TYPE=INTERVAL
		1...		RRPABYTE	=1 FOR STGTEST TYPE=BYTE
		.111 111.		*	UNUSED BY MEMDEL
11	(B)	ADDRESS	1	RRPACOD	ORIGINAL ENTRY SYSEVENT CODE
12	(C)	ADDRESS	4	RRPAINP	INPUT PARAMETER ADDRS
12	(C)	CHARACTER	4	RRPAVLU	INPUT PARAMETER VALUE
16	(10)	BITSTRING	2	RRPAFLG	ROUTINE STATUS FLAGS
16	(10)	BITSTRING	1	RRPAFLGP1	
		11..		*	
		..1.		RRPASVC	ORIGINAL ENTRY VIA SVC
		...1		*	
		.... 1...		RRPACHM	CHAN MEAS UPDATE IN CONTROL
		.... .11.		*	
		1		RRPASRV	SERVICE ROUTINE (SEE DEPENDENCY SECTION IN PROLOG)
17	(11)	BITSTRING	1	RRPAFLGP2	
		1...		RRPARSM	SET WHEN SRM CALLS RSM SO THAT DURING RECOVERY SRM WILL NOT RETRY
		.1..		RRPAWAR	WORKLOD ACTIVITY RTNE CURRENT
		..1.		RRPAIPS	SET RTNE CURR
		...1		RRPACHP	FIP OR SET CHAP CALL
		.... 1...		RRPACTL	INVOKE IRARMCTL FOR 2ND FAIL
		.... .1..		RRPARTI	RECOVERY TERMINATE INDICATOR
		.... ..1.		RRPARFI	RECOVERY FAILURE INDICATOR
		1		RRPARCI	RECURSION INDICATOR
18	(12)	CHARACTER	1	RRPAKEY	ORIGINAL ENTRY PROTECT KEY
19	(13)	ADDRESS	1	RRPARTC	RETURN CODE FOR FINAL EXIT
20	(14)	UNSIGNED	4	RRPATOD	TIME OF DAY AT ENTRY
24	(18)	ADDRESS	4	RRPAFPT	STACK FRAME POINTER
28	(1C)	ADDRESS	4	RRPAEPA	RMEP ADDR OF RTNE IN CONTROL

RRPA Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
32	(20)	SIGNED	4	RRPARSVF	Reserved
36	(24)	BITSTRING	1	RRPAFLG1	MORE CURRENT STATUS
		1...		RRPAWLM	SRM has called WLM
		.1..		RRPATOUCHINGSSRB	Referencing an SSRB while unserialized. 0C4 is possible, no dump should be taken.
		..1.		RRPAREFESMB	Running ESMB tree, rebuild trees if recovery entered
		...1		RRPACALLGR	Global recovery should be called if an abend occurs
		.... 1...		RRPACPOOL	On during invocation of CPOOL service
		.... .1..		RRPAINENCLAVEWEBLOOP	On during CountReadSrbsInEnclave in IRACPSRP to prevent dumps
		.... ..1.		RRPA_RETRY_IN_ROUTINE	On if RMCTY_Err_Retry_Addr is filled with a retryaddress in same routine/subroutine.-
		1		*	reserved
37	(25)	BITSTRING	1	RRPAFLG2	More current status
		1...		RRPAFPREGISTERSAVED	SRM saved the FP registers
		.111 1111		*	reserved
38	(26)	BITSTRING	2	RRPALOCKINFO	Lock information See also SechtLock in IRASYSEV
38	(26)	BITSTRING	1	RRPALOCK	Locks Held The order of flags represents the hierarchy of the locks
		1...		RRPALOCKCPU	CPU lock held - Using the SRM Lock Stack - May not call anyone who would reenter SRM, unless SRM lock obtained first
		.11.		*	reserved for locks
		...1		RRPALOCKSRM	SRM lock held
		.... 11..		*	reserved for locks
		.... ..1.		RRPALOCKSRMENQ	SRMENQ lock held
		1		*	reserved for locks
39	(27)	BITSTRING	1	RRPALOCKEXT	Lock extension
		1111 111.		*	reserved for lock ext
		1		RRPALOCKEXTDISABLED	Entry is a disabled branch
40	(28)	CHARACTER	8	RRPATOC	Time of entry in STCK format
48	(30)	CHARACTER	8	RRPAFIRSTFRAME	Address of the first frame in the related SRM stack
48	(30)	SIGNED	4	*	High half
52	(34)	ADDRESS	4	RRPAFIRSTFRAME31	Address of the first frame in the related SRM stack
56	(38)	CHARACTER	16	RRPASYSEVENTOUTPUTAREA	16 Byte Sysevent output area
56	(38)	CHARACTER	12	RRPASYSEVENTOUTPUTAREA12	12 byte Sysevent output area, used by REQSERVC and STGTEST
56	(38)	SIGNED	4	RRPASYSEVENTOUTPUTAREAFLD1	
60	(3C)	SIGNED	4	RRPASYSEVENTOUTPUTAREAFLD2	
64	(40)	SIGNED	4	RRPASYSEVENTOUTPUTAREAFLD3	
68	(44)	CHARACTER	4	*	Reserved
72	(48)	CHARACTER	4	RRPARSV1	Reserved
76	(4C)	CHARACTER	4	RRPARSV2	Reserved
80	(50)	CHARACTER	4	RRPARSV3	Reserved
84	(54)	CHARACTER	4	RRPARSV4	Reserved
88	(58)	CHARACTER	0	RRPAEND	END OF RRPA

RRPA Cross Reference

Name	**Hex Offset**	**Hex Value**
RRPA	0	
RRPA_RETRY_IN_ROUTINE	24	02
RRPA_STACKEND	4	
RRPAASD	8	
RRPABYTE	A	80
RRPACALLGR	24	10
RRPACHM	10	08
RRPACHP	11	10
RRPACOD	B	
RRPACPOOL	24	08
RRPACTL	11	08
RRPAENCLAVEIDENTIFIER	8	80
RRPAEND	58	
RRPAEPA	1C	
RRPAFIRSTFRAME	30	
RRPAFIRSTFRAME31	34	
RRPAFLG	10	
RRPAFLGP1	10	
RRPAFLGP2	11	
RRPAFLG0	A	
RRPAFLG1	24	
RRPAFLG2	25	
RRPAFPREGISTERSAVED	25	80
RRPAFPT	18	
RRPAINC	8	
RRPAINENCLAVEWEBLOOP		

Name	Hex Offset	Hex Value
	24	04
RRPAINP	C	
RRPAINTV	A	80
RRPAIPS	11	20
RRPAKEY	12	
RRPALOCK	26	
RRPALOCKCPU	26	80
RRPALOCKEXT	27	
RRPALOCKEXTDISABLED		
	27	01
RRPALOCKINFO	26	
RRPALOCKSRM	26	10
RRPALOCKSRMENQ		
	26	02
RRPANAME	0	
RRPANSWI	A	80
RRPARCI	11	01
RRPAREFESMB	24	20
RRPARFI	11	02
RRPARSM	11	80
RRPARSVF	20	
RRPARSV1	48	
RRPARSV2	4C	
RRPARSV3	50	
RRPARSV4	54	
RRPARTC	13	
RRPARTI	11	04
RRPASRV	10	01
RRPASVC	10	20
RRPASYSEVENTOUTPUTAREA		
	38	
RRPASYSEVENTOUTPUTAREAFLD1		
	38	
RRPASYSEVENTOUTPUTAREAFLD2		
	3C	
RRPASYSEVENTOUTPUTAREAFLD3		
	40	
RRPASYSEVENTOUTPUTAREA12		
	38	
RRPATOC	28	
RRPATOD	14	
RRPATOUCHINGSSRB		
	24	40
RRPAVLU	C	
RRPAWAR	11	40
RRPAWLM	24	80

RSA Information

RSA Heading Information

Common Name: RING SYSTEM AUTHORITY MESSAGE
Macro ID: ISGRSA
DSECT Name: RSA

RSAIRCD

RSADINFO
Owning Component: Global Resource Serialization (SCSDS)
Eye-Catcher ID: RSA
Offset: 0
Length: 4
Storage Attributes: Subpool: 229 IN GRS ADDRESS SPACE
Key: 0
Size: 4088 BYTES FOR A MAINRING RSA
64 BYTES FOR A NON-MAINRING RSAIRCD
Created by: ISGBTC
Pointed to by: The RSA is addressable from the RSV.
THE GCB FOLLOWING EACH RSL POINTS AT A BUFFER (THE CONTENTS OF WHICH IS A RSAIRCD) SENT AND RECEIVED VIA THAT RSL.
Serialization: The mainring RSA is serialized by a field in the RSV.
A RSAIRCD OWNED BY A RSL IS SERIALIZED BY RSLWLOCK IN THAT RSL.
Function: COMMUNICATE GLOBAL ENQ/DEQ/RESERVE REQUESTS AND GRS COMMANDS AMONG GRS SYSTEMS. ALSO, BY ITS PRESENCE AT A SYSTEM, CONFER UPON THAT SYSTEM THE AUTHORITY TO REQUEST THAT GRS RESOURCES BE OBTAINED OR RELEASED.

RSA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	24	RSA	RSA MESSAGE
0	(0)	CHARACTER	24	RSAMRPFX	RSA MAINRING HEADER
0	(0)	CHARACTER	4	RSAID	EBCDIC ID RSA
4	(4)	BITSTRING	1	RSAMRFLG	MAINRING FLAGS
		1...		RSAFURC	UNRECEIVED-COMMAND BIT. IF 1, THE RSA CONTAINS A MAINRING COMMAND FROM ISGBCI
		.1..		RSAFBKQW	BLOCK-QWB BIT. IF 1, NO SYSTEM CAN PLACE A QWB IN THE MAINRING RSA. SET ONLY BY THE SYSTEM THAT SET RSAFURC.
		..1.		RSAFRQSR	REQUEST-SPANNING-RSA BIT. IF 1, THIS RSA CONTAINS AN INCOMPLETE REQUEST SO NO SYSTEM CAN PLACE A QWB IN THE MAINRING RSA UNTIL THE REQUEST IS COMPLETE. SET BY THE SYSTEM THAT PLACED INCOMPLETE REQUEST IN THE RSA
		...1 1111		*	RESERVED
5	(5)	CHARACTER	7	RSARCP	TOKEN AND SEQUENCE NUMBER
5	(5)	CHARACTER	3	RSARCTOK	RING-CREATION TOKEN
8	(8)	SIGNED	4	RSARCSEQ	SEQUENCE-NUMBER
12	(C)	SIGNED	2	RSAQWBCT	NUMBER OF QWB"S AND QWB-EXTENSIONS IN RSA
14	(E)	SIGNED	2	RSALNCA	LENGTH OF COMMAND-AREA THAT FOLLOWS RSADATA
16	(10)	SIGNED	2	RSALNCQD	LENGTH OF CONTINUATION QWB-DATA, OR 0
18	(12)	UNSIGNED	1	RSATYPCA	COMMAND-TYPE IN COMMAND-AREA
19	(13)	UNSIGNED	1	RSASYSCP	COMMAND-PHASE TO BE EXECUTED BY MAINRING SYSTEMS. SEE RSAR1XXX CONSTANTS FOR POSSIBLE VALUES
20	(14)	UNSIGNED	1	RSASYS	SYSID OF SYSTEM THAT SET RSAFURC, OR 0
21	(15)	UNSIGNED	1	RSATRGCA	SYSID OF SYSTEM THAT IS TARGET OF COMMAND-AREA, OR 0 IF ALL MAINRING SYSTEMS ARE TARGET
22	(16)	CHARACTER	2	*	RESERVED
24	(18)	CHARACTER	0	RSADATA	COMMAND AREA OR QWB DATA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	64	RSAIRCD	RSAIRCD. THIS MESSAGE MAY BE A CONTROL-INFORMATION RSAIRCD (RSAIID=RSAI) OR A COLLECT-RSA-RESPONSE RSAIRCD (RSAIID=RSAC)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	CHARACTER	4	RSAIID	EBCDIC ID RSAI. VALUE IN A COLLECT-RSA-RESPONSE RSAIRCD IS RSAC.
4	(4)	BITSTRING	1	RSAIFLGS	STATUS FLAGS
		1...		RSAIFCPQ	COMMAND-PREVIOUSLY-QUEUED BIT. IF 1, THE SYSTEM THAT SENT THIS COMMAND IS RE-SENDING A COMMAND THAT WAS PREVIOUSLY QUEUED AND HAS NOT YET EXECUTED.
		.1..		RSAIFIDR	IDENTITY-REQUESTED BIT. IF 1, THE SYSTEM THAT SENT THIS RSAIRCD IS REQUESTING THE IDENTITY OF THE RECEIVING SYSTEM
		..1.		RSAIFSRF	SEND-RSL-FUTURE BIT. IF 1, THE SYSTEM THAT SENT THIS RSAIRCD WILL SEND THE MAINRING RSA VIA THE SAME RSL
		...1		RSAIFRRF	RECEIVE-RSL-FUTURE BIT. IF 1, THE SYSTEM THAT SENT THIS RSAIRCD WILL EXPECT TO RECEIVE THE MAINRING RSA VIA THE SAME RSL
		.... 1...		RSAIFRQP	REQUEST PERMISSION TO STARTPOP
		.... .1..		RSAIFACK	ACK-TAP RSAIRCD. RSA WAS RECEIVED AND VALIDATED BY THIS SYSTEM. RSAIMRSC HAS THE SEND-COUNT OF THE RECEIVED RSA.
		11		*	RESERVED
5	(5)	UNSIGNED	1	RSARRSP1	PHASE-NUMBER. FOR VALUES OF RSARRSP1 AND REASONS, SEE THE CONSTANTS THAT ARE NAMED RSAR1XXX
6	(6)	UNSIGNED	2	RSACCMDC	COMMAND-COUNT. ID OF A COMMAND SENT IN RSAIRCD, OR 0
8	(8)	UNSIGNED	2	RSARCMDC	RESPONSE COMMAND-COUNT. ID OF COMMAND THAT TRIGGERED RESPONSE, OR 0
10	(A)	BITSTRING	1	RSAIFLG2	STATUS FLAGS 2
10	(A)	UNSIGNED	1	RSAICLD	LENGTH OF RSA-DATA. VALID ONLY IN A COLLECT-RSA-RESPONSE RSAIRCD. RSAICLD OF ZERO APPEARS ONLY IN AN END-OF-DATA RSAIRCD.
		1...		RSAIFNMD	NEW-MAINRING-DISCOVERED BIT. IF 1, THE SYSTEM SENDING THIS RSAIRCD HAS SEEN A MAINRING TOKEN NEWER THAN THE TOKEN IN RSAIMRTK. VALID ONLY IF RSACTBIX HAS A VALUE OF RSACTBIZ (=0).
		.111 1111		*	RESERVED
11	(B)	UNSIGNED	1	RSACTBIX	REQUESTED TABLE-INDEX. INDEX OF RSVENTY ENTRY THAT MUST BE PLACED IN FIELD RSAITBID, OR END-OF-DATA INDICATOR, OR COLLECT-RSA-RESPONSE INDICATOR. USED TO REQUEST RSVENTY INFORMATION FROM SOME OTHER SYSTEM. FOR VALUES OF RSACTBIX AND REASONS, SEE THE NAMED CONSTANTS THAT BEGIN WITH LETTERS RSACTBI.
12	(C)	CHARACTER	48	RSAICRDA	COLLECT-RSA DATA. VALID ONLY IN A COLLECT-RSA-RESPONSE RSAIRCD THAT IS NOT END-OF-DATA
12	(C)	CHARACTER	20	RSAITBID	TABLE SYSTEM ID. HAS SAME FORMAT AS RSVESNID.
12	(C)	CHARACTER	20	RSAISNID	SECTION OF RSAIRCD THAT IDENTIFIES SENDER. HAS SAME FORMAT AS RSVESNID. VALID ONLY IN A CONTROL-INFORMATION RSAIRCD THAT IS NOT END-OF-DATA.
12	(C)	CHARACTER	8	RSAISYNM	SYSNAME OF SYSTEM
20	(14)	SIGNED	4	RSAISYTK	TOKEN OF SYSTEM
20	(14)	SIGNED	4	RSAICSC	SEND-COUNT OF COLLECTED RSA. VALID ONLY IN AN END-OF-DATA RSAIRCD.
24	(18)	CHARACTER	1	*	RESERVED
25	(19)	UNSIGNED	1	RSAISYID	SYSID OF SYSTEM
26	(1A)	BITSTRING	1	RSAIMNRF	MAINRING FLAG. IF FF, THE NAMED SYSTEM IS IN THE MAINRING
27	(1B)	BITSTRING	1	RSAIUQDF	UPTODATE-QEL-DATA FLAG AND INITIATE-RESTART-ABILITY FLAG
		1...		RSAIUUDF	UPTODATE-QEL-DATA FLAG. IF 1, THE NAMED SYSTEM HAS UPTODATE QEL DATA
		.1..		RSAIUNRF	NO-RESTART FLAG. IF 1, THIS SYSTEM CANNOT INITIATE AUTO RESTART
		..1.		RSAIFNXS	When off, XCF system. When on, non-XCF system
		...1 1111		*	RESERVED
28	(1C)	SIGNED	4	RSAIMRTL	SEQUENCE-NUMBER OF LAST MAINRING RSA SENT BY THE NAMED SYSTEM BEFORE IT LEFT THE MAINRING, OR ZERO
32	(20)	CHARACTER	8	RSAIMRTK	CURRENT MAINRING TOKEN

Comment

@P2D

End of Comment

40	(28)	SIGNED	4	RSACREST	MAINRING RSA RESIDENCE TIME THAT WILL BE USED BY SENDING SYSTEM AFTER IT IS IN MAINRING. VALID IF RSAIRCD IS FROM A SYSTEM PERFORMING SENDCMD-RSCRADDS, AND PHASE-NUMBER RSARRSP1 IS LESS THAN RSAR1ATP
40	(28)	SIGNED	4	RSACAMRC	MAINRING CYCLE-TIME PASSED TO AN ADDSYS TARGET. VALID IF RSAIRCD IS SENT FROM A SYSTEM PERFORMING ADDSYS TO A SYSTEM PERFORMING SENDCMD-RSCRADDS, AND PHASE-NUMBER RSARRSP1 = RSAR1ATP AND RSACTBIX = 0

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
44	(2C)	SIGNED	4	RSARMRFT	MAINRING FAILURE TOKEN. SENT TO TARGET OF AN ADDSYS, WHEN ADDSYS IS USED TO BRING A SYSTEM INTO A NEW MAINRING AFTER A MAINRING FAILURE. INFORMS ADDSYS TARGET WHAT WAS LAST MAINRING RSA RECEIVED BEFORE THE PREVIOUS MAINRING FAILURE
48	(30)	SIGNED	2	*	RESERVED
50	(32)	CHARACTER	10	RSACDATA	COMMAND DATA OR AUTO RESTART PERMISSION DATA
50	(32)	UNSIGNED	1	RSACTYPE	COMMAND-TYPE OR COLLECT-RSA INDICATOR. IGNORED IF RSACCMDC IS ZERO OR RSAIFRQP IS ON. OR RSAIRCD IS A COLLECT-RSA RESPONSE. VALID VALUES ARE GIVEN BY NAMED CONSTANTS RSAICVXX, WHERE XX IS A NUMBER. VALUE OF 3 IS A VARY-RESTART COMMAND. VALUE OF 1 IS A COLLECT-RSA REQUEST.
50	(32)	BITSTRING	1	RSAPFLGS	FLAGS USED IN RESPONSE TO A REQUEST FOR PERMISSION TO PERFORM AUTO RESTART.
		1...		RSAPFPRM	PERMISSION FLAG. IF 1, THE SYSTEM IS GRANTING PERMISSION TO THE SYSTEM REQUESTING PERMISSION
		.1..		RSAPFSYN	VALID SYSNAME FLAG. IF 1, RSAPSYNM HAS THE NAME OF A SYSTEM THAT WAS PREVIOUSLY GIVEN PERMISSION OR IS EXPECTED TO INITIATE AUTO RESTART
		..1.		RSAPFNPG	NULL PERMISSION GRANT FLAG. SET TO 1 BY A SYSTEM THAT CAN NEITHER GRANT NOR DENY PERMISSION TO INITIATE AUTO RESTART
		...1		RSAPQMT	Process Queues on all systems are verified as empty during the SaveQWBs command, Drain_QWBs phase. If any system has QWBs that it has not processed yet, it will turn off this flag and the phase will be repeated.
		 1111		*	RESERVED
51	(33)	BITSTRING	1	RSACRSOP	CRBRSOPT OF CRB
52	(34)	CHARACTER	8	RSACSYNM	CRBSYSNM OF CRB
52	(34)	CHARACTER	8	RSAPSYNM	NAME OF SYSTEM THAT RECEIVED PERMISSION TO DO AUTO RESTART OR IS EXPECTED TO INITIATE AUTO RESTART. VALID ONLY IF RSAPFSYN IS 1. SET ZERO IF AN ACTIVE SYSTEM IS KNOWN TO EXIST.
60	(3C)	SIGNED	4	RSAIMRSC	MAINRING-SEND-COUNT. VALUE OF RSVRSASC WHEN THIS RSAIRCD WAS SENT. VALID WHEN RSARCMDC HAS A VALUE OF RSARRSCV (=0100 HEX).
60	(3C)	SIGNED	4	RSAITOL	VALUE OF GVTOLINT USED IN SETTING RSVMRCYC. VALID WHEN FIELD RSARRSP1 HAS VALUE RSAR1ATP (=27) OR RSAR1ATU (=28).
60	(3C)	SIGNED	4	RSAIRCDD	COLLECT-RSA REQUEST DATA DISPLACEMENT. VALID WHEN FIELD RSACTYPE HAS VALUE RSAICV01 (=01) OR RSAIRCD IS A COLLECT-RSA RESPONSE (RSAIID=RSAC).
64	(40)	CHARACTER	0	RSAIEND	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	64	RSADINFO	INFORMATION THAT APPEARS IN THE RSADATA SECTION OF THE RSA DURING ADDSYS/SUBSYS/ETC IF THE PHASE-NUMBER IS RSAR1NSN OR RSAR1SMP OR RSAR1VTU.
0	(0)	CHARACTER	4	RSADVSTR	VALIDATION-STRING. APPEARS IF THE PHASE NUMBER IS RSAR1NSN AND THE ADDSYS/SUBSYS IS BEING EXECUTED BY A SYSTEM THAT GENERATES PHASE RSAR1SMP. ALWAYS SET TO ' RCD'. RSADVSTR WILL NOT BE EQUAL TO ' RCD' IF THE ADDSYS/SUBSYS IS BEING EXECUTED BY A SYSTEM THAT DOES NOT USE GETCCS/RELCCS.
0	(0)	UNSIGNED	1	RSADVETO	VETO-FLAG. APPEARS IF THE PHASE NUMBER IS RSAR1VTU. SET NON-ZERO IF SOME SYSTEM VETOES THE ADVANCE TO THE NEXT PHASE.
1	(1)	CHARACTER	3	RSADRSV1	BYTES SKIPPED TO ENSURE A WORD ALIGNMENT.
4	(4)	SIGNED	4	RSADCISC	COMMAND-INITIATE SEND-COUNT. APPEARS ONLY IF RSADVSTR IS VALID. THE VALUE OF RSARCSEQ THAT APPEARED IN THE RSA WHEN THE CURRENT COMMAND WAS INITIATED. USED ONLY FOR DEBUGGING AND TRACKING.
8	(8)	CHARACTER	8	RSADCISN	COMMAND-INITIATE SYSTEM-NAME. APPEARS ONLY IF RSADVSTR IS VALID. THE VALUE OF THE SYSNAME THAT WAS BROADCAST IN PHASE RSAR1MRB OF THE CURRENT COMMAND. USED ONLY FOR DEBUGGING AND TRACKING.
16	(10)	CHARACTER	8	RSADCCSN	SYSNAME OF THE SYSTEM THAT HOLDS THE CCS-RESOURCE. APPEARS ONLY IF RSADVSTR IS VALID.
24	(18)	SIGNED	4	RSADCCSI	ID OF THE COMMAND THAT HOLDS THE CCS-RESOURCE. APPEARS ONLY IF RSADVSTR IS VALID. TAKEN FROM THE RSVNMRCC VALUE THAT WAS GENERATED FOR THE GETCCS.
28	(1C)	UNSIGNED	1	RSADSYCT	COUNT OF REMOTE SYSTEMS IN THE MAINRING. USED TO DETERMINE HOW MANY SYSTEMS ADJUSTED RSADQWBC AND RSADBFSZ
29	(1D)	UNSIGNED	1	RSADQWBC	QWB COMPRESSION-LEVEL. USED TO DETERMINE WHAT COMPRESSION-LEVEL SHOULD BE USED BY SYSTEMS IN THE MAINRING
30	(1E)	SIGNED	4	RSADBFSZ	RSA BUFFER-SIZE. USED TO DETERMINE WHAT RSA-SIZE SHOULD BE USED BY SYSTEMS IN THE MAINRING

RSA Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
34	(22)	UNSIGNED	1	RSADTHRS	THRESHOLD VALUE TO BE PROPAGATED TO ALL SYSTEMS IN THE MAINRING.
35	(23)	UNSIGNED	1	RSADSYLC	COUNT OF SYSTEMS IN THE MAINRING, OR ZERO. THIS ALSO TELLS HOW MANY ENTRIES APPEAR IN STRUCTURE RSADSYSL.
36	(24)	CHARACTER	28	RSADRSV9	RESERVED. APPEARS ONLY IF RSADVSTR IS VALID. SET TO ZERO IN PHASE RSAR1NSN.
64	(40)	CHARACTER	0	RSADEND	END OF FIXED-LENGTH PART OF RSAD. STRUCTURE RSADSYSL BEGINS AT THIS ADDRESS IF RSADSYLC IS NON-ZERO.

RSA Constants

Len	Type	Value	Name	Description
		Comment		
		VALID VALUES OF RSACTBIX		
		End of Comment		
1	DECIMAL	0	RSACTBIZ	VALUE FOR RSACTBIX WHICH INDICATES RSAISNID DESCRIBES THE SYSTEM WHICH SENT THE RSAIRCD.
1	DECIMAL	254	RSACTBIC	VALUE FOR RSACTBIX WHICH INDICATES RSAIRCD IS A COLLECT-RSA-RESPONSE RSAIRCD.
1	DECIMAL	255	RSACTBIE	VALUE FOR RSACTBIX WHICH INDICATES END-OF-DATA CONTROL-INFORMATION RSAIRCD OR END-OF-DATA COLLECT-RSA-RESPONSE.
		Comment		
		VALID VALUES OF RSACTYPE		
		End of Comment		
1	DECIMAL	1	RSAICV01	VALUE FOR RSACTYPE WHICH INDICATES RSAIRCD IS A COLLECT-RSA REQUEST.
1	DECIMAL	3	RSAICV03	VALUE FOR RSACTYPE WHICH INDICATES RSAIRCD CONTAINS A VARY-RESTART OR JOIN COMMAND. NOTE THIS IS THE SAME VALUE AS CONSTANT CRBRSTRQ IN THE CRB MAPPING MACRO.
		Comment		
		VALID VALUES OF RSARRSP1 AND RSVCPHNO		
		End of Comment		
1	DECIMAL	1	RSAR1CNQ	COMMAND NOW QUEUED. COMMAND WAS RECEIVED AND QUEUED DUE TO THIS RSAIRCD
1	DECIMAL	2	RSAR1CPQ	COMMAND PREVIOUSLY QUEUED. COMMAND WAS RECEIVED IN A PREVIOUS RSAIRCD AND QUEUED, BUT HAS NOT YET BEEN EXECUTED
1	DECIMAL	3	RSAR1CPF	COMMAND PREVIOUSLY FAILED. COMMAND FAILED DURING EXECUTION, AND THIS SYSTEM IS NOW REPORTING THE FAILURE TO THE REMOTE SYSTEM
1	DECIMAL	4	RSAR1CNS	COMMAND NOT SUPPORTED. COMMAND HAS AN INVALID FORMAT
1	DECIMAL	5	RSAR1CNO	COMMAND ROUTER NOT OPERATIONAL. THIS SYSTEM WILL NOT ACCEPT COMMANDS.
1	DECIMAL	8	RSAR1MRB	MAINRING-BROADCAST PHASE. MAINRING RSA CONTAINS RSVENTY UPDATE INFORMATION
1	DECIMAL	9	RSAR1NCC	NEIGHBOR-CHECK-COMPLETION PHASE. EACH MAINRING SYSTEM MUST REPORT WHETHER IT COMPLETED PROCESSING REQUESTED IN PHASE RSAR1MRB. THIS PHASE MAY BE REPEATED.
1	DECIMAL	10	RSAR1PMC	PROPOSED-MAINRING CONFIGURATION PHASE. MAINRING RSA CONTAINS A PROPOSED MAINRING CONFIGURATION. EVERY MAINRING SYSTEM MUST REPORT WHETHER IT HAS LINKS TO ITS NEIGHBORS IN THE PROPOSED CONFIGURATION
1	DECIMAL	11	RSAR1MCC	MAINRING CONFIGURATION CHOSEN PHASE. MAINRING WILL HAVE THE CONFIGURATION DESCRIBED IN THE PREVIOUS RSAR1PMC RECORD. EACH MAINRING SYSTEM MUST SEND AN RSAIRCD TO A NEIGHBOR IN THE FUTURE MAINRING.

Len	Type	Value		Name	Description
1	DECIMAL		12	RSAR1MCQ	MAINRING CONFIGURATION QUERY PHASE. EACH MAINRING SYSTEM IN THE NEW MAINRING MUST REPORT WHETHER IT HAS DETERMINED WHAT RSL"S WILL BE USED IN THE FUTURE MAINRING. THIS PHASE MAY BE REPEATED.
1	DECIMAL		26	RSAR1TPI	TARGET PROCESSING INITIATION PHASE. THE TARGET OF ADDSYS WILL RECEIVE RESPONSIBILITY TO ADVANCE ADDSYS PHASE WHEN ITS NEXT RSAIRCD ARRIVES AT SYSTEM PERFORMING ADDSYS
1	DECIMAL		27	RSAR1ATP	ADDSYS TARGET PROCESSING PHASE. THE TARGET OF ADDSYS WILL ADVANCE ADDSYS TO THE NEXT PHASE AFTER IT HAS UPDATED ITS RSVENTY TABLES
1	DECIMAL		28	RSAR1ATU	ADDSYS TARGET TABLES UPDATED PHASE. THE TARGET OF ADDSYS HAS COPIED RSVENTY TABLE OF SYSTEM PERFORMING ADDSYS
1	DECIMAL		29	RSAR1CTB	CYCLE-TIME BROADCAST PHASE. THE MAINRING RSA CONTAINS MAINRING CYCLE-TIME INFORMATION. THIS PHASE MAY BE REPEATED.
1	DECIMAL		30	RSAR1NSO	SEND LAST RSA IN OLD MAINRING
1	DECIMAL		31	RSAR1NSN	SEND FIRST RSA IN NEW MAINRING
1	DECIMAL		32	RSAR1SMP	SET MAINRING PARAMETERS. THIS PHASE IS SKIPPED IF ADDSYS/SUBSYS/ETC IS DONE BY A SYSTEM THAT DOES NOT USE GETCCS/RELCCS.
1	DECIMAL		33	RSAR1VTU	VERIFY-TABLE-UPDATE PHASE. EVERY MAINRING SYSTEM WILL REPORT WHETHER IT HAS UPDATED ITS RSVENTY TABLE. THIS PHASE MAY BE REPEATED.
1	DECIMAL		35	RSAR1CTU	CANCEL-TABLE-UPDATE PHASE. SHOWS THAT ADDSYS OR SUBSYS CANNOT BE COMPLETED AND MUST BE BACKED OUT.
2	HEX	0100		RSARRSCV	VALUE FOR RSARCMDC IF THE RSAIRCD CONTAINS A VALID RSA SEND COUNT IN RSAIMRSC

RSA Cross Reference

Name	Hex Offset	Hex Value
RSA	0	
RSACAMRC	28	
RSACCMDC	6	
RSACDATA	32	
RSACREST	28	
RSACRSOP	33	
RSACSYNM	34	
RSACTBIX	B	
RSACTYPE	32	
RSADATA	18	
RSADBFSZ	1E	
RSADCCSI	18	
RSADCCSN	10	
RSADCISC	4	
RSADCISN	8	
RSADEND	40	
RSADINFO	0	
RSADQWBC	1D	
RSADRSV1	1	
RSADRSV9	24	
RSADSYCT	1C	
RSADSYLC	23	
RSADTHRS	22	
RSADVETO	0	
RSADVSTR	0	
RSAFBKQW	4	40
RSAFRQSR	4	20
RSAFURC	4	80
RSAICLD	A	
RSAICRDA	C	
RSAICSC	14	
RSAID	0	
RSAIEND	40	
RSAIFACK	4	04
RSAIFCPQ	4	80
RSAIFIDR	4	40
RSAIFLGS	4	
RSAIFLG2	A	
RSAIFNMD	A	80
RSAIFNXS	1B	20
RSAIFRQP	4	08
RSAIFRRF	4	10
RSAIFSRF	4	20
RSAIID	0	
RSAIMNRF	1A	
RSAIMRSC	3C	
RSAIMRTK	20	
RSAIMRTL	1C	
RSAIRCD	0	
RSAIRCDD	3C	
RSAISNID	C	
RSAISYID	19	
RSAISYNM	C	
RSAISYTK	14	
RSAITBID	C	
RSAITOL	3C	
RSAIUNRF	1B	40
RSAIUQDF	1B	
RSAIUUDF	1B	80
RSALNCA	E	
RSALNCQD	10	
RSAMRFLG	4	
RSAMRPFX	0	
RSAPFLGS	32	
RSAPFNPG	32	20
RSAPFPRM	32	80
RSAPFSYN	32	40
RSAPQMT	32	10
RSAPSYNM	34	
RSAQWBCT	C	
RSARCMDC	8	
RSARCP	5	
RSARCSEQ	8	
RSARCTOK	5	
RSARMRFT	2C	
RSARRSP1	5	
RSASYS	14	
RSASYSCP	13	
RSATRGCA	15	
RSATYPCA	12	

RSRRB Information

RSRRB Heading Information

Common Name: RSM Real Storage Reconfiguration Block
Macro ID: IHARSRRB
DSECT Name: RSRRB
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: None
Storage Attributes: Virtual Storage: Yes
Subpool: USER SPECIFIED.
Key: USER SPECIFIED.
Residency: USER SPECIFIED.
Size: 24 bytes
Created by: Vary Storage (CONFIG command)
Pointed to by: RSRRBPTR, RSRSTATP, RSRRRBNP
Serialization: Not applicable (only one exists at one time)
Function: Contains information about a storage reconfiguration request.

RSRRB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	60	RSRRB	REAL STORAGE RECONFIGURATION REQUEST BLOCK
0	(0)	CHARACTER	8	RSRSTR64	
					Comment
					Starting real address (in 64-bit mode) of storage to be processed
					End of Comment
0	(0)	UNSIGNED	4	*	Reserved
4	(4)	SIGNED	4	RSRSTART	Starting real address (in 31-bit mode) of storage to be processed
8	(8)	ADDRESS	4	RSRECBP	POINTER TO ECB FOR RSR TO POST WHEN A VARY OFFLINE IS COMPLETE. Valid only for first RSRRB of a queue
12	(C)	ADDRESS	4	RSRFLAGP	POINTER TO STATUS BYTES' STORAGE
16	(10)	SIGNED	4	RSRFRCNT	COUNT OF FRAMES TO BE PROCESSED
20	(14)	SIGNED	2	RSRSKIP	SKIP FACTOR, EG., 2 MEANS EVERY OTHER FRAME, 3 MEANS EVERY THIRD FRAME
22	(16)	SIGNED	2	RSRFUNC	FUNCTION INDICATOR - 1 = ONLINE, 2 = OFFLINE, 3 = STATUS, 4 = CANCEL, 5 = DO-IT 6 = STATUS AND PREPARE 7 = AISTAT 8 = Online/Offline Status Valid only for first RSRRB of a queue 9 = Build Quad AI 10 = Build Pageable Large AI
24	(18)	UNSIGNED	4	RSRFLAG	Flag bytes.
24	(18)	BITSTRING	1	RSRFLAGBYTE1	Flag byte 1
		1...		RSRRANGE	1 = Bring a range of Pftes on line 0 = Bring an AI worth of Pftes on line
		.1..		RSRRRBN	RSRRRBNP exists. Valid only for first RSRRB of a queue
		..1.		RSRFRB	First RSRRB in the queue
		...1		RSROFFLN	The address increment is already offline. Used when configuring storage offline
		.... 1...		RSRFREE	This RSRRB will be freed on a call to cleanup
		.... .1..		RSRRSU	This RSRRB contains reconfigurable storage
		.... ..1.		RSRPREFF	This RSRRB contains frames being used to back preferred data
		1		RSREXISTED	The RSRRB was already added to the RSRRB queue and does not have to be allocated and requeued on a subsequent call to status-and-prepare
25	(19)	BITSTRING	1	RSRFLAGBYTE2	Flag byte 2
		1...		RSRM8	Reconfig called from IAXM8
		.1..		RSRONLINESTATUS	1 - The status of all of the real addresses in the report range is online. 0 - The status of all the real addresses in the report range is offline Only valid for RSRFUNC_ONLINE_STATUS
		..11 1111		*	RESERVED
26	(1A)	CHARACTER	2	*	RESERVED
28	(1C)	ADDRESS	4	RSRRRBNP	RSRRB Next Pointer. 0 indicates "no next"
32	(20)	UNSIGNED	8	RSRFORWARDDELIMREALADDRESS	All real addresses from the input address up to and including the forward delimeter real address have the same online/offline status. Only valid for RSRFUNC_ONLINE_STATUS
40	(28)	UNSIGNED	8	RSRBACKWARDDELIMREALADDRESS	All real addresses from the input address down to and including the backward delimeter real address have the same online/offline status. Only valid for RSRFUNC_ONLINE_STATUS

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
48	(30)	CHARACTER	12	*	Reserved

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	2	RSRFLAGS	STATUS BYTES ARRAY
		1...		*	RESERVED
		.1..		RSRPFCAD	BACKS PFT CAD PAGES
		..1.		RSRPREF	PREFERRED FRAME
		...1		RSRVRCAN	V=R CANDIDATE - CAN GO OFFLINE
		.... 1...		RSRLNSWP	LONG-TERM NON-SWAPPABLE ADDRESS SPACE
		.... .1..		RSRNSWAS	NON-SWAPPABLE ADDRESS SPACE
		.... ..1.		RSRPSTER	PREVIOUS STORAGE ERROR IN FRAME
		1		RSRCHNGD	CHANGED FRAME
1	(1)	1...		RSROFLN	OFFLINE OR GOING OFFLINE
		.1..		RSRINTRC	INTERCEPTED BECAUSE OF A STORAGE ERROR MACHINE CHECK, A VARY OFFLINE, OR A REQUEST FOR V=R
		..1.		RSRSTERR	STORAGE ERROR IN FRAME
		...1		RSRPRMRS	PERMANENTLY RESIDENT - CAN'T GO OFFLINE
		.... 1...		RSRSQA	SQA FRAME
		.... .1..		RSRLSQA	LSQA FRAME - CAN GO OFFLINE
		.... ..1.		RSRFIXED	FIXED FRAME
		1		RSRVR	V=R IN USE FRAME - CAN'T GO OFFLINE

RSRRB Constants

Len	Type	Value	Name	Description
4	DECIMAL	1	RSRFUNC_ONLINE	
4	DECIMAL	2	RSRFUNC_OFFLINE	
4	DECIMAL	3	RSRFUNC_STATUS	
4	DECIMAL	4	RSRFUNC_CANCEL	
4	DECIMAL	5	RSRFUNC_OFFLINE_DO_IT	Go ahead and process the offline request
4	DECIMAL	6	RSRFUNC_STATUS_AND_PREPARE	Collect status and prepare for the offline request
4	DECIMAL	7	RSRFUNC_AI_STATUS	Get RSU status of the address increment
4	DECIMAL	8	RSRFUNC_ONLINE_STATUS	Report on the online/offline status of the real storage address.
4	DECIMAL	9	RSRFUNC_BUILDQUADAIS	Bring the rest of the quad area online at MSI
4	DECIMAL	10	RSRFUNC_BUILDPLAIS	Bring the rest of the Pageable Large frame area online at MSI

RSRRB Cross Reference

Name	Hex Offset	Hex Value
RSRBACKWARDDELIMREALADDRESS	28	
RSRCHNGD	0	01
RSRECBP	8	
RSREXISTED	18	01
RSRFIXED	1	02
RSRFLAG	18	
RSRFLAGBYTE1	18	
RSRFLAGBYTE2	19	
RSRFLAGP	C	
RSRFLAGS	0	
RSRFORWARDDELIMREALADDRESS	20	
RSRFRB	18	20
RSRFRCNT	10	
RSRFREE	18	08
RSRFUNC	16	
RSRINTRC	1	40
RSRLNSWP	0	08
RSRLSQA	1	04
RSRM8	19	80
RSRNSWAS	0	04
RSROFFLN	18	10
RSROFLN	1	80
RSRONLINESTATUS	19	40
RSRPFCAD	0	40
RSRPREF	0	20
RSRPREFF	18	02
RSRPRMRS	1	10
RSRPSTER	0	02
RSRRANGE	18	80
RSRRB	0	
RSRRRBN	18	40
RSRRRBNP	1C	
RSRRSU	18	04
RSRSKIP	14	
RSRSQA	1	08
RSRSTART	4	
RSRSTERR	1	20
RSRSTR64	0	
RSRVR	1	01
RSRVRCAN	0	10

RTCT Information

RTCT Heading Information

Common Name: Recovery/Termination Control Table
Macro ID: IHARTCT
DSECT Name: RTCT
Owning Component: SVC Dump (SCDMP)
Eye-Catcher ID: RTCT
Offset: 0
Length: 4
Storage Attributes: Main Storage: One per system
Subpool: 245
Key: 0
Residency: below 16M line
Size: 436 bytes
Created by: IEAVNPA6 at NIP time.
Pointed to by: The CVTRTMCT field of the CVT data area.
Serialization: Dump options: Compare & Swap - SVC dump fields: RTCTSDPL
Function: The RTCT provides a communication area between the various functions associated with dumping facilities, for SYSABEND, SYSMDUMP, SYSUDUMP, and SVC dumps. It is used for coordination of the dump related processes of task and system recovery, the memory termination controller, installation and operator defined dump requirements.

RTCT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RTCT	, - BAL MAPPING OF TABLE
0	(0)	X'80'	0	BIT0	"128"
0	(0)	X'40'	0	BIT1	"64"
0	(0)	X'20'	0	BIT2	"32"
0	(0)	X'10'	0	BIT3	"16"
0	(0)	X'8'	0	BIT4	"8"
0	(0)	X'4'	0	BIT5	"4"
0	(0)	X'2'	0	BIT6	"2"
0	(0)	X'1'	0	BIT7	"1"
0	(0)	CHARACTER	4	RTCTNAME	- CONTAINS C'RTCT' AS IDENTIFIER.
				Comment	
				SNAP/ABEND PARMLIB VALUES	
				End of Comment	
4	(4)	CHARACTER	12	RTCTPLIB (0)	
4	(4)	BITSTRING	4	RTCTSAP (0)	- **SYSABEND INITIAL PARMLIB VALUES**
4	(4)	BITSTRING	1	RTCTSAP1	- (BYTE 1 OF SDATA OPTIONS:)
		1...		RTCTSAB0	"BIT0" 1=DISPLAY NUCLEUS
		.1..		RTCTSAB1	"BIT1" 1=DISPLAY SQA
		..1.		RTCTSAB2	"BIT2" 1=DISPLAY LSQA
		...1		RTCTSAB3	"BIT3" 1=DISPLAY SWA
		.... 1...		RTCTSAB4	"BIT4" 1=DISPLAY GTF OR SUPERVISOR TRACE
		.... .1..		RTCTSAB5	"BIT5" 1=DISPLAY CONTROL BLOCKS FOR TASK
		.... ..1.		RTCTSAB6	"BIT6" 1=DISPLAY ENQUEUE CONTROL BLOCKS
		1		RTCTSAB7	"BIT7" 1=FORMAT DATA MGMT C.B.S
5	(5)	BITSTRING	1	RTCTSAP2	(BYTE 2 OF SDATA OPTIONS:)
		1...		RTCTSABG	"BIT0" 1=FORMAT IOS CONTROL BLOCKS
		.1..		RTCTSABH	"BIT1" 1=FORMAT ERROR CONTROL BLKS
		..1.		RTCTSABI	"BIT2" 1=FORMAT PCDATA INFORMATION
		...1		RTCTABSU	"BIT3" 1=SUMMARY DUMP REQUEST
		.... 1...		RTCTABAN	"BIT4" 1=DUMP ALL VIRTUAL NUCLEUS
		.... .1..		RTCTABNS	"BIT5" 1=NO SYMPTOM DUMP REQUESTED
				Comment	
				EQU BIT6 RESERVED EQU BIT7 RESERVED	
				End of Comment	
6	(6)	BITSTRING	1	RTCTSAP3	- (BYTE 1 OF PDATA OPTIONS:)

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1...		RTCTSAB8	"BIT0" 1=DISPLAY SAVE AREA TRACE(SA KEYWORD)
		.1..		RTCTSAB9	"BIT1" 0=DISPLAY ENTIRE SAVE AREA 1=DISPLAY SAVE AREA HEADINGS(SAH KWD)
		..1.		RTCTSABA	"BIT2" 1=DISPLAY REGISTERS
		...1		RTCTSABB	"BIT3" 1=DISPLAY LINK PACK AREA
		.... 1...		RTCTSABC	"BIT4" 1=DISPLAY JOB PACK AREA
		.... .1..		RTCTSABD	"BIT5" 1=DISPLAY PSW
		.... ..1.		RTCTSABE	"BIT6" 1=DISPLAY USER SUBPOOLS: 0-127
		1		RTCTABST	"BIT7" 1=DUMP ALL SUBTASKS
7	(7)	BITSTRING	1	RTCTSAP4	RESERVED
8	(8)	BITSTRING	4	RTCTSUP (0)	- **SYSUDUMP INITIAL PARMLIB VALUES**
8	(8)	BITSTRING	1	RTCTSUP1	- (BYTE 1 OF SDATA OPTIONS:)
		1...		RTCTSUD0	"BIT0" 1=DISPLAY NUCLEUS
		.1..		RTCTSUD1	"BIT1" 1=DISPLAY SQA
		..1.		RTCTSUD2	"BIT2" 1=DISPLAY LSQA
		...1		RTCTSUD3	"BIT3" 1=DISPLAY SWA
		.... 1...		RTCTSUD4	"BIT4" 1=DISPLAY GTF OR SUPERVISOR TRACE
		.... .1..		RTCTSUD5	"BIT5" 1=DISPLAY CNTRL BLKS FOR TASK
		.... ..1.		RTCTSUD6	"BIT6" 1=DISPLAY ENQUEUE CNTRL BLKS
		1		RTCTSUD7	"BIT7" 1=FORMAT DATA MGMT C.B.S
9	(9)	BITSTRING	1	RTCTSUP2	(BYTE 2 OF SDATA OPTIONS:)
		1...		RTCTSUDG	"BIT0" 1=FORMAT IOS CONTROL BLOCKS
		.1..		RTCTSUDH	"BIT1" 1=FORMAT ERROR CONTROL BLKS
		..1.		RTCTSUDI	"BIT2" 1=FORMAT PCDATA INFORMATION
		...1		RTCTSUSU	"BIT3" 1=SUMMARY DUMP REQUESTED
		.... 1...		RTCTSUAN	"BIT4" 1=DUMP ALL VIRTUAL NUCLEUS
		.... .1..		RTCTSUNS	"BIT5" 1=NO SYMPTOM DUMP REQUESTED

Comment

```
EQU BIT6 RESERVED
EQU BIT7 RESERVED
```

End of Comment

10	(A)	BITSTRING	1	RTCTSUP3	- (BYTE 1 OF PDATA OPTIONS:)
		1...		RTCTSUD8	"BIT0" 1=DISPLAY SAVE AREA TRACE(SA KEYWORD)
		.1..		RTCTSUD9	"BIT1" 0=DISPLAY ENTIRE SAVE AREA 1=DISPLAY SAVE AREA HEADINGS(SAH KWD)
		..1.		RTCTSUDA	"BIT2" 1=DISPLAY REGISTERS
		...1		RTCTSUDB	"BIT3" 1=DISPLAY LINK PACK AREA
		.... 1...		RTCTSUDC	"BIT4" 1=DISPLAY JOB PACK AREA
		.... .1..		RTCTSUDD	"BIT5" 1=DISPLAY PSW
		.... ..1.		RTCTSUDE	"BIT6" 1=DISPLAY USER SUBPOOLS: 0-127
		1		RTCTSUST	"BIT7" 1=DISPLAY SUBTASK DATA
11	(B)	BITSTRING	1	RTCTSUP4	RESERVED
12	(C)	BITSTRING	4	RTCTSYD (0)	**SYSMDUMP INITIAL PARMLIB VALUES**
12	(C)	BITSTRING	1	RTCTSY01	(BYTE 1 OF SDATA OPTIONS:)
		1...		RTCTSYM0	"BIT0" 1=DISPLAY NUCLEUS
		.1..		RTCTSYM1	"BIT1" 1=DISPLAY SQA
		..1.		RTCTSYM2	"BIT2" 1=DISPLAY LSQA
		...1		RTCTSYM3	"BIT3" 1=DISPLAY SWA
		.... 1...		RTCTSYM4	"BIT4" 1=DISPLAY GTF OR SUPV TRACE
		.... .1..		RTCTSYM5	"BIT5" 1=DISPLAY REGION
		.... ..1.		RTCTSYM6	"BIT6" 1=DISPLAY LPA FOR REGION
		1		RTCTSYM7	"BIT7" 1=DISPLAY CSA
13	(D)	BITSTRING	1	RTCTSY02	(BYTE 2 OF SDATA OPTIONS)
		1...		RTCTSYMS	"BIT0" 1=SUMMARY DUMP REQUEST
		.1..		RTCTSYMA	"BIT1" 1=DUMP ALL VIRTUAL NUCLEUS
		..1.		RTCTSYMN	"BIT2" 1=NO SYMPTOM DUMP

Comment

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EQU BIT3 RESERVED
EQU BIT4 RESERVED
EQU BIT5 RESERVED
EQU BIT6 RESERVED
EQU BIT7 RESERVED
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End of Comment

14	(E)	BITSTRING	1	RTCTSY03	RESERVED
15	(F)	BITSTRING	1	RTCTSY04	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
				Comment	
RTM INFORMATION					
				End of Comment	
16	(10)	SIGNED	2	RTCTCTSA	ASID where RtctCtsl was set
18	(12)	SIGNED	2	RTCTSDLA	ASID of last address space to be processed
20	(14)	BITSTRING	4	RTCTMECB	- ECB WAIT'ED ON BY MEMORY TERMINATION CONTROLLER
24	(18)	ADDRESS	4	RTCTFASB	- ADDRESS OF FIRST ASCB ON MEMORY TERMINATION QUEUE.
28	(1C)	SIGNED	4	RTCTNAS	NUM OF ADDR SPACE TO BE CAPTURED
32	(20)	ADDRESS	4	RTCTEEDA	GLOBAL ANCHOR FOR EED's
36	(24)	SIGNED	4	RTCTSDDS	ANCHOR OF THE SDDSQ QUEUE 0 - IF THE SDDSQ IS EMPTY
40	(28)	SIGNED	4	RTCTCOUN (0)	Used for Compare and Swap
40	(28)	SIGNED	2	RTCTSDDC	COUNT OF THE NUMBER OF ENTRIES IN THE SDDSQ.
42	(2A)	SIGNED	2	RTCTMTCT	Used to keep a count of the subtasks belonging to Memory Termination
44	(2C)	SIGNED	4	RTCTDSV	POINTER TO THE DUMPSRV ADDRESS SPACE CONTROL BLOCK.
48	(30)	SIGNED	4	RTCTSSTK	POINTER TO STACK ADDRESS TABLE
52	(34)	SIGNED	4	RTCTADGL	SNAP GLUE ROUTINE ADDRESS FOR QMNGRIO
56	(38)	SIGNED	4	RTCTADG1	SNAP GLUE ROUTINE ADDRESS FOR IEAVAD11
60	(3C)	SIGNED	4	RTCTADG2	SNAP GLUE ROUTINE ADDRESS FOR IEAVAD21
64	(40)	SIGNED	4	RTCTADG3	SNAP GLUE ROUTINE ADDRESS FOR IEAVAD81
68	(44)	SIGNED	4	RTCTADG4	SNAP GLUE ROUTINE ADDRESS FOR IEAVAD08
72	(48)	SIGNED	4	RTCTADG5	SNAP GLUE ROUTINE ADDRESS FOR IEAVAD12
76	(4C)	SIGNED	4	RTCTTABG	ABDUMP GLUE FOR EODAD ROUTINE
80	(50)	SIGNED	4	RTCTTABQ	ABDUMP GLUE FOR QMNGRIO
84	(54)	SIGNED	4	RTCTTABR	ABDUMP GLUE FOR READ/CHECK
88	(58)	SIGNED	4	RTCTDSCA	POINTER TO DAE COMMUNICATION AREA
92	(5C)	SIGNED	4	RTCTDIND	POINTER TO SDDIE CONTROL BLOCK FOR SYSTEM NON-DISPATCHABILITY
96	(60)	SIGNED	4	RTCTDIRS	POINTER TO SDDIE CONTROL BLOCK FOR THE REAL STORAGE BUFFER
100	(64)	SIGNED	4	RTCTSDAT	POINTER TO SVC DUMP DATA AREAS CONTROL BLOCK - SDDAT
104	(68)	SIGNED	4	RTCTSMOD	POINTER TO CONTROL BLOCK CONTAINING SVC DUMP MODULE ADDRESSES - SDMOD
108	(6C)	SIGNED	4	RTCTSCON	POINTER TO CONTROL BLOCK CONTAINING CONSTANTS USED IN SVC DUMP PROCESSING - SDCON
112	(70)	CHARACTER	4	RTCTCPID	CELL POOL ID FOR THE EEDS
116	(74)	ADDRESS	4	RTCTRPAR	ADDRESS OF RSM PAGE ANALYSIS ROUTINE (IARQD)
120	(78)	ADDRESS	4	RTCTBPXP	OpenMVS Dump Adjunct Address
124	(7C)	SIGNED	4	RTCTTABO	ABDUMP DCB OPEN EXIT
128	(80)	SIGNED	4	RTCTSDSU	Available enabled summary dump buffer size
132	(84)	SIGNED	4	RTCTMXSN	Maximum time interval that an SDUMP will keep a system non-dispatchable
136	(88)	SIGNED	4	RTCTTR2A	Address of IEAVTR2A
140	(8C)	SIGNED	4	RTCTTRSC	Address of IEAVTRSC
144	(90)	SIGNED	4	RTCTLUCT	Count to track number of processes interested in serializing-unseriali- zing sdump
148	(94)	CHARACTER	8	RTCTRSVS	Reserved
156	(9C)	ADDRESS	4	RTCTSDPL	ADDRESS OF SVC DUMP PARAMETER LIST
160	(A0)	ADDRESS	4	RTCTFMT	USED FOR TESTING RTM MODULES
164	(A4)	SIGNED	4	RTCTMLCK	LOCK FOR MEM TERM POST SRB
168	(A8)	SIGNED	4	RTCTMSRB	PTR TO MEM TERM POST SRB
172	(AC)	SIGNED	4	RTCTTEST	USED FOR TESTING RTM MODULES
176	(B0)	CHARACTER	4	RTCTSEQW (0)	FULLWORD FIELD CONTAINING THE SEQ # IN SECOND HALFWORD
176	(B0)	BITSTRING	2	RTCTRSV2	RESERVED
178	(B2)	SIGNED	2	RTCTSEQ#	ERRORID SEQUENCE NUMBER
180	(B4)	ADDRESS	4	RTCTSDSW	ADDRESS OF SUMMARY SVC DUMP (SUMDUMP) WORK AREA (IHASMWK)
184	(B8)	CHARACTER	36	RTCTTDCB	TAPE DCB FOR SVCDUMP
220	(DC)	ADDRESS	4	RTCTSDWK	ADDRESS OF SVC DUMP WORK AREA
224	(E0)	CHARACTER	10	RTCTERID (0)	ERRORID FOR THIS FAILURE'S SVC DUMP HEADER
224	(E0)	CHARACTER	2	RTCTESEQ	ERRORID SEQUENCE NUMBER
226	(E2)	CHARACTER	2	RTCTECPU	ERRORID LOGICAL CPU ID
228	(E4)	CHARACTER	2	RTCTEASD	ERRORID ASID
230	(E6)	CHARACTER	4	RTCTETIM	ERRORID TIMESTAMP
234	(EA)	CHARACTER	2	RTCTXXX2	RESERVED
				Comment	
DEFAULT DUMP OPTIONS, WHICH CAN BE CHANGED BY THE CHNGDUMP OPERATOR COMMAND					
				End of Comment	
236	(EC)	CHARACTER	16	RTCTOPT (0)	
236	(EC)	BITSTRING	4	RTCTSAO (0)	**SYSABEND EFFECTIVE OPTIONS**

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
236	(EC)	BITSTRING	2	RTCTSASD (0)	
236	(EC)	BITSTRING	1	RTCTSAO1	(BYTE 1 OF SDATA OPTIONS:)
		1...		RTCTSAD0	"BIT0" 1=DISPLAY NUCLEUS
		.1..		RTCTSAD1	"BIT1" 1=DISPLAY SQA
		..1.		RTCTSAD2	"BIT2" 1=DISPLAY LSQA
		...1		RTCTSAD3	"BIT3" 1=DISPLAY SWA
		.... 1...		RTCTSAD4	"BIT4" 1=DISPLAY GTF OR SUPERVISOR TRACE
		.... .1..		RTCTSAD5	"BIT5" 1=DISPLAY CONTROL BLOCKS FOR TASK
		.... ..1.		RTCTSAD6	"BIT6" 1=DISPLAY ENQUEUE CONTROL BLOCKS
		1		RTCTSAD7	"BIT7" 1=FORMAT DATA MGMT C.B.S
237	(ED)	BITSTRING	1	RTCTSAO2	(BYTE 2 OF SDATA OPTIONS:)
		1...		RTCTSADG	"BIT0" 1=FORMAT IOS CONTROL BLOCKS
		.1..		RTCTSADH	"BIT1" 1=FORMAT ERROR CONTROL BLKS
		..1.		RTCTSADI	"BIT2" 1=FORMAT PC INFORMATION
		...1		RTCTSADJ	"BIT3" 1=SUMMARY DUMP
		.... 1...		RTCTSADK	"BIT4" 1=DUMP ALL VIRTUAL NUCLEUS
		.... .1..		RTCTSADL	"BIT5" 1=NO SYMPTOM DUMP

Comment

BIT6 RESERVED
BIT7 RESERVED

End of Comment

238	(EE)	BITSTRING	2	RTCTSAPD (0)	
238	(EE)	BITSTRING	1	RTCTSAO3	(BYTE 1 OF PDATA OPTIONS:)
		1...		RTCTSAD8	"BIT0" 1=DISPLAY SAVE AREA TRACE(SA KEYWORD)
		.1..		RTCTSAD9	"BIT1" 0=DISPLAY ENTIRE SAVE AREA 1=DISPLAY SAVE AREA HEADINGS(SAH KWD)
		..1.		RTCTSADA	"BIT2" 1=DISPLAY REGISTERS
		...1		RTCTSADB	"BIT3" 1=DISPLAY LINK PACK AREA
		.... 1...		RTCTSADC	"BIT4" 1=DISPLAY JOB PACK AREA
		.... .1..		RTCTSADD	"BIT5" 1=DISPLAY PSW
		.... ..1.		RTCTSADE	"BIT6" 1=DISPLAY USER SUBPOOLS: 0-127
		1		RTCTSADF	"BIT7" 1=DISPLAY SUBTASKS
239	(EF)	BITSTRING	1	RTCTSAO4	(BYTE 1 OF OTHER OPTIONS:)
		.... ..1.		RTCTSAMG	"BIT6" SEE RTCTSAOV
		.... ..1.		RTCTSAOV	"BIT6" 1=OVER MODE 0=ADD MODE
		1		RTCTISAB	"BIT7" IGNORE REQUESTS FOR SYSABEND
240	(F0)	BITSTRING	4	RTCTSUO (0)	**SYSUDUMP EFFECTIVE OPTIONS**
240	(F0)	BITSTRING	2	RTCTSUSD (0)	
240	(F0)	BITSTRING	1	RTCTSUO1	(BYTE 1 OF SDATA OPTIONS:)
		1...		RTCTSYD0	"BIT0" 1=DISPLAY NUCLEUS
		.1..		RTCTSYD1	"BIT1" 1=DISPLAY SQA
		..1.		RTCTSYD2	"BIT2" 1=DISPLAY LSQA
		...1		RTCTSYD3	"BIT3" 1=DISPLAY SWA
		.... 1...		RTCTSYD4	"BIT4" 1=DISPLAY GTF OR SUPERVISOR TRACE
		.... .1..		RTCTSYD5	"BIT5" 1=DISPLAY CNTRL BLKS FOR TASK
		.... ..1.		RTCTSYD6	"BIT6" 1=DISPLAY ENQUEUE CNTRL BLKS
		1		RTCTSYD7	"BIT7" 1=FORMAT DATA MGMT C.B.S
241	(F1)	BITSTRING	1	RTCTSUO2	(BYTE 2 OF SDATA OPTIONS:)
		1...		RTCTSYDG	"BIT0" 1=FORMAT IOS CONTROL BLOCKS
		.1..		RTCTSYDH	"BIT1" 1=FORMAT ERROR CONTROL BLKS
		..1.		RTCTSYDI	"BIT2" 1=FORMAT PC INFORMATION
		...1		RTCTSYDJ	"BIT3" 1=SUMMARY DUMP
		.... 1...		RTCTSYDK	"BIT4" 1=DUMP ALL VIRTUAL NUCLEUS
		.... .1..		RTCTSYDL	"BIT5" 1=NO SYMPTOM DUMP

Comment

EQU BIT6 RESERVED
EQU BIT7 RESERVED

End of Comment

242	(F2)	BITSTRING	2	RTCTSUPD (0)	
242	(F2)	BITSTRING	1	RTCTSUO3	(BYTE 1 OF PDATA OPTIONS:)
		1...		RTCTSYD8	"BIT0" 1=DISPLAY SAVE AREA TRACE(SA KEYWORD)
		.1..		RTCTSYD9	"BIT1" 0=DISPLAY ENTIRE SAVE AREA 1=DISPLAY SAVE AREA HEADINGS(SAH KWD)
		..1.		RTCTSYDA	"BIT2" 1=DISPLAY REGISTERS
		...1		RTCTSYDB	"BIT3" 1=DISPLAY LINK PACK AREA
		.... 1...		RTCTSYDC	"BIT4" 1=DISPLAY JOB PACK AREA
		.... .1..		RTCTSYDD	"BIT5" 1=DISPLAY PSW

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.... ..1.		RTCTSYDE	"BIT6" 1=DISPLAY USER SUBPOOLS: 0-127
		1		RTCTSYDF	"BIT7" 1=DISPLAY SUBTASKS
243	(F3)	BITSTRING	1	RTCTSUO4	(BYTE 1 OF OTHER OPTIONS:)
		.... ..1.		RTCTSUMG	"BIT6" SEE RTCTSUOV
		.... ..1.		RTCTSUOV	"BIT6" 1=OVER MODE 0=ADD MODE
		1		RTCTISYU	"BIT7" IGNORE REQUESTS FOR SYSUDUMP
244	(F4)	BITSTRING	4	RTCTSYO (0)	**SYSMDUMP EFFECTIVE OPTIONS**
244	(F4)	BITSTRING	1	RTCTSD01	(BYTE 1 OF SDATA OPTIONS:)
		1...		RTCTSDS0	"BIT0" 1=DISPLAY NUCLEUS
		.1..		RTCTSDS1	"BIT1" 1=DISPLAY SQA
		..1.		RTCTSDS2	"BIT2" 1=DISPLAY LSQA
		...1		RTCTSDS3	"BIT3" 1=DISPLAY SWA
		.... 1...		RTCTSDS4	"BIT4" 1=DISPLAY GTF OR SPV.TRACE
		.... .1..		RTCTSDS5	"BIT5" 1=DISPLAY REGION
		.... ..1.		RTCTSDS6	"BIT6" 1=DISPLAY ACTIVE LPA FOR RGN
		1		RTCTSDS7	"BIT7" 1=DISPLAY CSA
245	(F5)	BITSTRING	1	RTCTSD02	DUMP FLAGS TWO
		1...		RTCTSDS8	"BIT0" 1=SUMMARY DUMP
		.1..		RTCTSDS9	"BIT1" 1=DUMP ALL VIRTUAL NUCLEUS
		..1.		RTCTSDSA	"BIT2" 1=NO SYMPTOM DUMP

Comment

```
EQU BIT3 RESERVED
EQU BIT4 RESERVED
EQU BIT5 RESERVED
EQU BIT6 RESERVED
EQU BIT7 RESERVED
```

End of Comment

246	(F6)	BITSTRING	1	RTCTSD03	RESERVED
247	(F7)	BITSTRING	1	RTCTSD04	(BYTE 1 OF OTHER OPTIONS:)
		.... ..1.		RTCTSMMG	"BIT6" SEE RTCTSMOV
		.... ..1.		RTCTSMOV	"BIT6" 1=OVER MODE 0=ADD MODE
		1		RTCTISYM	"BIT7" IGNORE REQUESTS FOR SYSMDUMP
248	(F8)	BITSTRING	4	RTCTSDO (0)	**SVC DUMP EFFECTIVE OPTIONS** **(CHANGEDUMP DEFAULTS)**
248	(F8)	BITSTRING	2	RTCTSDOD (0)	
248	(F8)	BITSTRING	1	RTCTSDO1	(BYTE 1 OF SDATA OPTIONS:)
		1...		RTCTSDP0	"BIT0" 1=DISPLAY ALL PSA'S IN SYSTEM
		.1..		RTCTSDP1	"BIT1" 1=DISPLAY CURRENT PSA
		..1.		RTCTSDP2	"BIT2" 1=DISPLAY NUCLEUS
		...1		RTCTSDP3	"BIT3" 1=DISPLAY SQA
		.... 1...		RTCTSDP4	"BIT4" 1=DISPLAY LSQA
		.... .1..		RTCTSDP5	"BIT5" 1=DISPLAY REGION (PRIVATE AREA)
		.... ..1.		RTCTSDP6	"BIT6" 1=DISPLAY ACTIVE LPA MODULES FOR RGN
		1		RTCTSDP7	"BIT7" 1=DISPLAY GTF OR SUPERVISOR TRACE
249	(F9)	BITSTRING	1	RTCTSDO2	-
		1...		RTCTSDP8	"BIT0" 1=DISPLAY CSA
		.1..		RTCTSDP9	"BIT1" 1=DISPLAY SWA
		..1.		RTCTSDPA	"BIT2" 1=DISPLAY SUMMARY SVC DUMP (SUMDUMP)
		...1		RTCTSDPB	"BIT3" 1=NO SUMMARY DUMP DISPLAY
		.... 1...		RTCTSDPC	"BIT4" 1=NO ALL PSA DISPLAY
		.... .1..		RTCTSDPD	"BIT5" 1=NO SQA DISPLAY
		.... ..1.		RTCTSDPE	"BIT6" 1=DUMP ALL NUCLEUS
		1		RTCTDEF	"BIT7" 1=DEFAULTS
250	(FA)	BITSTRING	1	RTCTSDO3	(BYTE 1 OF OTHER OPTIONS:)
		1...		RTCTSDPG	"BIT0" 1 MEANS QUIESCE=YES SPECIFIED ON CHNGDUMP COMMAND
		.1..		RTCTSDPH	"BIT1" 1 MEANS QUIESCE=NO SPECIFIED ON CHNGDUMP COMMAND

Comment

```
EQU BIT2 RESERVED
EQU BIT3 RESERVED
EQU BIT4 RESERVED
EQU BIT5 RESERVED
EQU BIT6 RESERVED
EQU BIT7 RESERVED
```

End of Comment

251	(FB)	BITSTRING	1	RTCTSDO4	(BYTE 2 OF OTHER OPTIONS:)
		.... ..1.		RTCTSDMG	"BIT6" SEE RTCTSDOV
		.... ..1.		RTCTSDOV	"BIT6" 1=OVER MODE 0=ADD MODE
		1		RTCTISVC	"BIT7" IGNORE REQUESTS FOR SVCDUMP

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					ADDITIONAL SVC DUMP INFORMATION AND FLAGS
					End of Comment
252	(FC)	BITSTRING	2	RTCTRSV1	**OLD RTCTASO FIELD - RESERVED
254	(FE)	BITSTRING	2	RTCTSDI (0)	**SVC DUMP INFORMATION**
254	(FE)	BITSTRING	1	RTCTSDNA	NUMBER ADDR SPACES TO DUMP
255	(FF)	BITSTRING	1	RTCTINDX	INDEX FOR ASID LIST ENTRY
256	(100)	BITSTRING	1	RTCTSDPR	PERMANENT RETURN CODE
257	(101)	CHARACTER	4	RTCTBUFV	CD SET,SDUMP,BUFFERS=nnnn value
261	(105)	BITSTRING	3	RTCTZZZ2	RESERVED
264	(108)	BITSTRING	2	RTCTSDF (0)	**SVC DUMP FLAGS**
264	(108)	BITSTRING	1	RTCTSDF1	(BYTE 1 OF FLAGS:)
		1...		RTCTSDNS	"BIT0" INDICATES ADDRESS SPACE HAS BEEN SET NON-SWAPPABLE
		.1..		RTCTSDND	"BIT1" SVC DUMP SET SYSTEM NON-DISP
		..1.		RTCTSDSH	"BIT2" SCHEDULE DUMP (IEAVTSDX) REQUEST
		...1		RTCTSDMA	"BIT3" MULTIPLE ADDR SPACE DUMP IN PROGRESS
		.... 1...		RTCTDSBK	"BIT4" DumpServ Broken
		.... .1..		RTCTSDSD	"BIT5" SUMMARY DUMP (IEAVTSSD) RECEIVED CONTROL
		.... ..1.		RTCTMTDP	"BIT6" Memterm dump: the single ASID that has ASSBMTCI set. Set only for scheduled dump.
		1		RTCTSDSC	"BIT7" SUMMARY DUMP (IEAVTSSD) COMPLETED PROCESSING
265	(109)	BITSTRING	1	RTCTSDF2	(BYTE 2 OF FLAGS:)
		1...		RTCTSDMR	"BIT0" DUMP MASTER ADDR SPACE REQD
		.1..		RTCTSYSF	"BIT1" Indicates SYSEVENT AVQDELTA has been issued by SRB IEAVTSD2.
		..1.		RTCTDSFX	"BIT2" Used by IEAVTSDS to save TCBFX
					Comment
					EQU BIT3 reserved
					End of Comment
		.... 1...		RTCTSDWF	"BIT4" SUMDUMP WRITER (IEAVTSDW) HAS COMPLETED
					Comment
					EQU BIT5 reserved
					End of Comment
		.... ..1.		RTCTSDRW	"BIT6" SUMDUMP RECORDS (FROM IEAVTSSD) TO WRITE
		1		RTCTSDFX	"BIT7" SVC DUMP SET TCBFX BIT. THIS PREVENTS ASYNCHRONOUS INTERRUPT EXITS FROM RECEIVING CONTROL UNDER THE CURRENT TASK. THESE EXITS RESULT FROM I/O ERRORS TO NON-DASD DEVICES OR FROM USER REQUESTED TIMER EXITS
					Comment
					NOTE THE FOLLOWING BITS SHOULD NOT BE REINITIALIZED BETWEEN SDUMPS NOTE
					End of Comment
266	(10A)	BITSTRING	2	RTCTZZZ3	ADD. SVC DUMP FLAGS
		1...		RTCTDFND	"BIT0" 1= Defer setting TCB nondispatchable
		.1..		RTCTSDNO	"BIT1" NO SYS1.DUMP DATASETS DEFINED
		..1.		RTCTBPXC	"BIT2" OpenMVS install checked
		...1		RTCTNNEW	"BIT3" Do not allow new SVC dumps
		.... 1...		RTCTZDPL	"BIT4" Zero RTCTSDPL to unserialize
		.... .1..		RTCTAXON	"BIT5" 1=Use Aux storage mgmt
		.... ..1.		RTCTNNW2	"BIT6" AUX shortage exists
		1		RTCTCTSL	"BIT7" Indicate IEAVTSDX will UnLOCK 8 bits - RESERVED
268	(10C)	CHARACTER	64	RTCTASTB (0)	SVC DUMP ASID TABLE
268	(10C)	CHARACTER	64	RTCTSDF3 (0)	**ARRAY OF INFO FOR SVC DUMP OF MULTIPLE ADDRESS SPACES**
268	(10C)	BITSTRING	2	RTCTSDAS	ASID OF THIS ADDRESS SPACE (A.S.)
270	(10E)	BITSTRING	1	RTCTSDF4	(BYTE 1 OF FLAGS:)
		1...		RTCTSDSS	"BIT0" DUMP SRB SCHEDULED
		.1..		RTCTSDNC	"BIT1" DUMP SRB RECEIVED CONTROL
		..1.		RTCTSDAN	"BIT2" ADDRESS SPACE SET NON-DISPATCHABLE
		...1		RTCTSDRM	"BIT3" DUMP TASK HAS BEEN RESUMED
		.... 1...		RTCTSDTR	"BIT4" DUMP TASK RUNNING
		.... .1..		RTCTSDEQ	"BIT5" DUMP TASK ENQUEUED ON DUMP RESOURCE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.... ..1.		RTCTSDEN	"BIT6" SVC DUMP (IEAVAD00 OR IEAVTSDT) IS PROCESSING THIS A.S.
		1		RTCTSDDO	"BIT7" DUMP ATTEMPTED FOR THIS ASID
271	(10F)	BITSTRING	1	RTCTSDF5	0 = NO LISTA SPECIFIED FOR THIS ASID
		1...		RTCTLSTA	"BIT0" LISTA STORAGE RANGES REQUESTED FOR THIS ADDRESS SPACE
		.1..		RTCTOLST	"BIT1" LISTA ONLY SPECIFIED OPTION FOR THIS ADDRESS SPACE
		..1.		RTCTTERM	"BIT2" IEAVTSRB should terminate
		...1		RTCTOCDS	"BIT3" THIS ASID IS ONLY THE OWNING ASID OF A SCOPE(ALL) DATA SPACE
		.... 1...		RTCTOSTP	"BIT4" This ASID is only stopped and should not be dumped
		.... .1..		RTCTCS	"BIT5" IEAVTSDT is in CS logic to decrement field RTCTNAS
		.... ..1.		RTCTDONE	"BIT6" IEAVTSDT capture complete
271	(10F)	X'4'	0	RTCTSDEL	"*-RTCTSDF3" LENGTH OF ELEMENT OF ADDR SPACE ARRAY
272	(110)	CHARACTER	60		REMAINING 15 ASID ENTRIES
				Comment	
RTM INFORMATION					
				End of Comment	
332	(14C)	ADDRESS	4	RTCTMRMQ	ADDRESS OF QUEUE OF STORAGE AREAS (USED FOR SYSMDUMPS) TO BE FREED AT MEMTERM
336	(150)	ADDRESS	4	RTCTSTE	ADDRESS OF QUEUE OF SLIP TSO ELEMENTS (STE)
340	(154)	SIGNED	4	RTCTEEDC	RTM COUNTER CONTAINS THE NUMBER OF TIMES EEDS WERE NOT OBTAINED, INCREMENTED BY 1 FOR EACH OCCURENCE EEDS NOT OBTAINED
344	(158)	BITSTRING	4	RTCTSD1 (0)	SDUMP TYPE AND EXIT DEFAULT OPTIONS
344	(158)	BITSTRING	2	RTCTSDTY (0)	SDUMP TYPE FLAGS
344	(158)	BITSTRING	1	RTCTTYP1	SDUMP TYPE FLAG 1
		1...		RTCTXMEM	"BIT0" 1=TYPE=XMEM REQUESTED
		.1..		RTCTXMEE	"BIT1" 1=TYPE=XMEME REQUESTED
		..1.		RTCTNOLC	"BIT2" 1=TYPE=NOLOCAL REQUESTED
345	(159)	BITSTRING	1		RESERVED
346	(15A)	BITSTRING	2	RTCTSDEX (0)	SDUMP EXIT FLAGS
346	(15A)	BITSTRING	1	RTCTEX1	SDUMP EXIT FLAG 1
		1...		RTCTGRSQ	"BIT0" 1=GRSQ EXIT
		.1..		RTCTMSTR	"BIT1" 1=MASTER TRACE & GTF GLOBAL EXIT
		..1.		RTCTSMSX	"BIT2" 1=SMSX LOCAL EXIT
		...1		RTCTCPL	"BIT3" 1=COUPLE EXIT
		.... 1...		RTCTXES	"BIT4" 1=XES LOCAL AND GLOBAL EXITS
		.... .1..		RTCTIOS	"BIT5" 1=IOS GLOBAL EXIT
		.... ..1.		RTCTWLM	"BIT6" 1=WLM EXIT
		1		RTCTRSM	"BIT7" 1=RSM EXIT
347	(15B)	BITSTRING	1	RTCTEX2	SDUMP EXIT FLAG 2
		1...		RTCTSLIP	"BIT0" 1=SLIP EXIT
		.1..		RTCTOPEN	"BIT1" 1=OPEN EDITION EXIT
		..1.		RTCTSVCD	"BIT2" 1=TAILORED SVC DUMP EXIT
		...1		RTCTRTM	"BIT3" 1=RTM Exit
348	(15C)	BITSTRING	4	RTCTSM10 (0)	SYSMDUMP TYPE AND EXIT FLAGS
348	(15C)	BITSTRING	2	RTCTSMTY	SYSMDUMP TYPE OPTIONS
350	(15E)	BITSTRING	2	RTCTSMEX (0)	SYSMDUMP EXIT OPTIONS
350	(15E)	BITSTRING	1	RTCTSMX1	1ST SYSMDUMP EXIT BYTE
		1...		RTCTMGRS	"BIT0" 1=GRSQ OPTION SPECIFIED
351	(15F)	BITSTRING	1	RTCTSMX2	2ND EXIT BYTE
352	(160)	SIGNED	4	RTCTZZZ8	RESV FOR SYSUDUMP TYPE/EXIT
356	(164)	SIGNED	4	RTCTZZZ7	RESV FOR SYSABEND TYPE/EXIT
360	(168)	BITSTRING	4	RTCTSMD2 (0)	SYSMDUMP PARMLIB DEFAULTS FOR TYPE AND EXIT OPTIONS
360	(168)	BITSTRING	2	RTCTMTYP	SYSMDUMP TYPE DEFAULTS
362	(16A)	BITSTRING	2	RTCTMEXT (0)	SYSMDUMP EXIT DEFAULTS
362	(16A)	BITSTRING	1	RTCTMEX1	FIRST EXIT BYTE
		1...		RTCTMXGR	"BIT0" 1=GRSQ OPTION
363	(16B)	BITSTRING	1	RTCTMEX2	2ND SYSMDUMP EXIT BYTE
364	(16C)	SIGNED	4	RTCTRTSD	POINTER TO RTCT SDUMP EXTENSION
368	(170)	SIGNED	4	RTCTSMEW	POINTER TO SUMMARY DUMP EXTENDED WORKAREA IN THE DUMPSRV ADDRESS SPACE
372	(174)	SIGNED	4	RTCTASCB	ADDRESS OF DUMPSRV ASCB
376	(178)	SIGNED	4	RTCTSTIE	Parallel Detach init ECB
380	(17C)	SIGNED	4	RTCTDMP#	SDUMP sequence number
384	(180)	ADDRESS	4	RTCTXBT	For use by IPCS
388	(184)	CHARACTER	16	RTCTPTRD (0)	Data for permanent task restart
388	(184)	BITSTRING	4	RTCTPTRF	Task restart flags
392	(188)	ADDRESS	4	RTCTMTIE	Memterm task initialization ECB
396	(18C)	ADDRESS	4	RTCTDTIE	Dump task initialization ECB
400	(190)	ADDRESS	4	RTCTRTIE	Record task initialization ECB
404	(194)	ADDRESS	4	RTCTQDDS	POINTER TO IARQDSPD

RTCT Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
408	(198)	ADDRESS	4	RTCTDPLF	POINTER TO FRONT OF DPL Q
412	(19C)	ADDRESS	4	RTCTDPLB	POINTER TO BACK OF DPL Q
416	(1A0)	SIGNED	4	RTCTCIDI	DUMP ID COUNTER
420	(1A4)	SIGNED	4	RTCTMMTI	MAX MESSAGE WAIT TIME
424	(1A8)	DBL WORD	8	RTCTCNT (0)	MAXSPACE COUNT
424	(1A8)	SIGNED	4	RTCTMCNT	THE AMOUNT OF DATA SPACE STORAGE IN MEG WHICH SDUMP HAS CAPTURED DATA IN
428	(1AC)	SIGNED	4	RTCTPCNT	THE REMAINING AMOUNT OF DATA SPACE STORAGE IN PAGES WHICH SDUMP IS USING THAT DOES NOT ADD UP TO A MEG
432	(1B0)	SIGNED	4	RTCTMXSP	SDUMPS MAXSPACE amount. This amount can be exceeded by a small amount, but once SDUMP realizes that it has been exceeded the capturing of the current SDUMP will be stopped and the user will be given a partial dump.
436	(1B4)	CHARACTER	16	RTCTDRSN (0)	Mask used to determine which SDRSN bits will not be used to mark an SVC dump as partial. The bit is OFF in this mask to consider a dump complete The bits are set in IEAVNPA6. See IHASDRSN for bit description
436	(1B4)	SIGNED	4	RTCTCDMP	Corresponds to SDRSCDMP
440	(1B8)	SIGNED	4	RTCTVCD1	Corresponds to SDRSVCD1
444	(1BC)	SIGNED	4	RTCTVCD2	Corresponds to SDRSVCD2
448	(1C0)	SIGNED	4	RTCTVCD3	Corresponds to SDRSVCD3
448	(1C0)	X'1C4'	0	RTCTLEN	"*-RTCT" TOTAL LENGTH OF RTM CONTROL TABLE.

RTCT Cross Reference

Name	Hex Offset	Hex Value
BIT0	0	80
BIT1	0	40
BIT2	0	20
BIT3	0	10
BIT4	0	8
BIT5	0	4
BIT6	0	2
BIT7	0	1
RTCT	0	
RTCTABAN	5	8
RTCTABNS	5	4
RTCTABST	6	1
RTCTABSU	5	10
RTCTADGL	34	
RTCTADG1	38	
RTCTADG2	3C	
RTCTADG3	40	
RTCTADG4	44	
RTCTADG5	48	
RTCTASCB	174	
RTCTASTB	10C	
RTCTAXON	10A	4
RTCTBPXC	10A	20
RTCTBPXP	78	
RTCTBUFV	101	
RTCTCDMP	1B4	
RTCTCIDI	1A0	
RTCTCNT	1A8	
RTCTCOUN	28	
RTCTCPID	70	
RTCTCPL	15A	10
RTCTCS	10F	4
RTCTCTSA	10	
RTCTCTSL	10A	1
RTCTDEF	F9	1
RTCTDFND	10A	80
RTCTDIND	5C	
RTCTDIRS	60	
RTCTDMP#	17C	
RTCTDONE	10F	2
RTCTDPLB	19C	
RTCTDPLF	198	
RTCTDRSN	1B4	
RTCTDSBK	108	8
RTCTDSCA	58	
RTCTDSFX	109	20
RTCTDSV	2C	
RTCTDTIE	18C	
RTCTEASD	E4	
RTCTECPU	E2	
RTCTEEDA	20	
RTCTEEDC	154	
RTCTERID	E0	
RTCTESEQ	E0	
RTCTETIM	E6	
RTCTEX1	15A	
RTCTEX2	15B	
RTCTFASB	18	
RTCTFMT	A0	
RTCTGRSQ	15A	80
RTCTINDX	FF	
RTCTIOS	15A	4
RTCTISAB	EF	1
RTCTISVC	FB	1
RTCTISYM	F7	1
RTCTISYU	F3	1
RTCTLEN	1C0	1C4
RTCTLSTA	10F	80
RTCTLUCT	90	
RTCTMCNT	1A8	
RTCTMECB	14	
RTCTMEXT	16A	
RTCTMEX1	16A	
RTCTMEX2	16B	
RTCTMGRS	15E	80
RTCTMLCK	A4	
RTCTMMTI	1A4	
RTCTMRMQ	14C	
RTCTMSRB	A8	
RTCTMSTR	15A	40
RTCTMTCT	2A	
RTCTMTDP	108	2
RTCTMTIE	188	
RTCTMTYP	168	
RTCTMXGR	16A	80
RTCTMXSN	84	
RTCTMXSP	1B0	
RTCTNAME	0	
RTCTNAS	1C	
RTCTNNEW	10A	10
RTCTNNW2	10A	2
RTCTNOLC	158	20
RTCTOCDS	10F	10
RTCTOLST	10F	40
RTCTOPEN	15B	40
RTCTOPT	EC	

Name	Hex Offset	Hex Value
RTCTOSTP	10F	8
RTCTPCNT	1AC	
RTCTPLIB	4	
RTCTPTRD	184	
RTCTPTRF	184	
RTCTQDDS	194	
RTCTRPAR	74	
RTCTRSM	15A	1
RTCTRSVS	94	
RTCTRSV1	FC	
RTCTRSV2	B0	
RTCTRTIE	190	
RTCTRTM	15B	10
RTCTRTSD	16C	
RTCTSABA	6	20
RTCTSABB	6	10
RTCTSABC	6	8
RTCTSABD	6	4
RTCTSABE	6	2
RTCTSABG	5	80
RTCTSABH	5	40
RTCTSABI	5	20
RTCTSAB0	4	80
RTCTSAB1	4	40
RTCTSAB2	4	20
RTCTSAB3	4	10
RTCTSAB4	4	8
RTCTSAB5	4	4
RTCTSAB6	4	2
RTCTSAB7	4	1
RTCTSAB8	6	80
RTCTSAB9	6	40
RTCTSADA	EE	20
RTCTSADB	EE	10
RTCTSADC	EE	8
RTCTSADD	EE	4
RTCTSADE	EE	2
RTCTSADF	EE	1
RTCTSADG	ED	80
RTCTSADH	ED	40
RTCTSADI	ED	20
RTCTSADJ	ED	10
RTCTSADK	ED	8
RTCTSADL	ED	4
RTCTSAD0	EC	80
RTCTSAD1	EC	40
RTCTSAD2	EC	20
RTCTSAD3	EC	10
RTCTSAD4	EC	8
RTCTSAD5	EC	4
RTCTSAD6	EC	2
RTCTSAD7	EC	1
RTCTSAD8	EE	80
RTCTSAD9	EE	40
RTCTSAMG	EF	2
RTCTSAO	EC	
RTCTSAOV	EF	2
RTCTSAO1	EC	
RTCTSAO2	ED	
RTCTSAO3	EE	
RTCTSAO4	EF	
RTCTSAP	4	
RTCTSAPD	EE	
RTCTSAP1	4	
RTCTSAP2	5	
RTCTSAP3	6	
RTCTSAP4	7	
RTCTSASD	EC	
RTCTSCON	6C	
RTCTSDAN	10E	20
RTCTSDAS	10C	
RTCTSDAT	64	
RTCTSDDC	28	
RTCTSDDO	10E	1
RTCTSDDS	24	
RTCTSDEL	10F	4
RTCTSDEN	10E	2
RTCTSDEQ	10E	4
RTCTSDEX	15A	
RTCTSDF	108	
RTCTSDFX	109	1
RTCTSDF1	108	
RTCTSDF2	109	
RTCTSDF3	10C	
RTCTSDF4	10E	
RTCTSDF5	10F	
RTCTSDI	FE	
RTCTSDLA	12	
RTCTSDMA	108	10
RTCTSDMG	FB	2
RTCTSDMR	109	80
RTCTSDNA	FE	
RTCTSDNC	10E	40
RTCTSDND	108	40
RTCTSDNO	10A	40
RTCTSDNS	108	80
RTCTSDO	F8	
RTCTSDOD	F8	
RTCTSDOV	FB	2
RTCTSDO1	F8	
RTCTSDO2	F9	
RTCTSDO3	FA	
RTCTSDO4	FB	
RTCTSDPA	F9	20
RTCTSDPB	F9	10
RTCTSDPC	F9	8
RTCTSDPD	F9	4
RTCTSDPE	F9	2
RTCTSDPG	FA	80
RTCTSDPH	FA	40
RTCTSDPL	9C	
RTCTSDPR	100	
RTCTSDP0	F8	80
RTCTSDP1	F8	40
RTCTSDP2	F8	20
RTCTSDP3	F8	10
RTCTSDP4	F8	8
RTCTSDP5	F8	4
RTCTSDP6	F8	2
RTCTSDP7	F8	1
RTCTSDP8	F9	80
RTCTSDP9	F9	40
RTCTSDRM	10E	10
RTCTSDRW	109	2
RTCTSDSA	F5	20
RTCTSDSC	108	1
RTCTSDSD	108	4
RTCTSDSH	108	20
RTCTSDSS	10E	80
RTCTSDSU	80	
RTCTSDSW	B4	
RTCTSDS0	F4	80
RTCTSDS1	F4	40
RTCTSDS2	F4	20
RTCTSDS3	F4	10
RTCTSDS4	F4	8
RTCTSDS5	F4	4
RTCTSDS6	F4	2
RTCTSDS7	F4	1
RTCTSDS8	F5	80
RTCTSDS9	F5	40
RTCTSDTR	10E	8
RTCTSDTY	158	
RTCTSDWF	109	8
RTCTSDWK	DC	
RTCTSD01	F4	
RTCTSD02	F5	
RTCTSD03	F6	

RTCT Cross Reference

Name	Hex Offset	Hex Value
RTCTSD04	F7	
RTCTSD1	158	
RTCTSEQ#	B2	
RTCTSEQW	B0	
RTCTSLIP	15B	80
RTCTSMD2	168	
RTCTSMEW	170	
RTCTSMEX	15E	
RTCTSMMG	F7	2
RTCTSMOD	68	
RTCTSMOV	F7	2
RTCTSMSX	15A	20
RTCTSMTY	15C	
RTCTSMX1	15E	
RTCTSMX2	15F	
RTCTSM10	15C	
RTCTSSTK	30	
RTCTSTE	150	
RTCTSTIE	178	
RTCTSUAN	9	8
RTCTSUDA	A	20
RTCTSUDB	A	10
RTCTSUDC	A	8
RTCTSUDD	A	4
RTCTSUDE	A	2
RTCTSUDG	9	80
RTCTSUDH	9	40
RTCTSUDI	9	20
RTCTSUD0	8	80
RTCTSUD1	8	40
RTCTSUD2	8	20
RTCTSUD3	8	10
RTCTSUD4	8	8
RTCTSUD5	8	4
RTCTSUD6	8	2
RTCTSUD7	8	1
RTCTSUD8	A	80
RTCTSUD9	A	40
RTCTSUMG	F3	2
RTCTSUNS	9	4
RTCTSUO	F0	
RTCTSUOV	F3	2
RTCTSUO1	F0	
RTCTSUO2	F1	
RTCTSUO3	F2	
RTCTSUO4	F3	
RTCTSUP	8	
RTCTSUPD	F2	
RTCTSUP1	8	
RTCTSUP2	9	
RTCTSUP3	A	
RTCTSUP4	B	
RTCTSUSD	F0	
RTCTSUST	A	1
RTCTSUSU	9	10
RTCTSVCD	15B	20
RTCTSYD	C	
RTCTSYDA	F2	20
RTCTSYDB	F2	10
RTCTSYDC	F2	8
RTCTSYDD	F2	4
RTCTSYDE	F2	2
RTCTSYDF	F2	1
RTCTSYDG	F1	80
RTCTSYDH	F1	40
RTCTSYDI	F1	20
RTCTSYDJ	F1	10
RTCTSYDK	F1	8
RTCTSYDL	F1	4
RTCTSYD0	F0	80
RTCTSYD1	F0	40
RTCTSYD2	F0	20
RTCTSYD3	F0	10
RTCTSYD4	F0	8
RTCTSYD5	F0	4
RTCTSYD6	F0	2
RTCTSYD7	F0	1
RTCTSYD8	F2	80
RTCTSYD9	F2	40
RTCTSYMA	D	40
RTCTSYMN	D	20
RTCTSYMS	D	80
RTCTSYM0	C	80
RTCTSYM1	C	40
RTCTSYM2	C	20
RTCTSYM3	C	10
RTCTSYM4	C	8
RTCTSYM5	C	4
RTCTSYM6	C	2
RTCTSYM7	C	1
RTCTSYO	F4	
RTCTSYSF	109	40
RTCTSY01	C	
RTCTSY02	D	
RTCTSY03	E	
RTCTSY04	F	
RTCTTABG	4C	
RTCTTABO	7C	
RTCTTABQ	50	
RTCTTABR	54	
RTCTTDCB	B8	
RTCTTERM	10F	20
RTCTTEST	AC	
RTCTTRSC	8C	
RTCTTR2A	88	
RTCTTYP1	158	
RTCTVCD1	1B8	
RTCTVCD2	1BC	
RTCTVCD3	1C0	
RTCTWLM	15A	2
RTCTXBT	180	
RTCTXES	15A	8
RTCTXMEE	158	40
RTCTXMEM	158	80
RTCTXXX2	EA	
RTCTZDPL	10A	8
RTCTZZZ2	105	
RTCTZZZ3	10A	
RTCTZZZ7	164	
RTCTZZZ8	160	

RTM2WA Information

RTM2WA Heading Information

Common Name: RTM2 WORK AREA
Macro ID: IHARTM2A
DSECT Name: RTM2WA
Owning Component: RECOVERY TERMINATION MANAGER (SCRTM)
Eye-Catcher ID: RTM2
Offset: 0
Length: 4
Storage Attributes: Subpool: 255
Key: 0
Residency: Above the 16M line
Size: 1792 bytes
Created by: IEAVTRT2, IEAVEMIN, IEAVNPA6
Pointed to by: TCBRTWA FIELD OF THE TCB DATA AREA
ESART2WA FIELD OF THE RTM2ESA DATA AREA IN THE ABEND SVRB
RTM2PREV FIELD OF THE RTM2WA (FOR PREVIOUS ONE, IF ANY)
RTM2ANCH FIELD OF THE RTM2WA (FIRST RTM2WA ACQUIRED)
ASSBRTMA (this is a preallocated RTM2WA and is never freed)
Serialization: NONE
Function: IHARTM2A MAPS THE RTM2 WORK AREA. THIS WORK AREA IS USED BY RTM2 TO MAINTAIN THE ADDRESSES OF CONTROL BLOCKS USED THROUGHOUT ITS PROCESSING, TO MAINTAIN THE ERROR DATA DESCRIBING THE REASONS FOR ITS CALL AND AS A WORKAREA AND COMMUNICATIONS AREA FOR ITS PROCESSING.

RTM2WA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	1936	RTM2WA	MAPPNG OF WORK AREA
0	(0)	CHARACTER	12	RTM2DESC	RTM2 SELF DESCRIPTION
0	(0)	CHARACTER	4	RTM2ID	CONTAINS 'RTM2' AS ID
4	(4)	ADDRESS	4	RTM2ADDR	CONTAINS ADDR OF THIS RTM2WA
8	(8)	CHARACTER	4	RTM2RT2D	DESCRIPTION OF RTM2WA
8	(8)	UNSIGNED	1	RTM2SPID	CONTAINS SPID OF THIS RTM2WA
9	(9)	UNSIGNED	3	RTM2LGTH	CONTAINS LENGTH OF THIS RTM2WA
12	(C)	ADDRESS	4	RTM2CVT	CONTAINS ADDRESS OF THE CVT
16	(10)	ADDRESS	4	RTM2TCBC	ADDRESS OF THE CURRENT TCB
20	(14)	ADDRESS	4	RTM2VRBC	ADDRESS OF THE CURRENT SVRB
24	(18)	ADDRESS	4	RTM2ASC	ADDRESS OF CURRENT ASCB
28	(1C)	CHARACTER	4	RTM2CODE	CONTAINS COMPLETION CODE,FLAGS
28	(1C)	BITSTRING	1	RTM2CCF	FLAGS
		1...		RTM2DREQ	DUMP REQUESTED
		.1..		RTM2STEP	STEP REQUESTED
		..1.		RTM2R0DP	REG 0 CONTAINS PARAMETERS
		...1		RTM2EOM	MEMORY TERMINATION REQUESTED
		.... 1...		RTM2EOT	TASK TERMINATION REQUESTED
		.... .1..		RTM2REAF	REASON CODE SPECIFIED
		11		*	NOT USED
29	(1D)	CHARACTER	3	RTM2CC	COMPLETION CODE
32	(20)	CHARACTER	16	RTM2SFWA	WORK AREA FOR COMPILER TEMPS
48	(30)	ADDRESS	4	RTM2TCBT	ADDRESS OF TOP TCB IN THE FAILING TREE
52	(34)	ADDRESS	4	RTM2VRBT	RTM2 SVRB QUEUED FROM TOP TCB IN FAILING TREE
56	(38)	ADDRESS	4	RTM2CT	ADDRESS OF RTCT
60	(3C)	CHARACTER	126	RTM2PGCY	THE FOLLOWING FIELDS ARE COPIED INTO THE RTM2WA WHEN RTM2 IS ENTERED FOR PURGE ONLY
60	(3C)	CHARACTER	126	RTM2TRRY	THE FOLLOWING ARE TASK RECOVERY FIELDS
60	(3C)	CHARACTER	80	RTM2EEDR	THE FOLLOWING CONTAINS ERROR REGISTERS AND PSW
60	(3C)	CHARACTER	64	RTM2EREG	GENERAL PURPOSE REGISTERS AT TIME OF ERROR
60	(3C)	ADDRESS	4	RTM2ER0	REGISTER 0
64	(40)	ADDRESS	4	RTM2ER1	REGISTER 1
68	(44)	ADDRESS	4	RTM2ER2	REGISTER 2
72	(48)	ADDRESS	4	RTM2ER3	REGISTER 3
76	(4C)	ADDRESS	4	RTM2ER4	REGISTER 4
80	(50)	ADDRESS	4	RTM2ER5	REGISTER 5
84	(54)	ADDRESS	4	RTM2ER6	REGISTER 6
88	(58)	ADDRESS	4	RTM2ER7	REGISTER 7
92	(5C)	ADDRESS	4	RTM2ER8	REGISTER 8
96	(60)	ADDRESS	4	RTM2ER9	REGISTER 9
100	(64)	ADDRESS	4	RTM2ER10	REGISTER 10

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
104	(68)	ADDRESS	4	RTM2ER11	REGISTER 11
108	(6C)	ADDRESS	4	RTM2ER12	REGISTER 12
112	(70)	ADDRESS	4	RTM2ER13	REGISTER 13
116	(74)	ADDRESS	4	RTM2ER14	REGISTER 14
120	(78)	ADDRESS	4	RTM2ER15	REGISTER 15
124	(7C)	CHARACTER	16	RTM2APSW	EXTENDED CONTROL PSW AT TIME OF ERROR
124	(7C)	CHARACTER	8	RTM2EPSW	EXTENDED CONTROL PSW AT TIME OF ERROR - FIRST DBL WORD
124	(7C)	CHARACTER	4	RTM2PSW1	EXTENDED CONTROL PSW AT TIME OF ERROR - FIRST WORD
124	(7C)	BITSTRING	1	RTM2EMK1	INTERRUPT INFORMATION MASKS
		1...		*	NOT USED
		.1..		RTM2PER1	PROGRAM EVENT RECORDING
		..11		*	NOT USED
		.... 1...		RTM2EAM1	EXTENDED ADDRESSING MODE
		.... 1...		RTM2XAM	EXTENDED ADDRESSING MODE
		.... .1..		RTM2TRM1	ADDRESS TRANSLATION ACTIVE
		.... ..1.		RTM2AIO1	OFF, I/O INTERRUPTION CANNOT OCCUR ON, I/O INTERRUPTIONS CAN OCCUR SUBJECT TO EXTERNAL SUBCLASS MASK BITS OF CONTROL REG 0
		1		RTM2EXT1	OFF, EXTERNAL INTERRUPTIONS CANNOT OCCUR ON, EXTERNAL INTERRUPTIONS CAN OCCUR SUBJECT TO EXTERNAL SUBCLASS MASK BITS OF CONTROL REG 0
125	(7D)	BITSTRING	1	RTM2MWP1	PSW KEY AND 'M-W-P'
		1111		RTM2KEY1	PSW KEY
		.... 1...		RTM2ECT1	EXTENDED CONTROL MODE
		.... .1..		RTM2MCK1	OFF, MACHINE CHECK CANNOT OCCUR ON, MACHINE CHECK DUE TO SYSTEM DAMAGE AND INSTRUCTION PROCESSING DAMAGE CAN OCCUR OTHER MACHINE CHECKS SUBJECT TO MASK BITS IN CONTROL REG 14
		.... ..1.		RTM2WAT1	ON, CPU IN WAIT STATE
		1		RTM2PGM1	ON, PROBLEM STATE OFF, SUPERVISOR STATE
126	(7E)	BITSTRING	1	RTM2INT1	CONDITION CODE AND PROGRAM MASK
		11..		RTM2ASCM	ADDRESS SPACE CONTROL MODE BITS 00 - PRIMARY MODE 01 - AR MODE 10 - SECONDARY MODE 11 - HOME SPACE MODE
		1...		RTM2S1	FIRST ASC MODE BIT
		.1..		RTM2S2	SECOND ASC MODE BIT
		..11		RTM2CC1	CONDITION CODE
		.... 1...		RTM2FPO1	FIXED POINT OVERFLOW
		.... .1..		RTM2DEC1	DECIMAL OVERFLOW
		.... ..1.		RTM2EXP1	EXPONENT OVERFLOW
		1		RTM2SGN1	SIGNIFICANCE
127	(7F)	BITSTRING	1	*	RESERVED
		1111 111.		*	
		1		RTM2MOD64	
128	(80)	ADDRESS	4	RTM2NXT1	ADDRESS OF NEXT INSTRUCTION
128	(80)	CHARACTER	1	*	RESERVED
		1...		RTM2MOD1	=0 NEXT INSTRUCTION TO BE EXECUTED IN 24-BIT MODE. =1 NEXT INSTRUCTION TO BE EXECUTED IN 31-BIT MODE.
129	(81)	ADDRESS	3	RTM2ADD1	INSTRUCTION ADDRESS
132	(84)	CHARACTER	8	RTM2AEC1	ADDITIONAL EC MODE INFORMATION
132	(84)	CHARACTER	1	*	RESERVED
133	(85)	BITSTRING	1	RTM2ILC1	INSTRUCTION LENGTH CODE
		1111 1...		*	RESERVED
		.... .11.		RTM2IL1	ILC
		1		*	RESERVED
134	(86)	UNSIGNED	2	RTM2INC1	INTERRUPT CODE
134	(86)	CHARACTER	1	RTM2ICD0	
		1111 11..		*	
		.... ..1.		RTM2TXPROG	Program check within transactional execution
135	(87)	ADDRESS	1	RTM2ICD1	8 BIT INTERRUPT CODE
		1...		RTM2IPR1	PER INTERRUPT OCCURRED
		.1..		RTM2IMC1	MONITOR CALL INTERRUPT
		..11 1111		RTM2IPC1	AN UNSOLICITED PROGRAM CHECK HAS OCCURRED
136	(88)	ADDRESS	4	RTM2TRAN	TRANSLATION EXCEPTION ADDRESS
136	(88)	CHARACTER	3	*	First 3 bytes
139	(8B)	UNSIGNED	1	RTM2DXC	Data exception code
140	(8C)	CHARACTER	8	RTM2ABNM	NAME OF ABENDING PROGRAM
148	(94)	ADDRESS	4	RTM2ABEP	ENTRY POINT ADDRESS OF ABENDING PROGRAM
152	(98)	CHARACTER	28	RTM2EEDH	THE FOLLOWING FIELDS CONTAIN DATA CONCERNING MACHINE CHECKS
152	(98)	CHARACTER	8	RTM2STCK	BEGINNING AND ENDING STORAGE CHECK ADDRESSES
152	(98)	ADDRESS	4	RTM2SCKB	BEGINNING STORAGE CHECK ADDR
156	(9C)	ADDRESS	4	RTM2SCKE	ENDING STORAGE CHECK ADDR
160	(A0)	CHARACTER	2	RTM2MCHI	ADDITIONAL MCH INFORMATION FLAGS
160	(A0)	BITSTRING	1	RTM2MCHS	MCH FLAG BYTE

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1...		RTM2SRVL	ON STORAGE ADDRESS SUPPLIED (RTM2STCK, RTM2RFSA) ARE VALID.
		.1..		RTM2RCDF	ON, MACHINE CHECK RECORD NOT RECORDED
		..1.		RTM2TSVL	ON, TIME STAMP VALID
		...1		RTM2INVP	ON, STORAGE IS RECONFIGURED, PAGE IS INVALIDATED.
		.... 1...		RTM2RSRC	ON,STORAGE RECONFIGURATION STATUS AVAILABLE (RTM2RSR1,RTM2RSR2)
		.... .1..		RTM2RSRF	ON, STORAGE RECONFIGURATION NOT ATTEMPTED (RTM2RSR1, RTM2RSR2 ARE INVALID)
		.... ..1.		RTM2VRIV	ON, VECTOR REGISTERS ARE UNPREDICTABLE
		1		*	RESERVED
161	(A1)	BITSTRING	1	RTM2MCHD	ADDITIONAL INFORMATION IF ERROR WAS MACHINE CHECK
		1...		RTM2SKYF	ON, STORAGE KEY FAILURE
		.1..		RTM2REGU	ON, REGISTERS AT TIME OF ERROR MAY BE INVALID
		..1.		RTM2PSWU	ON, PSW AT TIME OF ERROR MAY BE INVALID
		...1		RTM2SCK	ON, STORAGE CHECK
		.... 1...		RTM2ACR	ON, ACR
		.... .1..		RTM2INSF	ON, INSTRUCTION FAILURE
		.... ..1.		RTM2SOFT	ON,SOFT ERROR
		1		RTM2TERR	ON,TIMER ERROR
162	(A2)	CHARACTER	2	RTM2CPID	ID OF FAILING CPU CAUSING ACR
164	(A4)	CHARACTER	1	RTM2RSR1	ADDITIONAL STORAGE FRAME ERROR INDICATORS AS RETURNED FROM REAL STORAGE RECONFIGURATION
		1111 11..		*	RESERVED
		.... ..1.		RTM2MSER	STORAGE ERROR ALREADY SET IN FRAME
		1		RTM2CHNG	CHANGE INDICATOR WAS ON IN FRAME
165	(A5)	CHARACTER	1	RTM2RSR2	ADDITIONAL STORAGE ERROR INDICATORS.
		1...		RTM2OFLN	FRAME OFFLINE OR SCHEDULED TO GO OFFLINE IF RTM2INTC IS ON
		.1..		RTM2INTC	INTERCEPT THE FRAME IS SCHEDULED TO GO OFFLINE OR THE FRAME HAS INCURRED A STORAGE ERROR OR IS V=R
		..1.		RTM2SPER	STORAGE ERROR PERMANENT ON FRAME
		...1		RTM2NUCL	FRAME CONTAINS PERMANENT RESIDENT STORAGE, I.E., NUCLEUS
		.... 1...		RTM2FSQA	FRAME IN SQA
		.... .1..		RTM2FLSQ	FRAME IN LSQA
		.... ..1.		RTM2PGFX	FRAME IS PAGE FIXED
		1		RTM2VEQR	FRAME IS VIRTUAL=REAL OR SCHEDULED FOR VIRTUAL= REAL IF RTM2INTC IS ON
166	(A6)	CHARACTER	2	*	RESERVED
168	(A8)	ADDRESS	4	RTM2RFSA	REAL STORAGE FAILING ADDRESS. (VALID ONLY IF INDICATED BY RTM2SRVL)
172	(AC)	CHARACTER	8	RTM2TIME	TIME STAMP OF ASSOCIATED MACHINE CHECK
180	(B4)	CHARACTER	4	RTM2FLGS	INPUT FLAGS DESCRIBING REASONS AND CONDITIONS FOR ENTERING RTM2
180	(B4)	BITSTRING	1	RTM2ERRA	ERROR TYPE CAUSING ENTRY TO RTM2
		1...		RTM2MCHK	ON, MACHINE CHECK
		.1..		RTM2PCHK	ON, PROGRAM CHECK
		..1.		RTM2RKEY	ON, CONSOLE RESTART KEY WAS DEPRESSED
		...1		RTM2SVCD	ON, TASK ISSUED SVC 13
		.... 1...		RTM2ABTM	ON, ENTRY VIA ABTERM
		.... .1..		RTM2SVCE	ON,INDICATES AN SVC WAS ISSUED BY A LOCKED OR SRB ROUTINE.
		.... ..1.		RTM2TEXC	ON,INDICATES AN UNRECOVERABLE TRANSLATION FAILURE
		1		RTM2STRM	ON,INDICATES AN STERM ERROR
181	(B5)	CHARACTER	1	RTM2ERRB	ADDITIONAL ERROR ENTRY INFORMATION
		1...		RTM2PDIP	ON INDICATES THAT THIS TASK WAS PARALLEL DETACHED
		.1..		RTM2NMFS	Not My Fault Summary (see SDWANMFS for details)
		..1.		RTM2SRBT	On, abend was an SRBTERM
		...1		*	In RTM1, this bit is SDWASRBS and it is used to indicate that the SDWA was allocated for an SRB. In RTM2 this bit is not used and IEAVTAS1 always sets SDWASRBS off
		.... 1...		RTM2TYP1	ON, TYPE 1 SVC IN CONTROL AT TIME OF ERROR
		.... .1..		RTM2ENRB	ON, ENABLED RB IN CONTROL AT TIME OF ERROR
		.... ..1.		RTM2LDIS	ON, A LOGICALLY OR PHYSICALLY DISABLED ROUTINE (OTHER THAN A TYPE 1 SVC) WAS IN CONTROL AT TIME OF ERROR
		1		RTM2SRBM	ON, SYSTEM IN SRB MODE AT TIME OF ERROR
182	(B6)	CHARACTER	1	RTM2ERRC	ADDITIONAL ERROR ENTRY INFORMATION
		1...		RTM2STAF	ON, A PREVIOUS (E)STAE EXIT FAILED
		.1..		RTM2STAI	ON, A (E)STAI EXIT PREVIOUSLY RECEIVED CONTROL
		..1.		RTM2IRB	ON, AN IRB PRECEDED THE RB THAT IS ASSOCIATED WITH THIS EXIT
		...1		RTM2PERC	ON, THIS RECOVERY ROUTINE IS BEING PERCOLATED TO
		.... 1...		RTM2EAS	ON, A LOWER LEVEL EXIT HAS RECOGNIZED AN ERROR AND PROVIDED SERVICEABILITY INFO.
		.... .111		*	RESERVED
183	(B7)	CHARACTER	1	RTM2ERRD	ADDITIONAL ERROR ENTRY INFORMATION

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1...		RTM2CLUP	ON, INDICATES RECOVERY ROUTINE ONLY TO CLEAN UP AND NOT RETRY (IF 33E COMPLETION CODE THE DUMP IS TAKEN AFTER ENTRY TO THE RECOVERY ROUTINE, IF THE COMPLETION CODE IS OTHER THAN 33E, THE DUMP IS TAKEN BEFORE ENTRY TO THE RECOVERY ROUTINE)
		.1..		RTM2NRBE	ON, RB ASSOCIATED WITH THIS ESTA EXIT WAS NOT IN CONTROL AT TIME OF ERROR
		..1.		RTM2STAE	ON, THIS ESTA EXIT HAS BEEN ENTERED FOR A PREVIOUS ABEND.
		...1		RTM2CTS	ON, THIS TASK WAS NOT IN CONTROL AT TIME OF ERROR BUT A TASK WITHIN THE SAME JOBSTEP TREE REQUESTED A 'STEP' ABEND. ONLY ON IF RTM2CLUP IS ON.
		.... 1...		RTM2MABD	ON, THIS TASK WAS NOT IN CONTROL AT TIME OF ERROR BUT AN ANCESTOR OF THIS TASK HAS ABENDED. ONLY ON IF RTM2CLUP IS ON.
		.... .1..		RTM2RPIV	ON, THE REGISTERS AND PSW AT TIME OF ERROR ARE UNAVAILABLE
		.... ..1.		RTM2MCIV	ON, MACHINE CHECK ERROR INFORMATION IS UNAVAILABLE
		1		RTM2ERFL	ON, ERRORID INFORMATION AVAILABLE
184	(B8)	CHARACTER	2	RTM2FMID	ASID OF MEMORY IN WHICH ERROR OCCURRED. EQUAL TO ZERO IF CURRENT MEMORY FAILED. NOT EQUAL TO ZERO IF CROSS MEMORY ABTERM.
186	(BA)	CHARACTER	522	RTM2CVER	THE FOLLOWING FIELDS ARE ZEROED IN THE RTM2WA WHEN RTM2 IS ENTERED FOR CONVERT TO STEP
186	(BA)	CHARACTER	50	RTM2TRRC	TASK RECOVERY FIELDS CONTINUED
186	(BA)	BITSTRING	1	RTM2IOFS	CURRENT I/O STATUS
		1...		RTM2IOQR	ON, I/O FOR TASK HAS BEEN QUIESCED AND IS RESTORABLE
		.1..		RTM2IOHT	ON, I/O FOR FAILING TASK HAS BEEN HALTED AND IS NOT RESTORABLE
		..1.		RTM2NOIO	ON, FAILING TASK HAS NO OUTSTANDING I/O
		...1		RTM2NIOP	ON, TASK REQUESTED NO I/O PROCESSING
		 1111		*	RESERVED
187	(BB)	BITSTRING	1	RTM2SDWK	USER SDWA STORAGE PROTECTION KEY
188	(BC)	ADDRESS	4	RTM2PRB	SDWA_SYNCH PRB address
192	(C0)	ADDRESS	4	RTM2RBST	STOPPER RB USED BY TASK RECOVERY WHEN CHECKING FOR AN INTERVENING IRB
196	(C4)	ADDRESS	4	RTM2LSRT	LINKAGE STACK RESUME TOKEN
200	(C8)	CHARACTER	12	RTM2SCBS	BEGINNING, ENDING,AND CURRENT SCB ADDRESSES TO BE ENTERED
200	(C8)	ADDRESS	4	RTM2SCBC	ADDRESS OF CURRENT SCB
204	(CC)	ADDRESS	4	RTM2SCBN	ADDRESS OF NEWEST SCB
208	(D0)	ADDRESS	4	RTM2SCBO	ADDRESS OF OLDEST SCB
212	(D4)	CHARACTER	1	RTM2FLAG	DYNAMIC RESOURCE MANAGER FLAGS
		1...		RTM2GLBA	GLOBAL ADDRESS SPACE QUEUE PROCESSING
		.1..		RTM2GLBT	GLOBAL TASK RELATED QUEUE PROCESSING
		..1.		RTM2SPEA	SPECIFIC ADDRESS SPACE QUEUE PROCESSING
		...1		RTM2SPET	SPECIFIC TASK RELATED QUEUE PROCESSING
		.... 1...		RTM2LOCT	LOCAL TASK RELATED QUEUE PROCESSING
		.... .111		*	RESERVED
213	(D5)	BITSTRING	1	RTM2FLG2	DYNAMIC RESOURCE MANAGER FLAGS
		1...		RTM2ERME	ROUTING CONTROL TO RME
		.1..		RTM2TR2D	ROUTING CONTROL TO IEAVTR2D
214	(D6)	CHARACTER	1	RTM2RCT2	FLAGS USED TO MANAGE RECOVERY PROCESSING. SEE ALSO RCTL
		1...		RTM2IRBP	ON, AN IRB PRECEEDED THE RB CURRENTLY BEING PROCESSED
		.1..		RTM2FOUN	ON, RB OWNER FOUND FOR ARR
		..1.		RTM2IENV	ON, ARR COULD NOT BE ROUTED TO BECAUSE OF IMPROPER ENVIRONMENT. THIS MEANS THAT A STACKING PC THAT HAS AN ARR WAS ENTERED WITH AN FRR ALREADY ESTABLISHED.
		...1		RTM2PCAX	ON, PCAX INDICATED THAT THE INPUT PC NUMBER/ASID PAIR WAS NOT VALID
215	(D7)	CHARACTER	1	*	RESERVED
216	(D8)	ADDRESS	4	RTM2RBPR	PREVIOUS RB
220	(DC)	ADDRESS	4	RTM2COMP	USED TO SAVE SDWACOMP DURING PERCOLATION
224	(E0)	ADDRESS	4	RTM2RTYA	RETRY ADDRESS RETURNED FROM A RECOVERY EXIT
224	(E0)	BITSTRING	3	*	
227	(E3)	1		RTM2RA64	When on, retry is AMODE 64. This bit must cause PSW.4 to be set.
228	(E4)	ADDRESS	4	RTM2RYRB	ADDRESS OF THE RB AT WHICH THE RETRY WILL OCCUR
232	(E8)	CHARACTER	4	RTM2PARQ	USED TO SAVE RECOVERY ROUTINE FLAGS DURING PERCOLATION
232	(E8)	CHARACTER	1	RTM2RCDE	RETURN CODE FROM RECOVERY ROUTINE TO INDICATE RETRY OR TERMINATION: 0, CONTINUE WITH TERMINATION - IMPLIES PERCOLATION 4, RETRY 8, RETRY (ONLY VALID FROM STAE) 12, RETRY (ONLY VALID FROM STAE/STAI) 16, PREVENT FURTHER STAI/ESTAI PROCESSING AND CONTINUE WITH TERMINATION.
233	(E9)	CHARACTER	3	*	RESERVED
236	(EC)	CHARACTER	8	RTM2CTL1	BC MODE PSW AT TIME OF ERROR
236	(EC)	CHARACTER	1	RTM2CMKA	CHANNEL INTERRUPTS MASKS.
		1111 111.		RTM2IOA	I/O INTERRUPTS (ALL ZEROES OR ALL ONES.
		1		RTM2EXTA	EXTERNAL INTERRUPT.
237	(ED)	CHARACTER	1	RTM2MWPA	PSW KEY AND 'M-W-P'.

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1111		RTM2KEYA	PSW KEY.
		.... 1...		*	RESERVED
		.... .1..		RTM2MCKA	MACHINE CHECK INTERRUPT
		.... ..1.		RTM2WATA	WAIT STATE
		1		RTM2SPVA	SUPERVISOR/PROBLEM-PROGRAM MODE
238	(EE)	CHARACTER	2	RTM2INTA	INTERRUPT CODE (LAST 2 BYTES OF INTERRUPT CODE IF I/O INTERRUPT
240	(F0)	CHARACTER	1	RTM2PMKA	INSTRUCTION LENGTH CODE, CONDITION CODE, AND PROGRAM MASKS.
		11..		RTM2ILA	INSTRUCTION LENGTH CODE
		..11		RTM2CCA	LAST CONDITION CODE
		.... 1...		RTM2FPA	FIXED-POINT OVERFLOW
		.... .1..		RTM2DOA	DECIMAL OVERFLOW
		.... ..1.		RTM2EUA	EXPONENT OVERFLOW
		1		RTM2SGA	SIGNIFICANCE
241	(F1)	ADDRESS	3	RTM2NXTA	ADDRESS OF NEXT INSTRUCTION TO BE EXECUTED
244	(F4)	CHARACTER	8	RTM2CTL2	BC MODE PSW FROM LAST PRB ON RB CHAIN
244	(F4)	CHARACTER	1	RTM2CMKP	CHANNEL INTERRUPTS MASKS.
		1111 111.		RTM2IOP	I/O INTERRUPTS (ALL ZEROES OR ALL ONES.
		1		RTM2EXTP	EXTERNAL INTERRUPT.
245	(F5)	CHARACTER	1	RTM2MWPP	PSW KEY AND 'M-W-P'.
		1111		RTM2KEYP	PSW KEY
		.... 1...		*	RESERVED
		.... .1..		RTM2MCKP	MACHINE CHECK INTERRUPT
		.... ..1.		RTM2WATP	WAIT STATE
		1		RTM2SPVP	SUPERVISOR/PROBLEM PROGRAM MODE
246	(F6)	CHARACTER	2	RTM2INTP	INTERRUPT CODE (LAST 2 BYTES OF INTERRUPT CODE IF I/O INTERRUPT
248	(F8)	CHARACTER	1	RTM2PMKP	INSTRUCTION LENGTH CODE, CONDITION CODE, AND PROGRAM MASKS
		11..		RTM2ILP	INSTRUCTION LENGTH CODE
		..11		RTM2CCP	LAST CONDITION CODE
		.... 1...		RTM2FPP	FIXED - POINT OVERFLOW
		.... .1..		RTM2DOP	DECIMAL OVERFLOW
		.... ..1.		RTM2EUP	EXPONENT UNDERFLOW
		1		RTM2SGP	SIGNIFICANCE
249	(F9)	ADDRESS	3	RTM2NXTP	ADDRESS OF NEXT INSTRUCTION TO BE EXECUTED
252	(FC)	CHARACTER	72	RTM2SNAP	THE FOLLOWING FIELDS ARE INVOLVED WITH DUMP PROCESSING
252	(FC)	ADDRESS	4	RTM2DPLA	ADDRESS OF THE DUMP PARAMETER LIST
256	(100)	CHARACTER	28	RTM2SPRM	SNAP PARM LIST
256	(100)	CHARACTER	16	RTM2SNPL	SNAP PARMS
272	(110)	ADDRESS	4	RTM2SPSL	ADDRESS OF STORAGE LISTS (RTM2DPSL)
276	(114)	ADDRESS	4	RTM2HLST	ADDRESS OF HEADER LIST
280	(118)	ADDRESS	4	RTM2SPSP	ADDRESS OF SUBPOOL LIST (RTM2SPLE)
284	(11C)	CHARACTER	24	RTM2SNPX	SNAPX PARAMETERS
284	(11C)	CHARACTER	16	RTM2SALE	ALETS ASSOCIATED WITH SNAP PARAMETER LIST
300	(12C)	CHARACTER	8	RTM2STKN	SNAPX TOKENS
300	(12C)	ADDRESS	4	RTM2DSPP	ADDRESS OF DATA SPACE RANGE LIST
304	(130)	SIGNED	4	RTM2DSAL	ALET FOR DATA SPACE RANGE LIST
308	(134)	CHARACTER	8	RTM2DD	DDNAME FOR DUMP DATA SET
316	(13C)	SIGNED	4	RTM2SNCC	RETURN CODE FROM SNAP/ABDUMP 0, SUCCESSFUL COMPLETION 4, INVALID DCB OR UPR ON DCB 8, INVALID TCB, UPR ON TCB, OR INSUFFICIENT STORAGE 12, INVALID DCB TYPE
320	(140)	ADDRESS	4	RTM2DTCB	ADDR OF TOP TCB IN TREE TO BE DUMPED
324	(144)	CHARACTER	32	RTM2SECB	ADDRESSES OF ECB LIST AND ECBS USED IN STACKING
324	(144)	ADDRESS	4	RTM2ECBA	ADDRESS OF ECBS
				(4294967300:562187416)	
		1...		RTM2LECB	ON, LAST ECB USED
340	(154)	SIGNED	4	RTM2ECBS	ECBS
				(4294967300:562167656)	
356	(164)	CHARACTER	8	*	
364	(16C)	ADDRESS	4	RTM2PREV	ADDRESS OF PREVIOUS RTM2WA ACQUIRED FOR THIS TASK
368	(170)	ADDRESS	4	RTM2PRWA	ADDRESS OF PREVIOUS RTM2WA PERTINENT TO THIS RECURSION
372	(174)	CHARACTER	72	RTM2SFRG	SUBFUNCTION REGISTER SAVE AREA
372	(174)	ADDRESS	4	RTM2SFSA	SUBFUNCTION REGISTER SAVE AREA
				(4294967314:562167656)	
444	(1BC)	BITSTRING	1	RTM2PKEY	HOLDS CALLER'S PROTECT KEY FOR MODSET
445	(1BD)	CHARACTER	7	RTM2SCTL	FLAGS USED TO MANAGE PATHS WITHIN RTM2
445	(1BD)	BITSTRING	2	RTM2CCTL	FLAGS USED TO MANAGE CONTROLLER PATHS
		1...		RTM2STPT	ON, SCOPE OF ABEND IS STEP
		.1..		RTM2CNCL	ON, ENTRY IS FOR A 'CANCEL'
		..1.		RTM2DETF	ON, ENTRY IS FOR A DETACH X'13E' OR X'33E' ABEND
		...1		RTM2ISPC	ON, INITIAL SUBTASK PROCESSING HAS BEEN DONE.
		.... 1...		RTM2REED	SET ON WHEN RTM2 FINDS A REGISTER TYPE EED ON THE QUEUE
		.... .1..		RTM2HEED	SET ON WHEN RTM2 FINDS A HARDWARE EED
		.... ..1.		RTM2SLIP	ON WHEN SLIP REQUESTED FOR THIS ERROR

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1		RTM2CONT	USED BY RTM2 AS A CONTROL FLAG IN SEGMENT RTCFTCB
446	(1BE)	1...		RTM2RSCN	USED BY RTM2 AS A CONTROL BIT DURING STACKING. ON INDICATES A SUBTASK IN RTM2 HAS BEEN FOUND
		.1..		RTM2DEND	USED BY RTM2 AS A CONTROL BIT WHEN PROCESSING DUMP OPTIONS
		..1.		RTM2RGEB	USED BY RTM2 AS A CONTROL BIT WHEN PROCESSING DUMP OPTIONS
		...1		RTM2NODP	ON=SLIP HAS SPECIFIED THAT ALL DUMP REQUESTS OUT OF THIS CALL TO RTM SHOULD BE IGNORED
		.... 1...		RTM2INPG	USED BY RTM2 AS CONTROL BIT IN RTCINPRG
		.... .1..		RTM2PPIO	USED BY RTM2 AS CONTROL BIT IN RTCINPRG
		.... ..1.		RTM2PGIO	USED BY RTM2 AS A CONTROL BIT IN RTCINPRG
		1		RTM2SUBR	Used while doing initial processing of subtasks to indicate that at least one subtask being processed was already in resource manager processing
447	(1BF)	BITSTRING	1	RTM2TCTL	RESERVED FOR TASK TERMINATION
		1...		RTM2NTAS	On, the RTM2 controller has called IEAVTAS1 for normal estae/arr processing
		.111 1111		*	Reserved - for future use
448	(1C0)	BITSTRING	1	RTM2MCTL	RESERVED FOR MEMORY TERMINATION
449	(1C1)	BITSTRING	2	RTM2ABDR	ABDUMP FLAGS
449	(1C1)	BITSTRING	1	*	RESERVED
450	(1C2)	BITSTRING	1	RTM2ABND	ABDUMP FLAGS - RTM2STAT REMOVED
		1...		RTM2NDMP	REQ'D INFOR FOR DUMP MISSING - NO DUMP PROVIDED
451	(1C3)	BITSTRING	1	RTM2RCTL	FLAGS USED TO MANAGE TASK RECOVERY PATHS
		1...		RTM2STA2	ON, STAE EXIT ENTERED FOR THIS ERROR
		.1..		RTM2WAIN	ON, SDWA INVALID ON RETURN FROM EXIT
		..1.		RTM2WANA	ON, SDWA NOT ACQUIRED
		...1		RTM2TRSW	USED BY TASK RECOVERY FOR LOOP CONTROL
		.... 1...		RTM2BFTL	USED BY TASK RECOVERY AS FIRST TIME LOGIC INDICATOR
		.... .1..		RTM2LPAQ	USED BY TASK RECOVERY WHEN THE* LINK PACK AREA CDE CHAIN IS BEING SEARCHED
		.... ..1.		RTM2JPAQ	USED BY TASK RECOVERY WHEN THE JOB PACK AREA CDE CHAIN IS BEING SEARCHED
		1		RTM2SDAB	The current SDWA was obtained above the 16M line and has an SDWARC4 extension
452	(1C4)	BITSTRING	8	RTM2INTF	FLAGS USED TO MANAGE PATHS ACROSS RTM2 SUBFUNCTIONS
452	(1C4)	BITSTRING	4	RTM2COMF	FLAGS USED TO COMMUNICATE WITH VARIOUS SUBFUNCTIONS
452	(1C4)	BITSTRING	1	RTM2CTLR	FLAGS USED TO COMMUNICATE WITH THE CONTROLLER
		1...		RTM2RECR	ON, THIS IS RECURSIVE ENTRY
		.1..		RTM2RETR	ON, RETRY REQUESTED BY EXIT
		..1.		RTM2TMEM	ON, TASK TERMINATION HAS ENDED THE LAST TASK IN THE MEMORY
		...1		RTM2WRAP	ON, INDICATES STORAGE RANGES WRAPPED AROUND
		.... 1...		RTM2STRV	ON, INDICATES STORAGE RANGES ACCESS IN PROGRESS
		.... .1..		RTM2SPLV	ON, INDICATES SUBPOOL LIST ACCESS IN PROGRESS
		.... ..1.		RTM2XWRP	ON, INDICATES DATA SPACE STORAGE RANGES WRAPPED AROUND
		1		RTM2XSTV	ON, INDICATES DATA SPACE STORAGE RANGES ACCESS IN PROGRESS
453	(1C5)	BITSTRING	1	RTM2DMPC	FLAGS USED TO COMMUNICATE WITH THE DUMP FUNCTIONS
		1...		RTM2NOSV	ON, INDICATES SLIP HAS SPECIFIED THAT SVC DUMP REQUESTS OUT OF THIS CALL TO RTM SHOULD BE IGNORED
		.1..		RTM2NOSA	ON, INDICATES SLIP HAS SPECIFIED THAT SYSABEND DUMP REQUESTS OUT OF THIS CALL TO RTM SHOULD BE IGNORED
		..1.		RTM2NOSM	ON, INDICATES SLIP HAS SPECIFIED THAT SYSMDUMP REQUESTS OUT OF THIS CALL TO RTM SHOULD BE IGNORED
		...1		RTM2NOSU	ON, INDICATES SLIP HAS SPECIFIED THAT SYSUDUMP REQUESTS OUT OF THIS CALL TO RTM SHOULD BE IGNORED
		.... 1...		RTM2NOSP	ON, INDICATES SLIP HAS SPECIFIED THAT DUMP REQUESTS OUT OF THIS CALL TO RTM SHOULD NOT BE SUPPRESSED BY DUPLICATE DUMP SUPPRESSION
		.... .111		*	RESERVED
454	(1C6)	BITSTRING	1	RTM2TSKT	FLAGS USED TO COMMUNICATE WITH TASK TERMINATION
		1...		RTM2PURG	ON, PURGE ONLY ENTRY
		.111 1111		*	RESERVED
455	(1C7)	BITSTRING	1	RTM2MEMT	RESERVED FOR MEMORY TERMINATION
456	(1C8)	BITSTRING	1	RTM2ABDP	FLAGS USED TO COMMUNICATE WITH ABDUMP
		1...		RTM2DMP1	ON, DUMP ONLY ONE TASK (RETRY WITH DUMP WAS REQUESTED)
		.111 1111		*	RESERVED
457	(1C9)	BITSTRING	1	RTM2ASIR	FLAGS USED TO COMMUNICATE WITH TASK RECOVERY
		1...		RTM2TRME	ON, ENTER ONLY TERM EXITS
		.1..		RTM2UPRG	ALL REGS TO BE UPDATED
		..1.		RTM2RCRD	ACTION=RECORD FROM SLIP. RTM2 MUST RECORD
		...1		RTM2TERM	SOME RTM2WA WAS FOR TERM
		.... 1...		RTM2SPIS	SPI ISSUED SVC D
		.... .1..		*	RESERVED
		.... ..1.		RTM2UP64	64-bit halves in RTM2G64H are to be used for retry
		1		*	RESERVED
458	(1CA)	BITSTRING	2	RTM2FLX	FLAGS USED TO COMMUNICATE WITH THE EXIT HANDLER
458	(1CA)	BITSTRING	1	RTM2FLX1	

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1...		RTM2MTX	ON, MEMORY PURGE EXIT
		.1..		RTM2EOTX	ON, NORMAL END OF TASK EXIT
		..1.		RTM2ABX	ON, ABEND EXIT
		...1		RTM2DWX	ON, SUBTASK WAITING EXIT
		.... 1...		RTM2CVX	ON, CONVERT TO STEP EXIT
		.... .1..		RTM2PRX	ON, PERMANENT TASK EXIT
		.... ..1.		RTM2LTX	ON, LAST TASK EXIT
		1		RTM2RTRX	ON, RETRY EXIT
459	(1CB)	BITSTRING	1	RTM2FLX2	
		1...		RTM2RCRX	ON, RECURSION EXIT
		.1..		RTM2CERX	ON, THE RTM2 CONTROLLER HAS DETECTED AN UNRECOVERABLE ERROR. EXIT IS TO CRITICAL ERROR ROUTINE
		..11 1111		*	RESERVED
460	(1CC)	CHARACTER	20	RTM2RECL	FLAGS USED TO MAINTAIN TRACKS FOR RECURSIVE ENTRIES
460	(1CC)	CHARACTER	12	RTM2SECF	RTM2 SECTION FLAGS. THIS AREA IS MAPPED BY RTM2SECM
460	(1CC)	BITSTRING	4	RTM2SCTC	CURRENT SECTION FLAG
464	(1D0)	BITSTRING	4	RTM2SCTR	PREVIOUS SECTION FLAGS INDICATING WHICH SECTIONS HAVE SUFFERED RECURSION
468	(1D4)	BITSTRING	4	RTM2SCTX	EXIT TYPE SECTION FLAGS INDICATING WHICH SECTIONS RECURSION ADDRESS SHOULD RECEIVE CONTROL
472	(1D8)	BITSTRING	2	*	RESERVED, RTM2DCTL AND RTM2ECTL REMOVED
474	(1DA)	BITSTRING	2	RTM2TMER	RESERVED FOR EOT, MEMORY TERMINATION, TASK TERMINATION
476	(1DC)	BITSTRING	4	RTM2TRYR	RESERVED FOR TASK RECOVERY AND TERM EXIT PROCESSOR
476	(1DC)	BITSTRING	2	RTM2TRF1	EXTERNAL ROUTINE INDICATORS (TASK RECOVERY)
		1...		RTM2IOQS	QUIESCE IN CONTROL
		.1..		RTM2IOHS	HALT IN CONTROL
		..1.		RTM2FTLS	FIRST TIME LOGIC
		...1		RTM2GMS	GETMAIN IN CONTROL
		.... 1...		RTM2ABR	SNAP/ABDUMP IN CONTROL
		.... .1..		RTM2HOOK	GTF IN CONTROL
		.... ..1.		RTM2COPY	COPY SDWA SECTION (IN IEAVTAS2) IN CONTROL
		1		RTM2FMS	FREEMAIN IN CONTROL
477	(1DD)	1...		RTM2RCD	RECORD IN CONTROL
		.1..		RTM2RTYS	RETRY SECTION IN CONTROL
		..1.		RTM2XIP	EXIT IN PROGRESS
		...1		RTM2XABD	EXIT ABENDED
		.... 1...		RTM2XFLG	EXIT HAS BEEN ENTERED
		.... .1..		RTM2AS1R	AS1 IN CONTROL
		.... ..1.		RTM2AS2R	AS2 IN CONTROL
		1		RTM2AS3R	AS3 IN CONTROL
478	(1DE)	BITSTRING	1	RTM2TRF2	PRE EXIT RECURSION INDICATORS
		1...		RTM2IOR	I/O RECURSION
		.1..		RTM2FTLR	FIRST TIME PROCESSING RECURSION
		..1.		RTM2GMR	GETMAIN RECURSION
		...1		RTM2STXR	STAX RECURSION
		.... 1...		RTM2TIOA	TAS2 ACCESSING TIOT
		.... .111		*	RESERVED
479	(1DF)	BITSTRING	1	RTM2TRF3	EXTERNAL ROUTINE INDICATORS (TASK RECOVERY)
		1...		RTM2STX2	STAX IN CONTROL
		.1..		RTM2STXS	STAX SECTION FLAG
		..11 1111		*	RESERVED
480	(1E0)	CHARACTER	16	RTM2RECH	RECURSION HANDLER ADDRESSES
480	(1E0)	ADDRESS	4	RTM2TRRA	ADDRESS OF SUBFUNCTION RECURSION HANDLER
484	(1E4)	ADDRESS	4	RTM2SKRA	ADDRESS OF CONTROLLER RECURSION HANDLER
488	(1E8)	ADDRESS	4	RTM2STRA	ADDRESS OF STEP CONVERSION RECURSION HANDLER
492	(1EC)	ADDRESS	4	RTM2CTRA	ADDRESS OF CRITICAL RECURSION HANDLER
496	(1F0)	SIGNED	2	RTM2RECT	EXIT ROUTINE RECURSION COUNT
498	(1F2)	ADDRESS	1	RTM2WARG	WORK AREA REGISTER
499	(1F3)	ADDRESS	1	RTM2RBRG	RB REGISTER FOR RTM2 SVRB
500	(1F4)	CHARACTER	64	RTM2RRG	RECURSION REGISTERS
500	(1F4)	ADDRESS	4	RTM2RREG (4294967312:562167696)	REGISTER VALUES TO BE LOADED BEFORE GOING TO A SUBFUNCTION RECURSION ROUTINE
564	(234)	CHARACTER	72	RTM2CRG	SAVE AREA FOR IEAVTRT2
564	(234)	ADDRESS	4	RTM2CREG (4294967314:562167656)	REGISTER SAVE AREA FOR IEAVTRTC AND IEAVTRTE
636	(27C)	ADDRESS	4	RTM2TRSA (4294967314:562131392)	REGISTER SAVE AREA FOR IEAVTAS2 AND IEAVTAS3
708	(2C4)	CHARACTER	64	RTM2ARER	ACCESS REGISTERS AT TIME OF ERROR
708	(2C4)	ADDRESS	4	RTM2ARE0	ACCESS REGISTER 0
712	(2C8)	ADDRESS	4	RTM2ARE1	ACCESS REGISTER 1
716	(2CC)	ADDRESS	4	RTM2ARE2	ACCESS REGISTER 2
720	(2D0)	ADDRESS	4	RTM2ARE3	ACCESS REGISTER 3
724	(2D4)	ADDRESS	4	RTM2ARE4	ACCESS REGISTER 4
728	(2D8)	ADDRESS	4	RTM2ARE5	ACCESS REGISTER 5

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
732	(2DC)	ADDRESS	4	RTM2ARE6	ACCESS REGISTER 6
736	(2E0)	ADDRESS	4	RTM2ARE7	ACCESS REGISTER 7
740	(2E4)	ADDRESS	4	RTM2ARE8	ACCESS REGISTER 8
744	(2E8)	ADDRESS	4	RTM2ARE9	ACCESS REGISTER 9
748	(2EC)	ADDRESS	4	RTM2AREA	ACCESS REGISTER 10
752	(2F0)	ADDRESS	4	RTM2AREB	ACCESS REGISTER 11
756	(2F4)	ADDRESS	4	RTM2AREC	ACCESS REGISTER 12
760	(2F8)	ADDRESS	4	RTM2ARED	ACCESS REGISTER 13
764	(2FC)	ADDRESS	4	RTM2AREE	ACCESS REGISTER 14
768	(300)	ADDRESS	4	RTM2AREF	ACCESS REGISTER 15
772	(304)	CHARACTER	64	RTM2ARRT	ACCESS REGISTERS FOR RETRY
772	(304)	ADDRESS	4	RTM2ARR0	ACCESS REGISTER 0
776	(308)	ADDRESS	4	RTM2ARR1	ACCESS REGISTER 1
780	(30C)	ADDRESS	4	RTM2ARR2	ACCESS REGISTER 2
784	(310)	ADDRESS	4	RTM2ARR3	ACCESS REGISTER 3
788	(314)	ADDRESS	4	RTM2ARR4	ACCESS REGISTER 4
792	(318)	ADDRESS	4	RTM2ARR5	ACCESS REGISTER 5
796	(31C)	ADDRESS	4	RTM2ARR6	ACCESS REGISTER 6
800	(320)	ADDRESS	4	RTM2ARR7	ACCESS REGISTER 7
804	(324)	ADDRESS	4	RTM2ARR8	ACCESS REGISTER 8
808	(328)	ADDRESS	4	RTM2ARR9	ACCESS REGISTER 9
812	(32C)	ADDRESS	4	RTM2ARRA	ACCESS REGISTER 10
816	(330)	ADDRESS	4	RTM2ARRB	ACCESS REGISTER 11
820	(334)	ADDRESS	4	RTM2ARRC	ACCESS REGISTER 12
824	(338)	ADDRESS	4	RTM2ARRD	ACCESS REGISTER 13
828	(33C)	ADDRESS	4	RTM2ARRE	ACCESS REGISTER 14
832	(340)	ADDRESS	4	RTM2ARRF	ACCESS REGISTER 15
836	(344)	ADDRESS	4	RTM2ALRC	Used to save STCBALOV in some recursion situations
840	(348)	ADDRESS	4	RTM2LNRC	Used to save STCBALD in some recursion situations
844	(34C)	ADDRESS	4	RTM2RBSV	Used to remember the owning RB address of the last recovery routine which received control for this level of RTM
848	(350)	CHARACTER	4	*	Rtm2ReleaseCode
848	(350)	CHARACTER	1	*	Reserved
849	(351)	CHARACTER	3	RTM2RELEASECODE	Release code when the abended RB level was interrupted for RTM processing after it was Released but before it could regain control. Only valid when Rtm2ReleaseCodeValid is on
852	(354)	CHARACTER	16	RTM2RTCD	DESCRIPTION OF THE SDWA
852	(354)	ADDRESS	4	RTM2RTCA	ADDRESS OF THE USER'S SDWA
852	(354)	ADDRESS	4	RTM2SDW1	ADDRESS OF THE USER'S SDWA
856	(358)	CHARACTER	4	RTM2SPLL	SUBPL & LNGTH OF SDWA
856	(358)	UNSIGNED	1	RTM2SUBP	SUBPOOL ID OF SDWA
857	(359)	UNSIGNED	3	RTM2SIZE	LENGTH OF SDWA
860	(35C)	BITSTRING	4	RTM2ALET	ALET OF THE USERS SDWA
864	(360)	CHARACTER	4	RTM2XDES	FURTHER DESCRIPTION OF THE USERS SDWA
		1...		RTM2ALES	RTM2 HAS ISSUED ALESERV TO OBTAIN AN ALET FOR THE PRIMARY ADDRESS SPACE AT TIME OF ESTAE CREATE
868	(364)	ADDRESS	4	RTM2STRR	SAVED RTM2TRRA VALUE
872	(368)	CHARACTER	10	RTM2ERID	ERRORID
872	(368)	CHARACTER	2	RTM2SEQ#	SEQUENCE NUMBER
874	(36A)	CHARACTER	2	RTM2CPUI	LOGICAL CPUID
876	(36C)	CHARACTER	2	RTM2ERAS	ASID FOR ERROR MEMORY
878	(36E)	CHARACTER	4	RTM2ERTM	TIME STAMP
882	(372)	CHARACTER	1	*	RESERVED
883	(373)	CHARACTER	1	RTM2SFLG	SUBSPACE FLAGS
		1...		RTM2SVLD	ON IF SUBSPACE INFORMATION AT THE TIME OF ERROR (RTM2SSTK AND RTM2SNM) IS AVAILABLE AND VALID
		.1..		RTM2SSA	A SUBSPACE WAS ACTIVE AT THE TIME OF ERROR
		..11 11..		*	RESERVED
		.... ..1.		RTM2BSA	Indicates that the workunit was running in Reduced Authority via the BSA instruction at the time of error
		1		*	RESERVED
884	(374)	CHARACTER	4	RTM2PLST	FIELDS USED BY THE SNAPTRC MACRO
884	(374)	ADDRESS	4	RTM2TRSN	POINTER TO THE TRACE SNAPTRACE PARAMETER LIST (TRSN)
888	(378)	CHARACTER	8	*	
896	(380)	ADDRESS	4	RTM2SNPP	SNAP PARM LIST WORK PTR
896	(380)	CHARACTER	4	RTM2SNPW	SNAP PARM LIST WORK WORD
896	(380)	ADDRESS	2	RTM2SNPH	FIRST HALFWORD OF SNPP
898	(382)	ADDRESS	2	*	2ND HALFWORD OF SNPP
900	(384)	ADDRESS	1	RTM2TEAR	TRANSLATION EXCEPTION ADDRESS ACCESS REGISTER NUMBER
901	(385)	CHARACTER	1	RTM2MFLG	MISCELLANEOUS FLAG BYTE
		1...		RTM2TBNC	ON, INDICATES DATA IN ASSOCIATED TRACE BUFFER DOES NOT REFLECT CURRENT ERROR
		.1..		RTM2FRPR	ON, INDICATES FRR PROCESSING RECEIVED CONTROL

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		..1.		RTM2TEAV	ON, INDICATES RTM2TRAN CONTAINS VALID ADDRESS
		...1		RTM2TEIV	ON, INDICATES RTM2TRAN CONTAINS VALID ASID
		.... 1...		RTM2TEPC	ON, INDICATES RTM2TRAN CONTAINS VALID PC NUMBER
		.... .1..		*	RESERVED
		.... ..1.		RTM2RELEASECODEVALID	On, indicates that the abended RB level was interrupted for RTM processing after it had been Released but before it could regain control, and that RTM2ReleaseCode contains its Release code
		1		RTM2DBKR	Processed a BAKR request
902	(386)	UNSIGNED	2	RTM2CTR	WORKAREA COUNTER, STARTING FROM ZERO
904	(388)	CHARACTER	68	RTM2RYRG	REG SAVEAREA FOR RETRY
904	(388)	ADDRESS	4	RTM2RYRS	SAVEAREA ARRAY (4294967312:562233648)
968	(3C8)	SIGNED	4	RTM2TECB	TRACE PROCESS ECB
972	(3CC)	ADDRESS	4	RTM2LSBT	LINKAGE STACK BEGIN TOKEN FOR THIS LEVEL OF RTM2
976	(3D0)	ADDRESS	4	RTM2LSET	LINKAGE STACK END TOKEN FOR THIS LEVEL OF RTM2
980	(3D4)	CHARACTER	8	RTM2COMU	FRR TO ESTAE COMMUNICATION BUFFER
988	(3DC)	ADDRESS	4	RTM2SDW2	ADDRESS OF THE RTM2'S SDWA
992	(3E0)	CHARACTER	4	RTM2CRC	SAVED COPY OF REASON CODE
996	(3E4)	ADDRESS	4	RTM2ANCH	ADDRESS OF ORIGINAL RTM2WA
1000	(3E8)	ADDRESS	4	RTM2SCBP	PSEUDO SCB POINTER
1004	(3EC)	ADDRESS	4	RTM2LSCT	LINKAGE STACK CURRENT TOKEN FOR THIS LEVEL OF RTM2
1008	(3F0)	CHARACTER	16	RTM2IOMA	ADDITIONAL IO MACHINE CHECK DATA
1008	(3F0)	CHARACTER	4	*	RESERVED
1012	(3F4)	CHARACTER	4	*	RESERVED
1016	(3F8)	CHARACTER	8	RTM2MCIC	MACHINE CHECK INTERRUPTION CODE
1024	(400)	CHARACTER	28	RTM2RRD	USED FOR EXPANDED SDWARC1. THESE FIELDS ARE PASSED FROM RTM1 TO RTM2 VIA EED'S.
1024	(400)	CHARACTER	12	RTM2FAIN	SAVED COPY OF SDWAFAIN
1036	(40C)	ADDRESS	4	RTM2ASCB	SAVED COPY OF SDWAASCB
1040	(410)	ADDRESS	4	RTM2ASST	SAVED COPY OF SDWAASST
1044	(414)	CHARACTER	4	RTM2SABC	SAVED COPY OF SDWASABC
1044	(414)	CHARACTER	1	RTM2OABF	SAVED COPY OF SDWAOABF
		1111 1...		*	ENTRY FLAGS
		.... .1..		RTM2ORCF	SAVED COPY OF SDWAORCF
		11		*	ENTRY FLAGS
1045	(415)	CHARACTER	3	RTM2OCMP	SAVED COPY OF SDWAOCMP
1048	(418)	CHARACTER	4	RTM2OCRC	SAVED COPY OF SDWAOCRC
1052	(41C)	CHARACTER	64	*	Unused. Was RTM2CRER
1052	(41C)	CHARACTER	20	RTM2SXMV	SYNCH XMENV plist
1116	(45C)	CHARACTER	64	RTM2DUCT	DISPATCHABLE UNIT CONTROL TABLE
1180	(49C)	ADDRESS	4	RTM2RMNP	ADDRESS OF RESOURCE MANAGER PARAMETER AREA BELOW 16M
1184	(4A0)	CHARACTER	4	RTM2TEMP	GENERAL WORK AREA
1188	(4A4)	ADDRESS	4	RTM2RMAD	Address of the last resource manager called during memory termination processing
1192	(4A8)	CHARACTER	16	RTM2RMNM	Name of the last resource manager called during memory termination processing
1208	(4B8)	ADDRESS	4	RTM2ORET	Used to save the 4-byte version of the returning programs completion code for normal termination
1212	(4BC)	ADDRESS	4	RTM2IRBX	USED TO SAVE IRB POINTER DURING ACCESS LIST RESTORATION CALCULATIONS
1216	(4C0)	ADDRESS	4	RTM2SAVE	USED TO SAVE AN RTM2WA POINTER DURING ACCESS LIST RESTORATION CALCULATIONS
1220	(4C4)	ADDRESS	4	RTM2ALSV	USED TO SAVE ALOV VALUE DURING ACCESS LIST RESTORATION CALCULATIONS
1224	(4C8)	ADDRESS	4	RTM2LNSV	USED TO SAVE ALD VALUE DURING ACCESS LIST RESTORATION CALCULATIONS
1228	(4CC)	ADDRESS	4	RTM2LSRM	Used to hold the current level of the Linkage Stack upon RTMs first entry to resource mgrs for the current Task
1232	(4D0)	CHARACTER	96	RTM2SLPL	USED FOR SLPL
1328	(530)	ADDRESS	4	RTM2NSCB	First SCB to cause STCBNOAB to be set
1332	(534)	CHARACTER	24	RTM2RECP	USED TO HOLD RTM2'S COPY OF THE RECORDING PARAMETERS
1332	(534)	CHARACTER	8	RTM2MODN	THE LOAD MODULE NAME INVOLVED IN THE ABEND
1340	(53C)	CHARACTER	8	RTM2CSCT	THE CSECT NAME INVOLVED IN THE ABEND
1348	(544)	CHARACTER	8	RTM2REXN	THE RECOVERY ROUTINE NAME INVOLVED IN THE ABEND
1356	(54C)	ADDRESS	4	RTM2RME	ADDRESS OF RME IN ERROR
1360	(550)	ADDRESS	4	RTM2RETY	DYNAMIC RESOURCE MANAGER RETRY ADDRESS
1364	(554)	CHARACTER	68	RTM2ARRW	STRUCTURE FOR ARR DATA AND PARAMETER LIST
1364	(554)	CHARACTER	16	RTM2ARRP	IEAVXREX PARAMETER LIST
1364	(554)	ADDRESS	4	RTM2ARP1	ADDR OF ARR PC NUMBER
1368	(558)	ADDRESS	4	RTM2ARP2	ADDR OF ARR ASN
1372	(55C)	ADDRESS	4	RTM2ARP3	ADDR OF ARR 32 BYTE ETE COPY
1376	(560)	ADDRESS	4	RTM2ARP4	ADDR OF ARR RECOVERY DATA
1380	(564)	CHARACTER	4	*	Unused
1384	(568)	CHARACTER	4	RTM2ARID	ARR ASN

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
1388	(56C)	ADDRESS	4	RTM2LVL	LEVEL FOR ARR RECOVERY
1392	(570)	CHARACTER	32	RTM2ARET	ETE COPY FOR THIS ARR
1424	(590)	CHARACTER	8	RTM2ARRX	ARR RECOVERY DATA
1424	(590)	ADDRESS	4	RTM2ARAD	ARR RECOVERY ADDRESS
1428	(594)	BITSTRING	4	RTM2ARRO	ARR OPTIONS
		1...		RTM2ANCN	ON, ARR CAN NOT BE CANCELLED
		.1..		RTM2ANAS	ON, ARR CAN NOT BE INTERRUPTED BY ASYNCHRONOUS EVENTS
		..1.		RTM2ARCO	ON, ARR is conditional and should not result in a LOGREC entry if skipped due to non-RTM2 environment
1432	(598)	ADDRESS	4	RTM2CSCB	FIRST SCB CAUSING STCBNCNL BIT TO BE SET ON
1436	(59C)	ADDRESS	4	RTM2LSO	INDICATES END OF LINKAGE STACK FOR TERM PROCESSING
1440	(5A0)	ADDRESS	4	RTM2CDEO	WORKING COPY OF DUCT_SSASTEO FOR RTM2 INTERNAL PROCESSING USE
		1...		RTM2CDSA	WORKING COPY OF DUCTSA FOR RTM2 INTERNAL PROCESSING
1444	(5A4)	ADDRESS	4	RTM2CDSN	WORKING COPY OF DUCT_SSASTESN FOR RTM2 INTERNAL PROCESSING USE
1448	(5A8)	CHARACTER	8	RTM2SSTK	STOKEN OF THE ACTIVE SUBSPACE AT TIME OF ERROR - ONLY VALID IF RTM2SVLD IS ON
1456	(5B0)	CHARACTER	8	RTM2SNM	NAME OF THE ACTIVE SUBSPACE AT TIME OF ERROR - ONLY VALID IF RTM2SVLD IS ON
1464	(5B8)	CHARACTER	8	RTM2ARCP	CSID and PCNum
1464	(5B8)	UNSIGNED	4	RTM2ARCP_CSID	Called space ID
1464	(5B8)	UNSIGNED	2	RTM2ARCP_CSID_ASN	
1466	(5BA)	UNSIGNED	2	RTM2ARCP_CSID_SEQ	
1468	(5BC)	UNSIGNED	4	RTM2ARCP_PCNUM	
1468	(5BC)	CHARACTER	4	RTM2ARPC	ARR PC NUMBER
1472	(5C0)	CHARACTER	8	*	RESERVED FOR SERVICE USE
1480	(5C8)	CHARACTER	64	RTM2G64H	High halves of 64-bit GPRs from Time Of Error
1480	(5C8)	ADDRESS	4	RTM2G64HR (15:562131392)	Individual high-half
1544	(608)	CHARACTER	64	RTM2G6RT	G64HS FOR RETRY
1544	(608)	ADDRESS	4	RTM2G6R0	G64H 0
1548	(60C)	ADDRESS	4	RTM2G6R1	G64H 1
1552	(610)	ADDRESS	4	RTM2G6R2	G64H 2
1556	(614)	ADDRESS	4	RTM2G6R3	G64H 3
1560	(618)	ADDRESS	4	RTM2G6R4	G64H 4
1564	(61C)	ADDRESS	4	RTM2G6R5	G64H 5
1568	(620)	ADDRESS	4	RTM2G6R6	G64H 6
1572	(624)	ADDRESS	4	RTM2G6R7	G64H 7
1576	(628)	ADDRESS	4	RTM2G6R8	G64H 8
1580	(62C)	ADDRESS	4	RTM2G6R9	G64H 9
1584	(630)	ADDRESS	4	RTM2G6RA	G64H 10
1588	(634)	ADDRESS	4	RTM2G6RB	G64H 11
1592	(638)	ADDRESS	4	RTM2G6RC	G64H 12
1596	(63C)	ADDRESS	4	RTM2G6RD	G64H 13
1600	(640)	ADDRESS	4	RTM2G6RE	G64H 14
1604	(644)	ADDRESS	4	RTM2G6RF	G64H 15
1608	(648)	CHARACTER	128	RTM2C64S	ESAME CRs
1608	(648)	CHARACTER	8	RTM2CRE0	CONTROL REGISTER 0
1616	(650)	CHARACTER	8	RTM2CRE1	CONTROL REGISTER 1
1624	(658)	CHARACTER	8	RTM2CRE2	CONTROL REGISTER 2
1632	(660)	CHARACTER	16	RTM2XM	CROSS MEMORY INFO
1632	(660)	CHARACTER	8	RTM2CRE3	CONTROL REGISTER 3
1632	(660)	CHARACTER	8	RTM2CR3	CONTROL REGISTER 3 (OLD NAME)
1632	(660)	UNSIGNED	4	RTM2SINS	SASTE IN
1636	(664)	CHARACTER	4	RTM2KMSA	KM/SASID
1636	(664)	CHARACTER	2	RTM2KM	KEY MASK
1638	(666)	CHARACTER	2	RTM2SASD	SASID
1640	(668)	CHARACTER	8	RTM2CRE4	CONTROL REGISTER 4
1640	(668)	CHARACTER	8	RTM2CR4	CONTROL REGISTER 4 (OLD NAME)
1640	(668)	UNSIGNED	4	RTM2PINS	PASTE IN
1644	(66C)	CHARACTER	4	RTM2AXPA	AX/PASID
1644	(66C)	CHARACTER	2	RTM2AX	AUTHORIZATION INDEX
1646	(66E)	CHARACTER	2	RTM2PASD	PASID
1648	(670)	CHARACTER	8	RTM2CRE5	CONTROL REGISTER 5
1656	(678)	CHARACTER	8	RTM2CRE6	CONTROL REGISTER 6
1664	(680)	CHARACTER	8	RTM2CRE7	CONTROL REGISTER 7
1672	(688)	CHARACTER	8	RTM2CRE8	CONTROL REGISTER 8
1680	(690)	CHARACTER	8	RTM2CRE9	CONTROL REGISTER 9
1688	(698)	CHARACTER	8	RTM2CREA	CONTROL REGISTER 10
1696	(6A0)	CHARACTER	8	RTM2CREB	CONTROL REGISTER 11
1704	(6A8)	CHARACTER	8	RTM2CREC	CONTROL REGISTER 12
1712	(6B0)	CHARACTER	8	RTM2CRED	CONTROL REGISTER 13

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
1720	(6B8)	CHARACTER	8	RTM2CREE	CONTROL REGISTER 14
1728	(6C0)	CHARACTER	8	RTM2CREF	CONTROL REGISTER 15
1728	(6C0)	CHARACTER	4	RTM2CRFH	CR15 high half
1732	(6C4)	CHARACTER	4	RTM2CRFL	CR15 low half
1736	(6C8)	CHARACTER	32	RTM2PGCY2	THE FOLLOWING FIELDS ARE COPIED INTO THE RTM2WA WHEN RTM2 IS ENTERED FOR PURGE ONLY
1736	(6C8)	CHARACTER	8	RTM2TRNE	8-byte TEA
1744	(6D0)	CHARACTER	8	RTM2BEA	Breaking event address
1752	(6D8)	CHARACTER	16	RTM2PSW16	16-byte PSW, analog of RTM2EPSW
1752	(6D8)	CHARACTER	8	*	
1760	(6E0)	CHARACTER	8	RTM2PSW16_IA	
1768	(6E8)	CHARACTER	4	RTM2HLHI	Time of Error PSAHLHI
1772	(6EC)	CHARACTER	4	RTM2SUPR	Time of Error PSASUPER
1776	(6F0)	CHARACTER	4	RTM2SPN	Time of Error LCCASPIN
1780	(6F4)	CHARACTER	4	RTM2CLSE	Time of Error PSACLHSE
1784	(6F8)	CHARACTER	8	*	Reserved
1792	(700)	CHARACTER	64	RTM2TXG64H	Time of transaction high
1856	(740)	CHARACTER	64	RTM2TXG64L	Time of transaction regs
1920	(780)	CHARACTER	16	RTM2TXPSW16	Time of transaction PSW
1936	(790)	CHARACTER	0	RTM2END	END OF THE RTM2WA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	240	RTM2DPSL	DUMP STORAGE LISTS
0	(0)	CHARACTER	8	RTM2DPSA (4294967326:562115832)	DUMP STORAGE RANGE PAIRS (MAXIMUM OF 30)
0	(0)	ADDRESS	4	RTM2DB	BEGIN ADDRESS
4	(4)	ADDRESS	4	RTM2DE	END ADDRESS
		1...		RTM2LDE	ON, LAST RANGE SPECIFIED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	RTM2SPLE	UP TO 7 SUBPOOL IDS SPECIFIED VIA DUMPOPT ON ABEND, CALLRTM AND/OR SETRP.
0	(0)	SIGNED	2	RTM2SPLN	NUMBER OF SUBPOOLS
2	(2)	SIGNED	2	RTM2SPLS (4294967303:562130640)	IDS OF SUBPOOLS TO BE DUMPED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	240	RTM2DXSL	DUMPOPX STORAGE LISTS
0	(0)	CHARACTER	16	RTM2DXSR (4294967311:562116984)	DUMPOPX STORAGE RANGES AND STOKENS
0	(0)	ADDRESS	4	RTM2DXBG	BEGIN ADDRESS
4	(4)	ADDRESS	4	RTM2DXEN	END ADRRESS
		1...		RTM2DXLE	ON, LAST RANGE SPECIFIED
8	(8)	CHARACTER	8	RTM2DXST	STOKEN FOR RANGE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	200	RTM2RMIN	RESOURCE MANAGER PARAMETER AREA, ALWAYS BELOW THE LINE
0	(0)	ADDRESS	4	*	Available - was RTM2RMLN when RMPLs were getmained together with an RTM2WA below the line
4	(4)	ADDRESS	4	RTM2RMPS	ADDR OF THE RESOURCE MANAGER PARAMETER LIST (RTM2RMPL)
4	(4)	ADDRESS	4	RTM2RMP1	ADDRESS OF THE RESOURCE MANAGER PARAMETER LIST
8	(8)	ADDRESS	4	RTM2RMP2	ADDRESS OF PARAM VALUE SPECIFIED ON RESMGR INVOCATION OR ZERO IF PARAM NOT SPECIFIED
12	(C)	CHARACTER	24	RTM2RMPL	R/M PARM LIST
36	(24)	CHARACTER	8	RTM2PARM	PARAM VALUE SPECIFIED ON RESMGR INVOCATION
44	(2C)	CHARACTER	64	RTM2RMWA	FIELD REFERENCE NAME FOR RTM2RMWS
44	(2C)	ADDRESS	4	RTM2RMWS (4294967312:562120048)	WORK AREA FOR RESOURCE MANAGER USE
108	(6C)	ADDRESS	4	RTM2RMSA (4294967314:562120048)	RESOURCE MGR SAVE AREA
180	(B4)	ADDRESS	4	RTM2IOBP	4-BYTE PTR TO I/O RESTORE CHAIN
180	(B4)	CHARACTER	1	*	RESERVED

RTM2WA Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
181	(B5)	ADDRESS	3	RTM2FIOB	ADDRESS OF I/O RESTORE CHAIN
184	(B8)	CHARACTER	8	RTM2EXCL	PARM LIST FOR EXCPEXCL
192	(C0)	CHARACTER	8	RTM2CLPL	CLOSE PARMLIST
192	(C0)	ADDRESS	4	RTM2DCBA	ADDRESS OF A DCB TO BE CLOSED BY TASK RECOVERY PRIOR TO RETRY
196	(C4)	ADDRESS	4	*	SECOND WORD OF CLOSE PARMLIST - MUST BE ZEROS

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4	RTM2SECM	DOCUMENT THE MEANING OF THE RTM2 RECURSION BITS
		1...		RTMIPCS	ON, INITIAL PURGES INVOKED BY CONTROLLER HAS CONTROL
		.1..		RTMT1MS	ON, TYPE 1 MESSAGE WRITE ROUTINE HAS CONTROL
		..1.		RTMDMPS	ON, COPY DUMP OPTIONS HAS CONTROL
		...1		RTMASYS	ON, FLAGGING ASYNCHRONOUS EXITS FROM POTENTIALLY RECOVERABLE PATH HAS CONTROL.
		.... 1...		RTMSMCS	ON, DETERMINE SCOPE HAS CONTROL
		.... .1..		RTMIQES	ON, FLAGGING ASYNCHRONOUS EXITS FROM EXPRESS PATH HAS CONTROL
		.... ..1.		RTMEPS	ON, INITIAL SUBTASK PROCESSING FROM EXPRESS PATH HAS CONTROL
		1		RTMISPS	ON, INITIAL SUBTASK PROCESSING FROM ABEND HAS CONTROL
1	(1)	1...		RTMSTKS	ON, STACKING HAS CONTROL
		.1..		RTMEXTS	ON, EXIT IN PROGRESS
		..1.		RTMTRS	ON, TASK RECOVERY HAS CONTROL
		...1		RTMSLPS	ON, SLIP PROCESSING HAS CONTROL
		.... 1...		RTMDPS	ON, DUMP HAS CONTROL
		.... .1..		RTMTSTS	ON, TASK TERMINATION HAS CONTROL
		.... ..1.		RTMSRBS	ON,RBSETUP HAS CONTROL
		1		RTMTREC	ON,TRACE COPY RECURSION
2	(2)	1...		RTMTREF	ON, FREEING TRACE TABLE COPY HEADER HAS CONTROL
		.1..		RTMFINS	ON, GATHER FAILING INSTRUCTION SUBROUTINE HAS CONTROL
		..1.		RTMPRG	ON, TASK PURGE RECURSION
		...1		RTMEQPG	On, ENQ/DEQ purge recursion for early cleanup in IEAVTRTC

RTM2WA Constants

Len	Type	Value	Name	Description
4	DECIMAL	4	MAXECBS	DEFINES THE NUMBER OF ECBS TO BE USED WHILE STACKING
4	DECIMAL	1	RTM2IPCS	ON, INITIAL PURGES INVOKED BY CONTROLLER HAS CONTROL
4	DECIMAL	2	RTM2T1MS	ON, TYPE 1 MESSAGE WRITE ROUTINE HAS CONTROL
4	DECIMAL	3	RTM2DMPS	ON, COPY DUMP OPTIONS HAS CONTROL
4	DECIMAL	4	RTM2ASYS	ON, FLAGGING ASYNCHRONOUS EXITS FROM POTENTIALLY RECOVERABLE PATH HAS CONTROL.
4	DECIMAL	5	RTM2SMCS	ON, DETERMINE SCOPE HAS CONTROL
4	DECIMAL	6	RTM2IQES	ON, FLAGGING ASYNCHRONOUS EXITS FROM EXPRESS PATH HAS CONTROL
4	DECIMAL	7	RTM2EPS	ON, INITIAL SUBTASK PROCESSING FROM EXPRESS PATH HAS CONTROL.
4	DECIMAL	8	RTM2ISPS	ON, INITIAL SUBTASK PROCESSING FROM ABEND HAS CONTROL
4	DECIMAL	9	RTM2STKS	ON, STACKING HAS CONTROL
4	DECIMAL	10	RTM2EXTS	ON, EXIT IN PROGRESS
4	DECIMAL	11	RTM2TRS	ON, TASK RECOVERY HAS CONTROL
4	DECIMAL	12	RTM2SLPS	ON, SLIP PROCESSING HAS CONTROL
4	DECIMAL	13	RTM2DPS	ON, DUMP HAS CONTROL
4	DECIMAL	14	RTM2TSTS	ON, TASK TERMINATION HAS CONTROL
4	DECIMAL	15	RTM2SRBS	ON,RBSETUP HAS CONTROL
4	DECIMAL	16	RTM2TREC	ON,TRACE COPY RECURSION
4	DECIMAL	17	RTM2TREF	ON, FREEING TRACE TABLE COPY HEADER HAS CONTROL
4	DECIMAL	18	RTM2FINS	ON, GATHER FAILING INSTRUCTION SUBROUTINE HAS CONTROL
4	DECIMAL	19	RTM2PRG	ON, TASK PURGE RECURSION
4	DECIMAL	20	RTM2EQPG	On, ENQ/DEQ purge recursion for early cleanup in IEAVTRTC

Comment

WHEN THIS LIST IS ADDED TO, ADD TO THE RTM2SECM LIST ALSO

End of Comment

4	DECIMAL	1	SCBSTART	CONSTANT USED TO INDICATE INITIAL ENTRY TO TASK RECOVERY

Len	Type	Value	Name	Description
4	DECIMAL	255	RTM2WASP	This is the subpool used for all of RTM2's storage, including RTM2RMIN below the line. If this ever changes then IEAVEMIN (which does not use this) must also be updated
4	DECIMAL	144	RTM2USAVELEN	144 bytes for amode64

RTM2WA Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
RTMASYS	0	10	RTM2ARE6	2DC	
RTMDMPS	0	20	RTM2ARE7	2E0	
RTMDPS	1	08	RTM2ARE8	2E4	
RTMEPS	0	02	RTM2ARE9	2E8	
RTMEQPG	2	10	RTM2ARID	568	
RTMEXTS	1	40	RTM2ARPC	5BC	
RTMFINS	2	40	RTM2ARP1	554	
RTMIPCS	0	80	RTM2ARP2	558	
RTMIQES	0	04	RTM2ARP3	55C	
RTMISPS	0	01	RTM2ARP4	560	
RTMPRG	2	20	RTM2ARRA	32C	
RTMSLPS	1	10	RTM2ARRB	330	
RTMSMCS	0	08	RTM2ARRC	334	
RTMSRBS	1	02	RTM2ARRD	338	
RTMSTKS	1	80	RTM2ARRE	33C	
RTMTREC	1	01	RTM2ARRF	340	
RTMTREF	2	80	RTM2ARRO	594	
RTMTRS	1	20	RTM2ARRP	554	
RTMTSTS	1	04	RTM2ARRT	304	
RTMT1MS	0	40	RTM2ARRW	554	
RTM2ABDP	1C8		RTM2ARRX	590	
RTM2ABDR	1C1		RTM2ARR0	304	
RTM2ABEP	94		RTM2ARR1	308	
RTM2ABND	1C2		RTM2ARR2	30C	
RTM2ABNM	8C		RTM2ARR3	310	
RTM2ABR	1DC	08	RTM2ARR4	314	
RTM2ABTM	B4	08	RTM2ARR5	318	
RTM2ABX	1CA	20	RTM2ARR6	31C	
RTM2ACR	A1	08	RTM2ARR7	320	
RTM2ADDR	4		RTM2ARR8	324	
RTM2ADD1	81		RTM2ARR9	328	
RTM2AEC1	84		RTM2ASC	18	
RTM2AIO1	7C	02	RTM2ASCB	40C	
RTM2ALES	360	80	RTM2ASCM	7E	C0
RTM2ALET	35C		RTM2ASIR	1C9	
RTM2ALRC	344		RTM2ASST	410	
RTM2ALSV	4C4		RTM2AS1R	1DD	04
RTM2ANAS	594	40	RTM2AS2R	1DD	02
RTM2ANCH	3E4		RTM2AS3R	1DD	01
RTM2ANCN	594	80	RTM2AX	66C	
RTM2APSW	7C		RTM2AXPA	66C	
RTM2ARAD	590		RTM2BEA	6D0	
RTM2ARCO	594	20	RTM2BFTL	1C3	08
RTM2ARCP	5B8		RTM2BSA	373	02
RTM2ARCP_CSID			RTM2CC	1D	
	5B8		RTM2CCA	F0	30
RTM2ARCP_CSID_ASN			RTM2CCF	1C	
	5B8		RTM2CCP	F8	30
RTM2ARCP_CSID_SEQ			RTM2CCTL	1BD	
	5BA		RTM2CC1	7E	30
RTM2ARCP_PCNUM			RTM2CDEO	5A0	
	5BC		RTM2CDSA	5A0	80
RTM2AREA	2EC		RTM2CDSN	5A4	
RTM2AREB	2F0		RTM2CERX	1CB	40
RTM2AREC	2F4		RTM2CHNG	A4	01
RTM2ARED	2F8		RTM2CLPL	C0	
RTM2AREE	2FC		RTM2CLSE	6F4	
RTM2AREF	300		RTM2CLUP	B7	80
RTM2ARER	2C4		RTM2CMKA	EC	
RTM2ARET	570		RTM2CMKP	F4	
RTM2ARE0	2C4		RTM2CNCL	1BD	40
RTM2ARE1	2C8		RTM2CODE	1C	
RTM2ARE2	2CC		RTM2COMF	1C4	
RTM2ARE3	2D0		RTM2COMP	DC	
RTM2ARE4	2D4		RTM2COMU	3D4	
RTM2ARE5	2D8		RTM2CONT	1BD	01

RTM2WA Cross Reference

Name	Hex Offset	Hex Value
RTM2COPY	1DC	02
RTM2CPID	A2	
RTM2CPUI	36A	
RTM2CRC	3E0	
RTM2CREA	698	
RTM2CREB	6A0	
RTM2CREC	6A8	
RTM2CRED	6B0	
RTM2CREE	6B8	
RTM2CREF	6C0	
RTM2CREG	234	
RTM2CRE0	648	
RTM2CRE1	650	
RTM2CRE2	658	
RTM2CRE3	660	
RTM2CRE4	668	
RTM2CRE5	670	
RTM2CRE6	678	
RTM2CRE7	680	
RTM2CRE8	688	
RTM2CRE9	690	
RTM2CRFH	6C0	
RTM2CRFL	6C4	
RTM2CRG	234	
RTM2CR3	660	
RTM2CR4	668	
RTM2CSCB	598	
RTM2CSCT	53C	
RTM2CT	38	
RTM2CTLR	1C4	
RTM2CTL1	EC	
RTM2CTL2	F4	
RTM2CTR	386	
RTM2CTRA	1EC	
RTM2CTS	B7	10
RTM2CVER	BA	
RTM2CVT	C	
RTM2CVX	1CA	08
RTM2C64S	648	
RTM2DB	0	
RTM2DBKR	385	01
RTM2DCBA	C0	
RTM2DD	134	
RTM2DE	4	
RTM2DEC1	7E	04
RTM2DEND	1BE	40
RTM2DESC	0	
RTM2DETF	1BD	20
RTM2DMPC	1C5	
RTM2DMP1	1C8	80
RTM2DOA	F0	04
RTM2DOP	F8	04
RTM2DPLA	FC	
RTM2DPSA	0	
RTM2DPSL	0	
RTM2DREQ	1C	80
RTM2DSAL	130	
RTM2DSPP	12C	
RTM2DTCB	140	
RTM2DUCT	45C	
RTM2DWX	1CA	10
RTM2DXBG	0	
RTM2DXC	8B	
RTM2DXEN	4	
RTM2DXLE	4	80
RTM2DXSL	0	
RTM2DXSR	0	
RTM2DXST	8	
RTM2EAM1	7C	08
RTM2EAS	B6	08
RTM2ECBA	144	
RTM2ECBS	154	
RTM2ECT1	7D	08
RTM2EEDH	98	
RTM2EEDR	3C	
RTM2EMK1	7C	
RTM2END	790	
RTM2ENRB	B5	04
RTM2EOM	1C	10
RTM2EOT	1C	08
RTM2EOTX	1CA	40
RTM2EPSW	7C	
RTM2ERAS	36C	
RTM2EREG	3C	
RTM2ERFL	B7	01
RTM2ERID	368	
RTM2ERME	D5	80
RTM2ERRA	B4	
RTM2ERRB	B5	
RTM2ERRC	B6	
RTM2ERRD	B7	
RTM2ERTM	36E	
RTM2ER0	3C	
RTM2ER1	40	
RTM2ER10	64	
RTM2ER11	68	
RTM2ER12	6C	
RTM2ER13	70	
RTM2ER14	74	
RTM2ER15	78	
RTM2ER2	44	
RTM2ER3	48	
RTM2ER4	4C	
RTM2ER5	50	
RTM2ER6	54	
RTM2ER7	58	
RTM2ER8	5C	
RTM2ER9	60	
RTM2EUA	F0	02
RTM2EUP	F8	02
RTM2EXCL	B8	
RTM2EXP1	7E	02
RTM2EXTA	EC	01
RTM2EXTP	F4	01
RTM2EXT1	7C	01
RTM2FAIN	400	
RTM2FIOB	B5	
RTM2FLAG	D4	
RTM2FLGS	B4	
RTM2FLG2	D5	
RTM2FLSQ	A5	04
RTM2FLX	1CA	
RTM2FLX1	1CA	
RTM2FLX2	1CB	
RTM2FMID	B8	
RTM2FMS	1DC	01
RTM2FOUN	D6	40
RTM2FPA	F0	08
RTM2FPO1	7E	08
RTM2FPP	F8	08
RTM2FRPR	385	40
RTM2FSQA	A5	08
RTM2FTLR	1DE	40
RTM2FTLS	1DC	20
RTM2GLBA	D4	80
RTM2GLBT	D4	40
RTM2GMR	1DE	20
RTM2GMS	1DC	10
RTM2G6RA	630	
RTM2G6RB	634	
RTM2G6RC	638	
RTM2G6RD	63C	
RTM2G6RE	640	
RTM2G6RF	644	
RTM2G6RT	608	
RTM2G6R0	608	
RTM2G6R1	60C	
RTM2G6R2	610	

Name	Hex Offset	Hex Value
RTM2G6R3	614	
RTM2G6R4	618	
RTM2G6R5	61C	
RTM2G6R6	620	
RTM2G6R7	624	
RTM2G6R8	628	
RTM2G6R9	62C	
RTM2G64H	5C8	
RTM2G64HR	5C8	
RTM2HEED	1BD	04
RTM2HLHI	6E8	
RTM2HLST	114	
RTM2HOOK	1DC	04
RTM2ICD0	86	
RTM2ICD1	87	
RTM2ID	0	
RTM2IENV	D6	20
RTM2ILA	F0	C0
RTM2ILC1	85	
RTM2ILP	F8	C0
RTM2IL1	85	06
RTM2IMC1	87	40
RTM2INC1	86	
RTM2INPG	1BE	08
RTM2INSF	A1	04
RTM2INTA	EE	
RTM2INTC	A5	40
RTM2INTF	1C4	
RTM2INTP	F6	
RTM2INT1	7E	
RTM2INVP	A0	10
RTM2IOA	EC	FE
RTM2IOBP	B4	
RTM2IOFS	BA	
RTM2IOHS	1DC	40
RTM2IOHT	BA	40
RTM2IOMA	3F0	
RTM2IOP	F4	FE
RTM2IOQR	BA	80
RTM2IOQS	1DC	80
RTM2IOR	1DE	80
RTM2IPC1	87	3F
RTM2IPR1	87	80
RTM2IRB	B6	20
RTM2IRBP	D6	80
RTM2IRBX	4BC	
RTM2ISPC	1BD	10
RTM2JPAQ	1C3	02
RTM2KEYA	ED	F0
RTM2KEYP	F5	F0
RTM2KEY1	7D	F0
RTM2KM	664	
RTM2KMSA	664	
RTM2LDE	4	80
RTM2LDIS	B5	02
RTM2LECB	144	80
RTM2LGTH	9	
RTM2LNRC	348	
RTM2LNSV	4C8	
RTM2LOCT	D4	08
RTM2LPAQ	1C3	04
RTM2LSBT	3CC	
RTM2LSCT	3EC	
RTM2LSET	3D0	
RTM2LSO	59C	
RTM2LSRM	4CC	
RTM2LSRT	C4	
RTM2LTX	1CA	02
RTM2LVL	56C	
RTM2MABD	B7	08
RTM2MCHD	A1	
RTM2MCHI	A0	
RTM2MCHK	B4	80
RTM2MCHS	A0	
RTM2MCIC	3F8	
RTM2MCIV	B7	02
RTM2MCKA	ED	04
RTM2MCKP	F5	04
RTM2MCK1	7D	04
RTM2MCTL	1C0	
RTM2MEMT	1C7	
RTM2MFLG	385	
RTM2MODN	534	
RTM2MOD1	80	80
RTM2MOD64	7F	01
RTM2MSER	A4	02
RTM2MTX	1CA	80
RTM2MWPA	ED	
RTM2MWPP	F5	
RTM2MWP1	7D	
RTM2NDMP	1C2	80
RTM2NIOP	BA	10
RTM2NMFS	B5	40
RTM2NODP	1BE	10
RTM2NOIO	BA	20
RTM2NOSA	1C5	40
RTM2NOSM	1C5	20
RTM2NOSP	1C5	08
RTM2NOSU	1C5	10
RTM2NOSV	1C5	80
RTM2NRBE	B7	40
RTM2NSCB	530	
RTM2NTAS	1BF	80
RTM2NUCL	A5	10
RTM2NXTA	F1	
RTM2NXTP	F9	
RTM2NXT1	80	
RTM2OABF	414	
RTM2OCMP	415	
RTM2OCRC	418	
RTM2OFLN	A5	80
RTM2ORCF	414	04
RTM2ORET	4B8	
RTM2PARM	24	
RTM2PARQ	E8	
RTM2PASD	66E	
RTM2PCAX	D6	10
RTM2PCHK	B4	40
RTM2PDIP	B5	80
RTM2PERC	B6	10
RTM2PER1	7C	40
RTM2PGCY	3C	
RTM2PGCY2	6C8	
RTM2PGFX	A5	02
RTM2PGIO	1BE	02
RTM2PGM1	7D	01
RTM2PINS	668	
RTM2PKEY	1BC	
RTM2PLST	374	
RTM2PMKA	F0	
RTM2PMKP	F8	
RTM2PPIO	1BE	04
RTM2PRB	BC	
RTM2PREV	16C	
RTM2PRWA	170	
RTM2PRX	1CA	04
RTM2PSWU	A1	20
RTM2PSW1	7C	
RTM2PSW16	6D8	
RTM2PSW16_IA	6E0	
RTM2PURG	1C6	80
RTM2RA64	E3	01
RTM2RBPR	D8	
RTM2RBRG	1F3	
RTM2RBST	C0	
RTM2RBSV	34C	
RTM2RCD	1DD	80
RTM2RCDE	E8	

RTM2WA Cross Reference

Name	Hex Offset	Hex Value
RTM2RCDF	A0	40
RTM2RCRD	1C9	20
RTM2RCRX	1CB	80
RTM2RCTL	1C3	
RTM2RCT2	D6	
RTM2REAF	1C	04
RTM2RECH	1E0	
RTM2RECL	1CC	
RTM2RECP	534	
RTM2RECR	1C4	80
RTM2RECT	1F0	
RTM2REED	1BD	08
RTM2REGU	A1	40
RTM2RELEASECODE	351	
RTM2RELEASECODEVALID	385	02
RTM2RETR	1C4	40
RTM2RETY	550	
RTM2REXN	544	
RTM2RFSA	A8	
RTM2RGEB	1BE	20
RTM2RKEY	B4	20
RTM2RMAD	4A4	
RTM2RME	54C	
RTM2RMIN	0	
RTM2RMNM	4A8	
RTM2RMNP	49C	
RTM2RMPL	C	
RTM2RMPS	4	
RTM2RMP1	4	
RTM2RMP2	8	
RTM2RMSA	6C	
RTM2RMWA	2C	
RTM2RMWS	2C	
RTM2RPIV	B7	04
RTM2RRD	400	
RTM2RREG	1F4	
RTM2RRG	1F4	
RTM2RSCN	1BE	80
RTM2RSRC	A0	08
RTM2RSRF	A0	04
RTM2RSR1	A4	
RTM2RSR2	A5	
RTM2RTCA	354	
RTM2RTCD	354	
RTM2RTRX	1CA	01
RTM2RTYA	E0	
RTM2RTYS	1DD	40
RTM2RT2D	8	
RTM2RYRB	E4	
RTM2RYRG	388	
RTM2RYRS	388	
RTM2R0DP	1C	20
RTM2SABC	414	
RTM2SALE	11C	
RTM2SASD	666	
RTM2SAVE	4C0	
RTM2SCBC	C8	
RTM2SCBN	CC	
RTM2SCBO	D0	
RTM2SCBP	3E8	
RTM2SCBS	C8	
RTM2SCK	A1	10
RTM2SCKB	98	
RTM2SCKE	9C	
RTM2SCTC	1CC	
RTM2SCTL	1BD	
RTM2SCTR	1D0	
RTM2SCTX	1D4	
RTM2SDAB	1C3	01
RTM2SDWK	BB	
RTM2SDW1	354	
RTM2SDW2	3DC	
RTM2SECB	144	
RTM2SECF	1CC	
RTM2SECM	0	
RTM2SEQ#	368	
RTM2SFLG	373	
RTM2SFRG	174	
RTM2SFSA	174	
RTM2SFWA	20	
RTM2SGA	F0	01
RTM2SGN1	7E	01
RTM2SGP	F8	01
RTM2SINS	660	
RTM2SIZE	359	
RTM2SKRA	1E4	
RTM2SKYF	A1	80
RTM2SLIP	1BD	02
RTM2SLPL	4D0	
RTM2SNAP	FC	
RTM2SNCC	13C	
RTM2SNM	5B0	
RTM2SNPH	380	
RTM2SNPL	100	
RTM2SNPP	380	
RTM2SNPW	380	
RTM2SNPX	11C	
RTM2SOFT	A1	02
RTM2SPEA	D4	20
RTM2SPER	A5	20
RTM2SPET	D4	10
RTM2SPID	8	
RTM2SPIS	1C9	08
RTM2SPLE	0	
RTM2SPLL	358	
RTM2SPLN	0	
RTM2SPLS	2	
RTM2SPLV	1C4	04
RTM2SPN	6F0	
RTM2SPRM	100	
RTM2SPSL	110	
RTM2SPSP	118	
RTM2SPVA	ED	01
RTM2SPVP	F5	01
RTM2SRBM	B5	01
RTM2SRBT	B5	20
RTM2SRVL	A0	80
RTM2SSA	373	40
RTM2SSTK	5A8	
RTM2STAE	B7	20
RTM2STAF	B6	80
RTM2STAI	B6	40
RTM2STA2	1C3	80
RTM2STCK	98	
RTM2STEP	1C	40
RTM2STKN	12C	
RTM2STPT	1BD	80
RTM2STRA	1E8	
RTM2STRM	B4	01
RTM2STRR	364	
RTM2STRV	1C4	08
RTM2STXR	1DE	10
RTM2STXS	1DF	40
RTM2STX2	1DF	80
RTM2SUBP	358	
RTM2SUBR	1BE	01
RTM2SUPR	6EC	
RTM2SVCD	B4	10
RTM2SVCE	B4	04
RTM2SVLD	373	80
RTM2SXMV	41C	
RTM2S1	7E	80
RTM2S2	7E	40
RTM2TBNC	385	80
RTM2TCBC	10	
RTM2TCBT	30	

Name	Hex Offset	Hex Value
RTM2TCTL	1BF	
RTM2TEAR	384	
RTM2TEAV	385	20
RTM2TECB	3C8	
RTM2TEIV	385	10
RTM2TEMP	4A0	
RTM2TEPC	385	08
RTM2TERM	1C9	10
RTM2TERR	A1	01
RTM2TEXC	B4	02
RTM2TIME	AC	
RTM2TIOA	1DE	08
RTM2TMEM	1C4	20
RTM2TMER	1DA	
RTM2TRAN	88	
RTM2TRF1	1DC	
RTM2TRF2	1DE	
RTM2TRF3	1DF	
RTM2TRME	1C9	80
RTM2TRM1	7C	04
RTM2TRNE	6C8	
RTM2TRRA	1E0	
RTM2TRRC	BA	
RTM2TRRY	3C	
RTM2TRSA	27C	
RTM2TRSN	374	
RTM2TRSW	1C3	10
RTM2TRYR	1DC	
RTM2TR2D	D5	40
RTM2TSKT	1C6	
RTM2TSVL	A0	20
RTM2TXG64H	700	
RTM2TXG64L	740	
RTM2TXPROG	86	02
RTM2TXPSW16	780	
RTM2TYP1	B5	08
RTM2UPRG	1C9	40
RTM2UP64	1C9	02
RTM2VEQR	A5	01
RTM2VRBC	14	
RTM2VRBT	34	
RTM2VRIV	A0	02
RTM2WA	0	
RTM2WAIN	1C3	40
RTM2WANA	1C3	20
RTM2WARG	1F2	
RTM2WATA	ED	02
RTM2WATP	F5	02
RTM2WAT1	7D	02
RTM2WRAP	1C4	10
RTM2XABD	1DD	10
RTM2XAM	7C	08
RTM2XDES	360	
RTM2XFLG	1DD	08
RTM2XIP	1DD	20
RTM2XM	660	
RTM2XSTV	1C4	01
RTM2XWRP	1C4	02

RTSD Information

RTSD Heading Information

Common Name: RTCT SDUMP EXTENSION
Macro ID: IHARTSD
DSECT Name: RTSD
Owning Component: SVC Dump (SCDMP)
Eye-Catcher ID: RTSD
Offset: 0
Length: 4
Storage Attributes: Main Storage: One per system
Subpool: 239
Key: 0
Residency: Above 16M
Size: 2684 bytes
Created by: IEAVTSDI
Pointed to by: RTCTRTSD
Serialization: Same as the RTCT
Note: Field RTSDSHLE is serialized by the same ENQ as we used to serialize high virtual shared table in IEAVTSDO.
Function: USED TO SAVE THE FOLLOWING SVC DUMP INFORMATION THAT MUST BE IN FIXED STORAGE:
1. USER PARAMETER LIST
2. USER HEADER DATA
3 STORAGE RANGES FROM STORAGE, LIST OR LISTA
4. SUBPOOL LIST
5. KEY LIST
6. CROSS MEMORY STATUS INFORMATION
7. REGISTERS AND PSW AT TIME OF SLIP TRAP
8. CALLERS CONTROL REGISTERS
9. CALLERS ACCESS REGISTERS
10. CALLERS SDWA ON BRANCH ENTRY SDUMPS
11. OTHER INFORMATION DESCRIBING THE CALLERS STATE
THE RTSD ALSO CONTAINS WORKING STORAGE USED BY IEAVTSDX, IEAVAD00, AND IEAVTSPR TO PROCESS THE USER PARAMETER LIST.

RTSD Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	3400	RTSD	RTCT SDUMP EXTENSION
0	(0)	CHARACTER	64	RTSDKEEP	STATIC PORTION OF RTSD
0	(0)	CHARACTER	4	RTSDID	EBCDIC IDENTIFIER -RTSD-
4	(4)	ADDRESS	4	RTSDEXTB	ADDRESS OF SDUMP EXIT TABLE
8	(8)	CHARACTER	4	RTSDDNUM	DUMP NUMBER FOR PRDSEQ
8	(8)	UNSIGNED	2	RTSDSVNM	NUMBER OF SVC DUMPS
10	(A)	UNSIGNED	2	RTSDALLD	NUMBER OF SVC DUMPS AND SYSMDUMPS
12	(C)	ADDRESS	4	RTSDDPPF	DPL-in-DUMPSRV-private queue first element (forward)
16	(10)	ADDRESS	4	RTSDDPPB	DPL-in-DUMPSRV-private queue last element (backward)
20	(14)	CHARACTER	8	RTSDSTKN	STOKEN of DUMPSRV address space
28	(1C)	ADDRESS	4	RTSDSTRB	Pointer to the beginning of the STRLIST structure and range table - used by SDUMP only
32	(20)	ADDRESS	4	RTSDSTRE	Pointer to the last byte of the STRLIST structure and range table
36	(24)	ADDRESS	4	RTSDCRSA	Pointer to the CRSA passed as input to IXLXLFXR
40	(28)	ADDRESS	4	RTSDDBUF	Pointer to data buffer passed as input to IXLXLFXR
44	(2C)	ADDRESS	4	RTSDXADR	Address of RTSD extension
48	(30)	UNSIGNED	2	RTSDCNTT	ABDUMP contention detection interval value (sec)
50	(32)	CHARACTER	2	*	Reserved
52	(34)	UNSIGNED	4	RTSDHDCT	Hung Sdump count
56	(38)	BITSTRING	8	RTSDFHDT	Time Stamp for first hung dump
64	(40)	ADDRESS	4	RTSDSD3R	IEAVTSD3/4 RMTR address
68	(44)	SIGNED	2	RTSDHAID	ASID OF THE CALLERS HOME ADDRESS SPACE
70	(46)	SIGNED	2	*	RESERVED
72	(48)	CHARACTER	8	RTSDXPSW	CROSS MEMORY PSW ON ENTRY TO SDUMP
80	(50)	CHARACTER	8	*	Reserved. Was RTSDRSAD
88	(58)	ADDRESS	4	RTSDASCB	ASCB ADDR OF ADDRESSABLE ADDRESS SPACE OF SDUMP CALLER ON ENTRY
92	(5C)	ADDRESS	4	RTSDCMLA	ASCB ADDR OF ASID WHOSE CML LOCK IS HELD

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
96	(60)	SIGNED	4	RTSDZDCT	COUNT OF DUMPED ZERO PAGE SUPPRESSION RECORDS
100	(64)	CHARACTER	4	*	Reserved
104	(68)	ADDRESS	4	RTSDMTRB	IEAVTSRB RMTR addr
108	(6C)	ADDRESS	4	RTSDRTRN	COMMON SDUMP RETURN SAVE
112	(70)	ADDRESS	4	RTSDCTCB	ADDRESS OF CALLERS TCB
116	(74)	ADDRESS	4	RTSDCSAV	ADDRESS OF THE SDUMP CALLERS SAVE AREA
120	(78)	ADDRESS	4	RTSDCSDW	ADDRESS OF CALLERS SDWA
124	(7C)	CHARACTER	4	RTSDCSW1	Fullword holding ASID/flags
124	(7C)	CHARACTER	2	RTSDASID	ASID OF CALLERS SDWA
126	(7E)	BITSTRING	2	RTSDFLG1	WORKING FLAGS FOR SVC DUMP
		1...		RTSDASCK	USED TO CAUSE THE SDADASID SUBROUTINE TO ONLY CHECK AN ASID AND NOT ADD IT TO THE ASID TABLE.
		.1..		RTSDSPGF	0 - PROCESS LOCAL SUBPOOLS 1 - PROCESS GLOBAL SUBPOOLS
		..1.		RTSDISPR	1 - IEAVTSPR IS IN CONTROL 0 - SPR NOT IN CONTROL
		...1		RTSDASTF	ADDRESS SPACE TABLE FLAG TELLS SUBROUTINE SDADASID WHETHER TO TURN ON THE RTSDDMPA FLAG FOR AN ASID. 1 - DO NOT TURN ON THE DMPA FLAG 0 - TURN ON THE DMPA FLAG
		.... 1...		RTSDAMOD	ADDRESS MODE OF SDUMP CALLER 1 - AMODE 31 0 - AMODE 24
		.... .1..		RTSDASDW	THE COPIED SDWA IS USABLE
		.... ..1.		RTSDVCPU	ALL CPUS ARE VALID AND NOT VM
		1		RTSDRSCD	1 - RSM SERIALIZATION MUST BE OBTAINED IN MODULE IEAVTSDX. 0 - RSM SER. CAN BE OBTAINED IN IEAVTSSD.
127	(7F)	1...		RTSDDOTR	1 - DO THE SNAPTRC AT THE FIRST AVAILABLE MOMENT. FOR SCHEDULED DUMPS ONLY.
		.1..		RTSDSKIP	1 - SKIP FULL SRB PROCESSING FOR SUCCESSFUL TRACE SRB.
		..1.		RTSDSDSC	1 - SDS IS COMPLETE
		...1		RTSDTERM	1 - AD00 HAS TERMINATED
		.... 1...		RTSDDYNF	Used by IEAVTS3F to indicate if any IEAVTSD3 was unable to establish a dynamic area
		.... .1..		RTSDGDSC	If '1'B then IEAVTSDS is in the processes of storing information about the Global Data Space in the DPL
		.... ..1.		RTSDSD2S	SD2 SRB has been scheduled
		1		RTSD2SD2	SD2 invoked second time
128	(80)	BITSTRING	1	RTSDFLG2	Flag byte 2
		1...		RTSDSNRQ	1 - SNAPTRC requested
		.1..		RTSDONLYSNAP_INPROGRESS	1 - The "OnlySnapTrc" processing in IEAVTSD2 is in progress
		..1.		RTSDF2R2	Reserved
		...1		RTSDF2R3	Reserved
		.... 1...		RTSDF2R4	Reserved
		.... .1..		RTSDF2R5	Reserved
		.... ..1.		RTSDF2R6	Reserved
		1		RTSDF2R7	Reserved
129	(81)	UNSIGNED	1	*	RESERVED
130	(82)	SIGNED	2	RTSDFNCD	FUNCTION CODE PASSED TO IEAVTSPR 1 - COPYPARM 2 - VALDCB 3 - SDSUBPL 4 - SDASIDS
132	(84)	CHARACTER	8	RTSDRSV1	RESERVED
140	(8C)	CHARACTER	3260	RTSDDATA	USER DATA FIELD WHICH IS ZEROED AT THE START OF EACH DUMP.
140	(8C)	CHARACTER	16	RTSDSES	Data related to dumping the STRLIST
140	(8C)	UNSIGNED	2	RTSDSTR#	Number of structures in the STRLSIT structure table
142	(8E)	UNSIGNED	2	*	Reserved
144	(90)	SIGNED	4	RTSDSTRI	Index for looping through SFDPL table when doing conversions. Set by IEAVTSDS, checked by SDSESTAE
148	(94)	CHARACTER	8	*	Reserved
156	(9C)	CHARACTER	36	RTSDSRR	Work area used by SDESTAEX in SRR
156	(9C)	ADDRESS	4	RTSD14S1	Save return address
160	(A0)	ADDRESS	4	RTSDSDWA	Save pointer to SDWA
164	(A4)	SIGNED	4	RTSDABND	Save Abend code
168	(A8)	ADDRESS	4	RTSD14S2	Save 2nd return address
172	(AC)	CHARACTER	20	RTSDDEQ	Storage for DEQ macro
172	(AC)	CHARACTER	16	RTSDPOST	Storage for POST macro
192	(C0)	CHARACTER	4	RTSDDIND	RTSDDATA scope indicators
192	(C0)	CHARACTER	1	RTSDDFL0	RTSDDATA area scope flags
		1...		RTSDL64T	0=the LISTD table contains LISTD type entries. 1=the LISTD table contains LIST64 type entries
		.111 1111		*	Reserved
193	(C1)	CHARACTER	3	*	Reserved
196	(C4)	CHARACTER	132	*	Reserved
328	(148)	CHARACTER	32	RTSDSCNV	IARSSCNV parameter list
328	(148)	CHARACTER	32	*	Error if parmlist grows to more than 32 bytes long
360	(168)	SIGNED	4	RTSDDSL1	SIZE OF 1ST A.S. IN SUMMARY DUMP DATA SPACE
364	(16C)	SIGNED	4	RTSDDSPL	SIZE OF ALL OTHER A.S. IN SUMMARY DUMP DATA SPACE
368	(170)	ADDRESS	4	RTSDSPGL	PTR TO GLOBAL SUBPOOL LIST
372	(174)	CHARACTER	192	RTSDREUS	REUSABLE WORK AREA

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
372	(174)	ADDRESS	4	RTSDLSTN	ADDR OF NEXT FREE LISTA TABLE ENTRY
372	(174)	ADDRESS	4	RTSDSVSD	SAVE AREA FOR ADDRESS OF SDWA ACROSS CALL TO SDXFRSUM.
376	(178)	ADDRESS	4	RTSDLEND	ADDRESS OF END OF THE LISTA AREA
376	(178)	ADDRESS	4	RTSDSVFR	SAVE AREA USED BY SCHFRR TO SAVE THE RETURN ADDRESS TO RTM.
380	(17C)	SIGNED	2	RTSDIDX1	SVC DUMP WORKING INDEX ONE
382	(17E)	SIGNED	2	RTSDIDX2	SVC DUMP WORKING INDEX TWO
384	(180)	ADDRESS	4	RTSDPTR1	SVC DUMP WORKING POINTER
384	(180)	ADDRESS	4	RTSDFR6W	FRR 6 WORD WORK AREA ADDRESS
388	(184)	ADDRESS	4	RTSDPTR2	SVC DUMP WORKING POINTER
392	(188)	ADDRESS	4	RTSDENDA	ADDRESS OF END OF A USER SUPPLIED DATA AREA.
396	(18C)	ADDRESS	4	RTSDLPRN	TEMP PTR INTO USER DATA AREA
400	(190)	SIGNED	4	RTSDRTN1	REG 14 SAVE AREA
404	(194)	SIGNED	4	RTSDRTN2	REG 14 SAVE AREA
408	(198)	ADDRESS	4	RTSDSPPT	WORK PTR FOR PROCESSING THE USER SUPPLIED SUBPOOL LIST
412	(19C)	SIGNED	4	RTSDRSV3	RESERVED
416	(1A0)	SIGNED	4	RTSDRSV4	RESERVED
420	(1A4)	CHARACTER	72	RTSDSDXS	REGISTER SAVE AREA FOR IEAVTSDX PROCESSING
492	(1EC)	CHARACTER	72	RTSDSRBS	REGISTER SAVE AREA FOR IEAVTSRB PROCESSING
564	(234)	CHARACTER	484	RTSDLTBL	STORAGE RANGE TABLE USED FOR STORAGE, LIST, AND LISTA.
1048	(418)	CHARACTER	200	RTSDSUBL	SUBPOOL LISTS. RTSDSPGL AND RTSDSPLC POINT INTO THIS AREA.@G860P31
1248	(4E0)	CHARACTER	16	RTSDKEYS	COPY OF USER KEY LIST
1248	(4E0)	UNSIGNED	1	RTSDKEYC	COUNT OF NUMBER OF KEYS
1249	(4E1)	UNSIGNED	1	RTSDKEY (4294967311:562127960)	ARRAY OF KEYS
1264	(4F0)	CHARACTER	64	*	Was RTSDCRGS
1328	(530)	CHARACTER	64	RTSDARGS	CALLERS ACCESS REGISTERS
1328	(530)	UNSIGNED	4	RTSDAREG (15:562127960)	ACCESS REGISTERS 0 - 15
1392	(570)	CHARACTER	16	RTSDXP16	16-byte XM PSW on entry to SDUMP
1408	(580)	CHARACTER	24	*	Reserved
1432	(598)	CHARACTER	100	RTSDAFPR	Additional FP regs
1432	(598)	CHARACTER	8	RTSDFPR1	FPR 1
1440	(5A0)	CHARACTER	8	RTSDFPR3	FPR 3
1448	(5A8)	CHARACTER	8	RTSDFPR5	FPR 5
1456	(5B0)	CHARACTER	8	RTSDFPR7	FPR 7
1464	(5B8)	CHARACTER	8	RTSDFPR8	FPR 8
1472	(5C0)	CHARACTER	8	RTSDFPR9	FPR 9
1480	(5C8)	CHARACTER	8	RTSDFP10	FPR 10
1488	(5D0)	CHARACTER	8	RTSDFP11	FPR 11
1496	(5D8)	CHARACTER	8	RTSDFP12	FPR 12
1504	(5E0)	CHARACTER	8	RTSDFP13	FPR 13
1512	(5E8)	CHARACTER	8	RTSDFP14	FPR 14
1520	(5F0)	CHARACTER	8	RTSDFP15	FPR 15
1528	(5F8)	CHARACTER	4	RTSDFPCR	FP Control Reg
1532	(5FC)	ADDRESS	4	RTSDRSDW	Address of RTM's SDWA
1536	(600)	CHARACTER	64	RTSDG64H	Bits 0-31 of GPRs
1600	(640)	CHARACTER	101	RTSDHDRD	DUMP HEADER DATA
1600	(640)	UNSIGNED	1	RTSDHDRL	LENGTH OF HEADER
1601	(641)	CHARACTER	100	RTSDHDR	USER SUPPLIED DUMP TITLE
1701	(6A5)	CHARACTER	3	*	RESERVED
1704	(6A8)	ADDRESS	4	RTSDPADR	ADDRESS OF THE SDUMP PARAMETER LIST
1708	(6AC)	CHARACTER	51	RTSDCIDD	CALLER'S ID DATA
1708	(6AC)	UNSIGNED	1	RTSDCIDL	LENGTH OF ID
1709	(6AD)	CHARACTER	50	RTSDCID	CALLER'S ID
1759	(6DF)	CHARACTER	57	*	RESERVED
1816	(718)	CHARACTER	320	RTSDXATB	EXTENSION ASID TABLE - CONTAINS CONTROL BITS, LISTA AND LISTD POINTERS BY ASID
1816	(718)	CHARACTER	20	RTSDPTRT (4294967312:562144472)	TABLE HAS 16 ENTRIES AND IS INDEXED USING THE RTCTINDX FIELD
1816	(718)	BITSTRING	4	RTSDTCTL	CONTROL AND DATA BITS FOR ASID
		1...		RTSDDMPA	INDICATE THIS ADDRESS SPACE IS TO BE DUMPED FOR REQUESTED SDATA OPTIONS.
		.1..		RTSDTCT1	Reserved
		..1.		RTSDTCT2	Reserved
		...1		RTSDTCT3	Reserved
		.... 1...		RTSDLSRB	Indicate IEAMSCHD used to reset tasks dispatchable in this a/s
1820	(71C)	ADDRESS	4	RTSDTPTR	POINTER TO RANGES FOR THIS ASID
1824	(720)	ADDRESS	4	RTSDSPLC	POINTER TO LOCAL SUBPOOL LIST
1828	(724)	ADDRESS	4	RTSDLDAS	POINTER TO LISTD ADDRESS SPACE RANGES
1832	(728)	ADDRESS	4	RTSDLDDS	POINTER TO LISTD DATA SPACE RANGES
2136	(858)	ADDRESS	4	RTSDSHLE	Address of currently used last entry in the shared high virtual range table
2140	(85C)	CHARACTER	4	*	RESERVED
2144	(860)	ADDRESS	4	RTSDLDNX	INDEX INTO LISTD TABLE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
2148	(864)	CHARACTER	64	RTSDGRS	GRS PASSED FROM AD00 INTO SDH FOR SINGLE AS DUMP
2212	(8A4)	CHARACTER	8	RTSDMODN	MODULE NAME FROM AD00 INTO SDH FOR SINGLE AS DUMP
2220	(8AC)	CHARACTER	32	RTSDFPRS	FPRS PASSED FROM AD00 INTO SDH FOR SINGLE AS DUMP
2252	(8CC)	CHARACTER	220	RTSDSR	AREA FOR STOP/RESET INFO THE RESET AREAS ARE SERIALIZED BY THE RTSDRSET BIT. ONLY THE SETTER OF THE BIT CAN USE THE FIELDS.
2252	(8CC)	CHARACTER	4	RTSDSRLS	SAVE CR15 - LINKAGE STACK
2256	(8D0)	CHARACTER	40	RTSDSRPL	STOP/RESET PARAMETER LIST
2296	(8F8)	CHARACTER	64	RTSDSRRG	AREA TO SAVE REGS
2360	(938)	CHARACTER	8	RTSDSPSW	PSW FOR RESET
2368	(940)	CHARACTER	72	RTSDSRSA	SAVE AREA FOR CALLING S/R
2440	(988)	CHARACTER	16	RTSDSRXM	XM control regs
2456	(998)	CHARACTER	12	RTSDSROA	STOP/RESET OUTPUT AREA
2468	(9A4)	BITSTRING	4	RTSDSRFL	FLAGS
		1...		RTSDSTOC	STOP DONE
		.1..		RTSDRSET	RESET DONE, C/S SERIALIZED
		..1.		RTSDSTOS	STOP PROCESS STARTED
2468	(9A4)	BITSTRING	3	*	RESERVED
2472	(9A8)	CHARACTER	16	RTSDCMSV	CMSET savearea
2488	(9B8)	CHARACTER	400	RTSDSLRP	SLIP reg/PSW area. Must map same as SCVADPLS
2488	(9B8)	CHARACTER	8	RTSDSLPW	SLIP TRAP PSW
2496	(9C0)	CHARACTER	8	*	Was RTSDSLC3/C4
2504	(9C8)	CHARACTER	4	RTSDSLRG (4294967312:562150960)	SLIP TRAP REGS
2568	(A08)	CHARACTER	4	RTSDSLAR (4294967312:562150960)	SLIP TRAP ACCESS REGS
2632	(A48)	CHARACTER	64	*	Was RTSDSLCR
2632	(A48)	CHARACTER	8	RTSDSLC3	SLIP CR3 (SASID)
2640	(A50)	CHARACTER	8	RTSDSLC4	SLIP CR4 (PASID)
2648	(A58)	CHARACTER	16	RTSDSP16	16-byte SLIP trap PSW
2696	(A88)	CHARACTER	64	RTSDSLG6	SLIP G64H
2760	(AC8)	CHARACTER	128	RTSDC64S	ESAME CRs
2760	(AC8)	CHARACTER	8	*	CONTROL REGISTER 0
2768	(AD0)	CHARACTER	56	RTSDXMST	CROSS MEMORY STATUS
2768	(AD0)	CHARACTER	8	RTSDXCR1	CONTROL REGISTER ONE
2768	(AD0)	CHARACTER	8	RTSDPSTO	PRIMARY STO VALUE
2776	(AD8)	CHARACTER	8	*	CONTROL REGISTER 2
2784	(AE0)	CHARACTER	8	RTSDXCR3	CONTROL REGISTER 3
2784	(AE0)	UNSIGNED	4	RTSDSINS	SASTE IN
2788	(AE4)	UNSIGNED	2	RTSDXAKM	AUTHORIZATION KEY MASK
2790	(AE6)	UNSIGNED	2	RTSDSAID	SECONDARY ASID
2792	(AE8)	CHARACTER	8	RTSDXCR4	CONTROL REGISTER 4
2792	(AE8)	UNSIGNED	4	RTSDPINS	PASTE IN
2796	(AEC)	UNSIGNED	2	RTSDXMAI	AUTHORIZATION INDEX
2798	(AEE)	UNSIGNED	2	RTSDPAID	PRIMARY ASID
2800	(AF0)	CHARACTER	8	RTSDXCR5	CONTROL REGISTER 5
2808	(AF8)	CHARACTER	8	*	CONTROL REGISTER 6
2816	(B00)	CHARACTER	8	RTSDXCR7	CONTROL REGISTER 7
2816	(B00)	CHARACTER	8	RTSDSSTO	SECONDARY STO
2824	(B08)	CHARACTER	8	RTSDCRG8	CR 8
2824	(B08)	CHARACTER	4	*	Unused
2828	(B0C)	UNSIGNED	2	RTSDCEAX	EAX
2830	(B0E)	CHARACTER	2	*	Unused
2832	(B10)	CHARACTER	48	*	CRS 9-14
2880	(B40)	CHARACTER	8	RTSDCRGF	CONTROL REG 15
2888	(B48)	CHARACTER	512	RTSDZ1	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8	RTSDEXIT (*)	SDUMP EXIT TABLE
0	(0)	CHARACTER	4	RTSDEXFL	EXIT FLAGS
0	(0)	BITSTRING	2	RTSDEXMS	EXIT MASK USED TO IDENTIFY THE EXIT WITH AN SDUMP PARAMETER
2	(2)	CHARACTER	1	*	RESERVED
3	(3)	BITSTRING	1	RTSDEXAT	EXIT ATTRIBUTES
		1...		RTSDEXLC	ON FOR LOCAL EXIT
		.1..		RTSDEXGB	ON FOR GLOBAL EXIT
		..1.		RTSDEXSD	ON FOR SDUMP EXIT
		...1		RTSDEXSM	ON FOR SYSMDUMP EXIT
		.... 1...		RTSDEXON	ON FOR ONE TIME ONLY EXIT
		.... .1..		RTSDXDFP	ON FOR DFP EXIT - CHECK THAT SMSX IS INSTALLED
		.... ..1.		RTSDEXEG	ON FOR EARLY GLOBAL EXIT
		1		RTSDEXNC	ON FOR NUCLEUS RESIDENT EXIT

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
4	(4)	ADDRESS	4	RTSDEXAD	EXIT LOAD MODULE ADDRESS

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8	RTSDRANG	ADDRESS RANGE FOR LISTA
0	(0)	ADDRESS	4	RTSDBADR	BEGINNING ADDRESS
4	(4)	ADDRESS	4	RTSDEADR	ENDING ADDRESS

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	RTSDSPLS	SUBPOOL LIST FOR AN ASID OR A GLOBAL LIST
0	(0)	SIGNED	2	RTSDSPCT	COUNT OF SUBPOOLS TO FOLLOW
2	(2)	UNSIGNED	2	RTSDSPNM (*)	SUBPOOL NUMBERS

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	32	SCNVPL	?IARSSCNV PARM LIST
0	(0)	UNSIGNED	1	SCNVPL_XVERSION	INPUT XVERSION
1	(1)	UNSIGNED	1	SCNVPL_XSERVICE	XSERVICE
2	(2)	BITSTRING	1	SCNVPL_XFLAGS1	FIELD_LABEL
		1...		SCNVPL_VERIFY_YES	BIT
		.1..		SCNVPL_KEYUSED_NAME	BIT
		..11 1111		SCNVPL_XFLAGS1_RSVD1	BIT
3	(3)	BITSTRING	1	SCNVPL_XFLAGS2	XFLAGS2
4	(4)	SIGNED	4	SCNVPL_XASTE	XASTE
8	(8)	UNSIGNED	4	SCNVPL_XASTESQN	XASTESQN
12	(C)	SIGNED	4	SCNVPL_XASCB	XASCB
16	(10)	CHARACTER	8	SCNVPL_XSTOKEN	XSTOKEN
24	(18)	CHARACTER	8	SCNVPL_XNAME	XNAME
32	(20)	CHARACTER	0	*	

RTSD Constants

Len	Type	Value	Name	Description
4	CHARACTER	RTSD	RTSDID1	ACRONYM RTSD
4	HEX	40000000	RTSDRSON	TURN ON RTSDRSET
4	HEX	BFFFFFFF	RTSDRSOF	TURN OFF RTSDRSET
2	DECIMAL	240	RTSDABCT	Initial ABDUMP contention detection interval value (sec)
1	DECIMAL	1	SCNVPL_TOASTE	XSERVICE
1	DECIMAL	2	SCNVPL_FROMASTE	XSERVICE Subspace convert service parameter list

RTSD Cross Reference

Name	Hex Offset	Hex Value
RTSD	0	
RTSDABND	A4	
RTSDAFPR	598	
RTSDALLD	A	
RTSDAMOD	7E	08
RTSDAREG	530	
RTSDARGS	530	
RTSDASCB	58	
RTSDASCK	7E	80
RTSDASDW	7E	04
RTSDASID	7C	
RTSDASTF	7E	10
RTSDBADR	0	
RTSDCEAX	B0C	
RTSDCID	6AD	
RTSDCIDD	6AC	
RTSDCIDL	6AC	
RTSDCMLA	5C	
RTSDCMSV	9A8	
RTSDCNTT	30	
RTSDCRGF	B40	
RTSDCRG8	B08	
RTSDCRSA	24	
RTSDCSAV	74	
RTSDCSDW	78	
RTSDCSW1	7C	
RTSDCTCB	70	
RTSDC64S	AC8	
RTSDDATA	8C	
RTSDDBUF	28	
RTSDDEQ	AC	
RTSDDFL0	C0	
RTSDDIND	C0	
RTSDDMPA	718	80
RTSDDNUM	8	
RTSDDOTR	7F	80
RTSDDPPB	10	
RTSDDPPF	C	
RTSDDSL1	168	
RTSDDSPL	16C	
RTSDDYNF	7F	08
RTSDEADR	4	
RTSDENDA	188	
RTSDEXAD	4	
RTSDEXAT	3	
RTSDEXEG	3	02
RTSDEXFL	0	
RTSDEXGB	3	40
RTSDEXIT	0	
RTSDEXLC	3	80
RTSDEXMS	0	
RTSDEXNC	3	01
RTSDEXON	3	08
RTSDEXSD	3	20
RTSDEXSM	3	10
RTSDEXTB	4	
RTSDFHDT	38	
RTSDFLG1	7E	
RTSDFLG2	80	
RTSDFNCD	82	
RTSDFPCR	5F8	
RTSDFPRS	8AC	
RTSDFPR1	598	
RTSDFPR3	5A0	
RTSDFPR5	5A8	
RTSDFPR7	5B0	
RTSDFPR8	5B8	
RTSDFPR9	5C0	
RTSDFP10	5C8	
RTSDFP11	5D0	
RTSDFP12	5D8	
RTSDFP13	5E0	
RTSDFP14	5E8	
RTSDFP15	5F0	
RTSDFR6W	180	
RTSDF2R2	80	20
RTSDF2R3	80	10
RTSDF2R4	80	08
RTSDF2R5	80	04
RTSDF2R6	80	02
RTSDF2R7	80	01
RTSDGDSC	7F	04
RTSDGRS	864	
RTSDG64H	600	
RTSDHAID	44	
RTSDHDCT	34	
RTSDHDR	641	
RTSDHDRD	640	
RTSDHDRL	640	
RTSDID	0	
RTSDIDX1	17C	
RTSDIDX2	17E	
RTSDISPR	7E	20
RTSDKEEP	0	
RTSDKEY	4E1	
RTSDKEYC	4E0	
RTSDKEYS	4E0	
RTSDLDAS	724	
RTSDLDDS	728	
RTSDLDNX	860	
RTSDLEND	178	
RTSDLPRN	18C	
RTSDLSRB	718	08
RTSDLSTN	174	
RTSDLTBL	234	
RTSDL64T	C0	80
RTSDMODN	8A4	
RTSDMTRB	68	
RTSDONLYSNAP_INPROGRESS	80	40
RTSDPADR	6A8	
RTSDPAID	AEE	
RTSDPINS	AE8	
RTSDPOST	AC	
RTSDPSTO	AD0	
RTSDPTRT	718	
RTSDPTR1	180	
RTSDPTR2	184	
RTSDRANG	0	
RTSDREUS	174	
RTSDRSCD	7E	01
RTSDRSDW	5FC	
RTSDRSET	9A4	40
RTSDRSV1	84	
RTSDRSV3	19C	
RTSDRSV4	1A0	
RTSDRTN1	190	
RTSDRTN2	194	
RTSDRTRN	6C	
RTSDSAID	AE6	
RTSDSCNV	148	
RTSDSDSC	7F	20
RTSDSDWA	A0	
RTSDSDXS	1A4	
RTSDSD2S	7F	02
RTSDSD3R	40	
RTSDSES	8C	
RTSDSHLE	858	
RTSDSINS	AE0	
RTSDSKIP	7F	40
RTSDSLAR	A08	
RTSDSLC3	A48	
RTSDSLC4	A50	
RTSDSLG6	A88	

Name	Hex Offset	Hex Value
RTSDSLPW	9B8	
RTSDSLRG	9C8	
RTSDSLRP	9B8	
RTSDSNRQ	80	80
RTSDSPCT	0	
RTSDSPGF	7E	40
RTSDSPGL	170	
RTSDSPLC	720	
RTSDSPLS	0	
RTSDSPNM	2	
RTSDSPPT	198	
RTSDSPSW	938	
RTSDSP16	A58	
RTSDSR	8CC	
RTSDSRBS	1EC	
RTSDSRFL	9A4	
RTSDSRLS	8CC	
RTSDSROA	998	
RTSDSRPL	8D0	
RTSDSRR	9C	
RTSDSRRG	8F8	
RTSDSRSA	940	
RTSDSRXM	988	
RTSDSSTO	B00	
RTSDSTKN	14	
RTSDSTOC	9A4	80
RTSDSTOS	9A4	20
RTSDSTR#	8C	
RTSDSTRB	1C	
RTSDSTRE	20	
RTSDSTRI	90	
RTSDSUBL	418	
RTSDSVFR	178	
RTSDSVNM	8	
RTSDSVSD	174	
RTSDTCTL	718	
RTSDTCT1	718	40
RTSDTCT2	718	20
RTSDTCT3	718	10
RTSDTERM	7F	10
RTSDTPTR	71C	
RTSDVCPU	7E	02
RTSDXADR	2C	
RTSDXAKM	AE4	
RTSDXATB	718	
RTSDXCR1	AD0	
RTSDXCR3	AE0	
RTSDXCR4	AE8	
RTSDXCR5	AF0	
RTSDXCR7	B00	
RTSDXDFP	3	04
RTSDXMAI	AEC	
RTSDXMST	AD0	
RTSDXPSW	48	
RTSDXP16	570	
RTSDZDCT	60	
RTSDZ1	B48	
RTSD14S1	9C	
RTSD14S2	A8	
RTSD2SD2	7F	01
SCNVPL	0	
SCNVPL_KEYUSED_NAME	2	40
SCNVPL_VERIFY_YES	2	80
SCNVPL_XASCB	C	
SCNVPL_XASTE	4	
SCNVPL_XASTESQN	8	
SCNVPL_XFLAGS1	2	
SCNVPL_XFLAGS1_RSVD1	2	3F
SCNVPL_XFLAGS2	3	
SCNVPL_XNAME	18	
SCNVPL_XSERVICE	1	
SCNVPL_XSTOKEN	10	
SCNVPL_XVERSION	0	

RT1W Information

RT1W Heading Information

Common Name: RTM1 Work Area
Macro ID: IHART1W
DSECT Name: RT1W, RT1TRACK, RT1TRECC, RTMW
Owning Component: RECOVERY TERMINATION MANAGER (SCRTM)
Eye-Catcher ID: none
Storage Attributes: Subpool: 239 or in PSA
Key: 0
Residency: ABOVE OR BELOW THE 16M LINE
Size: 192 bytes for RT1W
464 bytes for RT1X
Created by: IEAVNIP0 or IEEVCPU
Pointed to by: FRRSRTMA field of the FRRS data area
Serialization: RTM1 INTERNAL SERIALIZATION
Function: The RT1W is used to describe the current error condition and to provide an internal work area for the RTM1 subfunctions.

RT1W Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	192	RT1W	THE RTM1 WORK AREA
0	(0)	CHARACTER	120	RT1WTRTM	PART OF RT1W USED FROM IEAVTRTM ON
0	(0)	CHARACTER	44	RT1WNPRS	DATA NOT PRESERVED ON VALID ANTICIPATED RECURSION
0	(0)	SIGNED	4	RT1WLPTA (4294967302:562135400)	TRACKING AREA FOR LOGICAL PHASE RECOVERY PROCESSING MAPPED BY RT1TRACK BELOW
24	(18)	ADDRESS	4	RT1WVARI (4294967301:562135232)	VARIABLE FIELDS IN WA
44	(2C)	CHARACTER	76	RT1WPRSV	DATA TO BE PRESERVED ON VALID ANTICIPATED RECURSION
44	(2C)	ADDRESS	4	RT1WRTCA	POINTR TO THE SDWA CURNTLY IN USE (USED BY RTS)
48	(30)	ADDRESS	4	RT1WEED	POINTER TO EEDS ACQUIRED
52	(34)	SIGNED	4	RT1WENTR	ENTRY POINT DATA
52	(34)	BITSTRING	1	RT1WMODE	SYSTEM MODE AT TIME OF ERROR (SEE MODEBYTE AT THE END OF RT1W FOR A DESCRIPTION OF THIS BYTE)
53	(35)	BITSTRING	1	RT1WSRMD	SYSTEM RECOVERY MODE
54	(36)	BITSTRING	1	RT1WCOVR	PRESERVED CARRY OVER INFORMATION ON VALID RECURSIONS
		1...		RT1WRCDR	RECURSION OCCURRED IN RECORD
		.1..		RT1WRTM	IF ON, INDICATES RTM'S FRR WAS IN CONTROL AT THE TIME OF THE ERROR
		..1.		RT1WFAIL	IF ON, RTM1 ISSUED A CMSET MACRO WHICH EITHER ISSUED ABEND OR RETURNED A NON ZERO RETURN CODE
		...1		RT1WRMGR	IF ON, RTM1 IS PROCESSING THE FRR STACK ON BEHALF OF A CALLRTM TYPE=RMGRCML
		.... 1...		RT1WEREX	USED IN EEDPROC TO INDICATE AN ERRORID HAS BEEN PLACED IN AN EED. IT IS SET OFF BEFORE EXITING FROM EEDPROC
		.... .1..		RT1WGFAI	IF ON, RTS ATTEMPTED TO ACQUIRE AN SDWA FROM SQA BUT THE GETMAIN REQUEST WAS UNSUCCESSFUL
		.... ..1.		RT1WGLBL	IF ON, GLOBAL FRRS ARE BEING PROCESSED FOR A DAT ERROR ON THE NORMAL STACK
		1		RT1WRCRD	SET BY SLIP FOR ACTION= RECORD. RTM1 MUST RECORD
55	(37)	UNSIGNED	1	RT1WLPN	INITIAL LOGICAL PHASE NUMBER ON ENTRY TO RTM
56	(38)	ADDRESS	4	RT1WASCB	ASCB ADDR OF CML ADDRESS SPACE
60	(3C)	CHARACTER	4	RT1WENT2	ENTRY POINT DATA
60	(3C)	BITSTRING	1	RT1WCOV2	PRESERVED CARRY OVER INFORMATION ON VALID RECURSIONS. THIS BYTE IS CHECK POINTED FOR NESTED FRR RETRY
		1...		RT1NODMP	SET BY SLIP TO INFORM DUMPING PROGRAMS THAT DUMP REQUESTS SHOULD BE IGNORED FOR THIS INVOCATION OF RTM
		.1..		RT1WNOSP	SET BY SLIP TO INFORM DUPLICATE DUMP SUPPRESSION THAT DUMP REQUESTS SHOULD NOT BE SUPPRESSED FOR THIS INVOCATION OF RTM
		..1.		RT1WNOSV	SET BY SLIP TO INFORM DUMPING PROGRAMS THAT SVCDUMP REQUESTS SHOULD BE IGNORED FOR THIS INVOCATION OF RTM
		...1		RT1WNOSA	SET BY SLIP TO INFORM DUMPING PROGRAMS THAT SYSABEND DUMP REQUESTS SHOULD BE IGNORED FOR THIS INVOCATION OF RTM
		.... 1...		RT1WNOSM	SET BY SLIP TO INFORM DUMPING PROGRAMS THAT SYSMDUMP REQUESTS SHOULD BE IGNORED FOR THIS INVOCATION OF RTM
		.... .1..		RT1WNOSU	SET BY SLIP TO INFORM DUMPING PROGRAMS THAT SYSUDUMP REQUESTS SHOULD BE IGNORED FOR THIS INVOCATION OF RTM
61	(3D)	BITSTRING	1	RT1WCOV3	FLAGS. THIS BYTE IS CHECK POINTED FOR NESTED FRR RETRY

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1...		RT1WSKIP	IF ON, AT LEAST ONE FRR IN THE PERCOLATION PATH HAS BEEN SKIPPED
		.1..		RT1WSLST	IF ON, AT LEAST ONE FRR HAS SUPPLIED DUMP RANGES TO RTS OR DUMP RANGES WERE SUPPLIED BY THE ISSUER OF ABEND
		..1.		RT1WCPOB	IF ON, CPU LOCK OBTAINED BY RTM1 TO SERIALIZE FRR STACK
		...1 1...		RT1WASC	PSW ASC value at time of error
62	(3E)	BITSTRING	1	RT1WCOV4	FLAGS.
		1...		RT1WRSTW	IF ON, RTS HAS ALREADY CLEARED THE CVT RESTART WORD (CVTRSTWD)
		.1..		RT1WNCNL	COPY OF STCBNCNL - CANCEL/ DETACH PROTECTION DURING RECOVERY ROUTINE
		..1.		RT1WCLUP	CLEANUP AND PERCOLATE INDICATION
		...1		RT1WNOAB	Copy of STCBNOAB
63	(3F)	CHARACTER	1	RT1WRSV	RESERVED
64	(40)	BITSTRING	4	RT1WLKMB	MASK OF LOCKS HELD
68	(44)	BITSTRING	4	RT1WLKFR	MASK OF LOCKS TO BE FREED
		1...		RT1WFCPU	CPU LOCK IS TO BE FREED
68	(44)	BITSTRING	3	*	
71	(47)	.... ..1.		RT1WFCMS	CMS LOCK IS TO BE FREED
		1		RT1WFLCL	LOCAL LOCK IS TO BE FREED
72	(48)	SIGNED	4	RT1WCPUB	COUNT OF CPU LOCKS HELD
76	(4C)	SIGNED	4	RT1WCPUN	COUNT OF CPU LOCKS TO BE FREED
80	(50)	CHARACTER	8	RT1WCCRC	STRUCTURE FOR NEXT 2 WORDS
80	(50)	CHARACTER	4	RT1WABCC	SAVED VERSION OF COMPCODE CODE AND FLAGS
80	(50)	CHARACTER	1	RT1WCMPF	FLAG BITS IN COMPLETION CODE.
		1...		RT1WREQ	ON, SYSABEND/SYSMDUMP/SYSUDUMP DUMP TO BE GIVEN. SET IF DUMP=YES REQUESTED ON ABEND, CALLRTM, OR SETRP MACRO.
		.1..		RT1WSTEP	ON, JOBSTEP TO BE TERMINATED.SET IF STEP OPTION SPECIFIED ON ABEND MACRO.
		..11 1...		*	RESERVED
		.... .1..		RT1WRCF	ON, REASON CODE SUPPLIED
		.... ..1.		RT1WRTYN	ON, RETRY=NO SPECIFIED
		1		*	RESERVED
81	(51)	CHARACTER	3	RT1WCPMC	SAVED VERSION OF COMPCODE
84	(54)	CHARACTER	4	RT1WCRC	SAVED VERSION OF REASON CODE
88	(58)	ADDRESS	4	RT1WGFRR	ADDRESS OF THE LAST FRR ROUTED TO BY RTM1
92	(5C)	CHARACTER	4	RT1WSINF	SDWA INFORMATION
92	(5C)	UNSIGNED	1	RT1WNGLB	NUMBER OF GLOBAL SDWAS ON RECURSION CHAIN
93	(5D)	UNSIGNED	1	RT1WNSQA	NUMBER OF SQA SDWAS ON RECURSION CHAIN
94	(5E)	UNSIGNED	1	RT1WRTYP	TYPE OF SDWA POINTED TO BY RT1WRTCA
96	(60)	ADDRESS	4	RT1WOFRR	ADDRESS OF THE FIRST FRR FOR THIS RECOVERY ENVIRONMENT
100	(64)	ADDRESS	4	RT1WRT1I	ADDRESS OF CHECK POINT SDWA ELEMENT
104	(68)	UNSIGNED	1	RT1WRRPI	INDICATOR OF RETRY/RESUME/ PERCOLATION
105	(69)	CHARACTER	3	*	RESERVED
108	(6C)	BITSTRING	4	RT1WLMB2	MASK OF LOCKS HELD (SECOND WORD)
112	(70)	BITSTRING	4	RT1WLFR2	MASK OF LOCKS TO BE FREED (SECOND WORD)
116	(74)	ADDRESS	4	RT1WRT1H	Address of current RT1I (used by FRRSCNV0)
120	(78)	CHARACTER	72	RT1WTRT1	PART OF RT1W USED FROM IEAVTRT1 ON
120	(78)	ADDRESS	4	RT1WTRNE	Checkpointed address of 8-byte TEA
124	(7C)	ADDRESS	4	RT1WPSW1	CHECKPOINTED PTR TO PSW1
128	(80)	ADDRESS	4	RT1WPSW2	CHECKPOINTED PTR TO PSW2
132	(84)	ADDRESS	4	RT1WSEAX	Value of the EAX control register on entry to an FRR (CR8)
136	(88)	ADDRESS	4	RT1WSLSE	Value of the LSED control register on entry to an FRR (CR15)
140	(8C)	ADDRESS	4	RT1WEAX	EAX at time of error
140	(8C)	CHARACTER	2	RT1WEAXC	EAX at time of error
144	(90)	ADDRESS	4	RT1WLLSR	The linkage stack register value obtained from the last FRR entry processed which contained one
148	(94)	ADDRESS	4	RT1WLSED	Linkage stack register value at time of error
152	(98)	CHARACTER	16	RT1WXM	CROSS MEMORY INFORMATION AT TIME OF ERROR
152	(98)	CHARACTER	8	RT1WCR3	CONTROL REGISTER 3 AT TIME OF ERROR
152	(98)	CHARACTER	4	*	
156	(9C)	CHARACTER	4	RT1WKMSA	KEY MASK/SASID
156	(9C)	CHARACTER	2	RT1WKM	KEY MASK
158	(9E)	CHARACTER	2	RT1WSAS	SASID
160	(A0)	CHARACTER	8	RT1WCR4	CONTROL REGISTER 4 AT TIME OF ERROR
160	(A0)	CHARACTER	4	*	
164	(A4)	CHARACTER	4	RT1WAXPA	AX / PASID
164	(A4)	CHARACTER	2	RT1WAX	AUTHORIZATION INDEX
166	(A6)	CHARACTER	2	RT1WPAS	PASID
168	(A8)	ADDRESS	4	RT1WBEA	Checkpointed pointer to breaking event address
172	(AC)	ADDRESS	4	RT1WPSW16	Checkpointed pointer to 16-byte program check PSW
176	(B0)	ADDRESS	4	RT1WSD24	Address of below the line SDWA or 0
180	(B4)	ADDRESS	4	RT1WSD31	Address of above the line SDWA or 0
184	(B8)	CHARACTER	8	*	Reserved

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
192	(C0)	CHARACTER	0	RT1WEND	THE RT1W EXTENSION STARTS HERE. THE EXTENSION DOES NOT GET SAVED BY FRRSCOPY

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4	RT1TRECC	RECURSION CONTROL DATA
0	(0)	UNSIGNED	1	RT1TLPN	LOGICAL PHASE NUMBER
1	(1)	UNSIGNED	1	RT1TLPID	LOGICAL PHASE REC RTN ID
2	(2)	CHARACTER	1	RT1TENPT	ORIGINAL ENTRY POINT
3	(3)	BITSTRING	1	RT1TACQR	RESOURCES ACQUIRED BY RTM1
		1...		RT1TDISP	DISPATCHER LOCK ACQUIRED
		.1..		RT1TLLCK	LOCAL LOCK ACQUIRED BY RT1
		..1.		RT1TRETY	RT1 ATTEMPTED RETRY
		...1		RT1TCINV	CABTERM entry mode invalid
		.... 1...		RT1TSERP	IF ON, SERIALIZE SRB TO TASK PERCOLATION (USED TO INDICATE SDWASERP WAS SET)
		.... .1..		RT1TTRTS	IF ON, IEAVTRTM HAS GONE TO IEAVTRTS
		.... ..1.		RT1TNSS	IF ON, RT1 WAS ENTERED WITH PSANSS BIT ON
		1		RT1TBINV	BTERM entry mode invalid or CABTERM TCB=0 while enabled

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	24	RT1TRACK	COMMON TRACKING AREA MAPPING FOR RTM1 RECOVERY
0	(0)	SIGNED	4	*	REMOVED RECURSION DATA, NOW MAPPED BY RT1TRECC
4	(4)	ADDRESS	4	RT1TREGS (4294967301:0)	CHECKPOINTED REGISTERS

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	*	
0	(0)	CHARACTER	1	*	Reserved
1	(1)	BITSTRING	1	RTSCFLGS	
		11..		RTSASC	Value of ASC used to give FRR control
2	(2)	CHARACTER	2	RTSOPASD	Value of PASID at time of error prior to first FRR, PASID of previous FRR thereafter.
4	(4)	CHARACTER	16	RTSCRGS	Value of cross memory control registers used to give FRR control
4	(4)	CHARACTER	8	RTSCR3	Control register 3
4	(4)	CHARACTER	4	*	x
8	(8)	CHARACTER	2	RTSKM	Key mask
10	(A)	CHARACTER	2	RTSSCND	ASID of secondary address space
12	(C)	CHARACTER	8	RTSCR4	Control register 4
12	(C)	CHARACTER	4	*	x
16	(10)	CHARACTER	2	RTSAX	Authorization index
18	(12)	CHARACTER	2	RTSPRIM	ASID of primary address space

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	3272	RTMW	CPU Work/Save area used by RTM
0	(0)	CHARACTER	72	RTMWTRTM	RTM register save area used by IEAVTRTM. This area can be used only during SLIH mode processing
72	(48)	CHARACTER	72	RTMWTRT1	DATERR processing register save area. Used by IEAVTRT1
144	(90)	CHARACTER	16	RTMWRTMP	Parameter area used by RTM to pass error information to the Supervisor Analysis Router
160	(A0)	CHARACTER	3112	RTMWDSAA	RTM1 Dynamic Storage Allocation Area (only used by R1N)
160	(A0)	ADDRESS	4	RTMWDSNA	Address of the next available dynamic area
164	(A4)	ADDRESS	4	RTMWDSLB	Address of the byte past the end of the dynamic storage
168	(A8)	CHARACTER	3104	RTMWDSTR	Dynamic area storage. This area must be large enough for RTM to process concurrently on more than one stack at a time. The deepest module path through RTM1 must be considered in defining the size of this area. This area is currently large enough for 4 trips through RTM at the deepest point on the stack.

RT1W Constants

Len	Type	Value	Name	Description
4	DECIMAL	192	RT1W_LEN	
		Comment		
	CONSTANTS USED WITH THE RT1WRRPI FIELD			
		End of Comment		
4	DECIMAL	0	RT1WPERC	Percolate to next FRR
4	DECIMAL	1	RT1WSRTY	Super FRR retry
4	DECIMAL	2	RT1WNRTY	Normal FRR retry
4	DECIMAL	3	RT1WRSUM	Resume from restart
4	DECIMAL	4	RT1WQUIT	Terminate RTM1 FRR processing
		Comment		
	CONSTANTS USED WITH THE RT1WRTYP FIELD			
		End of Comment		
4	DECIMAL	1	RT1WCSQA	RT1WRTCA POINTS TO A SQA (GETMAINED) SDWA
4	DECIMAL	2	RT1WCGLB	RT1WRTCA POINTS TO A GLOBAL (CPU) SDWA
		Comment		
	LOGICAL PHASE NUMBERS USED WITH THE RT1TLPN FIELD. THESE NUMBERS ARE ASSIGNED TO THE VARIOUS UNIQUE PHASES OF THE RTM1 TO ASSIST IN RECOVERY ACTIONS PERFORMED BY THE RTM1S FRRS OR LOGICAL PHASE RECOVERY ROUTINES (LPRRS).			
		End of Comment		
1	DECIMAL	255	VALIDREC	ANTICIPATED VALID RECURSION IN RTM MGR
1	DECIMAL	254	FRRRECUR	ANTICIPATED VALID RECURSION IN AN FRR
1	DECIMAL	253	RMGRCML	ANTICIPATED VALID RECURSION FOR INITIAL ENTRY OF CALLRTM TYPE=RMGRCML
1	DECIMAL	252	RCRDREC	ANTICIPATED RECURSION FOR CALL TO RECORD
1	DECIMAL	0	RT1MGRLO	BEGINNING OF MANAGEMENT FUNC- TIONS RANGE OF LPNS
1	DECIMAL	1	MGRINIT	LPN FOR MGR INITIALIZE PHASE
1	DECIMAL	1	SRMDRID	SRMs recovery ID, used in TR1A
1	DECIMAL	2	POSTRTS	COMPLETION OF SYSTEM RECOVERY PROCESSING LPN
1	DECIMAL	3	NORTS	NO SYSTEM RECOVERY PROCESSING LPN
1	DECIMAL	4	CPURSTRT	ISSUE RESTART ON LOOPING CPU LPN
1	DECIMAL	5	NORSTRT	UNSUCCESSFUL ISSUANCE OF RESTART LPN
1	DECIMAL	29	RT1MGRHI	ENDING OF MANAGEMENT FUNC- TIONS RANGE OF LPNS
1	DECIMAL	30	RT1MCHLO	BEGINNING OF MACHCK FUNC- TIONS RANGE OF LPNS
1	DECIMAL	32	RTHEEDS	RTHS EED PHASE LPN
1	DECIMAL	33	RTHTIMER	RTHS CLOCK REPAIR PHASE LPN
1	DECIMAL	34	RTHSTRG1	RTHS STORAGE REPAIR PHASE 1 LPN
1	DECIMAL	35	RTHPARMS	RTHS RSR PARAMETER LIST PHASE LPN
1	DECIMAL	36	RTHSTRG2	RTHS STORAGE REPAIR PHASE 2 LPN
1	DECIMAL	37	RTHRECRD	RTHS ERROR RECORDING PHASE LPN
1	DECIMAL	38	RTHSOFTW	RTHS SOFTWARE INFORMATION PHASE LPN
1	DECIMAL	39	RTHEXIT	RTHS EXIT PHASE LPN
1	DECIMAL	59	RT1MCHHI	ENDING OF MACHINE CHECK FUNCTIONS RANGE OF LPNS
1	DECIMAL	60	RT1SRMLO	BEGINNING OF RTS FUNCTIONS RANGE OF LPNS
1	DECIMAL	64	SRMSLIP	TRTS calling SLIP
1	DECIMAL	65	SRMROUTE	TRTS routing
1	DECIMAL	70	RTMGTF	TR1G tracing
1	DECIMAL	71	RTMRCRD	TR1R recording
1	DECIMAL	72	RTMADJST	TR1C adjust (TR1D entry)
1	DECIMAL	85	SRMEXIT	TRTS exiting
1	DECIMAL	86	RT1SRMHI	End of RTS function LPNs
1	DECIMAL	87	RT1RESLO	BEGINNING OF RESCHEDULE FUNCTIONS RANGE OF LPNS
		Comment		
	Caution: if you add any LPN numbers between RESRTYLO and RESRTYHI you must also adjust the RtryLabl array in IEAVTRTR			
		End of Comment		
1	DECIMAL	87	RESRTYLO	BEGINNING OF RESCHEDULE LPNS FOR WHICH RETRY IS POSSIBLE
1	DECIMAL	87	SRBABTM1	SRBTERM in TRTM (other processing)
1	DECIMAL	88	SRBABTM2	SRBTERM in TRTM (lock the WEB)

Len	Type	Value	Name	Description
1	DECIMAL	89	SRBABTM3	SRBTERM in TRTM (set up PURGEDQ)
1	DECIMAL	90	SRBEED	EED PROCESSING TO OBTAIN AN SRB LPN
1	DECIMAL	91	XMEEDS	EED PROCESSING FOR XMABTRM LPN
1	DECIMAL	92	RT2EEDS	EED PROCESSING FOR RTM2 RESCHEDULE LPN
1	DECIMAL	93	RT1EEDS	EED PROCESSING FOR RTM1 RESCHEDULE LPN
1	DECIMAL	94	RETRYRS1	MEMTERM PROCESSING WITH DAMAGED ASCB
1	DECIMAL	94	RESRTYHI	END OF RESCHEDULE LPNS FOR WHICH RETRY IS POSSIBLE
1	DECIMAL	95	RESCHED	RESCHEDULE FUNCTION LPN
1	DECIMAL	96	FREESRB	SRB PROCESSING PRIOR TO SCHEDULE LPN
1	DECIMAL	97	SRBCOMP	SRB PROCESSING COMPLETE LPN
1	DECIMAL	98	RESRTM2	RESCHEDULE RTM2 LPN
1	DECIMAL	99	TCBTERM	LOGICAL TERMINATION OF TASK LPN
1	DECIMAL	100	RESRTM1	RESCHEDULE RTM1 LPN
1	DECIMAL	101	MEMTERM	ENQUEUE OF ASCB ON MEMTERM QUEUE LPN
1	DECIMAL	102	MEMTCOMP	MEMTERM COMPLETION LPN
1	DECIMAL	103	MEMSCHED	TR1M scheduling the memterm SRB
1	DECIMAL	119	RT1RESHI	ENDING OF RESCHEDULE FUNC- TIONS RANGE OF LPNS
1	DECIMAL	120	RT1EXTLO	BEGINNING OF EXIT FUNC- TIONS RANGE OF LPNS
1	DECIMAL	120	EEDFREE	EED PROCESSING TO FREE UNNEEDED EEDS LPN
1	DECIMAL	121	FREERTCA	FREEMAIN ACQUIRED SDWA LPN
1	DECIMAL	122	EEDFREE2	EED PROCESSING TO FREE UNNEEDED EEDS LPN (FIRST CALL FROM SRBTSKDQ)
1	DECIMAL	123	EEDFREE3	EED PROCESSING TO FREE UNNEEDED EEDS LPN (SECOND CALL FROM SRBTSKDQ)
1	DECIMAL	124	EEDFREE4	EED PROCESSING TO FREE UNNEEDED EEDS LPN (THIRD CALL FROM SRBTSKDQ)
1	DECIMAL	125	EEDFREE5	EED PROCESSING TO FREE UNNEEDED EEDS LPN. Called from FRR retry/resume path
1	DECIMAL	128	MEMSLIP	Used while MEMTERM is calling SLIP
1	DECIMAL	149	RT1EXTHI	ENDING OF EXIT FUNC- TIONS RANGE OF LPNS
1	DECIMAL	190	RT1RTF1	LPN FOR IEAVTRTF PROCESSING
1	DECIMAL	191	RT1ABR1	LPN FOR IEAVTRTR ABORT PROCESSING
4	DECIMAL	304	RT1WFWLN	Size of FRR work area

RT1W Cross Reference

Name	Hex Offset	Hex Value
RTMW	0	
RTMWDSAA	A0	
RTMWDSLB	A4	
RTMWDSNA	A0	
RTMWDSTR	A8	
RTMWRTMP	90	
RTMWTRTM	0	
RTMWTRT1	48	
RTSASC	1	C0
RTSAX	10	
RTSCFLGS	1	
RTSCRGS	4	
RTSCR3	4	
RTSCR4	C	
RTSKM	8	
RTSOPASD	2	
RTSPRIM	12	
RTSSCND	A	
RT1NODMP	3C	80
RT1TACQR	3	
RT1TBINV	3	01
RT1TCINV	3	10
RT1TDISP	3	80
RT1TENPT	2	
RT1TLLCK	3	40
RT1TLPID	1	
RT1TLPN	0	
RT1TNSS	3	02
RT1TRACK	0	
RT1TRECC	0	
RT1TREGS	4	
RT1TRETY	3	20
RT1TSERP	3	08
RT1TTRTS	3	04
RT1W	0	
RT1WABCC	50	
RT1WASC	3D	18
RT1WASCB	38	
RT1WAX	A4	
RT1WAXPA	A4	
RT1WBEA	A8	
RT1WCCRC	50	
RT1WCLUP	3E	20
RT1WCMPF	50	
RT1WCOVR	36	
RT1WCOV2	3C	
RT1WCOV3	3D	
RT1WCOV4	3E	
RT1WCPMC	51	
RT1WCPOB	3D	20
RT1WCPUB	48	
RT1WCPUN	4C	
RT1WCRC	54	
RT1WCR3	98	
RT1WCR4	A0	
RT1WEAX	8C	
RT1WEAXC	8C	
RT1WEED	30	
RT1WEND	C0	
RT1WENTR	34	
RT1WENT2	3C	
RT1WEREX	36	08
RT1WFAIL	36	20
RT1WFCMS	47	02
RT1WFCPU	44	80
RT1WFLCL	47	01
RT1WGFAI	36	04
RT1WGFRR	58	
RT1WGLBL	36	02
RT1WKM	9C	
RT1WKMSA	9C	
RT1WLFR2	70	
RT1WLKFR	44	
RT1WLKMB	40	

RT1W Cross Reference

Name	Hex Offset	Hex Value
RT1WLLSR	90	
RT1WLMB2	6C	
RT1WLPN	37	
RT1WLPTA	0	
RT1WLSED	94	
RT1WMODE	34	
RT1WNCNL	3E	40
RT1WNGLB	5C	
RT1WNOAB	3E	10
RT1WNOSA	3C	10
RT1WNOSM	3C	08
RT1WNOSP	3C	40
RT1WNOSU	3C	04
RT1WNOSV	3C	20
RT1WNPRS	0	
RT1WNSQA	5D	
RT1WOFRR	60	
RT1WPAS	A6	
RT1WPRSV	2C	
RT1WPSW1	7C	
RT1WPSW16	AC	
RT1WPSW2	80	
RT1WRCDR	36	80
RT1WRCF	50	04
RT1WRCRD	36	01
RT1WREQ	50	80
RT1WRMGR	36	10
RT1WRRPI	68	
RT1WRSTW	3E	80
RT1WRSV	3F	
RT1WRTCA	2C	
RT1WRTM	36	40
RT1WRTYN	50	02
RT1WRTYP	5E	
RT1WRT1H	74	
RT1WRT1I	64	
RT1WSAS	9E	
RT1WSD24	B0	
RT1WSD31	B4	
RT1WSEAX	84	
RT1WSINF	5C	
RT1WSKIP	3D	80
RT1WSLSE	88	
RT1WSLST	3D	40
RT1WSRMD	35	
RT1WSTEP	50	40
RT1WTRNE	78	
RT1WTRTM	0	
RT1WTRT1	78	
RT1WVARI	18	
RT1WXM	98	

RWA Information

RWA Heading Information

Common Name: Machine Check Handler Recovery Work Area
Macro ID: IGFRWA
DSECT Name: RWA
Owning Component: Machine Check Handler (BB1CT)
Eye-Catcher ID: RWA
Storage Attributes: Subpool: NUCLEUS in IGFRWAC
Key: 0
Residency: Above 16M line
Size: 476 bytes
Created by: Contained within module IGFRWAC which is loaded into the DAT-ON NUCLEUS at NIP time.
Pointed to by: RVTRWA field of the RVT data area.
Serialization: None
Function: The RWA contains flags and footprints for Machine Check Handler mainline and recovery. It contains the Default Threshold Blocks (THBS) for machine checks. The RWA contains system termination information for the loadwait service, and work save area's for many MCH modules.

RWA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	RWA	
0	(0)	CHARACTER	4	RWAID	. AN ID FOR DUMPS
4	(4)	ADDRESS	4	RWAPCCAR_ADDR	Pointer to an array of real addresses of PCCAs for CPUs 0 - CVTMAXMP
8	(8)	BITSTRING	8	RWASRDF	INITIAL THRESHOLD BLOCK FOR SYSTEM RECOVERY EVENTS
16	(10)	BITSTRING	8	RWADGDF	INITIAL THRESHOLD BLOCK FOR DEGRADATION EVENTS
24	(18)	BITSTRING	1		. RESERVED
25	(19)	BITSTRING	1		. RESERVED
26	(1A)	BITSTRING	1	RWAFLCFL	. FIXED LOW CORE IN USE FLAGS
27	(1B)	BITSTRING	1		. RESERVED
28	(1C)	BITSTRING	1	RWAFLAGS	. MCH SYSTEM-WIDE FLAGS
		1...		RWAPCMC	"X'80'" . MACHINE CHECK OR PROGRAM CHECK OCCURRED IN IGFPMTHA.
		.1..		RWAINIT	"X'40'" . SET TO 1 BY IGFRIM00 WHEN MCH INITIALIZATION IS COMPLETE
		..1.		RWATSYS	"X'20'" . SET TO 1 BY LOADWAIT (BLWLDWT) WHEN SYSTEM TERMINATION IS IN PROGRESS OR IGFPWAIT.
29	(1D)	BITSTRING	3		. RESERVED
32	(20)	DBL WORD	8	RWATPSW	. SYSTEM TERMINATION PSW
32	(20)	BITSTRING	4	RWATPSW1	1ST WORD PSW
36	(24)	BITSTRING	4	RWATPSW2	2ND WORD PSW
40	(28)	CHARACTER	256	RWARV028	. RESERVED
296	(128)	BITSTRING	16	RWAPDDF (0)	INITIAL THRESHOLD
296	(128)	BITSTRING	8		BLOCK FOR INSTRUCTION
304	(130)	BITSTRING	8		PROCESSING DAMAGE EVENTS
312	(138)	BITSTRING	16	RWASDDF (0)	INITIAL THRESHOLD
312	(138)	BITSTRING	8		BLOCK FOR SYSTEM
320	(140)	BITSTRING	8		DAMAGE EVENTS
328	(148)	BITSTRING	16	RWAIVDF (0)	INITIAL THRESHOLD
328	(148)	BITSTRING	8		BLOCK FOR INVALID PSW OR
336	(150)	BITSTRING	8		REGISTER EVENTS
344	(158)	BITSTRING	16	RWATCDF (0)	INITIAL THRESHOLD
344	(158)	BITSTRING	8		BLOCK FOR TOD CLOCK
352	(160)	BITSTRING	8		DAMAGE EVENTS
360	(168)	BITSTRING	16	RWAPTDF (0)	INITIAL THRESHOLD
360	(168)	BITSTRING	8		BLOCK FOR CPU TIMER
368	(170)	BITSTRING	8		DAMAGE EVENTS
376	(178)	BITSTRING	16	RWACCDF (0)	INITIAL THRESHOLD
376	(178)	BITSTRING	8		BLOCK FOR CLOCK
384	(180)	BITSTRING	8		COMPARATOR DAMAGE EVENTS
392	(188)	BITSTRING	4	RWAVAR1	THRESHOLD BLOCK WORK AREA
396	(18C)	BITSTRING	4	RWAVAR2	THRESHOLD BLOCK WORK AREA
400	(190)	BITSTRING	16	RWAVSDF (0)	INITIAL THRESHOLD
400	(190)	BITSTRING	8		BLOCK FOR VECTOR
408	(198)	BITSTRING	8		SOURCE EVENTS
416	(1A0)	BITSTRING	1	RWAPMHCA (60)	IGFPMHCA WORK SAVE AREA

RWA Cross Reference

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
476	(1DC)	BITSTRING	1	RWAPMTHA (68)	IGFPMTHA WORK SAVE AREA
544	(220)	BITSTRING	1	(12)	RESERVED
556	(22C)	BITSTRING	16	RWAPSDF (0)	INITIAL THRESHOLD
556	(22C)	BITSTRING	8		BLOCK FOR PRIMARY SYNC (PS)
564	(234)	BITSTRING	8		DAMAGE EVENTS
572	(23C)	BITSTRING	16	RWAADDF (0)	INITIAL THRESHOLD
572	(23C)	BITSTRING	8		BLOCK FOR ETR ATTACHMENT
580	(244)	BITSTRING	8		DAMAGE EVENTS
588	(24C)	BITSTRING	16	RWASLDF (0)	INITIAL THRESHOLD
588	(24C)	BITSTRING	8		BLOCK FOR SWITCH TO LOCAL
596	(254)	BITSTRING	8		EVENTS
604	(25C)	BITSTRING	16	RWASCDF (0)	INITIAL THRESHOLD
604	(25C)	BITSTRING	8		BLOCK FOR ETR SYNC CHECK
612	(264)	BITSTRING	8		DAMAGE EVENTS
620	(26C)	BITSTRING	16	RWASTPSC (0)	INITIAL THRESHOLD
620	(26C)	BITSTRING	8		BLOCK FOR STP SYNC CHECK
628	(274)	BITSTRING	8		DAMAGE EVENTS
636	(27C)	BITSTRING	16	RWASTPIC (0)	INITIAL THRESHOLD
636	(27C)	BITSTRING	8		BLOCK FOR STP ISLAND COND.
644	(284)	BITSTRING	8		DAMAGE EVENTS
652	(28C)	BITSTRING	16	RWASTPCC (0)	INITIAL THRESHOLD
652	(28C)	BITSTRING	8		BLOCK FOR STP CONFIG CHANGE
660	(294)	BITSTRING	8		DAMAGE EVENTS
668	(29C)	BITSTRING	16	RWASTPCS (0)	INITIAL THRESHOLD
668	(29C)	BITSTRING	8		BLOCK FOR STP CLOCK SOURCE
676	(2A4)	BITSTRING	8		ERROR DAMAGE EVENTS
684	(2AC)	BITSTRING	1	RWASPSRB (44)	SERVICE PROCESSOR DAMAGE SRB
728	(2D8)	BITSTRING	1	(3)	RESERVED DO NOT USE
731	(2DB)	BITSTRING	1	RWASPSIU	RWASPSRB IN USE (TS TARGET) *

Comment

END OF MCH RECOVERY WORK AREA

End of Comment

RWA Cross Reference

Name	Hex Offset	Hex Value
RWA	0	
RWAADDF	23C	
RWACCDF	178	
RWADGDF	10	20000000
RWAFLAGS	1C	0
RWAFLCFL	1A	0
RWAID	0	D9E6C140
RWAINIT	1C	40
RWAIVDF	148	
RWAPCCAR_ADDR	4	
RWAPCMC	1C	80
RWAPDDF	128	
RWAPMHCA	1A0	0
RWAPMTHA	1DC	0
RWAPSDF	22C	
RWAPTDF	168	
RWARV028	28	F0404040
RWASCDF	25C	
RWASDDF	138	
RWASLDF	24C	
RWASPSIU	2DB	0
RWASPSRB	2AC	0
RWASRDF	8	20000000
RWASTPCC	28C	
RWASTPCS	29C	
RWASTPIC	27C	
RWASTPSC	26C	
RWATCDF	158	
RWATPSW	20	0
RWATPSW1	20	
RWATPSW2	24	
RWATSYS	1C	20
RWAVAR1	188	0
RWAVAR2	18C	0
RWAVSDF	190	

SCANPARM Information

SCANPARM Heading Information

Common Name: PARAMETER LIST FOR IEAVQ700 (THE COMM TASK QUEUE SCANNER)
Macro ID: IEZVQ100
DSECT Name: SCANPARM
Owning Component: CONSOLE (SC1CK)
Eye-Catcher ID: NONE
Storage Attributes: Subpool: CHOSEN BY CALLER OF IEAVQ700
Key: CHOSEN BY CALLER OF IEAVQ700
Residency: ABOVE/BELOW 16 MB IN REAL/VIRTUAL STORAGE.
Size: SCANPARM -- X'0068' bytes
Created by: CALLERS OF IEAVQ700
Pointed to by: REGISTER 1 ON ENTRY TO IEAVQ700
Serialization: NONE
Function: THIS MACRO MAPS THE PARAMETER LIST FOR THE COMM TASK QUEUE SCANNER MODULE IEAVQ700

SCANPARM Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	104	SCANPARM	IEAVQ700 PARM LIST
0	(0)	BITSTRING	3	SCANFUNC	SEARCH FUNCTIONS
0	(0)	BITSTRING	1	SCANFC1	1ST BYTE OF SEARCH FUNCTIONS
		1...		SCANMDSC	DESCRIPTOR CODE MATCH
		.1..		SCANMASD	ASID MATCH
		..1.		SCANMTCB	JOB STEP TCB MATCH
		...1		SCANMMID	MSG SEQ ID MATCH
		.... 1...		SCANMDSR	DISPLAY SEQUENCE NUMBER RANGE MATCH (USED BY K C ONLY)
		.... .1..		SCANMTXT	MSG TEXT MATCH
		.... ..1.		SCANMTKN	TOKEN MATCH
		1		SCANMSID	SYSID MATCH
1	(1)	BITSTRING	1	SCANFC2	2ND BYTE OF SEARCH FUNCTIONS
		1...		SCANMDSQ	DISPLAY SEQUENCE NUMBER MATCH (USED BY K C ONLY)
		.1..		SCANMSNM	SYSTEM NAME MATCH
		..1.		SCANMMLV	MESSAGE LEVEL MATCH
		...1		SCANMCID	CONSOLE ID MATCH
		.... 1...		SCANMRTC	ROUTING CODES MATCH
		.... .1..		SCANDALL	MATCH ALL DESC CODES
		11		*	RESERVED
2	(2)	BITSTRING	1	SCANFC3	3RD BYTE OF SEARCH FUNCTIONS - RESERVED
3	(3)	UNSIGNED	1	SCANQUE	CODE OF QUEUE TO SEARCH
4	(4)	BITSTRING	4	SCANDESC	Descriptor Codes to match
4	(4)	BITSTRING	1	SCANDBY1	1ST BYTE OF DESCRIPTOR CODES
		1...		SCANDC1	DESCRIPTOR CODE 1
		.1..		SCANDC2	DESCRIPTOR CODE 2
		..1.		SCANDC3	DESCRIPTOR CODE 3
		...1		SCANDC4	DESCRIPTOR CODE 4
		.... 1...		SCANDC5	DESCRIPTOR CODE 5
		.... .1..		SCANDC6	DESCRIPTOR CODE 6
		.... ..1.		SCANDC7	DESCRIPTOR CODE 7
		1		SCANDC8	DESCRIPTOR CODE 8
5	(5)	BITSTRING	1	SCANDBY2	2ND BYTE OF DESCRIPTOR CODES
		1...		SCANDC9	DESCRIPTOR CODE 9
		.1..		SCANDC10	DESCRIPTOR CODE 10
		..1.		SCANDC11	DESCRIPTOR CODE 11
		...1		SCANDC12	DESCRIPTOR CODE 12 - RESERVED
		.... 1...		SCANDC13	DESCRIPTOR CODE 13 - RESERVED
		.... .1..		SCANDC14	DESCRIPTOR CODE 14 - RESERVED
		.... ..1.		SCANDC15	DESCRIPTOR CODE 15 - RESERVED
		1		SCANDC16	DESCRIPTOR CODE 16 - RESERVED
6	(6)	BITSTRING	1	SCANDBY3	3rd byte of Descriptor Codes
		1...		SCANDC17	Descriptor Code 17 - Reserved
		.1..		SCANDC18	Descriptor Code 18 - Reserved
		..1.		SCANDC19	Descriptor Code 19 - Reserved
		...1		SCANDC20	Descriptor Code 20 - Reserved
		.... 1...		SCANDC21	Descriptor Code 21 - Reserved
		.... .1..		SCANDC22	Descriptor Code 22 - Reserved
		.... ..1.		SCANDC23	Descriptor Code 23 - Reserved
		1		SCANDC24	Descriptor Code 24 - Reserved
7	(7)	BITSTRING	1	SCANDBY4	4th byte of Descriptor Codes

SCANPARM Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		1...		SCANDC25	Descriptor Code 25 - Reserved
		.1..		SCANDC26	Descriptor Code 26 - Reserved
		..1.		SCANDC27	Descriptor Code 27 - Reserved
		...1		SCANDC28	Descriptor Code 28 - Reserved
		.... 1...		SCANDC29	Descriptor Code 29 - Reserved
		.... .1..		SCANDC30	Descriptor Code 30 - Reserved
		.... ..1.		SCANDC31	Descriptor Code 31 - Reserved
		1		SCANDC32	Descriptor Code 32 - Reserved
8	(8)	UNSIGNED	1	SCANQBP	Queue being processed
9	(9)	UNSIGNED	1	SCANMTYP	MSGTYP to match
10	(A)	BITSTRING	1	SCANFLG1	SEARCH SPECIFICATIONS
		1...		SCANCNRT	ACCEPT EITHER CONSOLE ID OR ROUTING CODE MATCH
		.1..		SCANMSCP	INDICATE CONSOLE MSCOPE MUST BE CHECKED
		..1.		SCANMRTA	Accept Misc Routing information match
		...1		SCANMSGT	Accept MSGTYP Routing information match
		 1111		*	RESERVED
11	(B)	UNSIGNED	1	SCANVRSN	VERSION LEVEL
12	(C)	UNSIGNED	1	SCANRV01	RESERVED (WAS SCANCNID)
13	(D)	BITSTRING	1	SCANMISC	MISCELLANEOUS ROUTING INFORMATION
14	(E)	CHARACTER	2	SCANASID	ASID TO MATCH
16	(10)	ADDRESS	4	SCANTCBP	TCB TO MATCH
20	(14)	SIGNED	4	SCANMID	MSG SEQ ID TO MATCH
24	(18)	SIGNED	4	SCANHDSR	HIGH END OF DISPLAY SEQUENCE NUMBER RANGE TO MATCH (USED BY K C ONLY)
28	(1C)	ADDRESS	4	SCANCENT	ADDR OF ENTRY FOUND OR ZERO
32	(20)	ADDRESS	4	SCANPENT	ADDR OF PRECEDING ENTRY OR ZERO. IF ENTRY IS FOUND AND THIS IS ZERO, ENTRY FOUND IS 1ST ON QUEUE
36	(24)	ADDRESS	4	SCANWKPT	ADDR OF WORK AREA FOR IEAVQ700
40	(28)	UNSIGNED	1	SCANLGTH	LENGTH OF TEXT TO MATCH
41	(29)	CHARACTER	8	SCANTEXT	TEXT TO MATCH
49	(31)	CHARACTER	1	SCANSYID	SYSTEM ID TO MATCH
50	(32)	BITSTRING	2	SCANMLVL	MESSAGE LEVEL TO MATCH
		1111 1...		*	
		.... .1..		SCANMLBC	BROADCAST ALLOWED
52	(34)	ADDRESS	4	SCANTOKN	TOKEN TO MATCH
56	(38)	SIGNED	4	SCANDSQN	DISPLAY SEQUENCE NUMBER TO MATCH OR LOW END OF DISPLAY SEQUENCE NUMBER RANGE MATCH (USED BY K C ONLY)
60	(3C)	CHARACTER	8	SCANSYNM	SYSTEM NAME TO MATCH
68	(44)	CHARACTER	16	SCANRTCD	ROUTING CODES TO MATCH
84	(54)	UNSIGNED	4	SCANCONS	4 BYTE CONSOLE ID
88	(58)	BITSTRING	8	SCANTOD	8 BYTE TIME STAMP
96	(60)	SIGNED	4	SCANMSNO	NUMBER OF MSCOPE VALUES
100	(64)	ADDRESS	4	SCANSYSE	POINTER TO MSCOPE VALUE LIST

SCANPARM Constants

Len	Type	Value	Name	Description
		Comment		
		CODES FOR QUEUES TO BE SEARCHED		
		End of Comment		
1	DECIMAL	1	SCANORE	ORE QUEUE
1	DECIMAL	2	SCANWQE	WQE QUEUE
1	DECIMAL	3	SCANRMSG	RETAINED MSG QUEUE
1	DECIMAL	4	SCANRIAM	RETAINED IMMEDIATE ACTION MSG QUEUE
1	DECIMAL	5	SCANREAM	RETAINED EVENTUAL ACTION MSG QUEUE
1	DECIMAL	6	SCANRCAM	RETAINED CRITICAL EVENTUAL ACTION MESSAGE QUEUE
1	DECIMAL	1	SCANMINQ	MINIMUM QUEUE NUMBER
1	DECIMAL	6	SCANMAXQ	MAXIMUM QUEUE NUMBER
		Comment		
		VALUES FOR VERSION LEVEL		
		End of Comment		
1	DECIMAL	1	SCANSP22	VERSION LEVEL IS OS/VS2 JBB2220
1	DECIMAL	2	SCANSP41	VERSION LEVEL IS OS/VS2 HBB4410
1	DECIMAL	3	SCANJBB7727	VERSION LEVEL IS z/OS JBB7727
1	DECIMAL	5	SCANHBB7730	VERSION LEVEL IS z/OS HBB7730
1	DECIMAL	5	SCANVRID	VERSION LEVEL - UPDATED FOR SIZE OR INCOMPATIBLE CHANGE

Len	Type	Value	Name	Description
		Comment		
	SIZE CONSTANTS			
		End of Comment		
4	DECIMAL	2048	SCANWKSZ	Size of IEAVQ700 dynamic area provided by caller

SCANPARM Cross Reference

Name	Hex Offset	Hex Value
SCANASID	E	
SCANCENT	1C	
SCANCNRT	A	80
SCANCONS	54	
SCANDALL	1	04
SCANDBY1	4	
SCANDBY2	5	
SCANDBY3	6	
SCANDBY4	7	
SCANDC1	4	80
SCANDC10	5	40
SCANDC11	5	20
SCANDC12	5	10
SCANDC13	5	08
SCANDC14	5	04
SCANDC15	5	02
SCANDC16	5	01
SCANDC17	6	80
SCANDC18	6	40
SCANDC19	6	20
SCANDC2	4	40
SCANDC20	6	10
SCANDC21	6	08
SCANDC22	6	04
SCANDC23	6	02
SCANDC24	6	01
SCANDC25	7	80
SCANDC26	7	40
SCANDC27	7	20
SCANDC28	7	10
SCANDC29	7	08
SCANDC3	4	20
SCANDC30	7	04
SCANDC31	7	02
SCANDC32	7	01
SCANDC4	4	10
SCANDC5	4	08
SCANDC6	4	04
SCANDC7	4	02
SCANDC8	4	01
SCANDC9	5	80
SCANDESC	4	
SCANDSQN	38	
SCANFC1	0	
SCANFC2	1	
SCANFC3	2	
SCANFLG1	A	
SCANFUNC	0	
SCANHDSR	18	
SCANLGTH	28	
SCANMASD	0	40
SCANMCID	1	10
SCANMDSC	0	80
SCANMDSQ	1	80
SCANMDSR	0	08
SCANMID	14	
SCANMISC	D	
SCANMLBC	32	04
SCANMLVL	32	
SCANMMID	0	10
SCANMMLV	1	20
SCANMRTA	A	20
SCANMRTC	1	08
SCANMSCP	A	40
SCANMSGT	A	10
SCANMSID	0	01
SCANMSNM	1	40
SCANMSNO	60	
SCANMTCB	0	20
SCANMTKN	0	02
SCANMTXT	0	04
SCANMTYP	9	
SCANPARM	0	
SCANPENT	20	
SCANQBP	8	
SCANQUE	3	
SCANRTCD	44	
SCANRV01	C	
SCANSYID	31	
SCANSYNM	3C	
SCANSYSE	64	
SCANTCBP	10	
SCANTEXT	29	
SCANTOD	58	
SCANTOKN	34	
SCANVRSN	B	
SCANWKPT	24	

SCB Information

SCB Programming Interface information

Programming Interface information

SCB

INCLUDE ONLY

End of Programming Interface information

SCB Heading Information

Common Name: STAE Control Block
Macro ID: IHASCB
DSECT Name: SCB, SCBX
Owning Component: Recovery Termination Manager (SCRTM)
Eye-Catcher ID: None
Storage Attributes: Subpool: 255
Key: 0
Size: 48 bytes
Created by: IEAVSTA0, IEAVSTA1
Pointed to by: TCBSTABB field of the TCB data area
SCBCHAIN field of the SCB data area
Serialization: Task Active
Function: The SCB is used to make STAE/ESTAE/ESTAEX recovery routines known to the system.

SCB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SCB	, SCBPTR
0	(0)	ADDRESS	4	SCBCHAIN	POINTER TO NEXT SCB ON CHAIN
4	(4)	ADDRESS	4	SCBEXIT	POINTER TO USER WRITTEN EXIT ROUTINE
8	(8)	ADDRESS	4	SCBPARM (0)	ADDRESS OF PARAMETER LIST FOR STA EXIT
8	(8)	BITSTRING	1	SCBFLGS1	FIRST FLAG BYTE
		1...		SCBSTAI	"X'80'" STAI SCB
		.1..		SCBASCM	"X'40'" ADDRESS SPACE CONTROL MODE FOR EXIT ROUTINE 0 = PRIMARY,1 = AR MODE
		..1.		SCBNCNL	"X'20'" NO CANCEL - ROUTINE RUNS PROTECTED FROM CANCELS AND DETACHES
		...1		SCBESTAE	"X'10'" ESTAE INDICATOR
		.... 1...		SCBTOKEN	"X'08'" ESTAE ESTABLISHED WITH TOKEN
		.... .1..		SCBASYNC	"X'04'" ALLOW ASYNCHRONOUS INTERRUPTS
		11		SCBIOPRC	"X'03'" I/O PROCESSING OPTION, BITS 6 AND 7-- 00 - QUIESCE I/O 01 - HALT I/O 10 - BYPASS I/O INTERVENTION 11 - (RESERVED)
		.... ..1.		SCBNOIOP	"X'02'" BYPASS I/O INTERVENTION
		1		SCBHALT	"X'01'" HALT I/O
9	(9)	ADDRESS	3	SCBPARMA	24 bit user parameter list address used for FESTAE only - otherwise non-FESTAE flags
		1...		SCBAM64	"X'80'" Extended AMODE - 64. Only valid when this is not a STAE/STAI.
		.1..		SCBPERCD	"X'40'" The recovery routine represented by this SCB has percolated
12	(C)	ADDRESS	4	SCBOWNR (0)	TCB/RB ADDRESS CONTROLLING THIS SCB
12	(C)	BITSTRING	1	SCBFLGS2	SECOND FLAG BYTE
		1...		SCBAMODE	"X'80'" USER IN 31 BIT ADDRESSING MODE
		1...		SCBAM31	"X'80'" USER IN 31 BIT ADDRESSING MODE
		.1..		SCBXCTL2	"X'40'" RETAIN THIS SCB ACROSS XCTL
		..1.		SCBARRFL	"X'20'" ON, THIS SCB WAS CREATED BY RTM2 TO MANAGE AN
Comment					
ASSOCIATED RECOVERY ROUTINE FROM THE LINKAGE STACK					
End of Comment					
		...1		SCBINUSE	"X'10'" THIS SCB IS IN USE OR HAS PERCOLATED OR ABENDED
		.... 1...		SCBLO31	"X'08'" SDWA is LOC 31
		.... .1..		SCBPC	"X'04'" PC ESTAE TYPE SCB
		.... ..1.		SCBKEY0	"X'02'" USER IN KEY 0
		1		SCBSUPER	"X'01'" USER IN SUPERVISOR MODE
13	(D)	ADDRESS	3	SCBOWNRA	RB ADDRESS IF STAE, TCB ADDRESS IF STAI.
16	(10)	ADDRESS	4	SCBDATA (0)	FLAGS AND DATA FIELD
16	(10)	BITSTRING	1	SCBFLGS3	OPTION FLAGS
		1...		SCBSTAUT	"X'80'" STAE REQUESTOR IS AUTHORIZED
		.1..		SCBTERMI	"X'40'" AUTHORIZED FOR TERM PROCESSING
		..1.		SCBRECRD	"X'20'" ERROR RECORD TO BE WRITTEN TO SYS1.LOGREC
		...1		SCBDUMMY	"X'10'" DUMMY SCB - (WILL NOT BE SCHEDULED)
		.... 1...		SCBPRNTR	"X'08'" SCB PREVIOUSLY ENTERED
		.... .1..		SCBBRNTR	"X'04'" FESTAE
		.... ..1.		SCBRB	"X'02'" SAVED STATUS OF RBSCB
		1		SCBUNSS	"X'01'" UNSTACK SUPPRESS STATUS OF THE LINKAGE STACK ENTRY THAT WAS CURRENT WHEN THIS SCB WAS CREATED. 1 - UNSTACK SUPPRESS WAS ACTIVE 0 - UNSTACK SUPPRESS WAS INACTIVE
17	(11)	CHARACTER	1	SCBPKEY	PROGRAM KEY
18	(12)	CHARACTER	1	SCBID	SCB IDENTIFIER
19	(13)	BITSTRING	1	SCBPCFLG	PC ESTAE USER FLAGS, VALID IF SCBPC IS ON

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
20	(14)	ADDRESS	4	SCBXPTR	POINTER TO SCB EXTENSION
		1...		SCBFTIME	"X'80'" SCB WAS IN THE FIRST GETMAIN
20	(14)	X'18'	0	SCBLEN	"*-SCB" LENGTH OF SCB

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	0	SCBX	, SCBXPTR - SCB EXTENSION
0	(0)	BITSTRING	8		Reserved
8	(8)	SIGNED	4	SCBXTOKN	ESTAE TOKEN VALUE
12	(C)	CHARACTER	8	SCBXPRMS (0)	64 bit user parameter list address
12	(C)	ADDRESS	4	SCBXPARM	31 BIT USER PARAMETER LIST ADDRESS
16	(10)	SIGNED	4	SCBXALET	ALET ASSOCIATED WITH PARAM VALUE
20	(14)	ADDRESS	4	SCBXLSEA	LINKAGE STACK ENTRY ADDRESS
24	(18)	CHARACTER	16	SCBXCR34 (0)	CONTROL REGISTERS 3 AND 4 (WITH EAX)
24	(18)	SIGNED	4	SCBXSINS	Secondary ASTE Instance#
28	(1C)	CHARACTER	2	SCBXKMSK	KEYMASK
30	(1E)	CHARACTER	2	SCBXSASN	SECONDARY ASN
32	(20)	SIGNED	4	SCBXPINS	Primary ASTE Instance#
36	(24)	CHARACTER	2	SCBXEAX	EXTENDED AUTH. INDEX
38	(26)	CHARACTER	2	SCBXPASN	PRIMARY ASN
38	(26)	X'28'	0	SCBXLEN	"*-SCBX" LENGTH OF SCB EXTENSION
38	(26)	X'40'	0	SCBTLEN	"SCBLEN+SCBXLEN" TOTAL LENGTH FOR GETMAIN

SCB Cross Reference

Name	Hex Offset	Hex Value
SCB	0	
SCBAMODE	C	80
SCBAM31	C	80
SCBAM64	9	80
SCBARRFL	C	20
SCBASCM	8	40
SCBASYNC	8	4
SCBBRNTR	10	4
SCBCHAIN	0	
SCBDATA	10	
SCBDUMMY	10	10
SCBESTAE	8	10
SCBEXIT	4	
SCBFLGS1	8	
SCBFLGS2	C	
SCBFLGS3	10	
SCBFTIME	14	80
SCBHALT	8	1
SCBID	12	
SCBINUSE	C	10
SCBIOPRC	8	3
SCBKEY0	C	2
SCBLEN	14	18
SCBLO31	C	8
SCBNCNL	8	20
SCBNOIOP	8	2
SCBOWNR	C	
SCBOWNRA	D	
SCBPARM	8	
SCBPARMA	9	
SCBPC	C	4
SCBPCFLG	13	
SCBPERCD	9	40
SCBPKEY	11	
SCBPRNTR	10	8
SCBRB	10	2
SCBRECRD	10	20
SCBSTAI	8	80
SCBSTAUT	10	80
SCBSUPER	C	1
SCBTERMI	10	40
SCBTLEN	26	40
SCBTOKEN	8	8
SCBUNSS	10	1

Name	Hex Offset	Hex Value
SCBX	0	
SCBXALET	10	
SCBXCR34	18	
SCBXCTL2	C	40
SCBXEAX	24	
SCBXKMSK	1C	
SCBXLEN	26	28
SCBXLSEA	14	
SCBXPARM	C	
SCBXPASN	26	
SCBXPINS	20	
SCBXPRMS	C	
SCBXPTR	14	
SCBXSASN	1E	
SCBXSINS	18	
SCBXTOKN	8	

SCCB Information

SCCB Programming Interface information

Programming Interface information

SCCB

ONLY the following fields are part of the programming interface information:

- SCCBETF
- SCCBETOD
- SCCBFSDM
- SCCBIR
- SCCBLPCL
- SCCBPLO
- SCCBRP
- SCCBSTSI
- SCCBUNIC

End of Programming Interface information

SCCB Heading Information

Common Name: SERVICE CALL CONTROL BLOCK (SCCB)
Macro ID: IHASCCB
DSECT Name: SCCB, SCCBCP, SCCBHSA, SCCBMPF
Owning Component: SERVICE PROCESSOR INTERFACE (SCSPI)
Eye-Catcher ID: NONE
Storage Attributes: Subpool: CALLER'S SUBPOOL - CALLER'S AREA. 245 - CVTSCPIN AREA, ECVTSCPIN AREA
Key: CALLER'S KEY - CALLER'S AREA. 0 - CVTSCPIN AREA, ECVTSCPIN AREA
Size: 4096 BYTES (DEFAULT). IF SCCBLEN IS SPECIFIED THEN SIZE CAN BE 8 TO 4096 BYTES.
Created by: ANYONE WHO COMMUNICATES WITH THE SERVICE PROCESSOR.
Pointed to by: CALLER'S POINTER, CVTSCPIN, OR ECVTSCPIN
Serialization: NOT APPLICABLE.
Function: MAPS THE COMMON FIELDS OF THE SCCB FOR ALL SERVICE PROCESSOR COMMANDS AND THE DATA AREA RETURNED FROM THE SERVICE PROCESSOR ARCHITECTURE COMMAND READ SCP INFO.

SCCB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4096	SCCB	SERVICE CALL CONTROL BLOCK.
0	(0)	CHARACTER	8	SCCBHEAD	SCCB HEADER.
0	(0)	UNSIGNED	2	SCCBLNG	LENGTH OF THE ENTIRE SCCB (MAXIMUM 4096).
2	(2)	BITSTRING	1	SCCBFLAG	CALLER FLAGS. COMMAND DEPENDENT.
3	(3)	CHARACTER	3	SCCBR003	RESERVED.
6	(6)	CHARACTER	2	SCCBRESP	SERVICE PROCESSOR RESPONSE.
6	(6)	BITSTRING	2	SCCBRSP	SERVICE PROCESSOR RESPONSE.
6	(6)	BITSTRING	1	SCCBREAS	SERVICE PROCESSOR REASON CODE.
7	(7)	BITSTRING	1	SCCBRCC	SERVICE PROCESSOR RESPONSE CLASS CODE.
8	(8)	CHARACTER	4088	SCCBCMDD	VARIABLE LENGTH COMMAND DEPENDENT DATA.
4096	(1000)	CHARACTER	0	SCCBEND	END OF SCCB.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
8	(8)	STRUCTURE	*	SCCBSCPI	MAPPING OF SCCB COMMAND DEPENDENT DATA FIELD, SCCBCMDD, FOR SERVICE PROCESSOR COMMAND READ SCP INFO.
8	(8)	UNSIGNED	2	SCCBSAR	REAL STORAGE ADDRESS RANGE. MAXIMUM STORAGE INCREMENT NUMBER INSTALLED. When 0, value is in SCCBSARX.
10	(A)	UNSIGNED	1	SCCBSAI	REAL STORAGE ADDRESS INCREMENT, IN UNITS OF 1M. WHEN 0, value is in SCCBSAIX
11	(B)	UNSIGNED	1	SCCBSBS	REAL STORAGE BLOCK SIZE IN UNITS OF 1K.
12	(C)	UNSIGNED	2	SCCBSII	REAL STORAGE INCREMENT BLOCK INTERLEAVE INTERVAL.
14	(E)	CHARACTER	2	SCCBR00E	RESERVED.
16	(10)	UNSIGNED	2	SCCBNCPS	NUMBER OF CPUS INSTALLED.
18	(12)	UNSIGNED	2	SCCBOCP	SCCB OFFSET TO CPU DATA ARRAY MAPPED BY SCCBCP.
20	(14)	UNSIGNED	2	SCCBNHSA	NUMBER OF HSAS.
22	(16)	UNSIGNED	2	SCCBOHSA	SCCB OFFSET TO HSA DATA ARRAY MAPPED BY SCCBHSA.
24	(18)	CHARACTER	8	SCCBPARM	LOAD PARAMETER INFORMATION FROM SERVICE PROCESSOR.
32	(20)	UNSIGNED	4	SCCBMESI	EXTENDED STORAGE ADDRESS RANGE. MAXIMUM EXTENDED STORAGE INCREMENT NUMBER INSTALLED.
36	(24)	UNSIGNED	4	SCCBNXSB	NUMBER OF 4K STORAGE BLOCKS IN AN EXTENDED STORAGE INCREMENT (BLOCK SIZE SCCBEBSZ).
40	(28)	UNSIGNED	2	SCCBMESE	MAXIMUM EXTENDED STORAGE ELEMENT NUMBER INSTALLED.
42	(2A)	CHARACTER	2	SCCBR02A	RESERVED.
44	(2C)	UNSIGNED	4	SCCBVPRM	VECTOR PARAMETERS.
44	(2C)	UNSIGNED	2	SCCBVSS	VECTOR SECTION SIZE.
46	(2E)	UNSIGNED	2	SCCBVPSM	VECTOR PARTIAL SUM NUMBER.
48	(30)	CHARACTER	8	SCCBIFM	INSTALLED FACILITY MAP.
48	(30)	CHARACTER	1	SCCBIFM1	INSTALLED FACILITY MAP BYTE 1.
		1...		SCCBCHPI	CHANNEL PATH INFORMATION INSTALLED.
		.1..		SCCBCHPS	CHANNEL PATH SUBSYSTEM COMMAND INSTALLED.
		..1.		SCCBCHPR	CHANNEL PATH RECONFIGURATION INSTALLED.
		...1		*	RESERVED.
		.... 1...		SCCBCPUI	CPU INFORMATION INSTALLED.
		.... .1..		SCCBCPUR	CPU RECONFIGURATION INSTALLED.
		11		*	RESERVED.
49	(31)	CHARACTER	1	SCCBIFM2	INSTALLED FACILITY MAP BYTE 2.
		1...		SCCBSGNL	SIGNAL ALARM INSTALLED.
		.1..		SCCBOMR	WRITE OPERATOR MESSAGE AND READ OPERATOR RESPONSE INSTALLED.
		..1.		SCCBSTST	STORE STATUS ON LOAD INSTALLED.

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		SCCBRSTR	RESTART REASONS INSTALLED.
		.... 1...		SCCBITRC	INSTRUCTION ADDRESS TRACE BUFFER INSTALLED.
		.... .1..		SCCBLPRM	LOAD PARAMETER INSTALLED.
		.... ..1.		SCCBWDAT	READ AND WRITE DATA INSTALLED.
		1		*	RESERVED.
50	(32)	CHARACTER	1	SCCBIFM3	INSTALLED FACILITY MAP BYTE 3.
		1...		SCCBSIR	REAL STORAGE INCREMENT RECONFIGURATION INSTALLED.
		.1..		SCCBSEI	REAL STORAGE ELEMENT INFORMATION INSTALLED.
		..1.		SCCBSER	REAL STORAGE ELEMENT RECONFIGURATION INSTALLED.
		...1		SCCBCARS	COPY AND REASSIGN STORAGE INSTALLED.
		.... 1...		SCCBESUM	EXTENDED STORAGE USABILITY MAP INSTALLED.
		.... .1..		SCCBESEI	EXTENDED STORAGE ELEMENT INFORMATION INSTALLED.
		.... ..1.		SCCBESER	EXTENDED STORAGE ELEMENT RECONFIGURATION INSTALLED.
		1		SCCBCARL	COPY AND REASSIGN STORAGE LIST INSTALLED.
51	(33)	CHARACTER	1	SCCBIFM4	INSTALLED FACILITY MAP BYTE 4.
		1...		SCCBVFR	VECTOR FEATURE RECONFIGURATION INSTALLED.
		.1..		SCCBEVNT	READ / WRITE EVENT INSTALLED.
		..1.		SCCBESUE	EXPANDED-STORAGE-USABILITY BIT MAP EXTENDED IS INSTALLED.
		...1		*	RESERVED.
		.... 1...		SCCBRRGI	READ RESOURCE GROUP INFORMATION INSTALLED.
		.... .111		*	RESERVED.
52	(34)	CHARACTER	4	SCCBIFM5	INSTALLED FACILITY MAP BYTES 5-8.
56	(38)	CHARACTER	8	SCCBR038	RESERVED.
64	(40)	BITSTRING	2	SCCBMRGI	MAXIMUM RESOURCE GROUP INSTALLED.
66	(42)	CHARACTER	6	SCCBR042	RESERVED.
72	(48)	UNSIGNED	2	SCCBMPFE	NUMBER OF ENTRIES.
74	(4A)	UNSIGNED	2	SCCBMPFO	OFFSET.
76	(4C)	CHARACTER	4	SCCBR04C	RESERVED.
80	(50)	CHARACTER	6	SCCBCONF	CONFIGURATION CHARACTERISTICS.
80	(50)	CHARACTER	1	SCCBCON1	BITS 0-7 OF CONFIGURATION CHARACTERISTICS.
		1...		SCCBBFY	CONFIGURATION IS RUNNING UNDER BFY.
		.1..		*	RESERVED.
		..1.		SCCBSOPF	SUPPRESSION ON PROTECTION FACILITY
		...1		SCCBIRIN	INITIATE RESET INSTALLED
		.... 1...		SCCBCSCF	STORE CHANNEL SUBSYSTEM / CHARACTERISTICS FACILITY IS INSTALLED.
		.... .11.		*	RESERVED.
		1		SCCBFSDM	Fast synchronous data mover
81	(51)	CHARACTER	1	SCCBCON2	BITS 8-15 OF CONFIGURATION CHARACTERISTICS.
		1...		*	RESERVED.
		.1..		SCCBCSLO	CSLO IS INSTALLED
		..11 1111		*	RESERVED.
82	(52)	CHARACTER	1	SCCBCON3	BITS 16-23 OF CONFIGURATION CHARACTERISTICS. (BYTE 82 OF SCCB).
		1...		*	RESERVED.
		.1..		SCCBDAOM	DEVICE-ACTIVE-ONLY MEASUREMENT FACILITY IS INSTALLED
		..11 111.		*	RESERVED.
		1		SCCBCKSM	CHECKSUM INSTR. INSTALLED
83	(53)	CHARACTER	1	SCCBCON4	BITS 24-31 OF CONFIGURATION CHARACTERISTICS. (BYTE 83 OF SCCB).
		1...		SCCBRP	RESUME PROGRAM INSTALLED
		.1..		SCCBPLO	PERFORM LOCKED OP. INST.
		..1.		*	Reserved
		...1		SCCBIR	Immediate and relative
		.... 1...		SCCBEL	extended length (MVCLE, CLCLE)
		.... .1..		SCCBBSA	BSA
		.... ..1.		SCCBBFP	Binary Floating Point is installed
		1		SCCBXLOG	Extended logical computation facility is installed
84	(54)	CHARACTER	1	SCCBCON5	BITS 32-39 OF CONFIGURATION CHARACTERISTICS. (BYTE 84 OF SCCB).
		1...		SCCBETOD	EXTENDED TOD CLOCK FACILITY IS INSTALLED.
		.1..		SCCBETF	Extended translation facility installed (TRE, CUUTF, CUTFU)
		..1.		SCCBLRF	Load-reversed facility
		...1		SCCBUNIC	Extended translation facility 2 installed (Unicode) (TP, PKA, UNPKA, PKU, UNPKU, MVCLU, CLCLU, TRTT, TRTO, TROT, TROO)
		.... 1...		SCCBSTSI	STSI INST. IS INSTALLED.
		.... .1..		*	RESERVED.
		.... ..1.		SCCBLPCL	LPAR CLUSTERING
		1		SCCBIFAF	IFA facility
85	(55)	CHARACTER	1	SCCBCON6	BYTE 85
		1111		*	RESERVED.
		.... 1...		SCCBSSRS	Sense Running Status is installed
		.... .11.		*	RESERVED.
		1		SCCBZARC	z/Architecture is installed
		1		SCCBESAM	z/Architecture is installed
86	(56)	UNSIGNED	4	SCCBRCCI	CAPACITY.
90	(5A)	CHARACTER	1	*	RESERVED.

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
91	(5B)	UNSIGNED	1	SCCBCARN	NUMBER OF ELEMENTS IN COPY AND REASSIGN LIST
91	(5B)	UNSIGNED	1	SCCBCN12	Flags, byte 91
		1111 1...		*	Reserved
		.... .1..		SCCBPER3	PER 3 is installed
		.... ..1.		*	Reserved
		1		SCCBLDI	List-directed IPL installed
92	(5C)	BITSTRING	4	SCCBETR	ETR-SYNC-CHECK TOLERANCE
96	(60)	CHARACTER	3	*	RESERVED.
99	(63)	UNSIGNED	1	SCCBGSTR	Guest real
100	(64)	UNSIGNED	4	SCCBSAIX	Real storage address increment in units of 1M. Size is a power of 2.
104	(68)	CHARACTER	8	SCCBSARX	Real Storage Address Range. Maximum storage increment number installed.
104	(68)	UNSIGNED	4	SCCBSARXH	High half of SCCBSARX
108	(6C)	UNSIGNED	4	SCCBSARXL	Low half of SCCBSARX
112	(70)	CHARACTER	4	*	RESERVED.
116	(74)	CHARACTER	1	SCCBB116	Byte 116
		1111		*	RESERVED
		.... 1...		SCCBSESF	
		.... .111		*	RESERVED
117	(75)	CHARACTER	1	SCCBB117	Byte 117
		111.		*	RESERVED
		...1		SCCBZAD	
118	(76)	CHARACTER	1	SCCBB118	Byte 118
		1111 11..		*	RESERVED
		.... ..1.		SCCBENCM	Ensemble communication facility is installed
		1		*	RESERVED
119	(77)	CHARACTER	1	*	RESERVED
120	(78)	SIGNED	2	SCCBHCPA	Highest possible CPU address
122	(7A)	CHARACTER	6	*	RESERVED.
128	(80)	CHARACTER	*	SCCBDATA	DATA ARRAYS.

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	16	SCCBCP	CPU INFORMATION ENTRY.
0	(0)	UNSIGNED	1	SCCBCPA	CPU ADDRESS.
1	(1)	UNSIGNED	1	SCCBTOD#	TOD CLOCK NUMBER FOR THIS CPU.
2	(2)	CHARACTER	2	*	RESERVED.
4	(4)	BITSTRING	1	SCCBCPFL	CPU CHARACTERISTIC FLAGS BYTE 1. (BIT POSITIONS 32-39.)
		1...		*	RESERVED, WAS SCCBVFIN
		.1..		*	RESERVED, WAS SCCBVFCN
		..1.		*	RESERVED, WAS SCCBVFSB
		...1		SCCBCRIN	CRYPTO FEATURE INSTALLED.
		 1111		*	RESERVED.
5	(5)	BITSTRING	1	SCCBCPF2	CPU CHARACTERISTIC FLAGS BYTE 2. (BIT POSITIONS 40-47.)
		1...		SCCBMPSB	PRIVATE SPACE BIT IS INSTALLED.
		.111 111.		*	RESERVED.
		1		SCCBPER2	PER 2 INSTALLED.
6	(6)	CHARACTER	1	*	RESERVED.
7	(7)	CHARACTER	7	*	RESERVED.
14	(E)	UNSIGNED	1	SCCBPTYP	Processor type code
15	(F)	CHARACTER	1	*	RESERVED.
		1111 111.		*	RESERVED.
		1		SCCBKSID	KSU ID OF INSTALLED CRYPTO FEATURE.

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	6	SCCBHSA	HSA INFORMATION ENTRY.
0	(0)	UNSIGNED	2	SCCBHSSZ	SIZE OF THIS HSA IN UNITS OF 4K (SCCBHUSZ).
2	(2)	ADDRESS	4	SCCBAHSA	ADDRESS OF THIS HSA.

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	2	SCCBMPF (*)	MPF INFO. ARRAY.
0	(0)	UNSIGNED	2	SCCBMPFY	MPF INFO. ENTRY.

SCCB Constants

Len	Type	Value	Name	Description
Comment				
SERVICE PROCESSOR ARCHITECTURE AND SUPPORTED CONSTANTS.				
End of Comment				
4	HEX	00000400	SCCB1K	ONE KILOBYTE (1K).
4	HEX	00100000	SCCB1M	ONE MEGABYTE (1M).
4	HEX	00001000	SCCBEBSZ	EXTENDED STORAGE BLOCK SIZE (4K).
2	DECIMAL	1	SCCBESII	EXTENDED STORAGE INCREMENT BLOCK INTERLEAVE INTERVAL.
4	HEX	00001000	SCCBFMSZ	FRAME SIZE (4K).
4	HEX	00001000	SCCBHUSZ	HSA UNIT SIZE (4K).
1	DECIMAL	0	SCCBPCPU	Processor type code for standard processor
1	DECIMAL	1	SCCBPICF	Processor type code for ICF
1	DECIMAL	2	SCCBPIFA	Processor type code for IFA
1	DECIMAL	5	SCCBPSUP	Processor type code for SUP

SCCB Cross Reference

Name	Hex Offset	Hex Value
SCCB	0	
SCCBAHSA	2	
SCCBBFP	53	02
SCCBBFY	50	80
SCCBBSA	53	04
SCCBB116	74	
SCCBB117	75	
SCCBB118	76	
SCCBCARL	32	01
SCCBCARN	5B	
SCCBCARS	32	10
SCCBCHPI	30	80
SCCBCHPR	30	20
SCCBCHPS	30	40
SCCBCKSM	52	01
SCCBCMDD	8	
SCCBCN12	5B	
SCCBCONF	50	
SCCBCON1	50	
SCCBCON2	51	
SCCBCON3	52	
SCCBCON4	53	
SCCBCON5	54	
SCCBCON6	55	
SCCBCP	0	
SCCBCPA	0	
SCCBCPFL	4	
SCCBCPF2	5	
SCCBCPUI	30	08
SCCBCPUR	30	04
SCCBCRIN	4	10
SCCBCSCF	50	08
SCCBCSLO	51	40
SCCBDAOM	52	40
SCCBDATA	80	
SCCBEL	53	08
SCCBENCM	76	02
SCCBEND	1000	
SCCBESAM	55	01
SCCBESEI	32	04
SCCBESER	32	02
SCCBESUE	33	20
SCCBESUM	32	08
SCCBETF	54	40
SCCBETOD	54	80
SCCBETR	5C	
SCCBEVNT	33	40
SCCBFLAG	2	
SCCBFSDM	50	01
SCCBGSTR	63	
SCCBHCPA	78	
SCCBHEAD	0	
SCCBHSA	0	
SCCBHSSZ	0	
SCCBIFAF	54	01
SCCBIFM	30	
SCCBIFM1	30	
SCCBIFM2	31	
SCCBIFM3	32	
SCCBIFM4	33	
SCCBIFM5	34	
SCCBIR	53	10
SCCBIRIN	50	10
SCCBITRC	31	08
SCCBKSID	F	01
SCCBLDI	5B	01
SCCBLNG	0	
SCCBLPCL	54	02
SCCBLPRM	31	04
SCCBLRF	54	20
SCCBMESE	28	
SCCBMESI	20	
SCCBMPF	0	
SCCBMPFE	48	
SCCBMPFO	4A	
SCCBMPFY	0	
SCCBMPSB	5	80
SCCBMRGI	40	
SCCBNCPS	10	
SCCBNHSA	14	
SCCBNXSB	24	
SCCBOCP	12	
SCCBOHSA	16	
SCCBOMR	31	40
SCCBPARM	18	
SCCBPER2	5	01
SCCBPER3	5B	04
SCCBPLO	53	40
SCCBPTYP	E	
SCCBRCC	7	
SCCBRCCI	56	
SCCBREAS	6	
SCCBRESP	6	
SCCBRP	53	80
SCCBRRGI	33	08
SCCBRSP	6	
SCCBRSTR	31	10
SCCBR00E	E	
SCCBR003	3	
SCCBR02A	2A	

SCCB Cross Reference

Name	Hex Offset	Hex Value
SCCBR038	38	
SCCBR04C	4C	
SCCBR042	42	
SCCBSAI	A	
SCCBSAIX	64	
SCCBSAR	8	
SCCBSARX	68	
SCCBSARXH	68	
SCCBSARXL	6C	
SCCBSBS	B	
SCCBSCPI	8	
SCCBSEI	32	40
SCCBSER	32	20
SCCBSESF	74	08
SCCBSGNL	31	80
SCCBSII	C	
SCCBSIR	32	80
SCCBSOPF	50	20
SCCBSSRS	55	08
SCCBSTSI	54	08
SCCBSTST	31	20
SCCBTOD#	1	
SCCBUNIC	54	10
SCCBVFR	33	80
SCCBVPRM	2C	
SCCBVPSM	2E	
SCCBVSS	2C	
SCCBWDAT	31	02
SCCBXLOG	53	01
SCCBZAD	75	10
SCCBZARC	55	01

SCD Information

SCD Heading Information

Common Name: SCD - HOT I/O Storage Collection Data
Macro ID: IOSDSCD
DSECT Name: SCD
Owning Component: I/O Supervisor (SC1C3)
Eye-Catcher ID: SCD
Offset: 0
Length: 4
Storage Attributes: Subpool: 245
Residency: Above 16Mb line
Size: 56 bytes
Created by: IOSRHDET
Pointed to by: The HDSCDANC fields in IOSRHIDT
Serialization: SCDs obtained and freed while holding the Hot I/O synch lock
Function: The SCD contains data used to determine whether a Hot condition exists. If a Hot condition is detected it also contains recovery information relating to the actions taken.

SCD Map

Offsets

Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	4048	SCDBLOCK	Block of SCDs
0	(0)	CHARACTER	4	SCDBID	Acronym 'SCDB'
4	(4)	UNSIGNED	1	SCDBDVGP	Device group this SCD block associated with (derived from 2nd character of device number - i.e. 0X00)
5	(5)	CHARACTER	3	*	Reserved
8	(8)	ADDRESS	4	SCDBNEXT	Pointer to the next SCD block for this device group
12	(C)	ADDRESS	4	SCDBPREV	Pointer to the previous SCD block for this device group
16	(10)	CHARACTER	56	SCDBSCD (4294967368:562130360)	SCDs
4048	(FD0)	CHARACTER	0	SCDBEND	

Offsets

Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	56	SCD	
0	(0)	CHARACTER	4	SCDID	Acronym 'SCD '
4	(4)	CHARACTER	2	SCDDEV#	Device number
6	(6)	UNSIGNED	1	SCDCHP	Channel path of interrupt which caused the Hot threshold to be exceeded
7	(7)	CHARACTER	1	SCDINUSE	SCD in use flags
		1...		SCDASSC	SCD associated with a UCB
		.1..		SCDSLIHW	SLIH waiting for interrupt in order to make device status status pending
		..1.		SCDRSCHC	Clear recovery scheduled but not completed
		...1		SCDRSCHF	Full recovery scheduled but not completed
		 1111		*	Reserved

Comment

Detection Information

End of Comment

8	(8)	CHARACTER	24	SCDDET	Detection information
8	(8)	CHARACTER	8	SCDHDTM	Time of Hot interrupt
8	(8)	UNSIGNED	4	SCDTM1	first word of time
12	(C)	CHARACTER	4	SCDTM2	second word of time
16	(10)	CHARACTER	1	SCDDIFPT	Different path flags - if on, interrupts have occurred over different path for the interrupt group (ICC/CCC, unit check, attention/DE, and other
		1...		SCDDPICC	ICC/CCC interrupt group
		.1..		SCDDPUC	Unit check interrupt group
		..1.		SCDDPATD	Attention/UDE interrupt group
		...1		SCDDPOTH	Other
17	(11)	CHARACTER	13	SCDGPTHS	
17	(11)	UNSIGNED	1	SCDGINDX	Group type of last interrupt - based on bit position (used as an index into SCDGLPUM and SCDGCNT)
18	(12)	BITSTRING	1	SCDGLPUM (4294967300:562121632)	LPUM from the last interrupt for the interrupt group or received for this group

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
22	(16)	SIGNED	2	SCDGCNT (4294967300:562119760)	Count of unsolicited interrupts for the interrupt group
30	(1E)	SIGNED	2	SCDTCNT	Total count of unsolicited interrupts
				Comment	
				Hot I/O detected information	
				End of Comment	
32	(20)	CHARACTER	8	SCDHOT	Information which is only filled in when a Hot condition is detected
32	(20)	ADDRESS	4	SCDHNEXT	Pointer to next hot device on the Hot device queue
36	(24)	ADDRESS	4	SCDXPTR	Pointer to extention which contains the last IRB
				Comment	
				Recovery Information	
				End of Comment	
40	(28)	CHARACTER	14	SCDRECV	Hot I/O recovery information
40	(28)	CHARACTER	5	SCDRINFO (4294967298:562123584)	Recovery action taken. Index: 01 = on non-recursion. 02 = on recursion
40	(28)	CHARACTER	1	*	Recovery flags
		1...		SCDRDFLT	Default processing specified in IOSRHIDT used
		.1..		SCDROPER	recovery action obtained from the operator
41	(29)	BITSTRING	1	SCDRMSG	Message type
		1...		SCDR110	IOS110A was the associated message
		.1..		SCDR111	IOS111A was the associated message
		..1.		SCDR112	IOS112A was the associated message
42	(2A)	UNSIGNED	1	SCDRACTN	Recovery action
43	(2B)	CHARACTER	2	SCDRDEVN	If this recovery was handled as a result of recovery for another device, that device number
50	(32)	CHARACTER	1	SCDROTHR	Miscellaneous recovery flags - Indicates actions (other than those listed above) which have been taken by recovery processing.
		1...		SCDRCLR	An attempt has been made to correct the Hot condition with a clear subchannel
		.1..		SCDRCHOF	Recovery has been bypassed because the channel path was offline when the Hot condition was detected.
		..1.		SCDRCHPI	Recovery has been bypassed because channel path recovery was in progress when the Hot condition was detected.
		...1		SCDRBOXD	Hot I/O recovery was not done because the device was already boxed when IOSRHREC was entered.
		.... 1...		SCDRUCBE	Hot I/O recovery was not done because the UCB could not be found when IOSRHREC was entered.
		.... .11.		*	Reserved
		1		SCDINDHI	An induced HOT I/O recovery condition exists.
51	(33)	CHARACTER	1	SCDCOTHR	Miscellaneous recovery flags - Indicates action (other than those listed above) or reason for no recovery action which pertains to the current recovery processing. These bits must correspond to those in SCDROTHR
		1...		SCDCCLR	An attempt being made to correct the Hot condition with a clear subchannel
		.1..		SCDCCHOF	Recovery is being bypassed because the channel path was offline when the Hot condition was detected.
		..1.		SCDCCHPI	Counts being decremented because channel path recovery was in progress when the Hot condition was detected. Channel path recovery may eliminate the problem
		...1		SCDCBOXD	Hot I/O recovery not being done because the device was already boxed when IOSRHREC was entered.
		.... 1...		SCDCUCBE	Hot I/O recovery was not done because the UCB could not be found when IOSRHREC was entered.
52	(34)	CHARACTER	1	SCDRINFO2 (4294967298:562128616)	More Recovery information that would not fit into SCDINFO
52	(34)	UNSIGNED	1	SCDRSSID	SSID
54	(36)	BITSTRING	1	SCDDIAG	Diagnostic Flags
		1...		SCDREVNT	Reset Event occured on a solicited status interrupt
		.111 1111		*	Reserved
55	(37)	UNSIGNED	1	SCDSSID	Subchannel set id
56	(38)	CHARACTER	0	SCDEND	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	68	SCDX	SCD storage which is obtain when hot I/O is detected and freed following recovery processing
0	(0)	CHARACTER	4	SCDXID	acronym 'SCDX'

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
4	(4)	CHARACTER	64	SCDXIRB	IRB from last interrupt
68	(44)	CHARACTER	0	*	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	24	PARMSRB	
0	(0)	ADDRESS	4	PARMUCB	UCB common segment address
4	(4)	ADDRESS	4	PARMSCD	SCD address or 0
8	(8)	CHARACTER	1	PARMRCTP	Type of Hot I/O recovery to be done: 01 = Full recovery. 02 = Clear subchannel only.
9	(9)	CHARACTER	1	PARMFLGS	
		1...		PARMRSCH	Recovery running as a result of an FRR reschedule
		.1..		PARMHREC	Schedule done by IOSRHREC
		..1.		PARMPIN	Pin token is valid
		...1 1111		*	Reserved
10	(A)	CHARACTER	2	*	Reserved
12	(C)	CHARACTER	8	PARMPTOK	Pin token
20	(14)	CHARACTER	0	*	Parm end

SCD Constants

Len	Type	Value	Name	Description
		Comment		
		constants for PARMRCTP		
		End of Comment		
1	DECIMAL	1	FULL	Full Hot I/O recovery to be done
1	DECIMAL	2	CLEAR	Clear subchannel recovery only
4	CHARACTER	SCDB	SCDBNAME	SCD block acronym
4	CHARACTER	SCD	SCDNAME	SCD element acronym
4	CHARACTER	SCDX	SCDXNAME	SCD extension acronym
1	DECIMAL	72	SCDB#SCD	Number of SCDs per SCD block

SCD Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
PARMFLGS	9		SCDDPOTH	10	10
PARMHREC	9	40	SCDDPUC	10	40
PARMPIN	9	20	SCDEND	38	
PARMPTOK	C		SCDGCNT	16	
PARMRCTP	8		SCDGINDX	11	
PARMRSCH	9	80	SCDGLPUM	12	
PARMSCD	4		SCDGPTHS	11	
PARMSRB	0		SCDHDTM	8	
PARMUCB	0		SCDHNEXT	20	
SCD	0		SCDHOT	20	
SCDASSC	7	80	SCDID	0	
SCDBDVGP	4		SCDINDHI	32	01
SCDBEND	FD0		SCDINUSE	7	
SCDBID	0		SCDRACTN	2A	
SCDBLOCK	0		SCDRBOXD	32	10
SCDBNEXT	8		SCDRCHOF	32	40
SCDBPREV	C		SCDRCHPI	32	20
SCDBSCD	10		SCDRCLR	32	80
SCDCBOXD	33	10	SCDRDEVN	2B	
SCDCCHOF	33	40	SCDRDFLT	28	80
SCDCCHPI	33	20	SCDRECV	28	
SCDCCLR	33	80	SCDREVNT	36	80
SCDCHP	6		SCDRINFO	28	
SCDCOTHR	33		SCDRINFO2	34	
SCDCUCBE	33	08	SCDRMSG	29	
SCDDET	8		SCDROPER	28	40
SCDDEV#	4		SCDROTHR	32	
SCDDIAG	36		SCDRSCHC	7	20
SCDDIFPT	10		SCDRSCHF	7	10
SCDDPATD	10	20	SCDRSSID	34	
SCDDPICC	10	80	SCDRUCBE	32	08

SCD Cross Reference

Name	Hex Offset	Hex Value
SCDR110	29	80
SCDR111	29	40
SCDR112	29	20
SCDSLIHW	7	40
SCDSSID	37	
SCDTCNT	1E	
SCDTM1	8	
SCDTM2	C	
SCDX	0	
SCDXID	0	
SCDXIRB	4	
SCDXPTR	24	

SCE Information

SCE Heading Information

Common Name: Slip Control Element
Macro ID: IHASCE
DSECT Name: SCE
Owning Component: SLIP (SCSLP)
Eye-Catcher ID: SCE
Offset: 0
Length: 4
Storage Attributes: Subpool: 239
Key: 0
Residency: ANY
Size: 96 bytes
Created by: IEECB909 when creating a SLIP trap.
Pointed to by: SHDRFWD field of the SHDR data area
SHDRBKWD field of the SHDR data area
SHDRFWD2 field of the SHDR data area
SHDRBWD2 field of the SHDR data area
SCEFWD field of the SCE data area
SCEBKWD field of the SCE data area
SCEIDQF field of the SCE data area
SCEIDQB field of the SCE data area
Serialization: Compare & Swap / Compare Double & Swap
on the following
fields: SHDRSEQ, SHDRCTR, SCECTR
Function: The SCE, with the variable area
(see IHASCVA), is the internal representation of the
SLIP operator command. SCE's are matched against system
conditions in order to determine SLIP trap
qualification.
The most recently set trap (SCE) is matched first.

SCE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	120	SCE	
0	(0)	CHARACTER	4	SCECBID	CONTROL BLOCK ID = SCE
4	(4)	ADDRESS	4	SCESCVA	PTR TO THE SCVA
8	(8)	CHARACTER	8	SCECTRFW	TO DELETE ENTRY,MUST CDS
8	(8)	ADDRESS	4	SCECTR	PROCESSOR SERIALIZATION CTR
12	(C)	ADDRESS	4	SCEFWD	FORWARD SCE CHAIN PRT
16	(10)	ADDRESS	4	SCEBKWD	BACKWARD SCE CHAIN PTR
20	(14)	CHARACTER	4	SCEFLGCS	LABEL TO CS FLAGS
20	(14)	BITSTRING	1	SCEFLG1	FLAGS
		1...		SCEDSABL	OFF=ENABLED,ON=DISABLED
		.1..		SCEDELP	DELETE IS PENDG ON PREV SCE
		..1.		SCEMATCH	TRAP HAS MATCHED AT LEAST ONCE SINCE ENABLED
		...1		SCETSO	OFF=CONSOLE ORIGN, ON=TSO ORIGIN
		.... 1...		SCEDEBUG	DEBUG SPEC
		.... .1..		SCEMHME	MODE=HOME SPEC
		.... ..1.		SCESTFP	STRACE, FAST PATH
		1		SCESASA	SYMBOLIC ASID SA
21	(15)	BITSTRING	1	SCEFLG2	FLAGS
		1...		SCERBERR	RBLEVEL=ERROR
		.1..		SCERBPRE	RBLEVEL=PREVIOUS
		..1.		SCERBNSV	RBLEVEL=NOTSVRB
		...1		SCEDPARM	DUMP PARMS SPEC
		.... 1...		SCELIST	LIST PARM SPEC
		.... .1..		SCETRDAT	TRDATA PARM SPEC
		.... ..1.		SCEDATA	DATA PARM SPEC
		1		SCEMLIM	MATCHLIM PARM SPEC
22	(16)	BITSTRING	1	SCEFLG3	Flags
		1...		SCETEXIT	Test Exit is used
		.1..		SCE742S	MSG IEE742I SENT
		..1.		SCETXIGD	TXIGD
		...1 1111		*	
23	(17)	BITSTRING	1	SCEPFLG	PER FLAGS
		1...		SCEPERSB	SUCCESSFUL BRANCH
		.1..		SCEPERIF	INSTRUCTION FETCH
		..1.		SCEPERSA	STORAGE ALTERATION

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		SCEPER	PER TYPE TRAP
		.... 1...		SCEPERSS	SA STURA
		.... .1..		SCEPERZAD	Zero Address Detection
		.... ..1.		SCEPERS2	PER 2 SB
		1		SCEM413S	IEA413I MSG SENT (SET ONLY FOR PER TRAPS)
24	(18)	BITSTRING	4	SCEAFLG	ACTION FLAGS
24	(18)	BITSTRING	1	SCEAFLG1	ACTION FLAGS
		1...		SCESVCD	ACTION IS SVC DUMP
		.1..		SCEWAIT	ACTION IS WAIT
		..1.		SCEIGNOR	ACTION IS IGNORE
		...1		SCENODMP	ACTION IS NODUMP
		.... 1...		SCETRACE	ACTION IS TRACE
		.... .1..		SCETRDMP	ACTION IS TRDUMP
		.... ..1.		SCESTRCE	ACTION IS STRACE WHICH IS MUTUALLY EXCLUSIVE WITH ALL OTHER ACTIONS
		1		SCESTDMP	ACTION IS STDUMP WHICH IS MUTUALLY EXCLUSIVE WITH ALL OTHER ACTIONS
25	(19)	BITSTRING	1	SCEAFLG2	ACTION FLAGS
		1...		SCENOSVD	ACTION IS NOSVCD
		.1..		SCENOSYA	ACTION IS NOSYSA
		..1.		SCENOSYM	ACTION IS NOSYSM
		...1		SCENOSYU	ACTION IS NOSYSU
		.... 1...		SCERCOVR	ACTION IS RECOVERY
		.... .1..		SCENOSUP	ACTION IS NOSUP
		.... ..1.		SCERECRD	ACTION IS RECORD
		1		SCESYNCD	ACTION IS SYNCH SVCD
26	(1A)	BITSTRING	1	SCEAFLG3	Action flags 3
		1...		SCEATARGETID	Activate target trap
		.1..		SCEAREFBEFOR	Refresh before other action
		..1.		SCEAREFAFTER	Refresh after other action
		...1		SCEASTOPGTF	Stop gtf
		.... 1...		SCESUBTRAP	Subtrap
		.... .1..		SCEAEXIT	Action is AEXIT
		11		*	changed from 2 bits
27	(1B)	BITSTRING	1	SCEAFLG4	Action flags 4
28	(1C)	BITSTRING	1	*	
		1...		SCESASELECT	SA SELECTION
		.1..		SCEREMOTE	Remote specified
		..1.		SCEREMOTECOND	Remote/Cond
		...1		SCEOK	Ok was specified
		.... 1...		SCELPAMOD	LPAMOD was specified
		.... .1..		SCELPAEP	LPAEP was specified
		.... ..1.		SCEMSGID	MSGID was specified
		1		SCESTDATA	STDATA was specified
29	(1D)	BITSTRING	1	*	This byte is serialized by ShdrSeq
		1...		SCESEEN	Sce was scanned
		.1..		SCESASELECTINEFFECT	ASTE bits have been set for all of the spaces specified in the trap
		..11 1111		*	Unused
30	(1E)	CHARACTER	2	*	Unused
32	(20)	CHARACTER	4	SCEID	TRAP IDENTIFIER
36	(24)	CHARACTER	4	SCEMSG	MESSAGE IND
36	(24)	BITSTRING	1	SCEMFLGS	MESSAGE FLAGS
		1...		SCEM411M	411 MATCHLIM MSG
		.1..		SCEM411P	411 PRCNTLIM MSG
		..1.		SCEM742	IEE742I MSG FLAG
		...1		SCEMDELY	NO MSG, JUST PAUSE
		.... 1...		SCEM413	IEA413I MSG FLAG
		.... .1..		SCEM743	IEE743I MSG FLAG
		.... ..1.		SCEM424	IEA424I MSG FLAG
		1		SCEM425	IEA425I MSG FLAG
37	(25)	ADDRESS	1	SCEM992	IEA992I MSG CTR
38	(26)	UNSIGNED	2	SCEM412	IEA412I MSG CTR
40	(28)	CHARACTER	8	SCETSOU	TSO USER ID
48	(30)	CHARACTER	8	SCEIDQE	-> SCE'S
48	(30)	ADDRESS	4	SCEIDQF	FORWARD POINTER
52	(34)	ADDRESS	4	SCEIDQB	BACKWARD POINTER
56	(38)	BITSTRING	3	SCEMSG2	MORE MESSAGES
		1...		SCEM426	MESSAGE IEA426I
		.1..		SCEM039	MESSAGE IEA039I
		..1.		SCEM406ADDRESSSPACEINACTIVE1	Issue msg406 with reason address space inactive for 1st range parm
		...1		SCEM406ADDRESSSPACEINACTIVE2	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Issue msg406 with reason address space inactive for 2nd range parm
		.... 1...		SCEM406RANGENOTDETERMINED1	
					Issue msg406 with reason address space inactive for 1st range parm
		.... .1..		SCEM406RANGENOTDETERMINED2	
					Issue msg406 with reason address space inactive for 2nd range parm
		.... ..1.		SCEM406PVTMODPROBLEM	
					Could not resolve pvtmod range
		1		SCEM406COULDNOTSETPERRANGE	
					Issue msg 406 with reason unable to set per range
57	(39)	1...		SCEM727	Trap was enabled dynamically
		.1..		SCEM405REFBEFOR	
					Must issue msg IEE405I for RefBefor keyword
		..1.		SCEM405REFAFTER	
					Must issue msg IEE405I for RefAfter keyword
		...1		SCEM417CANNOTRESOLVESYSLISTADDR	
					Issue msg 417I because one or more system names could not be looked up by address while processing a match with the SYSLIST keyword
		.... 1...		SCEM087CANNOTRESOLVEAPARM1	
					Issue message IEE087I because APARM1 could not be resolved so the AEXIT routine was not invoked when the trap hit
59	(3B)	BITSTRING	1	SCEACTIONIND	Other flags requiring CS when set
		1...		SCEPENDINGDISABLED	
					Indicates that the SCE is to be disabled by the command processor
		.111 1111		*	Unused
60	(3C)	CHARACTER	4	SCECONSID	CONSOLE ID FOR WTO
60	(3C)	CHARACTER	2	SCEORIGN	ORIGINATOR OF TRAP
60	(3C)	CHARACTER	2	SCEASID	TSO TERMINALS ASID
64	(40)	CHARACTER	8	SCECART	CART FOR WTO
72	(48)	ADDRESS	4	SCESTRLISTPTR	
					Pointer to the STRLIST options specified for the local system
76	(4C)	CHARACTER	16	SCEIDGROUP	Id group
92	(5C)	CHARACTER	4	SCETARGETID	Target id for PER traps only
96	(60)	CHARACTER	12	SCEMSG992INFO	
					Jobname/Asid info for msgs IEA992I/IEA989I
96	(60)	CHARACTER	4	SCEMSG992INFOLOCKWORD	
					Lockword for msg IEA992I
96	(60)	CHARACTER	2	SCEMSG992INFOASID	
					Asid of space where trap matched
98	(62)	CHARACTER	1	*	Reserved
99	(63)	UNSIGNED	1	SCEMSG992INFOLOCK	
					Lock field
100	(64)	CHARACTER	8	SCEMSG992INFOJOBNAME	
					Jobname of unit of work that matched
108	(6C)	CHARACTER	4	SCEMSG412INFO	
					Return/reason code pair for msg IEA412I
108	(6C)	CHARACTER	2	*	Reserved
110	(6E)	CHARACTER	1	SCEMSG412INFOREASONCODE	
					Reason code
111	(6F)	CHARACTER	1	SCEMSG412INFORETURNCODE	
					Return code
112	(70)	CHARACTER	8	*	Reserved
120	(78)	CHARACTER	0	*	Reserved

SCE Constants

Len	Type	Value	Name	Description
			Comment	
			The following fields are used to serialize the SceMsg992Info area.	
			End of Comment	
4	HEX	000000FF	SCEMSG992INFOLOCKMASK	
				Used for masking the lockword and obtaining the value of the lock
4	DECIMAL	2	SCEMSG992INFOVALUESET	
				Jobname/Asid has been inserted into Sce
4	DECIMAL	1	SCEMSG992INFOINUSE	
				Jobname/Asid is being inserted into Sce
4	DECIMAL	0	SCEMSG992INFOAVAILABLE	
				Jobname/Asid fields in Sce may be set with new values

SCE Cross Reference

Len	Type	Value	Name	Description
		Comment		
		THE FOLLOWING ARE CONSTANTS THAT SHOULD BE USED WHEN SETTING BITS IN THE SCE VIA THE CS INSTRUCTION. THE BIT WHICH EACH OF THE FOLLOWING MASKS SETS IS GIVEN IN THE COMMENT ON THAT LINE. THE SCEX.... FORM IS FOR SETTING THE BIT ON AND THE SCEY.... FORM IS FOR SETTING THE BIT OFF.		
		End of Comment		
4	HEX	80000000	SCEXDSBL	SCEDSABL
4	HEX	40000000	SCEXDELP	SCEDELP
4	HEX	BFFFFFFF	SCEYDELP	SCEDELP
4	HEX	20000000	SCEXMTCH	SCEMATCH
4	HEX	80000000	SCEX411M	SCEM411M
4	HEX	7FFFFFFF	SCEY411M	SCEM411M
4	HEX	40000000	SCEX411P	SCEM411P
4	HEX	BFFFFFFF	SCEY411P	SCEM411P
4	HEX	20000000	SCEX742	SCEM742
4	HEX	DFFFFFFF	SCEY742	SCEM742
4	HEX	10000000	SCEXDELY	SCEMDELY
4	HEX	EFFFFFFF	SCEYDELY	SCEMDELY
4	HEX	08000000	SCEX413	SCEM413
4	HEX	F7FFFFFF	SCEY413	SCEM413
4	HEX	04000000	SCEX743	SCEM743
4	HEX	FBFFFFFF	SCEY743	SCEM743
4	HEX	02000000	SCEX424	SCEM424
4	HEX	FDFFFFFF	SCEY424	SCEM424
4	HEX	01000000	SCEX425	SCEM425
4	HEX	FEFFFFFF	SCEY425	SCEM425
4	NUMB HEX	7FFFBAD	KUBSVSA	Used to prevent UBS msg
		Comment		
		The following mask PerEventBitMask is used to obtain the Per Event Bit for SB, IF, SA, SAS, or ZAD from SCEPFLG field. The Per Event Bit will then be set in the control register 9.		
		End of Comment		
1	HEX	EC	PEREVENTBITMASK	Per Event Bit Mask
		Comment		
		The following mask PerTrapTypeBitMask is used to obtain the Per Trap Type bit for SB, IF, SA, or ZAD and the PER type bit from SCEPFLG field.		
		End of Comment		
1	HEX	F4	PERTRAPTYPEBITMASK	Per Trap Type bit mask

SCE Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
SCE	0		SCECTRFW	8	
SCEACTIONIND	3B		SCEDATA	15	02
SCEAEXIT	1A	04	SCEDEBUG	14	08
SCEAFLG	18		SCEDELP	14	40
SCEAFLG1	18		SCEDPARM	15	10
SCEAFLG2	19		SCEDSABL	14	80
SCEAFLG3	1A		SCEFLGCS	14	
SCEAFLG4	1B		SCEFLG1	14	
SCEAREFAFTER	1A	20	SCEFLG2	15	
SCEAREFBEFOR	1A	40	SCEFLG3	16	
SCEASID	3C		SCEFWD	C	
SCEASTOPGTF	1A	10	SCEID	20	
SCEATARGETID	1A	80	SCEIDGROUP	4C	
SCEBKWD	10		SCEIDQB	34	
SCECART	40		SCEIDQE	30	
SCECBID	0		SCEIDQF	30	
SCECONSID	3C		SCEIGNOR	18	20
SCECTR	8		SCELIST	15	08

Name	Hex Offset	Hex Value
SCELPAEP	1C	04
SCELPAMOD	1C	08
SCEMATCH	14	20
SCEMDELY	24	10
SCEMFLGS	24	
SCEMHME	14	04
SCEMLIM	15	01
SCEMSG	24	
SCEMSGID	1C	02
SCEMSG2	38	
SCEMSG412INFO	6C	
SCEMSG412INFOREASONCODE	6E	
SCEMSG412INFORETURNCODE	6F	
SCEMSG992INFO	60	
SCEMSG992INFOASID	60	
SCEMSG992INFOJOBNAME	64	
SCEMSG992INFOLOCK	63	
SCEMSG992INFOLOCKWORD	60	
SCEM039	38	40
SCEM087CANNOTRESOLVEAPARM1	39	08
SCEM405REFAFTER	39	20
SCEM405REFBEFOR	39	40
SCEM406ADDRESSSPACEINACTIVE1	38	20
SCEM406ADDRESSSPACEINACTIVE2	38	10
SCEM406COULDNOTSETPERRANGE	38	01
SCEM406PVTMODPROBLEM	38	02
SCEM406RANGENOTDETERMINED1	38	08
SCEM406RANGENOTDETERMINED2	38	04
SCEM411M	24	80
SCEM411P	24	40
SCEM412	26	
SCEM413	24	08
SCEM413S	17	01
SCEM417CANNOTRESOLVESYSLISTADDR	39	10
SCEM424	24	02
SCEM425	24	01
SCEM426	38	80
SCEM727	39	80
SCEM742	24	20
SCEM743	24	04
SCEM992	25	
SCENODMP	18	10
SCENOSUP	19	04
SCENOSVD	19	80
SCENOSYA	19	40
SCENOSYM	19	20
SCENOSYU	19	10
SCEOK	1C	10
SCEORIGN	3C	
SCEPENDINGDISABLED	3B	80
SCEPER	17	10
SCEPERIF	17	40
SCEPERSA	17	20
SCEPERSB	17	80
SCEPERSS	17	08
SCEPERS2	17	02
SCEPERZAD	17	04
SCEPFLG	17	
SCERBERR	15	80
SCERBNSV	15	20
SCERBPRE	15	40
SCERCOVR	19	08
SCERECRD	19	02
SCEREMOTE	1C	40
SCEREMOTECOND	1C	20
SCESASA	14	01
SCESASELECT	1C	80
SCESASELECTINEFFECT	1D	40
SCESCVA	4	
SCESEEN	1D	80
SCESTDATA	1C	01
SCESTDMP	18	01
SCESTFP	14	02
SCESTRCE	18	02
SCESTRLISTPTR	48	
SCESUBTRAP	1A	08
SCESVCD	18	80
SCESYNCD	19	01
SCETARGETID	5C	
SCETEXIT	16	80
SCETRACE	18	08
SCETRDAT	15	04
SCETRDMP	18	04
SCETSO	14	10
SCETSOU	28	
SCETXIGD	16	20
SCEWAIT	18	40
SCE742S	16	40

SCFS Information

SCFS Heading Information

Common Name: SUPERVISOR CONTROL FLIH SAVEAREA
Macro ID: IHASCFS
DSECT Name: SCFS
Owning Component: SUPERVISOR CONTROL (SC1C5)
Eye-Catcher ID: SCFS
Offset: 0
Length: 4
Storage Attributes: Subpool: 239 (FETCH-PROTECTED)
Key: 0
Size: 824 BYTES
Created by: NIP AND RECONFIGURATION
Pointed to by: PSASCFS
WSACSCFS
Serialization: DISABLEMENT
Function: CONTAINS STATUS INFORMATION SAVED BY THE EXTERNAL FLIH AND THE I/O FLIH.

SCFS Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SCFS	
0	(0)	CHARACTER	4	SCFSSCFS	SCFS ACRONYM.
				Comment	
				I/O FLIH GENERAL REGISTER SAVEAREA 1	
				End of Comment	
4	(4)	BITSTRING	64	SCFSIGR1 (0)	I/O FLIH GENERAL REGISTER SAVEAREA 1.
4	(4)	SIGNED	4	SCFSI1G0	GENERAL REGISTER 0
8	(8)	SIGNED	4	SCFSI1G1	GENERAL REGISTER 1
12	(C)	SIGNED	4	SCFSI1G2	GENERAL REGISTER 2
16	(10)	SIGNED	4	SCFSI1G3	GENERAL REGISTER 3
20	(14)	SIGNED	4	SCFSI1G4	GENERAL REGISTER 4
24	(18)	SIGNED	4	SCFSI1G5	GENERAL REGISTER 5
28	(1C)	SIGNED	4	SCFSI1G6	GENERAL REGISTER 6
32	(20)	SIGNED	4	SCFSI1G7	GENERAL REGISTER 7
36	(24)	SIGNED	4	SCFSI1G8	GENERAL REGISTER 8
40	(28)	SIGNED	4	SCFSI1G9	GENERAL REGISTER 9
44	(2C)	SIGNED	4	SCFSI1GA	GENERAL REGISTER 10
48	(30)	SIGNED	4	SCFSI1GB	GENERAL REGISTER 11
52	(34)	SIGNED	4	SCFSI1GC	GENERAL REGISTER 12
56	(38)	SIGNED	4	SCFSI1GD	GENERAL REGISTER 13
60	(3C)	SIGNED	4	SCFSI1GE	GENERAL REGISTER 14
64	(40)	SIGNED	4	SCFSI1GF	GENERAL REGISTER 15
				Comment	
				I/O FLIH ACCESS REGISTER SAVEAREA 1	
				End of Comment	
68	(44)	BITSTRING	64	SCFSIAR1 (0)	I/O FLIH ACCESS REGISTER SAVEAREA 1.
68	(44)	SIGNED	4	SCFSI1A0	ACCESS REGISTER 0
72	(48)	SIGNED	4	SCFSI1A1	ACCESS REGISTER 1
76	(4C)	SIGNED	4	SCFSI1A2	ACCESS REGISTER 2
80	(50)	SIGNED	4	SCFSI1A3	ACCESS REGISTER 3
84	(54)	SIGNED	4	SCFSI1A4	ACCESS REGISTER 4
88	(58)	SIGNED	4	SCFSI1A5	ACCESS REGISTER 5
92	(5C)	SIGNED	4	SCFSI1A6	ACCESS REGISTER 6
96	(60)	SIGNED	4	SCFSI1A7	ACCESS REGISTER 7
100	(64)	SIGNED	4	SCFSI1A8	ACCESS REGISTER 8
104	(68)	SIGNED	4	SCFSI1A9	ACCESS REGISTER 9
108	(6C)	SIGNED	4	SCFSI1AA	ACCESS REGISTER 10
112	(70)	SIGNED	4	SCFSI1AB	ACCESS REGISTER 11
116	(74)	SIGNED	4	SCFSI1AC	ACCESS REGISTER 12
120	(78)	SIGNED	4	SCFSI1AD	ACCESS REGISTER 13
124	(7C)	SIGNED	4	SCFSI1AE	ACCESS REGISTER 14

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
128	(80)	SIGNED	4	SCFSI1AF	ACCESS REGISTER 15
				Comment	
				I/O FLIH CONTROL REGISTER SAVEAREA 1	
				End of Comment	
132	(84)	BITSTRING	64	SCFSR084	WAS SCFSICR1
196	(C4)	BITSTRING	1	SCFSIOFL	I/O FLIH FLAGS
		1...		SCFSIOSA	"X'80'" SUBSPACE ACTIVE AT TIME OF INTERRUPT
197	(C5)	BITSTRING	1	SCFSR0C5	RESERVED
198	(C6)	SIGNED	2	SCFSXNTC	EXTERNAL FLIH INTERRUPT CODE SAVE AREA.
200	(C8)	BITSTRING	8	SCFSXPS1	External FLIH PSW Savearea 1. While we have the real copy in SCFSXPSW161, we still set this, as we need to "scrunch" for RBOPSW.
				Comment	
				EXTERNAL FLIH GENERAL REGISTER SAVEAREA 1	
				End of Comment	
208	(D0)	BITSTRING	64	SCFSXGR1 (0)	EXTERNAL FLIH GENERAL REGISTER SAVEAREA 1.
208	(D0)	SIGNED	4	SCFSX1G0	GENERAL REGISTER 0
212	(D4)	SIGNED	4	SCFSX1G1	GENERAL REGISTER 1
216	(D8)	SIGNED	4	SCFSX1G2	GENERAL REGISTER 2
220	(DC)	SIGNED	4	SCFSX1G3	GENERAL REGISTER 3
224	(E0)	SIGNED	4	SCFSX1G4	GENERAL REGISTER 4
228	(E4)	SIGNED	4	SCFSX1G5	GENERAL REGISTER 5
232	(E8)	SIGNED	4	SCFSX1G6	GENERAL REGISTER 6
236	(EC)	SIGNED	4	SCFSX1G7	GENERAL REGISTER 7
240	(F0)	SIGNED	4	SCFSX1G8	GENERAL REGISTER 8
244	(F4)	SIGNED	4	SCFSX1G9	GENERAL REGISTER 9
248	(F8)	SIGNED	4	SCFSX1GA	GENERAL REGISTER 10
252	(FC)	SIGNED	4	SCFSX1GB	GENERAL REGISTER 11
256	(100)	SIGNED	4	SCFSX1GC	GENERAL REGISTER 12
260	(104)	SIGNED	4	SCFSX1GD	GENERAL REGISTER 13
264	(108)	SIGNED	4	SCFSX1GE	GENERAL REGISTER 14
268	(10C)	SIGNED	4	SCFSX1GF	GENERAL REGISTER 15
				Comment	
				EXTERNAL FLIH ACCESS REGISTER SAVEAREA 1	
				End of Comment	
272	(110)	BITSTRING	64	SCFSXAR1 (0)	EXTERNAL FLIH ACCESS REGISTER SAVEAREA 1.
272	(110)	SIGNED	4	SCFSX1A0	ACCESS REGISTER 0
276	(114)	SIGNED	4	SCFSX1A1	ACCESS REGISTER 1
280	(118)	SIGNED	4	SCFSX1A2	ACCESS REGISTER 2
284	(11C)	SIGNED	4	SCFSX1A3	ACCESS REGISTER 3
288	(120)	SIGNED	4	SCFSX1A4	ACCESS REGISTER 4
292	(124)	SIGNED	4	SCFSX1A5	ACCESS REGISTER 5
296	(128)	SIGNED	4	SCFSX1A6	ACCESS REGISTER 6
300	(12C)	SIGNED	4	SCFSX1A7	ACCESS REGISTER 7
304	(130)	SIGNED	4	SCFSX1A8	ACCESS REGISTER 8
308	(134)	SIGNED	4	SCFSX1A9	ACCESS REGISTER 9
312	(138)	SIGNED	4	SCFSX1AA	ACCESS REGISTER 10
316	(13C)	SIGNED	4	SCFSX1AB	ACCESS REGISTER 11
320	(140)	SIGNED	4	SCFSX1AC	ACCESS REGISTER 12
324	(144)	SIGNED	4	SCFSX1AD	ACCESS REGISTER 13
328	(148)	SIGNED	4	SCFSX1AE	ACCESS REGISTER 14
332	(14C)	SIGNED	4	SCFSX1AF	ACCESS REGISTER 15
336	(150)	BITSTRING	1	SCFSR150 (8)	RESERVED
344	(158)	BITSTRING	1	SCFSXRSA (72)	Register savearea for the External FLIH to use when calling services 1
				Comment	
				EXTERNAL FLIH GENERAL REGISTER SAVEAREA 2	
				End of Comment	
416	(1A0)	BITSTRING	64	SCFSXGR2 (0)	EXTERNAL FLIH GENERAL REGISTER SAVEAREA 2.
416	(1A0)	SIGNED	4	SCFSX2G0	GENERAL REGISTER 0
420	(1A4)	SIGNED	4	SCFSX2G1	GENERAL REGISTER 1
424	(1A8)	SIGNED	4	SCFSX2G2	GENERAL REGISTER 2
428	(1AC)	SIGNED	4	SCFSX2G3	GENERAL REGISTER 3

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
432	(1B0)	SIGNED	4	SCFSX2G4	GENERAL REGISTER 4
436	(1B4)	SIGNED	4	SCFSX2G5	GENERAL REGISTER 5
440	(1B8)	SIGNED	4	SCFSX2G6	GENERAL REGISTER 6
444	(1BC)	SIGNED	4	SCFSX2G7	GENERAL REGISTER 7
448	(1C0)	SIGNED	4	SCFSX2G8	GENERAL REGISTER 8
452	(1C4)	SIGNED	4	SCFSX2G9	GENERAL REGISTER 9
456	(1C8)	SIGNED	4	SCFSX2GA	GENERAL REGISTER 10
460	(1CC)	SIGNED	4	SCFSX2GB	GENERAL REGISTER 11
464	(1D0)	SIGNED	4	SCFSX2GC	GENERAL REGISTER 12
468	(1D4)	SIGNED	4	SCFSX2GD	GENERAL REGISTER 13
472	(1D8)	SIGNED	4	SCFSX2GE	GENERAL REGISTER 14
476	(1DC)	SIGNED	4	SCFSX2GF	GENERAL REGISTER 15
				Comment	
				EXTERNAL FLIH ACCESS REGISTER SAVEAREA 2	
				End of Comment	
480	(1E0)	BITSTRING	64	SCFSXAR2 (0)	EXTERNAL FLIH ACCESS REGISTER SAVEAREA 2.
480	(1E0)	SIGNED	4	SCFSX2A0	ACCESS REGISTER 0
484	(1E4)	SIGNED	4	SCFSX2A1	ACCESS REGISTER 1
488	(1E8)	SIGNED	4	SCFSX2A2	ACCESS REGISTER 2
492	(1EC)	SIGNED	4	SCFSX2A3	ACCESS REGISTER 3
496	(1F0)	SIGNED	4	SCFSX2A4	ACCESS REGISTER 4
500	(1F4)	SIGNED	4	SCFSX2A5	ACCESS REGISTER 5
504	(1F8)	SIGNED	4	SCFSX2A6	ACCESS REGISTER 6
508	(1FC)	SIGNED	4	SCFSX2A7	ACCESS REGISTER 7
512	(200)	SIGNED	4	SCFSX2A8	ACCESS REGISTER 8
516	(204)	SIGNED	4	SCFSX2A9	ACCESS REGISTER 9
520	(208)	SIGNED	4	SCFSX2AA	ACCESS REGISTER 10
524	(20C)	SIGNED	4	SCFSX2AB	ACCESS REGISTER 11
528	(210)	SIGNED	4	SCFSX2AC	ACCESS REGISTER 12
532	(214)	SIGNED	4	SCFSX2AD	ACCESS REGISTER 13
536	(218)	SIGNED	4	SCFSX2AE	ACCESS REGISTER 14
540	(21C)	SIGNED	4	SCFSX2AF	ACCESS REGISTER 15
544	(220)	BITSTRING	48	SCFSR220	RESERVED 1
592	(250)	BITSTRING	16	SCFSXPSW161	EXTERNAL FLIH 16-BYTE PSW SAVEAREA 1.
608	(260)	BITSTRING	16	SCFSXPSW162	EXTERNAL FLIH 16-BYTE PSW SAVEAREA 2.
				Comment	
				EXTERNAL FLIH GENERAL REGISTER SAVEAREA 3	
				End of Comment	
624	(270)	BITSTRING	64	SCFSXGR3 (0)	EXTERNAL FLIH GENERAL REGISTER SAVEAREA 3.
624	(270)	SIGNED	4	SCFSX3G0	GENERAL REGISTER 0
628	(274)	SIGNED	4	SCFSX3G1	GENERAL REGISTER 1
632	(278)	SIGNED	4	SCFSX3G2	GENERAL REGISTER 2
636	(27C)	SIGNED	4	SCFSX3G3	GENERAL REGISTER 3
640	(280)	SIGNED	4	SCFSX3G4	GENERAL REGISTER 4
644	(284)	SIGNED	4	SCFSX3G5	GENERAL REGISTER 5
648	(288)	SIGNED	4	SCFSX3G6	GENERAL REGISTER 6
652	(28C)	SIGNED	4	SCFSX3G7	GENERAL REGISTER 7
656	(290)	SIGNED	4	SCFSX3G8	GENERAL REGISTER 8
660	(294)	SIGNED	4	SCFSX3G9	GENERAL REGISTER 9
664	(298)	SIGNED	4	SCFSX3GA	GENERAL REGISTER 10
668	(29C)	SIGNED	4	SCFSX3GB	GENERAL REGISTER 11
672	(2A0)	SIGNED	4	SCFSX3GC	GENERAL REGISTER 12
676	(2A4)	SIGNED	4	SCFSX3GD	GENERAL REGISTER 13
680	(2A8)	SIGNED	4	SCFSX3GE	GENERAL REGISTER 14
684	(2AC)	SIGNED	4	SCFSX3GF	GENERAL REGISTER 15
				Comment	
				EXTERNAL FLIH ACCESS REGISTER SAVEAREA 3	
				End of Comment	
688	(2B0)	BITSTRING	64	SCFSXAR3 (0)	EXTERNAL FLIH ACCESS REGISTER SAVEAREA 3.
688	(2B0)	SIGNED	4	SCFSX3A0	ACCESS REGISTER 0
692	(2B4)	SIGNED	4	SCFSX3A1	ACCESS REGISTER 1
696	(2B8)	SIGNED	4	SCFSX3A2	ACCESS REGISTER 2
700	(2BC)	SIGNED	4	SCFSX3A3	ACCESS REGISTER 3
704	(2C0)	SIGNED	4	SCFSX3A4	ACCESS REGISTER 4

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
708	(2C4)	SIGNED	4	SCFSX3A5	ACCESS REGISTER 5
712	(2C8)	SIGNED	4	SCFSX3A6	ACCESS REGISTER 6
716	(2CC)	SIGNED	4	SCFSX3A7	ACCESS REGISTER 7
720	(2D0)	SIGNED	4	SCFSX3A8	ACCESS REGISTER 8
724	(2D4)	SIGNED	4	SCFSX3A9	ACCESS REGISTER 9
728	(2D8)	SIGNED	4	SCFSX3AA	ACCESS REGISTER 10
732	(2DC)	SIGNED	4	SCFSX3AB	ACCESS REGISTER 11
736	(2E0)	SIGNED	4	SCFSX3AC	ACCESS REGISTER 12
740	(2E4)	SIGNED	4	SCFSX3AD	ACCESS REGISTER 13
744	(2E8)	SIGNED	4	SCFSX3AE	ACCESS REGISTER 14
748	(2EC)	SIGNED	4	SCFSX3AF	ACCESS REGISTER 15
752	(2F0)	SIGNED	4	SCFSR2F0	RESERVED-XMEM REGS MUST BE ON DWORD BDY

Comment

EXTERNAL FLIH CONTROL REGISTER SAVEAREA 3

End of Comment

756	(2F4)	BITSTRING	64	SCFSR2F4	RESERVED. WAS SCFSXCR3
820	(334)	SIGNED	4	SCFSR334	RESERVED.
824	(338)	BITSTRING	1	SCFSI641 (64)	I/O FLIH GPR BITS 0-31 SAVEAREA 1
888	(378)	BITSTRING	1	SCFSX641 (64)	EXTERNAL FLIH GPR BITS 0-31 SAVEAREA 1
952	(3B8)	DBL WORD	8	(0)	
952	(3B8)	BITSTRING	128	SCFSXCR1 (0)	EXTERNAL FLIH CR SAVEAREA 1
952	(3B8)	DBL WORD	8	SCFSX1C0	CONTROL REGISTER 0
960	(3C0)	DBL WORD	8	SCFSX1C1	CONTROL REGISTER 1
968	(3C8)	DBL WORD	8	SCFSX1C2	CONTROL REGISTER 2
976	(3D0)	CHARACTER	16	SCFSXXM1 (0)	CROSS MEMORY CONTROL REGISTER SAVEAREA1
976	(3D0)	DBL WORD	8	SCFSX1C3 (0)	CONTROL REGISTER 3
976	(3D0)	SIGNED	4	SCFSX1SINS	SASTE IN
980	(3D4)	SIGNED	2	SCFSX1PK	PROGRAM KEY MASK
982	(3D6)	SIGNED	2	SCFSX1SS	SASN
984	(3D8)	DBL WORD	8	SCFSX1C4 (0)	CONTROL REGISTER 4
984	(3D8)	SIGNED	4	SCFSX1PINS	PASTE IN
988	(3DC)	SIGNED	2	SCFSX1AX	AX
990	(3DE)	SIGNED	2	SCFSX1PS	PASN
992	(3E0)	DBL WORD	8	SCFSX1C5	CONTROL REGISTER 5
1000	(3E8)	DBL WORD	8	SCFSX1C6	CONTROL REGISTER 6
1008	(3F0)	DBL WORD	8	SCFSX1C7	CONTROL REGISTER 7
1016	(3F8)	DBL WORD	8	SCFSX1C8 (0)	CONTROL REGISTER 8
1016	(3F8)	SIGNED	4		
1020	(3FC)	SIGNED	2	SCFS1EAX	EAX VALUE
1022	(3FE)	SIGNED	2		
1024	(400)	DBL WORD	8	SCFSX1C9	CONTROL REGISTER 9
1032	(408)	DBL WORD	8	SCFSX1CA	CONTROL REGISTER 10
1040	(410)	DBL WORD	8	SCFSX1CB	CONTROL REGISTER 11
1048	(418)	DBL WORD	8	SCFSX1CC	CONTROL REGISTER 12
1056	(420)	DBL WORD	8	SCFSX1CD	CONTROL REGISTER 13
1064	(428)	DBL WORD	8	SCFSX1CE	CONTROL REGISTER 14
1072	(430)	DBL WORD	8	SCFSX1CF (0)	CONTROL REGISTER 15
1072	(430)	SIGNED	4	SCFSX1CFH	CONTROL REGISTER 15 HIGH HALF
1076	(434)	SIGNED	4	SCFSX1CFL	CONTROL REGISTER 15 LOW HALF
1080	(438)	BITSTRING	1	SCFSX642 (64)	EXTERNAL FLIH GPR BITS 0-31 SAVEAREA 2
1144	(478)	BITSTRING	1	SCFSX643 (64)	EXTERNAL FLIH GPR BITS 0-31 SAVEAREA 3
1208	(4B8)	DBL WORD	8	(0)	
1208	(4B8)	BITSTRING	128	SCFSICR1 (0)	I/O FLIH CONTROL REGISTER SAVEAREA 1.
1208	(4B8)	DBL WORD	8	SCFSI1C0	CONTROL REGISTER 0
1216	(4C0)	DBL WORD	8	SCFSI1C1	CONTROL REGISTER 1
1224	(4C8)	DBL WORD	8	SCFSI1C2 (0)	CONTROL REGISTER 2
1224	(4C8)	SIGNED	4	SCFSI1C2H	CONTROL REGISTER 2 HIGH HALF
1228	(4CC)	SIGNED	4	SCFSI1C2L	CONTROL REGISTER 2 LOW HALF
1232	(4D0)	CHARACTER	16	SCFSIXM1 (0)	CROSS MEMORY CONTROL REGISTER SAVEAREA
1232	(4D0)	DBL WORD	8	SCFSI1C3 (0)	CONTROL REGISTER 3
1232	(4D0)	SIGNED	4	SCFSISSN	SASTE INSTANCE NUMBER
1236	(4D4)	SIGNED	2	SCFSIO1K	PROGRAM KEY MASK
1238	(4D6)	SIGNED	2	SCFSIO1S	SASN
1240	(4D8)	DBL WORD	8	SCFSI1C4 (0)	CONTROL REGISTER 4
1240	(4D8)	SIGNED	4	SCFSIPSN	PASTE INSTANCE NUMBER
1244	(4DC)	SIGNED	2	SCFSIO1A	AX
1246	(4DE)	SIGNED	2	SCFSIO1P	PASN
1248	(4E0)	DBL WORD	8	SCFSI1C5	CONTROL REGISTER 5
1256	(4E8)	DBL WORD	8	SCFSI1C6	CONTROL REGISTER 6
1264	(4F0)	DBL WORD	8	SCFSI1C7	CONTROL REGISTER 7
1272	(4F8)	DBL WORD	8	SCFSI1C8 (0)	CONTROL REGISTER 8

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
1272	(4F8)	SIGNED	4		
1276	(4FC)	SIGNED	2	SCFSIEAX	EAX VALUE
1278	(4FE)	SIGNED	2		
1280	(500)	DBL WORD	8	SCFSI1C9	CONTROL REGISTER 9
1288	(508)	DBL WORD	8	SCFSI1CA	CONTROL REGISTER 10
1296	(510)	DBL WORD	8	SCFSI1CB	CONTROL REGISTER 11
1304	(518)	DBL WORD	8	SCFSI1CC	CONTROL REGISTER 12
1312	(520)	DBL WORD	8	SCFSI1CD	CONTROL REGISTER 13
1320	(528)	DBL WORD	8	SCFSI1CE	CONTROL REGISTER 14
1328	(530)	DBL WORD	8	SCFSI1CF (0)	CONTROL REGISTER 15
1328	(530)	SIGNED	4	SCFSI1CFH	CONTROL REGISTER 15 HIGH HALF
1332	(534)	SIGNED	4	SCFSI1CFL	CONTROL REGISTER 15 LOW HALF
1336	(538)	DBL WORD	8	(0)	
1336	(538)	BITSTRING	128	SCFSXCR2 (0)	EXT FLIH CONTROL REGISTER SAVEAREA 2.
1336	(538)	DBL WORD	8	SCFSX2C0	CONTROL REGISTER 0
1344	(540)	DBL WORD	8	SCFSX2C1	CONTROL REGISTER 1
1352	(548)	DBL WORD	8	SCFSX2C2	CONTROL REGISTER 2
1360	(550)	CHARACTER	16	SCFSXXM2 (0)	CROSS MEMORY CONTROL REGISTER SAVEAREA2
1360	(550)	DBL WORD	8	SCFSX2C3 (0)	CONTROL REGISTER 3
1360	(550)	SIGNED	4		SASTE INSTANCE NUMBER
1364	(554)	SIGNED	2	SCFSX2PK	PROGRAM KEY MASK
1366	(556)	SIGNED	2	SCFSX2SS	SASN
1368	(558)	DBL WORD	8	SCFSX2C4 (0)	CONTROL REGISTER 4
1368	(558)	SIGNED	4		PASTE INSTANCE NUMBER
1372	(55C)	SIGNED	2	SCFSX2AX	AX
1374	(55E)	SIGNED	2	SCFSX2PS	PASN
1376	(560)	DBL WORD	8	SCFSX2C5	CONTROL REGISTER 5
1384	(568)	DBL WORD	8	SCFSX2C6	CONTROL REGISTER 6
1392	(570)	DBL WORD	8	SCFSX2C7	CONTROL REGISTER 7
1400	(578)	DBL WORD	8	SCFSX2C8 (0)	CONTROL REGISTER 8
1400	(578)	SIGNED	4		
1404	(57C)	SIGNED	2	SCFS2EAX	EAX VALUE
1406	(57E)	SIGNED	2		
1408	(580)	DBL WORD	8	SCFSX2C9	CONTROL REGISTER 9
1416	(588)	DBL WORD	8	SCFSX2CA	CONTROL REGISTER 10
1424	(590)	DBL WORD	8	SCFSX2CB	CONTROL REGISTER 11
1432	(598)	DBL WORD	8	SCFSX2CC	CONTROL REGISTER 12
1440	(5A0)	DBL WORD	8	SCFSX2CD	CONTROL REGISTER 13
1448	(5A8)	DBL WORD	8	SCFSX2CE	CONTROL REGISTER 14
1456	(5B0)	DBL WORD	8	SCFSX2CF	CONTROL REGISTER 15
1464	(5B8)	DBL WORD	8	(0)	
1464	(5B8)	BITSTRING	128	SCFSXCR3 (0)	EXT FLIH CONTROL REGISTER SAVEAREA 3.
1464	(5B8)	DBL WORD	8	SCFSX3C0	CONTROL REGISTER 0
1472	(5C0)	DBL WORD	8	SCFSX3C1	CONTROL REGISTER 1
1480	(5C8)	DBL WORD	8	SCFSX3C2	CONTROL REGISTER 2
1488	(5D0)	CHARACTER	16	SCFSXXM3 (0)	CROSS MEMORY CONTROL REGISTER SAVEAREA3
1488	(5D0)	DBL WORD	8	SCFSX3C3 (0)	CONTROL REGISTER 3
1488	(5D0)	SIGNED	4	SCFSX3SI	PASTE INSTANCE NUMBER
1492	(5D4)	SIGNED	2	SCFSX3PK	PROGRAM KEY MASK
1494	(5D6)	SIGNED	2	SCFSX3SS	SASN
1496	(5D8)	DBL WORD	8	SCFSX3C4 (0)	CONTROL REGISTER 4
1496	(5D8)	SIGNED	4	SCFSX3PI	SASTE INSTANCE NUMBER
1500	(5DC)	SIGNED	2	SCFSX3AX	AX
1502	(5DE)	SIGNED	2	SCFSX3PS	PASN
1504	(5E0)	DBL WORD	8	SCFSX3C5	CONTROL REGISTER 5
1512	(5E8)	DBL WORD	8	SCFSX3C6	CONTROL REGISTER 6
1520	(5F0)	DBL WORD	8	SCFSX3C7	CONTROL REGISTER 7
1528	(5F8)	DBL WORD	8	SCFSX3C8 (0)	CONTROL REGISTER 8
1528	(5F8)	SIGNED	4		
1532	(5FC)	SIGNED	2	SCFS3EAX	EAX VALUE
1534	(5FE)	SIGNED	2		
1536	(600)	DBL WORD	8	SCFSX3C9	CONTROL REGISTER 9
1544	(608)	DBL WORD	8	SCFSX3CA	CONTROL REGISTER 10
1552	(610)	DBL WORD	8	SCFSX3CB	CONTROL REGISTER 11
1560	(618)	DBL WORD	8	SCFSX3CC	CONTROL REGISTER 12
1568	(620)	DBL WORD	8	SCFSX3CD	CONTROL REGISTER 13
1576	(628)	DBL WORD	8	SCFSX3CE	CONTROL REGISTER 14
1584	(630)	DBL WORD	8	SCFSX3CF	CONTROL REGISTER 15
1592	(638)	BITSTRING	1	SCFS_DIAG838 (0)	IHAZONEO
2104	(838)	DBL WORD	8	SCFSEND (0)	END OF SCFS.

SCFS Cross Reference

Name	Hex Offset	Hex Value
SCFS	0	
SCFS_DIAG838	638	0
SCFSEND	838	
SCFSIAR1	44	
SCFSICR1	4B8	
SCFSIEAX	4FC	0
SCFSIGR1	4	
SCFSIOFL	C4	0
SCFSIOSA	C4	80
SCFSIO1A	4DC	0
SCFSIO1K	4D4	0
SCFSIO1P	4DE	0
SCFSIO1S	4D6	0
SCFSIPSN	4D8	0
SCFSISSN	4D0	0
SCFSIXM1	4D0	
SCFSI1AA	6C	0
SCFSI1AB	70	0
SCFSI1AC	74	0
SCFSI1AD	78	0
SCFSI1AE	7C	0
SCFSI1AF	80	0
SCFSI1A0	44	0
SCFSI1A1	48	0
SCFSI1A2	4C	0
SCFSI1A3	50	0
SCFSI1A4	54	0
SCFSI1A5	58	0
SCFSI1A6	5C	0
SCFSI1A7	60	0
SCFSI1A8	64	0
SCFSI1A9	68	0
SCFSI1CA	508	0
SCFSI1CB	510	0
SCFSI1CC	518	0
SCFSI1CD	520	0
SCFSI1CE	528	0
SCFSI1CF	530	
SCFSI1CFH	530	0
SCFSI1CFL	534	0
SCFSI1C0	4B8	0
SCFSI1C1	4C0	0
SCFSI1C2	4C8	
SCFSI1C2H	4C8	0
SCFSI1C2L	4CC	0
SCFSI1C3	4D0	
SCFSI1C4	4D8	
SCFSI1C5	4E0	0
SCFSI1C6	4E8	0
SCFSI1C7	4F0	0
SCFSI1C8	4F8	
SCFSI1C9	500	0
SCFSI1GA	2C	0
SCFSI1GB	30	0
SCFSI1GC	34	0
SCFSI1GD	38	0
SCFSI1GE	3C	0
SCFSI1GF	40	0
SCFSI1G0	4	0
SCFSI1G1	8	0
SCFSI1G2	C	0
SCFSI1G3	10	0
SCFSI1G4	14	0
SCFSI1G5	18	0
SCFSI1G6	1C	0
SCFSI1G7	20	0
SCFSI1G8	24	0
SCFSI1G9	28	0
SCFSI641	338	0
SCFSR0C5	C5	0
SCFSR084	84	
SCFSR150	150	0
SCFSR2F0	2F0	0
SCFSR2F4	2F4	
SCFSR220	220	0
SCFSR334	334	0
SCFSSCFS	0	E2C3C6E2
SCFSXAR1	110	
SCFSXAR2	1E0	
SCFSXAR3	2B0	
SCFSXCR1	3B8	
SCFSXCR2	538	
SCFSXCR3	5B8	
SCFSXGR1	D0	
SCFSXGR2	1A0	
SCFSXGR3	270	
SCFSXNTC	C6	0
SCFSXPSW161	250	0
SCFSXPSW162	260	0
SCFSXPS1	C8	0
SCFSXRSA	158	0
SCFSXXM1	3D0	
SCFSXXM2	550	
SCFSXXM3	5D0	
SCFSX1AA	138	0
SCFSX1AB	13C	0
SCFSX1AC	140	0
SCFSX1AD	144	0
SCFSX1AE	148	0
SCFSX1AF	14C	0
SCFSX1AX	3DC	0
SCFSX1A0	110	0
SCFSX1A1	114	0
SCFSX1A2	118	0
SCFSX1A3	11C	0
SCFSX1A4	120	0
SCFSX1A5	124	0
SCFSX1A6	128	0
SCFSX1A7	12C	0
SCFSX1A8	130	0
SCFSX1A9	134	0
SCFSX1CA	408	0
SCFSX1CB	410	0
SCFSX1CC	418	0
SCFSX1CD	420	0
SCFSX1CE	428	0
SCFSX1CF	430	
SCFSX1CFH	430	0
SCFSX1CFL	434	0
SCFSX1C0	3B8	0
SCFSX1C1	3C0	0
SCFSX1C2	3C8	0
SCFSX1C3	3D0	
SCFSX1C4	3D8	
SCFSX1C5	3E0	0
SCFSX1C6	3E8	0
SCFSX1C7	3F0	0
SCFSX1C8	3F8	
SCFSX1C9	400	0
SCFSX1GA	F8	0
SCFSX1GB	FC	0
SCFSX1GC	100	0
SCFSX1GD	104	0
SCFSX1GE	108	0
SCFSX1GF	10C	0
SCFSX1G0	D0	0
SCFSX1G1	D4	0
SCFSX1G2	D8	0
SCFSX1G3	DC	0
SCFSX1G4	E0	0
SCFSX1G5	E4	0
SCFSX1G6	E8	0
SCFSX1G7	EC	0
SCFSX1G8	F0	0

Name	Hex Offset	Hex Value
SCFSX1G9	F4	0
SCFSX1PINS	3D8	0
SCFSX1PK	3D4	0
SCFSX1PS	3DE	0
SCFSX1SINS	3D0	0
SCFSX1SS	3D6	0
SCFSX2AA	208	0
SCFSX2AB	20C	0
SCFSX2AC	210	0
SCFSX2AD	214	0
SCFSX2AE	218	0
SCFSX2AF	21C	0
SCFSX2AX	55C	0
SCFSX2A0	1E0	0
SCFSX2A1	1E4	0
SCFSX2A2	1E8	0
SCFSX2A3	1EC	0
SCFSX2A4	1F0	0
SCFSX2A5	1F4	0
SCFSX2A6	1F8	0
SCFSX2A7	1FC	0
SCFSX2A8	200	0
SCFSX2A9	204	0
SCFSX2CA	588	0
SCFSX2CB	590	0
SCFSX2CC	598	0
SCFSX2CD	5A0	0
SCFSX2CE	5A8	0
SCFSX2CF	5B0	0
SCFSX2C0	538	0
SCFSX2C1	540	0
SCFSX2C2	548	0
SCFSX2C3	550	
SCFSX2C4	558	
SCFSX2C5	560	0
SCFSX2C6	568	0
SCFSX2C7	570	0
SCFSX2C8	578	
SCFSX2C9	580	0
SCFSX2GA	1C8	0
SCFSX2GB	1CC	0
SCFSX2GC	1D0	0
SCFSX2GD	1D4	0
SCFSX2GE	1D8	0
SCFSX2GF	1DC	0
SCFSX2G0	1A0	0
SCFSX2G1	1A4	0
SCFSX2G2	1A8	0
SCFSX2G3	1AC	0
SCFSX2G4	1B0	0
SCFSX2G5	1B4	0
SCFSX2G6	1B8	0
SCFSX2G7	1BC	0
SCFSX2G8	1C0	0
SCFSX2G9	1C4	0
SCFSX2PK	554	0
SCFSX2PS	55E	0
SCFSX2SS	556	0
SCFSX3AA	2D8	0
SCFSX3AB	2DC	0
SCFSX3AC	2E0	0
SCFSX3AD	2E4	0
SCFSX3AE	2E8	0
SCFSX3AF	2EC	0
SCFSX3AX	5DC	0
SCFSX3A0	2B0	0
SCFSX3A1	2B4	0
SCFSX3A2	2B8	0
SCFSX3A3	2BC	0
SCFSX3A4	2C0	0
SCFSX3A5	2C4	0
SCFSX3A6	2C8	0
SCFSX3A7	2CC	0
SCFSX3A8	2D0	0

Name	Hex Offset	Hex Value
SCFSX3A9	2D4	0
SCFSX3CA	608	0
SCFSX3CB	610	0
SCFSX3CC	618	0
SCFSX3CD	620	0
SCFSX3CE	628	0
SCFSX3CF	630	0
SCFSX3C0	5B8	0
SCFSX3C1	5C0	0
SCFSX3C2	5C8	0
SCFSX3C3	5D0	
SCFSX3C4	5D8	
SCFSX3C5	5E0	0
SCFSX3C6	5E8	0
SCFSX3C7	5F0	0
SCFSX3C8	5F8	
SCFSX3C9	600	0
SCFSX3GA	298	0
SCFSX3GB	29C	0
SCFSX3GC	2A0	0
SCFSX3GD	2A4	0
SCFSX3GE	2A8	0
SCFSX3GF	2AC	0
SCFSX3G0	270	0
SCFSX3G1	274	0
SCFSX3G2	278	0
SCFSX3G3	27C	0
SCFSX3G4	280	0
SCFSX3G5	284	0
SCFSX3G6	288	0
SCFSX3G7	28C	0
SCFSX3G8	290	0
SCFSX3G9	294	0
SCFSX3PI	5D8	0
SCFSX3PK	5D4	0
SCFSX3PS	5DE	0
SCFSX3SI	5D0	0
SCFSX3SS	5D6	0
SCFSX641	378	0
SCFSX642	438	0
SCFSX643	478	0
SCFS1EAX	3FC	0
SCFS2EAX	57C	0
SCFS3EAX	5FC	0

SCHIB Information

SCHIB Heading Information

Common Name: Subchannel Information Block
Macro ID: IHASCHIB
DSECT Name: SCHIB
Owning Component: I/O Supervisor (SC1C3)
Eye-Catcher ID: None
Storage Attributes: Subpool: User
Key: 0
Data Space: No
Residency: : 31 bit
Size: 52-bytes
Created by: Issuers of Store Subchannel and
Modify Subchannel instructions.
Pointed to by: Register 1 on the issuance of the Store
Subchannl or Modify Subchannel instructions
Serialization: UCB Lock
Function: The SCHIB is an operand of the Store Subchannel (STSCH)
and the Modify Subchannel (MSCH) instructions.
It contains control information for path management
and subchannel status.

SCHIB Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	52	SCHIB	
0	(0)	CHARACTER	28	SCHPMCW	Path Management Control Word
0	(0)	BITSTRING	4	SCHIP	Interrupt parameter
0	(0)	SIGNED	4	SCHIPA	Interrupt parameter
0	(0)	ADDRESS	4	SCHIPP	Interrupt parameter
4	(4)	BITSTRING	1	SCHFLG1	Flags
		11..		*	o Reserved bits: See Declares
		..11 1...		SCHISC	o Interrupt Subclass
		.... .1..		SCHB	o I/O Initiation Control
		11		*	o Reserved bits: See Declares
5	(5)	BITSTRING	1	SCHFLG2	Flags
		1...		SCHE	o Enable
		.11.		SCHLM	o Limit mode
		...1 1...		SCHMM	o Measurement Mode
		...1		SCHMCMB	o Measurements to be stored in channel measurement block
		.... 1...		SCHMDCTI	o DCTI to be stored in ESW
		.... .1..		SCHD	o Dynamic pathing device
		.... ..1.		SCHT	o Timing facility available
		1		SCHV	o Device number valid
6	(6)	UNSIGNED	2	SCHDEVNO	Device number
8	(8)	BITSTRING	1	SCHLPM	Logical path mask
9	(9)	BITSTRING	1	SCHPNOM	Path not operational mask
10	(A)	BITSTRING	1	SCHLPUM	Last path used mask
11	(B)	BITSTRING	1	SCHPIM	Path installed mask
12	(C)	UNSIGNED	2	SCHMBI	Measurement block index
14	(E)	BITSTRING	1	SCHPOM	Path operational mask
15	(F)	BITSTRING	1	SCHPAM	Path available mask
16	(10)	UNSIGNED	1	SCHCHPID	Channel path ID's
				(4294967304:562114832)	
24	(18)	CHARACTER	3	*	Reserved
24	(18)	BITSTRING	1	*	Reserved
25	(19)	111.		SCHST	Subchannel type
25	(19)	BITSTRING	1	*	Reserved
27	(1B)	BITSTRING	1	SCHFLG6	Flags
		1111 1...		*	Reserved
		.... .1..		SCHF	Measurement block format control
		.... ..1.		SCHX	Extended Measurement Word (EMW) mode enable
		1		SCHCSF	Concurrent Sense Facility
28	(1C)	CHARACTER	12	SCHSCSW	Subchannel status word (SCSW). Mapped by the first 12 bytes of of the (IHAIRB)
40	(28)	CHARACTER	12	SCHMDATA	Model dependent data
40	(28)	CHARACTER	8	SCHCMBAD	Absolute storage address of Channel Measurement Block (when SCHF is set)
48	(30)	CHARACTER	4	*	Reserved

SCHIB Constants

Len	Type	Value	Name	Description
		Comment		
		SCHLM field declares		
		End of Comment		
0	BIT	00	SCHLNONE	No limit checking.
0	BIT	10	SCHLLT	Data address must be < limit
0	BIT	01	SCHLGTE	Data address must be >= limit
		Comment		
		SCHST field declares		
		End of Comment		
0	BIT	000	SCHIO	I/O subchannel
0	BIT	001	SCHST1	Subchannel type 1
0	BIT	010	SCHST2	Subchannel type 2
0	BIT	011	SCHST3	Subchannel type 3

SCHIB Cross Reference

Name	Hex Offset	Hex Value
SCHB	4	04
SCHCHPID	10	
SCHCMBAD	28	
SCHCSF	1B	01
SCHD	5	04
SCHDEVNO	6	
SCHE	5	80
SCHF	1B	04
SCHFLG1	4	
SCHFLG2	5	
SCHFLG6	1B	
SCHIB	0	
SCHIP	0	
SCHIPA	0	
SCHIPP	0	
SCHISC	4	38
SCHLM	5	60
SCHLPM	8	
SCHLPUM	A	
SCHMBI	C	
SCHMCMB	5	10
SCHMDATA	28	
SCHMDCTI	5	08
SCHMM	5	18
SCHPAM	F	
SCHPIM	B	
SCHPMCW	0	
SCHPNOM	9	
SCHPOM	E	
SCHSCSW	1C	
SCHST	19	E0
SCHT	5	02
SCHV	5	01
SCHX	1B	02

SCL Information

SCL Heading Information

Common Name: Scan Parameter List
Macro ID: IEEZB815
DSECT Name: None
Owning Component: System Command (SC1B8)
Storage Attributes: Subpool: caller's subpool
Key: caller's key
Size: 92 bytes
Created by: Callers of Generalized Parser (IEEMB887)
Pointed to by: N/a
Serialization: None
Function: The parameter list to IEEMB887 provides the basic information to perform a parse of the specified input.

SCL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	92	SCLPARM	PARSE PARM LIST
0	(0)	CHARACTER	4	SCLACRO	ACRONYM - SHOULD EQUAL 'SCL '
4	(4)	UNSIGNED	1	SCLVERSN	VERSION LEVEL
5	(5)	CHARACTER	3	SCLRESV1	RESERVED
8	(8)	ADDRESS	4	SCLCHAR	PTR TO STRING TO PARSE
12	(C)	ADDRESS	4	SCLDSC	PTR TO FIRST PARSE DESC.
16	(10)	ADDRESS	4	SCLIORTN	ADDR OF USER I/O EXIT ROUTINE
20	(14)	ADDRESS	4	SCLIOPAD	ADDR OF PARM FOR I/O ROUTINE
24	(18)	ADDRESS	4	SCLCURNT	ADDR OF CURRENT PARSE DESC.
28	(1C)	UNSIGNED	2	SCLINLN	INPUT RECORD LENGTH
30	(1E)	UNSIGNED	2	SCLSTRLN	PASSBACK LENGTH
32	(20)	BITSTRING	1	SCLFLG1	FLAG BYTE
		1...		SCLCONTC	CONTINUATION CHAR. CHECK
		.1..		SCLNOCT	NO CONTINUATION ALLOWED
		..1.		SCLCOMNT	COMMENTS ALLOWED
		...1		SCLNOSUC	AFTER CALL TO ROUT PROCESS ALTERNATE PARSE DESC. NEXT
		.... 1...		SCLSECS	AFTER CALL TO ROUT PROC. SEC. SUCCESSOR PARSE DESC. NEXT
		.... .1..		SCLNORT	DO NOT CALL ROUT EXIT ROUTINE UNLESS 'CALLRT=YES' IS GIVEN
		.... ..1.		SCLMULTR	ALLOW MULTIPLE RECORD SCANS
		1		*	RESERVED
33	(21)	UNSIGNED	1	SCLFLG2	RESERVED
34	(22)	UNSIGNED	1	SCLFLG3	RESERVED
35	(23)	UNSIGNED	1	SCLUFUNC	FUNCTION BYTE - HAS CONTENTS OF ADDRESS THAT TRT INSTR. STOPPED AT DURING TRT SCAN
36	(24)	ADDRESS	4	SCLUTAB	ADDR OF USER PROCESSING TABLE
40	(28)	SIGNED	2	SCLUINDX	INDEX WITHIN PROCESSING TABLE
42	(2A)	SIGNED	2	SCLRESV2	RESERVED
44	(2C)	ADDRESS	4	SCLUSER	USER-DEFINED WORK AREA
48	(30)	SIGNED	4	SCLDATA	USER'S DATA FOR THE ROUT
52	(34)	ADDRESS	4	SCLMBUFP	ADDR. OF MULTIPLE RECORD BUFFER
56	(38)	UNSIGNED	2	SCLMBUFL	LENGTH OF MULTIPLE RECORD BUFFER
58	(3A)	SIGNED	2	SCLFLG5	RESERVED
60	(3C)	ADDRESS	4	SCLMBUFU	ADDR. OF FIRST UNUSED BYTE OF MULT. REC. BUFFER
64	(40)	SIGNED	4	SCLRET	RETURN CODE
68	(44)	SIGNED	4	SCLRSN	REASON CODE
72	(48)	CHARACTER	4	SCLRSVD1	RESERVED
76	(4C)	CHARACTER	4	SCLRSVD2	RESERVED
80	(50)	CHARACTER	4	SCLRSVD3	RESERVED
84	(54)	CHARACTER	4	SCLRSVD4	RESERVED
88	(58)	CHARACTER	4	SCLRSVD5	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	DSCPARSE	PARSE MAPPING
0	(0)	ADDRESS	4	DSCALTAD	ADDR OF ALT PARSE DESC.
4	(4)	ADDRESS	4	DSCSUCAD	ADDR OF SUCC PARSE DESC.
8	(8)	ADDRESS	4	DSCSSAD	ADDR OF SEC. SUCC PARSE DESC.
12	(C)	ADDRESS	4	DSCRTAD	ADDR OF ROUT EXIT ROUTINE
16	(10)	SIGNED	4	DSCDATA	USER'S DATA FOR THE ROUT
20	(14)	CHARACTER	12	DSCRSVD	RESERVED

SCL Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
32	(20)	ADDRESS	4	DSCTRTAD	ADDR OF USER'S TRT TABLE
36	(24)	ADDRESS	4	DSCUTAB	ADDR OF USER PROCESSING TABLE
40	(28)	SIGNED	2	DSCUINDX	INDEX WITHIN PROCESSING TABLE
42	(2A)	BITSTRING	1	DSCFLG1	FLAG BYTE
		1...		DSCALPHA	ALPHA DATA EXPECTED
		.1..		DSCNUMER	NUMERIC DATA EXPECTED
		..1.		DSCHEX	HEX DATA EXPECTED
		...1		DSC1A	FIRST CHAR CAN ONLY BE ALPHA
		.... 1...		DSC1A@	FIRST CHAR ALPHA/NATIONAL
		.... .111		*	RESERVED
43	(2B)	BITSTRING	1	DSCFLG2	FLAG BYTE
		1...		DSCBLANK	DONT SKIP OVER BLANKS
		.1..		DSCIOYES	CALL I/O EXIT TO READ
		..1.		DSCCOMMA	DONT SKIP OVER COMMAS
		...1		DSCCALRT	ALWAYS CALL ROUT IF IT EXISTS
		.... 1...		DSCNOSCN	DO 'ROUT ONLY' PROCESSING
		.... .111		*	RESERVED
44	(2C)	UNSIGNED	2	DSCMAXLN	MAXIMUM VARIABLE LENGTH
46	(2E)	UNSIGNED	2	DSCMINLN	MINIMUM VARIABLE LENGTH
48	(30)	UNSIGNED	1	DSCKEYLN	'KEY' STRING LENGTH
49	(31)	UNSIGNED	1	DSCABBLN	MINIMUM ACCEPTABLE LENGTH FOR KEYWORD ABBREVIATION
50	(32)	UNSIGNED	1	DSCALSLN	'ALSO' STRING LENGTH
51	(33)	UNSIGNED	1	DSCDLMLN	'DELIM' STRING LENGTH
52	(34)	CHARACTER	8	DSCALSC	'ALSO' STRING
60	(3C)	CHARACTER	8	DSCDLMC	'DELIM' STRING
68	(44)	CHARACTER	*	DSCKEY	'KEY' STRING

SCL Constants

Len	Type	Value		Name	Description
4	CHARACTER	SCL		SCLNAME	CONSTANT - ACRONYM 'SCL '
1	DECIMAL		1	SCLSP212	VERSION LEVEL NUMBER
1	DECIMAL		1	SCLVERID	VERSION IDENTIFICATION

Comment

```
THE FOLLOWING CONSTANTS DEFINE THE VALUES IN SCLRET
EITHER RETURNED FROM THE ROUT OR I/O EXIT ROUTINE TO THE
PARSER OR FROM THE PARSER TO THE INVOKER:
RETURN CODES PASSED FROM THE PARSER TO THE INVOKER
   RETURN CODE = 0 (SCLGOOD)
   CONDITIONS = ALL AVAILABLE PARSE DESCRIPTIONS OR
            INPUT RECORDS PROCESSED SUCCESSFULLY
   RETURN CODE = 4 (SCLBAD)
   CONDITIONS = PARSE STOPPED BECAUSE:
         1) ROUT INDICATED STOP BY SETTING SCLRET=SCLSTOP
      OR 2) PARSE DESCRIPTION AND ALTERNATES EXHAUSTED
            WITHOUT FINDING MATCH WITH CHARACTER STRING
      OR 3) CHARACTERS DECLARED AS BEING VALID DELIMITERS
            HAVE ALREADY BEEN DEFINED AS ACCEPTABLE
   RETURN CODE = 8 (SCLSTOP)
   CONDITIONS = PARSER IS TO READ IN NEXT INPUT RECORD
                BUT NO I/O EXIT ROUTINE EXISTS, OR,
                AFTER CALLING I/O EXIT ROUTINE, IT
                IS FOUND THAT NO MORE INPUT IS
                AVAILABLE WHEN THERE SHOULD HAVE BEEN
                MORE INPUT TO READ
   RETURN CODE = 12 (SCLINVAL)
   CONDITIONS = SCL CONTAINS INVALID SYNTAX
   RETURN CODE = 16 (SCLERR)
   CONDITIONS = SERIOUS ERROR OCCURRED
   RETURN CODE = 20 (SCLTERM)
   CONDITIONS = TERMINAL ERROR OCCURRED
   RETURN CODE = 24 (SCLESTAE)
   CONDITIONS = PARSER ESTAE HAS NOT BEEN INVOKED
   RETURN CODE = 28 (SCLUNDEF)
   CONDITIONS = ROUT OR I/O EXIT ROUTINE GAVE A RETURN
                CODE OTHER THAN THOSE EXPECTED
   RETURN CODE = 32 (SCLABEND)
   CONDITIONS = AN ABEND OCCURRED EITHER IN THE PARSER
                OR WITHIN AN EXIT ROUTINE
RETURN CODES PASSED FROM ROUT EXIT ROUTINES TO PARSER
   RETURN CODE = 0 (SCLGOOD)
```

Len	Type	Value	Name	Description
	CONDITIONS = CONTINUE PARSING			
	OUTPUT = NONE			
	RETURN CODE = 4 (SCLNDSCN)			
	CONDITIONS = STOP PARSE BUT HAVE PARSER RETURN TO CALLER WITH RETURN CODE OF SCLGOOD			
	OUTPUT = RETURN CODE IN SCL			
	RETURN CODE = 8 (SCLSTOP)			
	CONDITIONS = STOP PARSE AND HAVE PARSER RETURN TO CALLER WITH RETURN CODE OF SCLBAD			
	OUTPUT = RETURN CODE IN SCL			
	RETURN CODE = 16 (SCLERR)			
	CONDITIONS = STOP PARSE AND HAVE PARSER RETURN TO CALLER WITH RETURN CODE OF SCLERR			
	OUTPUT = RETURN CODE IN SCL			
	RETURN CODE = 20 (SCLTERM)			
	CONDITIONS = STOP PARSE AND RETURN TO CALLER WITH RETURN CODE OF SCLTERM			
	OUTPUT = RETURN CODE IN SCL			
	RETURN CODE = OTHER			
	CONDITIONS = STOP PARSE AND RETURN TO CALLER WITH RETURN CODE OF SCLUNDEF			
	OUTPUT = RETURN CODE IN SCL			
	RETURN CODES PASSED FROM I/O EXIT ROUTINE TO PARSER			
	RETURN CODE = 0 (SCLGOOD)			
	CONDITIONS = INPUT BUFFER REFILLED SUCCESSFULLY			
	OUTPUT = NONE			
	RETURN CODE = 4 (SCLNDSCN)			
	CONDITIONS = NO MORE INPUT AVAILABLE - 1) IF WITHIN A COMMENT FIELD, PARSER SHOULD RETURN TO CALLER WITH RETURN CODE OF SCLBAD AND A REASON CODE OF SCLBCOM 2) IF A CONTINUATION CHARACTER WAS GIVEN, PARSER SHOULD RETURN TO CALLER WITH RETURN CODE OF SCLBAD AND A REASON CODE OF SCLBCONT 3) OTHERWISE, PARSER SHOULD RETURN TO CALLER WITH RETURN CODE OF SCLGOOD AND A REASON CODE OF SCLNOIO			
	OUTPUT = RETURN CODE IN SCL			
	RETURN CODE = 20 (SCLTERM)			
	CONDITIONS = STOP PARSE AND RETURN TO CALLER WITH RETURN CODE OF SCLTERM			
	OUTPUT = RETURN CODE IN SCL			
	RETURN CODE = OTHER			
	CONDITIONS = STOP PARSE AND RETURN TO CALLER WITH RETURN CODE OF SCLUNDEF			
	OUTPUT = RETURN CODE IN SCL			
	End of Comment			
1	DECIMAL	0	SCLGOOD	
1	DECIMAL	4	SCLNDSCN	
1	DECIMAL	4	SCLBAD	
1	DECIMAL	8	SCLSTOP	
1	DECIMAL	12	SCLINVAL	
1	DECIMAL	16	SCLERR	
1	DECIMAL	20	SCLTERM	
1	DECIMAL	24	SCLESTAE	
1	DECIMAL	28	SCLUNDEF	
1	DECIMAL	32	SCLABEND	

Len	Type	Value	Name	Description

Comment

THE FOLLOWING CONSTANTS DEFINE THE VALUES IN SCLRSN (REASON CODES) ON RETURN FROM THE PARSER TO THE INVOKER. IT IS ALSO POSSIBLE THAT THE PARSER WILL PASS ON THE REASON CODE FROM THE ROUT OR I/O EXIT ROUTINE AS ITS REASON CODE WHEN RETURNING TO THE INVOKER.

REASON CODE = 0 (SCLOK)
CONDITIONS = USED WITH SCLGOOD, OR WHEN
NO REASON IS DETERMINED

REASON CODE = 4 (SCLNOPRS)
CONDITIONS = PARSE DESCRIPTION DEFINED AS NEXT
(SUCCESSOR, SECONDARY SUCCESSOR OR
ALTERNATE) DOES NOT EXIST

REASON CODE = 8 (SCLNOIO)
CONDITIONS = PARSER IS TO READ IN NEXT INPUT RECORD
BUT NO MORE INPUT IS AVAILABLE BECAUSE
EITHER NO I/O EXIT ROUTINE EXISTS OR ALL
OF THE INPUT RECORDS HAVE BEEN PARSED

REASON CODE = 12 (SCLBCONT)
CONDITIONS = IF CONTINUATION CHARACTER IS FOUND AS
LAST CHARACTER OF PREVIOUS INPUT
RECORD AND:
1) NO I/O EXIT ROUTINE EXISTS, OR
2) I/O EXIT ROUTINE GIVES A RETURN
CODE INDICATING THAT NO MORE
INPUT IS AVAILABLE

REASON CODE = 16 (SCLBCOM)
CONDITIONS = IF, WHEN REACHING END OF INPUT RECORD,
PARSER IS WITHIN A COMMENT FIELD AND:
1) NO I/O EXIT ROUTINE EXISTS, OR
2) I/O EXIT ROUTINE GIVES A RETURN
CODE INDICATING THAT NO MORE
INPUT IS AVAILABLE

REASON CODE = 20 (SCLBDLM)
CONDITIONS = CHARACTERS DEFINED BY INVOKER AS
BEING VALID DELIMITERS HAVE ALREADY
BEEN DEFINED AS ACCEPTABLE FOR THE
SCAN

REASON CODE = 24 (SCLBKEY)
CONDITIONS = KEYWORD NOT FOUND (DURING KEY PROCESSING)

REASON CODE = 28 (SCLBLEN)
CONDITIONS = LENGTH OF WORD FOUND WHILE SCANNING
IS OUT OF RANGE

REASON CODE = 32 (SCLBCHAR)
CONDITIONS = CHARACTER THAT SCAN ENDED AT
IS AN INVALID DELIMITER
OR
'INVALID CHARACTER'

REASON CODE = 36 (SCLNOSPC)
CONDITIONS = NO PROCESSING OF ANY KIND WAS SPECIFIED
ON CURRENT PARSE DESCRIPTION

REASON CODE = 40 (SCLBADPD)
CONDITIONS = AN INVALID PARSE DESCRIPTION WAS SUPPLIED.
IT IS SUGGESTED THAT THE USER UNDERSTAND
HOW THE 'IEEPARSE' MACRO FUNCTIONS BEFORE
GENERATING HIS OWN PARSE DESCRIPTIONS.

REASON CODE = 44 (SCLBUFLN)
CONDITIONS = THE MULTIPLE RECORD INPUT BUFFER
SUPPLIED IS NOT LONG ENOUGH TO HOLD
THE DATA NECESSARY TO ADEQUATELY
PERFORM THE PARSE DESCRIPTION POINTED
TO BY SCLCURNT.

End of Comment

1	DECIMAL	0	SCLOK	
1	DECIMAL	4	SCLNOPRS	
1	DECIMAL	8	SCLNOIO	
1	DECIMAL	12	SCLBCONT	
1	DECIMAL	16	SCLBCOM	
1	DECIMAL	20	SCLBDLM	
1	DECIMAL	24	SCLBKEY	
1	DECIMAL	28	SCLBLEN	
1	DECIMAL	32	SCLBCHAR	

Len	Type	Value	Name	Description
1	DECIMAL	36	SCLNOSPC	
1	DECIMAL	40	SCLBADPD	
1	DECIMAL	44	SCLBUFLN	

SCL Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
DSCABBLN	31		SCLUTAB	24	
DSCALPHA	2A	80	SCLVERSN	4	
DSCALSC	34				
DSCALSLN	32				
DSCALTAD	0				
DSCBLANK	2B	80			
DSCCALRT	2B	10			
DSCCOMMA	2B	20			
DSCDATA	10				
DSCDLMC	3C				
DSCDLMLN	33				
DSCFLG1	2A				
DSCFLG2	2B				
DSCHEX	2A	20			
DSCIOYES	2B	40			
DSCKEY	44				
DSCKEYLN	30				
DSCMAXLN	2C				
DSCMINLN	2E				
DSCNOSCN	2B	08			
DSCNUMER	2A	40			
DSCPARSE	0				
DSCRSVD	14				
DSCRTAD	C				
DSCSSAD	8				
DSCSUCAD	4				
DSCTRTAD	20				
DSCUINDX	28				
DSCUTAB	24				
DSC1A	2A	10			
DSC1A@	2A	08			
SCLACRO	0				
SCLCHAR	8				
SCLCOMNT	20	20			
SCLCONTC	20	80			
SCLCURNT	18				
SCLDATA	30				
SCLDSC	C				
SCLFLG1	20				
SCLFLG2	21				
SCLFLG3	22				
SCLFLG5	3A				
SCLINLN	1C				
SCLIOPAD	14				
SCLIORTN	10				
SCLMBUFL	38				
SCLMBUFP	34				
SCLMBUFU	3C				
SCLMULTR	20	02			
SCLNOCT	20	40			
SCLNORT	20	04			
SCLNOSUC	20	10			
SCLPARM	0				
SCLRESV1	5				
SCLRESV2	2A				
SCLRET	40				
SCLRSN	44				
SCLRSVD1	48				
SCLRSVD2	4C				
SCLRSVD3	50				
SCLRSVD4	54				
SCLRSVD5	58				
SCLSECS	20	08			
SCLSTRLN	1E				
SCLUFUNC	23				
SCLUINDX	28				
SCLUSER	2C				

SCRA Information

SCRA Heading Information

Common Name: Supervisor Control Recovery Area
Macro ID: IHASCRA
DSECT Name: SCRA
Owning Component: Supervisor Control (SC1C5)
Eye-Catcher ID: SCRA
Offset: 0
Length: 4
Storage Attributes: Subpool: 245
Key: 0
Size: 24 bytes
Created by: IEAVESPR
Pointed to by: SDWAPARM field of the SDWA data area
Serialization: Disablement
Function: The Parm area pointed to by SDWAPARM is mapped by IHASCRA for Supervisor Control Recovery modules. IEAVESPR initializes and is primary user of the area. It serves as a communication area.

SCRA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SCRA	
0	(0)	DBL WORD	8	(0)	SUPERVISOR CONTROL RECOVERY AREA
0	(0)	CHARACTER	4	SCRASCRA	ACRONYM FOR SCRA
4	(4)	ADDRESS	4	SCRARTNP	RTM RETURN POINT ADDRESS
8	(8)	ADDRESS	4	SCRACSTK	CURRENT STACK POINTER
12	(C)	SIGNED	4	SCRAFLGS (0)	RECURSION AND ACTION FLAGS
12	(C)	BITSTRING	1	SCRARECF	RECURSION FLAGS
		1...		SCRAREC1	"X'80'" FIRST LEVEL RECURSION
		.1..		SCRAREC2	"X'40'" SECOND LEVEL RECURSION
		..1.		SCRARS02	"X'20'" RESERVED
		...1		SCRAXSRC	"X'10'" XES List Notification Recovery Recurstion indicator
13	(D)	BITSTRING	1	SCRAACTF	ACTION FLAGS
		1...		SCRAMTRM	"X'80'" MEMTERM INDICATOR
		.1..		SCRAABTM	"X'40'" ABTERM INDICATOR
		..1.		SCRANORE	"X'20'" Do not do logrec recording.
14	(E)	SIGNED	2	SCRARS04	RESERVED
16	(10)	SIGNED	4	SCRAWORK	RECOVERY WORK AREA PTR
20	(14)	SIGNED	4	SCRASUPR	SUPERVISOR CONTROL WORD FROM PSA
24	(18)	DBL WORD	8	SCRAEND (0)	END OF SCRA

SCRA Cross Reference

Name	Hex Offset	Hex Value
SCRA	0	
SCRAABTM	D	40
SCRAACTF	D	
SCRACSTK	8	
SCRAEND	18	
SCRAFLGS	C	
SCRAMTRM	D	80
SCRANORE	D	20
SCRARECF	C	
SCRAREC1	C	80
SCRAREC2	C	40
SCRARS02	C	20
SCRARS04	E	
SCRARTNP	4	
SCRASCRA	0	
SCRASUPR	14	
SCRAWORK	10	
SCRAXSRC	C	10

SCT Information

SCT Heading Information

Common Name: STEP CONTROL TABLE
Macro ID: IEFASCTB
DSECT Name: INSMSCT
Owning Component: Interpreter (SC1B9)
Eye-Catcher ID: 'SCT '
Offset: -4 (SWA prefix)
Length: 4 bytes
Storage Attributes: Subpool: 236 or 237 (SWA), or 241 for MSTR
Key: 1
Residency: Below
Size: 176 bytes
Frequency: One per step in a job
Created by: The Interpreter
Pointed to by: - JSCSCTP field (SVA) of the JSCB data area
Serialization: None
Function: Contains job step information used by Initiator and Interpreter routines

SCT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0		
0	(0)	DBL WORD	8	(0)	
0	(0)	X'0'	0	INSMSCT	"*" STEP CONTROL TABLE
0	(0)	CHARACTER	3	SCTDISKA	DISK ADDRESS OF SCT
3	(3)	CHARACTER	1	SCTTBLID	TABLE ID OF SCT=2
3	(3)	X'2'	0	SCTID	"2"
4	(4)	CHARACTER	1	SCTSSTAT	INTERNAL STEP STATUS
4	(4)	X'80'	0	EAADDRBT	"128" BIT 0 - ON FOR ADDRSPC=REAL Y01029
4	(4)	X'40'	0	EACAUSER	"64" BIT 1 - CAN CAUSE ROLLOUT AACA
4	(4)	X'20'	0	SCTNORST	"32" BIT 2 - NO RESTART TO BE DONE AACA
4	(4)	X'10'	0	SCTNOCKP	"16" BIT 3 - NO CHECKPOINT TO BE TAKEN AACA
4	(4)	X'8'	0	SCTDORST	"8" BIT 4 - DO RESTART IF NECESSARY AACA
4	(4)	X'4'	0	SCTKEY0	"4" BIT 5 - FOR GRAPHICS - ALTER PROTECT KEY AACA
4	(4)	X'2'	0	SCTGRPH	"2" BIT 6 - FOR GRAPHICS - ABEND EXIT AACA
4	(4)	X'1'	0	INCMSSTS	"1" BIT-7 - STEP FAILED
5	(5)	CHARACTER	1	SCTMEMPR	This byte is set when OMVS creates a child in order to propagate the parent's MEMLIMIT to the child.
6	(6)	CHARACTER	2	SCTRSVD1	RESERVED
8	(8)	CHARACTER	2	SCTSEXEC	STEP STATUS CODE PASSED TO THE INITIATOR AT TERMINATE OR THE LENGTH OF THE PARM FIELD IN THE SCTX. Y02641
10	(A)	CHARACTER	2	SCTLALOC	LENGTH OF ALLOCATION WORK AREA OR NUMBER OF GOOD DD CARDS
12	(C)	CHARACTER	4	SCTFSIOT	SVA of first SIOT. SVA is contained in the first three bytes of this field.
16	(10)	BITSTRING	4	SCTTR010	Reserved, was SCTTVFUT
20	(14)	CHARACTER	3	SCTANSCT	DISK ADDRESS OF NEXT SCT
23	(17)	CHARACTER	1		RESERVED
24	(18)	CHARACTER	4	SCTLSIOT	SVA of last SIOT for step. SVA is contained in the first three bytes of this field.
28	(1C)	CHARACTER	4	SCTDDNT	SWA ADDRESS OF DDNT
32	(20)	CHARACTER	4	SCTAFACT	SVA ADDRESS OF FIRST ACT FOR THIS STEP
36	(24)	CHARACTER	4	SCTSWB	SCHEDULER WORK BLOCK (SWB) STRUCTURE POINTER
40	(28)	CHARACTER	4	SCTADSTB	SVA ADDRESS OF DSNAME TABLE FOR THIS STEP
44	(2C)	CHARACTER	8	SCTSCLPC	NAME OF STEP THAT CALLED PROCEDURE
52	(34)	CHARACTER	8	SCTSNAME	STEPNAME
60	(3C)	CHARACTER	2	SCTRPACT	RELATIVE POINTER TO STEP ENTRY IN ACT
62	(3E)	CHARACTER	2	SCTSSYSC	Step SYStem Code. This code indicates failure before program ATTACH or after program completion. Currently, only Allocation's IEFBB410 module sets this field and SMF modules IEFTB721 and IEFTB726 use it.
62	(3E)	BITSTRING	0	SCTUSYSC	"X'8000'" Unallocation step SYStem Code.
64	(40)	CHARACTER	1	SCTSNUMB	FAILING STEP NUMBER FOR Y02641 AUTO RESTART
65	(41)	CHARACTER	1	SCTNSMSG	NUMBER OF SET UP MESSAGES
66	(42)	CHARACTER	1		RESERVED
67	(43)	CHARACTER	1	SCTSTYPE	STEP TYPE
67	(43)	X'80'	0	SCTGOSTP	"128" BIT 0- =1 IF PGM=*.(GO)STEP(FETCH DCB) 19874
67	(43)	X'40'	0	SCTINPUT	"64" BIT 1- =1 IF SYSIN IS SPECIFIED (DD *)
67	(43)	X'20'	0	SCTOUTMC	"32" BIT 2- =1 IF THE PARAMETER ASSOCIATED WITH A SYSOUT KEYWORD SPECIFIES THE MESSAGE CLASS 19874
67	(43)	X'10'	0	SCTSJFHK	"16" BIT 3 - JFCB H/K COMPLETE
67	(43)	X'E'	0	SCTINITB	"14"

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					BITS 4, 5, AND 6 ARE USED BY THE INITIATOR, AS FOLLOWS-- 000 - USE ACTION CODE 001 - GO TO AVR MODULE 010 - GO TO SPACE REQUEST 011 - GO TO EXTERNAL ACTION SETUP 100 - GO TO EXTERNAL ACTION VERIFY 101 - NULL 110 - NULL 111 - NULL
					End of Comment
67	(43)	X'1'	0	SCTJSCAT	"1" BIT 7- =1 PRVT CAT IS JOBCAT, =0 FOR STEPCAT Y01113
68	(44)	CHARACTER	3	SCTXBTTR	SVA OF SCT EXTENSION BLOCK CONTAINING PARAMETER
71	(47)	CHARACTER	1		RESERVED
72	(48)	SIGNED	4	SCTMSADR	ADDRESS OF REGION IN MAIN STORAGE - X'00 IN 1ST BYAACA
76	(4C)	CHARACTER	4	SCTSRBT	ACCUMULATED SRB TIME FOR STEP Y02652
					Comment
					THE FOLLOWING FOUR BYTES ARE I254 USED BY IEFSD41Q(MVT AND MFT-2), I254 IEFSD42Q(MVT AND MFT-2), I254 IEFW41SD(PCP), I254 IEFW42SD(PCP), I254 IEFYNIMP(ALL SYSTEMS) I254
					End of Comment
80	(50)	CHARACTER	4	SCTLDSTB	LENGTH OF DSNAME TABLE Y02670
84	(54)	CHARACTER	4	SCTPCAT	PRIVATE CATALOG SIOT DISK ADDRESS Y01113
88	(58)	SIGNED	2	SCTMSSZE	SIZE OF REGION IN MAIN STORAGE AACA
90	(5A)	SIGNED	2	SCTSSNUM	Sub-step number, used for exec()
92	(5C)	CHARACTER	2	SCTNIUSL	COUNT OF TOTAL NUMBER OF DD'S FOR A STEP Y02668
94	(5E)	CHARACTER	2	SCTSDP	STEP DISPATCHING PRIORITY- SET IN IEFVEA, I241 USED BY THE INITIATOR I241
94	(5E)	X'80'	0	SCTEPRFM	"128" BIT 0 = 1 PERFORM SPECIFIED ON EXEC STMT
94	(5E)	X'40'	0	SCTPRFM2	"64" BIT 1 = 1 IF TWO BYTE PERFORM FIELD USED
94	(5E)	X'20'	0	SCTFSTEP	"32" BIT 2-FIST STEP TO BE EXECUTED I241
96	(60)	SIGNED	4	SCTSMF	STEP SYSIN COUNT FOR SMF SMF
100	(64)	CHARACTER	4	SCTGOTTR	SVA OF PGM=*. SIOT AACA 2
104	(68)	CHARACTER	1	SCTSTAT2	EXTENSION OF STEP STATUS INDICATORS
104	(68)	X'68'	0	SCTBCT	"SCTSTAT2" STEP STATUS INDICATORS BIT 0 - RESERVED 19874
					Comment
					THE FOLLOWING BIT INDICATES THAT DIRECT SYSOUT FACILITIES ARE O99 REQUIRED FOR JOB SEPARATOR/SYSTEM MESSAGES O99
					End of Comment
104	(68)	X'20'	0	SCTMCVOL	"32" ALLOCATION FOR CVOL AACA
104	(68)	X'10'	0	SCTPRMDD	"16" BIT 3 - If set, the SCTXPARM field contains the DDname of the input parameter data set and the SCTSEXEC field will indicate that the SCTXPARM field contains a zero length parameter string.
104	(68)	X'8'	0	SCTSTPLB	"8" BIT 4 - STEPLIB PRESENT AACA
104	(68)	X'4'	0	SCTSPSYS	"4" BIT 5 - =1 IF SPOOLED SYSIN FOR STEP (EXPRESS O102 CANCEL)SET BYIEFVDA,TESTED BY IEESD575 O102 MVT AND MFT ONLY) O102
104	(68)	X'2'	0	SCTJBEND	"2" JOB ENDED BIT AACA
104	(68)	X'1'	0	SCTRSTST	"1" BIT 7 - RESTARTED STEP (SET BY IEFSD161)
105	(69)	CHARACTER	3	SCTCLDST	Copy of SCTLDSTB. Set/used by IEFDB414 and used by IEFDB413.
108	(6C)	CHARACTER	8	SCTPGMNM	PROGRAM NAME
116	(74)	CHARACTER	2	SCTPRFMF	PERFORMANCE GROUP NUMBER
118	(76)	CHARACTER	2	SCTSDPCD	FIRST STEP DEPENDENCY CODE
120	(78)	CHARACTER	1	SCTSDPOP	FIRST STEP DEPENDENCY OPERATOR
121	(79)	CHARACTER	3	SCTSDPSA	DISK ADDRESS OF DEPENDENCY SCT
124	(7C)	CHARACTER	36		SPACE FOR 6 MORE STEP DEPENDENCIES HW16
160	(A0)	CHARACTER	1	SCTABCND	8TH CONDITION CODE SLOT - IF EVEN OR HW16

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					ONLY WERE SPECIFIED, INFORMATION HERE. OTHERWISE, 8TH COND HW16 CODE OR ZERO HW16
					End of Comment
160	(A0)	X'10'	0	SCTABCAN	"16" STEP CANCEL-PRIOR ABEND NO EVEN/ONLY HW16
160	(A0)	X'8'	0	SCTONLYC	"8" STEP CANCEL-ONLY WITH NO PRIOR ABEND(S) HW16
160	(A0)	X'4'	0	SCTABEND	"4" THIS STEP ABENDED HW16
160	(A0)	X'2'	0	SCTEVEN	"2" COND=EVEN WAS SPECIFIED HW16
160	(A0)	X'1'	0	SCTONLY	"1" COND=ONLY WAS SPECIFIED HW16
161	(A1)	CHARACTER	5		TO COMPLETE CONDITION CODE SPACE HW16
166	(A6)	CHARACTER	2	SCTCATCT	COUNT OF PRIVATE CATALOG SIOTS Y01113
					Comment
					AACA NOTE!!! THE FOLLOWING INDENTED COMMENTS HAVE BEEN LEFT FOR REFERENCE ONLY. THE FIELDS HAVE BEEN INCORPORATED. IN ORDER TO IMPLEMENT MVT IT HAS BEEN NECESSARY TO ADD AACA THE FOLLOWING FIELDS TO THE SCT. TO AVOID CAUSING AACA ERRORS IN THE CASE OF THE REASSEMBLING OF ALREADY AACA EXISTING MODULES WHICH REFERENCE THESE FIELDS,THEY AACA ARE GENERATED HERE ONLY AS COMMENTS. NOTE THAT IN AACA ACTUALITY THESE FIELDS OCCUPY THE 1ST 5 BYTES OF Y02668 THE AREA THAT IMMEDIATELY FOLLOWS THESE COMMENTS. Y01113 UNTIL THESE FIELDS ARE ACTUALLY INCORPORATED INTO THIS AACA MACRO,THEY MUST BE REFERENCED BY DISPLACEMENT (GIVEN AACA BELOW), PREFERABLY THROUGH THE USE OF EQUATES WITH AACA THE SYMBOLS DESIGNATED BELOW. AACA AACA AACA
					End of Comment
168	(A8)	BITSTRING	4	SCTTR0A8	RESERVED, WAS SCTTVFAT
					Comment
					THE FOLLOWING BYTE WAS ASSUMED FOR THE SCTSTEND FIELD, BUT WAS NOT ACTUALLY DEFINED. IT IS DEFINED SO THAT THE 3 BYTES THAT WERE RESERVED MAY BE USED FOR THE SCTRGSZ FIELD. SEE THE PREVIOUS BLOCK COMMENT FOR MORE INFORMATION.
					End of Comment
172	(AC)	CHARACTER	1	SCTSTEND	
172	(AC)	X'80'	0	SCTSTSRT	"128" BIT 0= STEP STARTED
172	(AC)	X'40'	0	SCTSTPND	"64" BIT 1= STEP ENDED
172	(AC)	X'20'	0	SCTSYSCK	"32" BIT 2= RESTART REQUEST SYSTEM Y02641 INITIATED, C/P DATA SET Y02641 ALREADY BEEN VALIDATED. Y02641 DISPLACEMENT 172 (DECIMAL) AACA
172	(AC)	X'10'	0	SCTSTNRN	"16" BIT 3= THIS STEP WAS NOT RUN BECAUSE OF CONDITION CODE PROCESSING
172	(AC)	X'8'	0	SCTNDSI	"8" BIT 4= The PPT specified 'No Data Set Integrity'
172	(AC)	X'4'	0	SCTTEXEC	"4" BIT 5= This step was terminated by exec() processing
172	(AC)	X'2'	0	SCTDNTEM	"2" Dataset Name Table Length Copied (from SCTLSDTB into SCTCLDST). Set/checked by IEFDB414 after the first Dynamic Allocation of a Step causes an entry to be made in the DSNT.
172	(AC)	X'1'	0	SCTNHUSI	"1" When on, indicates that the NOHONORIEFUSIREGION (no honor IEFUSI region settings) was set in the Program Properties Table (PPT). Region sizes and limits, and associated MEMLIMIT values set or affected by the IEFUSI exit will not be honored when this bit is on. This is a propagation of PPTNHUSI. When set, SMF30NHU should also be set.
173	(AD)	CHARACTER	3	SCTRGSZ	REGION SIZE IN K BYTES
173	(AD)	X'B0'	0	SCTLNGTH	"*-INSMSCT" LENGTH OF SCT AACA
176	(B0)	SIGNED	4	INDMDSNT (45)	

SCT Cross Reference

Name	Hex Offset	Hex Value
EAADDRBT	4	80
EACAUSER	4	40
INCMSSTS	4	1
INDMDSNT	B0	
INSMSCT	0	0
SCTABCAN	A0	10
SCTABCND	A0	
SCTABEND	A0	4
SCTADSTB	28	
SCTAFACT	20	
SCTANSCT	14	
SCTBCT	68	68
SCTCATCT	A6	
SCTCLDST	69	
SCTDDNT	1C	
SCTDISKA	0	
SCTDNTEM	AC	2
SCTDORST	4	8
SCTEPRFM	5E	80
SCTEVEN	A0	2
SCTFSIOT	C	
SCTFSTEP	5E	20
SCTGOSTP	43	80
SCTGOTTR	64	
SCTGRPH	4	2
SCTID	3	2
SCTINITB	43	E
SCTINPUT	43	40
SCTJBEND	68	2
SCTJSCAT	43	1
SCTKEY0	4	4
SCTLALOC	A	
SCTLDSTB	50	
SCTLNGTH	AD	B0
SCTLSIOT	18	
SCTMCVOL	68	20
SCTMEMPR	5	
SCTMSADR	48	
SCTMSSZE	58	
SCTNDSI	AC	8
SCTNHUSI	AC	1
SCTNIUSL	5C	
SCTNOCKP	4	10
SCTNORST	4	20
SCTNSMSG	41	
SCTONLY	A0	1
SCTONLYC	A0	8
SCTOUTMC	43	20
SCTPCAT	54	
SCTPGMNM	6C	
SCTPRFMF	74	
SCTPRFM2	5E	40
SCTPRMDD	68	10
SCTRGSZ	AD	
SCTRPACT	3C	
SCTRSTST	68	1
SCTRSVD1	6	
SCTSCLPC	2C	
SCTSDP	5E	
SCTSDPCD	76	
SCTSDPOP	78	
SCTSDPSA	79	
SCTSEXEC	8	
SCTSJFHK	43	10
SCTSMF	60	
SCTSNAME	34	
SCTSNUMB	40	
SCTSPSYS	68	4
SCTSRBT	4C	
SCTSSNUM	5A	
SCTSSTAT	4	
SCTSSYSC	3E	
SCTSTAT2	68	
SCTSTEND	AC	
SCTSTNRN	AC	10
SCTSTPLB	68	8
SCTSTPND	AC	40
SCTSTSRT	AC	80
SCTSTYPE	43	
SCTSWB	24	
SCTSYSCK	AC	20
SCTTBLID	3	
SCTTEXEC	AC	4
SCTTR0A8	A8	
SCTTR010	10	
SCTUSYSC	3E	8000
SCTXBTTR	44	

SCTX Information

SCTX Heading Information

Common Name: STEP CONTROL TABLE EXTENSION
Macro ID: IEFSCTX
DSECT Name: SCTXIN
Owning Component: Interpreter (SC1B9)
Eye-Catcher ID: None
Storage Attributes: Subpool: 236 or 237 (SWA), 241 (MSTR)
Key: 1
Residency: Below
Size: 176 Below
FREQUENCY = One per step in a job
Created by: The Interpreter (IEFVEA)
Pointed to by: SCTXBTTR field of the Step Control Table (IEFASCTB)
Serialization: None
Function: THIS MACRO MAPS THE STEP CONTROL TABLE EXTENSION

SCTX Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	176	SCTXIN	STEP CONTROL TABLE EXTENSION (SCTX)
0	(0)	ADDRESS	3	SCTXSVA	SVA OF SCTX
3	(3)	CHARACTER	1	SCTXID	BLOCK ID OF SCTX = 0C
4	(4)	CHARACTER	104	SCTXRSWA	CHECKPOINT RESTART WORK AREA
4	(4)	CHARACTER	100	SCTXPARM	If SCTSEXEC is non-zero, SCTXPARM contains SCTSEXEC bytes of the EXEC statement PARM= parameter string. If SCTSEXEC is zero and the SCTPRMDD bit in SCTSTAT2 is one, SCTXPARM contains the DDname (padded on the right with blanks to 8 characters) of the input parameter data set as specified by the EXEC statement PARMDD= parameter.
104	(68)	CHARACTER	4	SCTXRSV1	RESERVED FOR CHECKPOINT RESTART
108	(6C)	UNSIGNED	4	SCTXSTL	MAXIMUM STEP RUNNING TIME
112	(70)	UNSIGNED	4	SCTXABCC	ABEND COMPLETION CODE FOR THIS STEP
116	(74)	CHARACTER	47	SCTXIFST	IFBS FOR THIS STEP TO EXECUTE
116	(74)	CHARACTER	2	SCTXIFEL	IF OR ELSE CLAUSE INDICATORS 0 - IF CLAUSE 1 - ELSE CLAUSE
118	(76)	UNSIGNED	3	SCTXIFSV (4294967311:562114560)	ARRAY OF IFB SVAS
163	(A3)	BITSTRING	1	SCTXMEMS	SOURCE OF MEMLIMIT VALUE
164	(A4)	CHARACTER	8	SCTXMLSZ	MEMLIMIT SIZE (IN MB)
172	(AC)	UNSIGNED	4	SCTXSTMT	STATEMENT NUMBER

SCTX Cross Reference

Name	Hex Offset	Hex Value
SCTXABCC	70	
SCTXID	3	
SCTXIFEL	74	
SCTXIFST	74	
SCTXIFSV	76	
SCTXIN	0	
SCTXMEMS	A3	
SCTXMLSZ	A4	
SCTXPARM	4	
SCTXRSV1	68	
SCTXRSWA	4	
SCTXSTL	6C	
SCTXSTMT	AC	
SCTXSVA	0	

SCVA Information

SCVA Heading Information

Common Name: Slip Control Element Variable Area
Macro ID: IHASCVA
DSECT Name: SCVA, SCVACOM, SCVAPLIM, SCVAMLIM, SCVACOMP, SCVAREAS, SCVAJOBN, SCVAADDR, SCVAMOD, SCVAERRT, SCVAMODE, SCVAAS, SCVASA, SCVAS, SCVASDAT, SCVALIST, SCVADATA, SCVADAEX, SCVADA, SCVATRD, SCVADMP, SCVAJL, SCVASTRLIST, SCVAREMSTRLIST, SCVAREMOTE, SCVAREMOTEENTRY SCVASYSLIST, SCVAREMIDGROUP, SCVAREMSDUMPTOKEN, SCVAWORK, ScvaRange ScvaAction, ScvaDsplist, ScvaListd, ScvaPasc, ScvaDssa, ScvaSt, ScvaDnl, ScvaDsplistAnEntry, ScvaDnt1, ScvaLe, ScvaListAds, ScvaInd
Owning Component: SLIP (SCSLP)
Eye-Catcher ID: SCVA
Offset: 0
Length: 4
Storage Attributes: Subpool: 239
Key: 0
Residency: ANY
Size: Variable
Created by: IEECB909 when creating a SLIP trap.
Pointed to by: SCESCVA filed of the SCE data area
Serialization: Compare & Swap / Compare Double & SWAP on the following fields: SHDRSEQ, SHDRCTR, SCECTR
Function: The SCVA is an extention of the Slip Control Element (IHASCE) and, along with the SCE, internally represents a SLIP operator command. The SCVA is a variable area and its size depends on the keywords and parameters specified on the SLIP trap.

SCVA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVA	
0	(0)	CHARACTER	8	SCVAHDR	NON VARIABLE PORTION
0	(0)	CHARACTER	4	SCVACBID	CONTROL BLOCK ID = SCVA
4	(4)	SIGNED	2	SCVALN	LENGTH OF SCVA
6	(6)	CHARACTER	2	*	RESERVED
8	(8)	CHARACTER	*	SCVAVA	VARIABLE AREA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	3	SCVACOM	COMMON, FOR USE WHEN SCANNING ENTRIES
0	(0)	SIGNED	2	SCVACOLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVACOID	ENTRY ID

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	24	SCVAPLIM	PRCNTLIM
0	(0)	SIGNED	2	SCVAPLLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAPLID	ENTRY ID
3	(3)	UNSIGNED	1	SCVAPLSP	PRCNTLIM SPECIFIED
4	(4)	SIGNED	4	SCVAPLSC	SPACE SWITCH INTERRUPT COUNTER
8	(8)	CHARACTER	8	SCVAPLST	START TIME
8	(8)	BITSTRING	4	SCVAPLTL	LEFT HALF
12	(C)	BITSTRING	4	SCVAPLTR	RIGHT HALF
16	(10)	CHARACTER	8	SCVAPLAC	ACCUMULATED TIME
16	(10)	BITSTRING	4	SCVAPLAL	LEFT HALF
20	(14)	BITSTRING	4	SCVAPLAR	RIGHT HALF

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	12	SCVAMLIM	MATCHLIM
0	(0)	SIGNED	2	SCVAMLLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAMLID	ENTRY ID

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
3	(3)	CHARACTER	1	*	RESERVED
4	(4)	ADDRESS	4	SCVAMLNO	NUMBER OF TIMES TRAP HAS MATCHED
8	(8)	UNSIGNED	2	SCVAMLSP	MATCHLIM SPEC
10	(A)	CHARACTER	2	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	12	SCVACOMP	COMP ENTRY
0	(0)	SIGNED	2	SCVACCLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVACCID	ENTRY ID
3	(3)	BITSTRING	1	SCVACCB	FLAGS
		1...		SCVACCU	ON=USER CODE, OFF=SYSTEM
		.111 1111		*	RESERVED
4	(4)	CHARACTER	3	SCVACCM	MASK FOR SIGNIFICANT DIGITS
7	(7)	CHARACTER	3	SCVACCD	COMPLETION CODE
10	(A)	CHARACTER	2	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	12	SCVAREAS	REASON CODE
0	(0)	SIGNED	2	SCVARCLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVARCID	ENTRY ID
3	(3)	BITSTRING	1	SCVARCB	FLAGS -- RESERVED
4	(4)	CHARACTER	4	SCVARCM	MASK FOR SIGNIFICANT DIGITS
8	(8)	UNSIGNED	4	SCVARCSP	REASON CODE

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	12	SCVAJOBN	JOBNAME-JSPGM ENTRY
0	(0)	SIGNED	2	SCVAJNLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAJNID	ENTRY ID
3	(3)	CHARACTER	1	*	RESERVED
4	(4)	CHARACTER	8	SCVAJND	JOBNAME OR JSPGM

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	SCVAADDR	ADDRESS ENTRY
0	(0)	SIGNED	2	SCVAADLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAADID	ENTRY ID
3	(3)	BITSTRING	1	SCVAAFLG	Mapped for RANGE, LPAMOD, PVTMOD, NUCMOD. ScvaMdPending must be in same bit position
		11..		*	This must be the same number of bits preceding ScvaMdPending in ScvaMOD
		..1.		SCVAMDPENDING	For LPAMOD/EP, indicates that we have not yet found the range
4	(4)	CHARACTER	16	SCVAADD	ADDRESS RANGE
4	(4)	CHARACTER	8	SCVAADD1	1st address
4	(4)	CHARACTER	4	SCVAADD1HIGH	
8	(8)	ADDRESS	4	SCVAADD1LOW	
12	(C)	CHARACTER	8	SCVAADD2	2nd address
12	(C)	CHARACTER	4	SCVAADD2HIGH	
16	(10)	ADDRESS	4	SCVAADD2LOW	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVARANGE	Range Entry
0	(0)	CHARACTER	24	SCVARANGEHEADER	Fixed portion
0	(0)	SIGNED	2	SCVARANGELN	Entry Length
2	(2)	UNSIGNED	1	SCVARANGEID	Entry ID
3	(3)	BITSTRING	1	SCVARANGEFL	FLAGS
		1...		SCVARANGESY	SYMBOLIC ASID QUALIFIERS SPECIFIED
		.1..		SCVARANGESAUSED	Sa qualifier used
		..11 1111		*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
4	(4)	SIGNED	2	SCVARANGENO	Number of addresses in range
6	(6)	CHARACTER	2	*	Reserved
8	(8)	CHARACTER	16	SCVARANGEEVALUATED	Evaluated range
8	(8)	CHARACTER	8	SCVARANGE1	Evaluated first address
8	(8)	ADDRESS	4	SCVARANGE1HIGH	
12	(C)	ADDRESS	4	SCVARANGE1LOW	
16	(10)	CHARACTER	8	SCVARANGE2	Evaluated second address
16	(10)	ADDRESS	4	SCVARANGE2HIGH	
20	(14)	ADDRESS	4	SCVARANGE2LOW	
24	(18)	CHARACTER	*	SCVARANGEAD	DATA -SEE SCVADAEX, SCVAIND, AND SVCADA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVASTDATA	STDATA Entry
0	(0)	CHARACTER	8	SCVASTDATAHEADER	Fixed portion
0	(0)	SIGNED	2	SCVASTDATALN	Entry Length
2	(2)	UNSIGNED	1	SCVASTDATAID	Entry ID
3	(3)	BITSTRING	1	SCVASTDATAFL	Flags
		1...		SCVASTDATASY	Symbolic ASID qualifiers specified
		.1..		SCVASTDATASAUSED	SA Qualifier used
		..11 1111		*	Reserved
4	(4)	SIGNED	2	SCVASTDATANO	Number of addresses
6	(6)	CHARACTER	2	*	Reserved
8	(8)	CHARACTER	*	SCVASTDATAAD	Data

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAMOD	PVTMOD-LPAMOD-NUCMOD ENTRY
0	(0)	CHARACTER	36	SCVAMDFIXED	Fixed portion
0	(0)	SIGNED	2	SCVAMDLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAMDID	ENTRY ID
3	(3)	BITSTRING	1	SCVAMDFL	FLAGS
		1...		SCVAMDEP	LPAEP/PVTEP/NUCEP
		.1..		SCVAMDPOSIXPATHNAMEUSED	Indicates the module name is really the last 8 chars of an POSIX pathname
		..1.		SCVAMDPENDING	For LPAMOD/EP, indicates that we have not yet found the range

Comment

Even though PVT, LPA, and NUC do not support addresses above 2G, they share the WAADDR mapping which also covers the ADDRESS keyword (which does support addresses above 2G). Thus, for convenience, we have 8-byte address fields here even though the processing will ignore the high 4 bytes

End of Comment

4	(4)	CHARACTER	8	SCVAMDA1_8	
4	(4)	CHARACTER	4	SCVAMDA1_8_HIGH	
8	(8)	ADDRESS	4	SCVAMDA1	FIRST ADDRESS
12	(C)	CHARACTER	8	SCVAMDA2_8	
12	(C)	CHARACTER	4	SCVAMDA2_8_HIGH	
16	(10)	ADDRESS	4	SCVAMDA2	SECOND ADDRESS
20	(14)	ADDRESS	4	SCVAMDO1	FIRST OFFSET
24	(18)	ADDRESS	4	SCVAMDO2	SECOND OFFSET
28	(1C)	CHARACTER	8	*	
28	(1C)	CHARACTER	4	SCVAMD_POSIX	
28	(1C)	UNSIGNED	1	SCVAMDPOSIXPATHNAMELENGTH	Length of path specified
29	(1D)	CHARACTER	3	*	Unused
28	(1C)	CHARACTER	8	SCVAMD_DYNLPA	
28	(1C)	ADDRESS	4	SCVAMDA1A	For dynamic LPA (or PVT) the "secondary" load point
32	(20)	ADDRESS	4	SCVAMDA2A	For dynamic LPA (or PVT) the end of the secondary load point
36	(24)	CHARACTER	*	SCVAMDVARIABLE	
36	(24)	CHARACTER	8	SCVAMDNM	MODULE NAME
36	(24)	CHARACTER	*	SCVAMDPOSIXPATHNAME	Posix pathname

SCVA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8	SCVAERRT	ERRTYP
0	(0)	SIGNED	2	SCVAERLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAERID	ENTRY ID
3	(3)	BITSTRING	1	SCVAERFL	ERRTYP FLAGS
		1...		SCVAEMCH	MACHINE CHECK
		.1..		SCVAEPRG	PROGRAM CHECK
		..1.		SCVAERST	RESTART INTERRUPT
		...1		SCVAEABN	ABEND
		.... 1...		SCVAEMEM	MEMORY TERMINATION
		.... .1..		SCVAESVC	SVC ERROR
		.... ..1.		SCVAEDAT	DAT ERROR
		1		SCVAEPIO	PAGING I/O ERROR
4	(4)	BITSTRING	1	SCVAERFL1	More ERRTYP flags
		1...		SCVAETXPROG	
5	(5)	CHARACTER	3	*	To match WAERRT. Not positive this is necessary, but does no harm other than use 3 extra bytes

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8	SCVAMODE	MODE ENTRY
0	(0)	SIGNED	2	SCVAMOLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAMOID	ENTRY ID
3	(3)	BITSTRING	1	SCVAMOC	MODE COMPARISON
		1...		SCVAMOE	ON=EVERY, OFF=ANY
		.111 1111		*	RESERVED
4	(4)	BITSTRING	2	SCVAMOFL	MODE FLAGS
4	(4)	BITSTRING	1	SCVAMO1	FIRST EIGHT
		1...		SCVAMSUP	SUPERVISOR CONTROL MODE
		.1..		SCVAMDIS	DISABLED
		..1.		SCVAMGSP	GLOBAL SPIN LOCK
		...1		SCVAMGSD	GLOBAL SUSPEND LOCK
		.... 1...		SCVAMLL	LOCAL LOCK
		.... .1..		SCVAMT1S	TYPE 1 SVC
		.... ..1.		SCVAMSRB	SRB
		1		SCVAMTCB	TCB
5	(5)	BITSTRING	1	SCVAMO2	SECOND EIGHT
		1...		SCVAMREC	RECOVERY IN CONTROL
		.1..		SCVAMPP	PROBLEM PROGRAM STATE
		..1.		SCVAMSS	SUPERVISOR STATE
		...1		SCVAMSK	SYSTEM KEY
		.... 1...		SCVAMPK	PROBLEM PROGRAM KEY
		.... .1..		SCVAMGL	ANY GLOBAL LOCK
		.... ..1.		SCVAMLOK	ANY LOCK
		1		SCVAMHME	HOME
6	(6)	CHARACTER	2	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4	SCVAPASC	PASC ENTRY
0	(0)	SIGNED	2	SCVAPALN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAPAID	ENTRY ID
3	(3)	BITSTRING	1	SCVAPAFL	PASC FLAGS
		1...		SCVAPAP	ASC-PRIMARY
		.1..		SCVAPAAR	ASC-AR
		..1.		SCVAPAS	ASC-SECONDARY
		...1		SCVAPAH	ASC-HOME

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAAS	ASID
0	(0)	CHARACTER	4	SCVAASHD	NON VARIABLE PART
0	(0)	SIGNED	2	SCVAASLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAASID	ENTRY ID
3	(3)	UNSIGNED	1	SCVAASNO	NUMBER OF ASID'S
4	(4)	CHARACTER	2	SCVAASD (*)	ASIDS

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVASA	ASIDSA
0	(0)	CHARACTER	8	SCVASAHD	NON VARIABLE PART
0	(0)	SIGNED	2	SCVASALN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVASAID	ENTRY ID
3	(3)	UNSIGNED	1	SCVASANO	NUMBER OF ASID'S
4	(4)	BITSTRING	1	SCVASAFL	FLAGS
		1...		SCVASASY	SYMBOLIC SPECIFIED
5	(5)	CHARACTER	3	*	RESERVED
8	(8)	CHARACTER	9	SCVASAD (*)	ASID'S
8	(8)	UNSIGNED	1	SCVASSA	SYMBOLIC SETTING CURRENT=00 PASID=01 SASID=02 HOME=03 OTHER=04
9	(9)	CHARACTER	8	SCVASAJOBNAME	Jobname
9	(9)	CHARACTER	2	SCVASNA	ASID

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVADSSA	DSSA
0	(0)	CHARACTER	8	SCVADSHD	NON VARIABLE PART
0	(0)	SIGNED	2	SCVADSLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVADSID	ENTRY ID
3	(3)	UNSIGNED	1	SCVADSNO	NUMBER OF DATASPACES
4	(4)	BITSTRING	1	SCVADSFL	FLAGS
		1...		SCVADSSY	SYMBOLIC SPECIFIED
5	(5)	CHARACTER	3	*	RESERVED
8	(8)	CHARACTER	17	SCVADSD (*)	ASID'S
8	(8)	UNSIGNED	1	SCVADSS	SYMBOLIC SETTING CURRENT=00 PASID=01 SASID=02 HOME=03 OTHER=04
9	(9)	CHARACTER	16	SCVADSAN	DATA SPACE ASID/NAME
9	(9)	CHARACTER	8	SCVADSJOBNAME	Jobname
9	(9)	CHARACTER	2	SCVADSA	ASID
17	(11)	CHARACTER	8	SCVADSN	NAME PORTION

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAS	ASIDLST
0	(0)	CHARACTER	14	SCVASHD	NON VARIABLE PART
0	(0)	CHARACTER	4	SCVASCS	
0	(0)	SIGNED	2	SCVASLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVASID	ENTRY ID
3	(3)	CHARACTER	1	*	RESERVED
4	(4)	ADDRESS	4	SCVAP1	POINTER TO AREA WITH SYMBOLIC ASIDS
8	(8)	ADDRESS	4	SCVAP2	POINTER TO AREA FOR BUILDING LISTD
12	(C)	UNSIGNED	1	SCVASNO	NUMBER OF ASID'S
13	(D)	CHARACTER	1	SCVASFL	FLAGS
		1...		SCVASYM	SYMBOLIC ASIDS SPECIFIED
		.111 1111		*	RESERVED
14	(E)	CHARACTER	*	SCVASD	LIST AREA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	3	SCVAST (*)	AREA WHICH HOLDS SYMBOLIC INDICATORS AND ASID
0	(0)	UNSIGNED	1	SCVASSYMB	SYMBOLIC CURRENT=00 PASID=01 SASID=02 HOME=03 OTHER-04 LLOC=05
1	(1)	CHARACTER	2	SCVASASID	ASID

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVADNL	DSPNAME
0	(0)	CHARACTER	12	SCVADNHD	NON VARIABLE PART
0	(0)	CHARACTER	4	SCVADNCS	
0	(0)	SIGNED	2	SCVADNLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVADNID	ENTRY ID
3	(3)	CHARACTER	1	*	RESERVED
4	(4)	ADDRESS	4	SCVADNP1	POINTER TO AREA FOR building the Dsplist structure

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
8	(8)	UNSIGNED	1	SCVADNNO	NUMBER OF DATASPACES
9	(9)	BITSTRING	3	SCVADNFL	FLAGS
		1...		SCVADNSY	SYMBOLIC ASIDS SPECIFIED
		.111 1111		*	RESERVED
12	(C)	CHARACTER	*	SCVADNA	DATASPACE NAMES

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	28	SCVALD64	LIST64 entry
0	(0)	CHARACTER	12	SCVALD64ENTRYHEADER	
0	(0)	CHARACTER	8	SCVALDSTKN	STOKEN
8	(8)	SIGNED	4	SCVALDCNT	COUNT
12	(C)	CHARACTER	16	SCVALD64ADDRESSPAIRS	
					Start of address pair section
12	(C)	CHARACTER	8	SCVALDSTRT64	
12	(C)	CHARACTER	4	SCVALDSTRTHIGH	
16	(10)	ADDRESS	4	SCVALDSTRTLOW	
					START ADDRESS
20	(14)	CHARACTER	8	SCVALDEND64	
20	(14)	CHARACTER	4	SCVALDENDHIGH	
24	(18)	ADDRESS	4	SCVALDENDLOW	END ADDRESS
28	(1C)	CHARACTER	0	SCVALISTDEND	END OF ENTRY

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	32	SCVASL64	Sumlist64 entry
0	(0)	CHARACTER	16	SCVASL64ENTRYHEADER	
0	(0)	CHARACTER	8	*	Stoken or alet
0	(0)	CHARACTER	8	SCVASLSTKN	STOKEN
0	(0)	CHARACTER	8	*	
0	(0)	CHARACTER	4	*	Reserved
4	(4)	CHARACTER	4	SCVASLALET	Alet
8	(8)	SIGNED	4	SCVASLCNT	COUNT
12	(C)	CHARACTER	4	SCVASLQUAL	Flag byte
		1...		SCVASLQUALIFIERISASTOKEN	
					ScvaSlStkn is really a stoken
		.1..		SCVASLQUALIFIERISANALET	
					ScvaSlAlet contains an alet
16	(10)	CHARACTER	16	SCVASL64ADDRESSPAIRS	
					Beginning of address pair section
16	(10)	CHARACTER	8	SCVASLSTRT64	
16	(10)	CHARACTER	4	SCVASLSTRTHIGH	
20	(14)	ADDRESS	4	SCVASLSTRTLOW	
					START ADDRESS
24	(18)	CHARACTER	8	SCVASLEND64	
24	(18)	CHARACTER	4	SCVASLENDHIGH	
28	(1C)	ADDRESS	4	SCVASLENDLOW	END ADDRESS
32	(20)	CHARACTER	0	SCVASLEND	END OF ENTRY

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8	SCVASL64HEADER	
0	(0)	SIGNED	4	SCVASL64HEADERTOTALLENGTH	
4	(4)	SIGNED	4	*	
8	(8)	CHARACTER	0	SCVASL64HEADERRESTOFDATA	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4	SCVALD64HEADER	
0	(0)	SIGNED	4	SCVALD64HEADERTOTALLENGTH	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVADSPLIST	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	SIGNED	4	SCVADSPLISTHEADER	Length of Dsplist
4	(4)	SIGNED	4	SCVADSPLISTENTRY (*)	Entries in dsplist

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	SCVADSPLISTANENTRY	Dsplist entry
0	(0)	CHARACTER	8	SCVADSPLISTJOBNAME	Jobname
0	(0)	CHARACTER	6	*	Reserved - must be zeros when ASID is specified
6	(6)	CHARACTER	2	SCVADSPLISTASID	Asid
8	(8)	CHARACTER	8	SCVADSPLISTNAME	Name of data space

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	17	SCVADNT1 (*)	
0	(0)	UNSIGNED	1	SCVADNSA	SYMBOLIC CURRENT=00 PASID=01 SASID=02 HOME=03 OTHER-04 LLOC=05
1	(1)	CHARACTER	8	SCVADNJOBNAME	Jobname qualifier
1	(1)	SIGNED	2	SCVADNAS	ASID
9	(9)	CHARACTER	8	SCVADNNM	NAME PORTION

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8	SCVASDAT	SDATA
0	(0)	SIGNED	2	SCVASDLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVASDID	ENTRY ID
3	(3)	CHARACTER	1	*	RESERVED
4	(4)	CHARACTER	3	SCVASDFL	SDATA FLAGS
4	(4)	CHARACTER	1	SCVASD1	FIRST EIGHT
		1...		SCVASAPS	DUMP ALL PSA'S
		.1..		SCVASPSA	DUMP CURRENT PSA
		..1.		SCVASNUC	DUMP THE NUCLEUS
		...1		SCVASSQA	DUMP SQA
		.... 1...		SCVASLSQ	DUMP LSQA
		.... .1..		SCVASRGN	DUMP RGN (PRIVATE AREA)
		.... ..1.		SCVASLPA	DUMP LPA MOD. FOR RGN
		1		SCVASTRT	DUMP TRACE TABLE/GTF BUF
5	(5)	CHARACTER	1	SCVASD2	SECOND EIGHT
		1...		SCVASCSA	DUMP CSA
		.1..		SCVASSWA	DUMP SWA
		..1.		SCVASSDP	DUMP SUMMARY DUMP
		...1		SCVASNSD	DO NOT DUMP SUMMARY DUMP
		.... 1...		SCVASNAP	DO NOT DUMP ALL PSA
		.... .1..		SCVASNSQ	DO NOT DUMP SQA
		.... ..1.		SCVASANU	DUMP DAT OFF NUC
		1		*	RESERVED
6	(6)	CHARACTER	1	SCVASDE1	EXTENDED SDATA OPT
		1...		SCVASGRS	DUMP GRSQ
		.1..		*	1=MASTER TRACE EXIT
		..1.		*	1=SMSX LOCAL EXIT
		...1		SCVASCPL	DUMP COUPLING DATA
		.... 1...		SCVASXES	Dump XESDATA data
		.... .1..		*	Reserved
		.... ..1.		SCVASWLM	1=WLM
		1		*	RESERVED
7	(7)	CHARACTER	1	*	RESERVED

SCVA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVALIST	LIST-SUMLIST ENTRY
0	(0)	CHARACTER	44	SCVALSHD	ENTRY HEADER
0	(0)	CHARACTER	4	SCVALSCS	WORD TO CS LOCK
0	(0)	SIGNED	2	SCVALSLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVALSID	ENTRY ID
3	(3)	CHARACTER	1	*	RESERVED
4	(4)	SIGNED	2	SCVALSNO	NUM OF ADDRS
6	(6)	CHARACTER	1	SCVALSFL	FLAGS
		1...		SCVALSYM	SYMBOLIC ASID QUALIFIER SPECIFIED
		.1..		SCVALSESAMEREQUIRED	Address string can only be evaluated on an ESAME system
		..11 1111		*	RESERVED
7	(7)	CHARACTER	6	SCVALSM4	CONTAINS SCVAXXM4
13	(D)	CHARACTER	7	SCVALSAE	CONTAINS SCVAXXAE
20	(14)	CHARACTER	7	SCVALSM1	CONTAINS SCVAXXM1
27	(1B)	CHARACTER	9	SCVALSM2	CONTAINS SCVAXXM2
36	(24)	ADDRESS	4	SCVALSSYMA	ADDRESS OF SYMBOLIC AREA FOR CONVERSION
40	(28)	ADDRESS	4	SCVALSDWAP	ADDRESS OF LIST64 or Sumlist64 AREA FOR SDUMP
44	(2C)	CHARACTER	*	SCVALT	
44	(2C)	ADDRESS	4	SCVALTHD	HEADER WITH LENGTH Note that for sumlist64 a reserved field of 4 bytes follows SCVALTHD
48	(30)	CHARACTER	*	SCVALSD	DATA -SEE SCVAIND, ALSO SPACE WILL BE PROVIDED AT THE END OF SCVALSD TO BUILD THE LIST OF RESOLVED ADDRESSES THAT WILL BE SENT TO SDUMP. THE WORK AREA IS POINTED TO BY SCVALSWAP. FOR SUMLIST, THE SUMLSTL AREA IS ALSO HERE, POINTED TO BY SCVALSDWAP

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	SCVALISTADS	ADDRESSES OF ENTRY
0	(0)	CHARACTER	8	SCVALISTAD164	Start address
0	(0)	CHARACTER	4	SCVALISTAD1HIGH	High portion of address
4	(4)	ADDRESS	4	SCVALISTAD1LOW	Low portion of address
8	(8)	CHARACTER	8	SCVALISTAD264	End address
8	(8)	CHARACTER	4	SCVALISTAD2HIGH	High portion of address
12	(C)	ADDRESS	4	SCVALISTAD2LOW	Low portion of address

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAIND	INDIRECT ADDRESS
0	(0)	UNSIGNED	1	SCVAINDL	LENGTH OF THE ADDRESS IN ITS ORIGINAL EBCDIC FORM FOR USE BY DISPLAY
0	(0)	UNSIGNED	1	SCVAINID	ID OF ELEMENT WITHIN ADDRESS
1	(1)	CHARACTER	*	SCVAINAD	ADDRESS, BROKEN INTO ELEMENTS EACH OF WHICH CONSISTS OF AN ID (WHICH IMPLIES A LENGTH) AND A HEX NUMBER IF THE ID IS 2, 3, 5 0R 6.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVADATA	DATA ENTRY
0	(0)	CHARACTER	12	SCVADAHD	ENTRY HEADER
0	(0)	SIGNED	2	SCVADALN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVADAID	ENTRY ID
3	(3)	CHARACTER	1	SCVADAFL	FLAGS
		1...		SCVADASY	SYMBOLIC ASID QUALIFIERS SPECIFIED
		.111 1111		*	RESERVED
4	(4)	CHARACTER	4	SCVADACS	LABEL TO CS UNAVAIL COUNT
4	(4)	ADDRESS	2	SCVADANO	NUMBER OF ELEMENTS
6	(6)	ADDRESS	2	SCVADAUN	DATA UNAVAIL COUNT
8	(8)	CHARACTER	4	SCVADACS1	
8	(8)	UNSIGNED	2	SCVADAERRORBADTRIPLETNUMBER	For data refresh indicates the number of the triplet causing the error

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
10	(A)	CHARACTER	1	SCVADAERROR	Indicates what is wrong with triplet
		1...		SCVADAERRORFIRSTOPERAND	Error occured on first operand
		.1..		SCVADAERRORASIDINACTIVE	Asid qualifier was inactive
		..1.		SCVADAERRORDATAINACCESSIBLE	Data was paged out or space was swapped out
		...1		SCVADAERRORDATAWRITEPROTECTED	Storage to refresh was write protected
11	(B)	UNSIGNED	1	SCVADATX	Code in IEAVTSL3 depends on this being the low byte of the CS word. Do not move unless you change IEAVTSL3 too
12	(C)	CHARACTER	*	SCVADAD	DATA -SEE SCVADAEX, SCVAIND, AND SVCADA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	2	SCVADAEX	DATA EXPRESSION ID
0	(0)	CHARACTER	1	SCVADAEI	ID DEFINING PARTS IN DATA (LEFT PAREN, RIGHT PAREN, OR, AND, TRIPLETS)
1	(1)	BITSTRING	1	SCVADAF2	FLAGS
		1...		SCVADAIM	IMPLICIT CHARACTER
		.1..		SCVADASM	SYMBOLIC AND/OR
		..11 1111		*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	18	SCVADA	OPERATOR AND VALUE PART OF SCVADATA The boundary specification causes the compiler to think that the value and valuemask fields are word-aligned (they might or might not actually be) and that results in better code being generated.
0	(0)	CHARACTER	1	SCVADAOP	OPERATOR SPEC
		1...		SCVADAN	NOT IND
		.1..		SCVADAEQ	EQUAL IND
		..1.		SCVADAGT	GREATER THAN IND
		...1		SCVADALT	LESS THAN IND
		.... 1...		SCVADAB	BINARY COMPARE
		.... .1..		SCVADACC	CONTENTS COMPARE
		.... ..1.		SCVADAAD	ADDRESS COMPARE
		1		SCVADAG64	64-bit GPR
1	(1)	UNSIGNED	1	SCVADAVL	LENGTH IN NUMBER OF BYTES TO BE COMPARED
2	(2)	CHARACTER	8	SCVADAVM8	VALUE MASK
2	(2)	CHARACTER	4	SCVADAVMZEROES	Zeroes when 4-byte
6	(6)	CHARACTER	4	SCVADAVM	VALUE MASK
10	(A)	CHARACTER	8	SCVADAV8	VALUE SPEC
10	(A)	CHARACTER	4	SCVADAVZEROES	Zeroes when 4-byte
14	(E)	CHARACTER	4	SCVADAV	VALUE SPEC

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVATRD	TRDATA ENTRY
0	(0)	CHARACTER	8	SCVATDHD	ENTRY HEADER
0	(0)	SIGNED	2	SCVATDLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVATDID	ENTRY ID
3	(3)	BITSTRING	1	SCVATDB	TRDATA FLAGS
		1...		SCVATDST	STD SPEC
		.1..		SCVATDRG	REGS SPEC
		..1.		SCVATDAD	ADDRESS LIST SPEC
		...1		SCVATDSY	SYMBOLIC ASID QUALIFIERS SPECIFIED
		 1111		*	RESERVED
4	(4)	SIGNED	2	SCVATDNO	NUMBER OF ELEMENTS
6	(6)	CHARACTER	2	*	RESERVED
8	(8)	CHARACTER	*	SCVATDD	DATA -SEE SCVAIND

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	640	SCVADMP	DUMP ID ENTRY
0	(0)	CHARACTER	4	SCVADPHR	ENTRY HEADER
0	(0)	SIGNED	2	SCVADPLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVADPID	ENTRY ID
3	(3)	CHARACTER	1	SCVADPLK	DUMP LIST LOCK
4	(4)	ADDRESS	4	SCVADPTEXTPTR	Pointer to the Slip text buffer
8	(8)	CHARACTER	20	SCVADMTL	DUMP TITLE & ID
8	(8)	UNSIGNED	1	SCVADPTH	LENGTH OF DUMP TITLE PLUS TRAP ID
9	(9)	CHARACTER	13	SCVADPVA	DUMP TITLE
22	(16)	CHARACTER	4	SCVADPD	TRAP ID
26	(1A)	CHARACTER	2	*	KEEP FULLWORD ALIGNED
28	(1C)	CHARACTER	400	SCVADPLS	DUMP LIST, USED TO PASS TIME OF ERROR/INTERRUPT INFO TO SDUMP, SERIALIZED BY SCVADPLK. See also SCVADPG6. Also mapped by PRDSLIP. Also mapped by RTSDSLRP.
28	(1C)	CHARACTER	8	SCVADPSW	PSW
28	(1C)	CHARACTER	4	SCVADPSL	LEFT HALF OF PSW
32	(20)	CHARACTER	4	SCVADPSR	RIGHT HALF OF PSW
36	(24)	CHARACTER	8	*	Reserved
44	(2C)	CHARACTER	64	SCVADPGR	REGISTERS 0-15
108	(6C)	CHARACTER	64	SCVADPAR	ARS 0-15
172	(AC)	CHARACTER	16	SCVADPXM	XM INFO (CRs 3/4)
188	(BC)	CHARACTER	16	SCVADPSW16	16-byte PSW
204	(CC)	CHARACTER	32	*	Reserved
236	(EC)	CHARACTER	192	SCVADPESAME	ESAME stuff
236	(EC)	CHARACTER	64	SCVADPG6	High halves of GPRs
300	(12C)	CHARACTER	128	SCVADPC64S	ESAME CRs

Comment

Currently 184 byte out of the 200 are used

End of Comment

428	(1AC)	CHARACTER	200	SCVADPSDPL	LENGTH OF SDUMP PLIST
628	(274)	ADDRESS	4	SCVADITOKENPTR	Address of the incident token area
632	(278)	ADDRESS	4	SCVADJOBLISTPTR	Pointer to the joblist
636	(27C)	CHARACTER	4	SCVADPLISTDA	AREA FOR LISTD ENTRY
636	(27C)	CHARACTER	4	SCVADPLISTDHDR	HEADER FOR LISTD

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	SCVAMSGID	MSGID entry
0	(0)	CHARACTER	4	SCVAMSGIDHD	
0	(0)	SIGNED	2	SCVAMSGIDLN	Entry length
2	(2)	UNSIGNED	1	SCVAMSGIDID	Entry id
3	(3)	CHARACTER	1	SCVAMSGIDFLGS	Flags
		1...		SCVAMSGIDQUOTED	Quotes were used
4	(4)	UNSIGNED	2	SCVAMSGIDTEXTLENGTH	Length of the msgid
6	(6)	CHARACTER	10	SCVAMSGIDTEXT	Msg id to match on

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE IsA(TSCVAEXIT)	16	SCVAAEXIT	
0	(0)	CHARACTER	3	SCVAEXITHD	
0	(0)	SIGNED	2	SCVAEXITLN	
2	(2)	UNSIGNED	1	SCVAEXITID	
3	(3)	CHARACTER	1	*	
4	(4)	CHARACTER	8	SCVAEXITNAME	
12	(C)	ADDRESS	4	SCVAEXITADDR	User Exit Type

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE IsA(TSCVAEXIT)	16	SCVATEXIT	
0	(0)	CHARACTER	3	SCVAEXITHD	
0	(0)	SIGNED	2	SCVAEXITLN	
2	(2)	UNSIGNED	1	SCVAEXITID	
3	(3)	CHARACTER	1	*	
4	(4)	CHARACTER	8	SCVAEXITNAME	
12	(C)	ADDRESS	4	SCVAEXITADDR	User Exit Type

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVASTRLIST	Strlist entry
0	(0)	CHARACTER	8	SCVASTRLISTHD	Entry header
0	(0)	SIGNED	2	SCVASTRLISTLN	Entry length
2	(2)	UNSIGNED	1	SCVASTRLISTID	Entry id
3	(3)	CHARACTER	1	*	Unused
4	(4)	SIGNED	4	SCVASPRSSTRLLEN	SprsStrl length
8	(8)	CHARACTER	*	SCVASPRSSTRL	Buffer for the formatted Strlist (SprsStrl)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAJL	Joblist entry
0	(0)	CHARACTER	4	SCVAJLHD	Entry header
0	(0)	SIGNED	2	SCVAJLLN	Entry length
2	(2)	UNSIGNED	1	SCVAJLID	Entry id
3	(3)	CHARACTER	1	*	unused (bdy align)
4	(4)	CHARACTER	*	SCVAJLPLIST	joblist
4	(4)	SIGNED	4	SCVAJLPLLEN	plist length
8	(8)	CHARACTER	8	SCVAJLENTRY (*)	job/stid entries
8	(8)	CHARACTER	8	SCVAJLJOB	jobname

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAREMSTRLIST	Remote Strlist entry
0	(0)	CHARACTER	8	SCVAREMSTRLISTHD	Entry header
0	(0)	SIGNED	2	SCVAREMSTRLLN	Entry length
2	(2)	UNSIGNED	1	SCVAREMSTRLID	Entry id
3	(3)	CHARACTER	1	*	Unused
4	(4)	SIGNED	4	SCVAREMSPRSSTRLLEN	SprsStrl length
8	(8)	CHARACTER	*	SCVAREMSPRSSTRL	Buffer for the formatted Strlist (SprsStrl)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAREMOTE	Remote Entry
0	(0)	CHARACTER	8	SCVAREMOTEHD	Entry Header
0	(0)	SIGNED	2	SCVAREMOTELN	Entry length
2	(2)	UNSIGNED	1	SCVAREMOTEID	Entry ID
3	(3)	CHARACTER	1	*	Reserved
4	(4)	SIGNED	2	SCVAREMOTE#	Number of elements
6	(6)	BITSTRING	1	*	Flags
		1...		SCVAREMOTECOND	Conditional Remote
7	(7)	CHARACTER	1	*	Reserved
8	(8)	CHARACTER	*	SCVAREMOTEINFO	Data -- See ScvaRemoteEntry

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4	SCVAREMOTEENTRY	
					Expression ID
0	(0)	UNSIGNED	1	SCVAREMOTEENTRYID	
					ID defining part of data
1	(1)	UNSIGNED	1	SCVAREMOTEENTRYKEYVAL	
					When not 0, indicates a key that matches the base.
2	(2)	CHARACTER	2	*	Reserved for alignment
4	(4)	CHARACTER	0	*	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVASYSLIST	
0	(0)	CHARACTER	4	SCVASYSLHD	HEADER
0	(0)	SIGNED	2	SCVASYSLLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVASYSLID	ENTRY ID
3	(3)	UNSIGNED	1	SCVASYSL#	Number of Systems
4	(4)	CHARACTER	768	SCVASYSLSYSTEMS	
					System Name
4	(4)	CHARACTER	24	SCVASYSLSYS	System Name
				(4294967328:562250848)	
4	(4)	CHARACTER	8	SCVASYSLSYSNAME	
					System name
4	(4)	CHARACTER	8	SCVASYSLGRPNAME	
					Group name
12	(C)	CHARACTER	16	SCVASYSLMEMNAME	
					Member name or "*"
12	(C)	CHARACTER	4	SCVASYSLISADDR	
					Flag to indicate that this entry has an address string, not a system, group, or member name
16	(10)	CHARACTER	4	SCVASYSLADDRSTR	
					Address of the system name address string
20	(14)	CHARACTER	8	*	
772	(304)	CHARACTER	*	SCVASYSLADDRS	
					Start of the system name address strings - see ScvasysListAddress

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVASYSLISTADDRESS	
					Maps one system name address string (this mapping must remain compatible with the output of AdrPairs()
0	(0)	CHARACTER	4	SCVASYSLADDRHD	
0	(0)	CHARACTER	4	*	Used by AdrPairs, can be reused when trap matches
4	(4)	SIGNED	2	SCVASYSLADDRLEN	
					Length of the address tokens
6	(6)	CHARACTER	*	SCVASYSLADDRDATA	
					The address tokens
6	(6)	CHARACTER	1	*	Reserved, see above
7	(7)	CHARACTER	*	SCVASYSLADDR	Tokens start here

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	20	SCVAREMIDGROUP	
					Remote deactivation via id group
0	(0)	CHARACTER	20	SCVAREMIDGROUPHD	
					Entry header
0	(0)	SIGNED	2	SCVAREMIDGROUPLEN	
					Entry length
2	(2)	UNSIGNED	1	SCVAREMIDGROUPID	
					Entry id
3	(3)	CHARACTER	1	*	Unused
4	(4)	CHARACTER	16	SCVAREMIDGROUPVAL	
					Value

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	36	SCVAREMSDUMPTOKEN	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
					Sdump token
0	(0)	CHARACTER	36	SCVAREMSDUMPTOKENHD	
					Entry header
0	(0)	SIGNED	2	SCVAREMSDUMPTOKENLEN	
					Entry length
2	(2)	UNSIGNED	1	SCVAREMSDUMPTOKENID	
					Entry id
3	(3)	CHARACTER	1	*	Unused
4	(4)	CHARACTER	32	SCVAREMSDUMPTOKENVAL	
					Token value

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	128	SCVAWORK	
0	(0)	CHARACTER	4	SCVAWORKHD	HEADER
0	(0)	SIGNED	2	SCVAWORKLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAWORKID	ENTRY ID
3	(3)	UNSIGNED	1	SCVAWORK#	Number of Jobs
4	(4)	CHARACTER	124	SCVAWORKENTRIES	
					Job name (max=15)
4	(4)	CHARACTER	4	SCVAWORKJFIXED	
					Non-variable part
4	(4)	SIGNED	2	SCVAWORKJKEY	Indicates job
6	(6)	SIGNED	2	SCVAWORKJLN	Indicates job
8	(8)	CHARACTER	8	SCVAWORKJ	Job name (max=15)
				(4294967311:562261096)	
8	(8)	CHARACTER	8	SCVAWORKJOB	Job name

Comment

Note - if further qualifiers are added to ScvaWorkEntries, they should be in the format: halfword key, halfword length, array(15). (Start a new level 4, not in level 5 of the preceding structure).

End of Comment

128	(80)	CHARACTER	0	*

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	8	SCVAACTION	
0	(0)	CHARACTER	4	SCVAACTHD	HEADER
0	(0)	SIGNED	2	SCVAACTLN	ENTRY LENGTH
2	(2)	UNSIGNED	1	SCVAACTID	ENTRY ID
3	(3)	CHARACTER	1	*	Reserved
4	(4)	CHARACTER	2	SCVAACTIONS	Actions
4	(4)	BITSTRING	1	SCVAACTION1	Actions
		1...		SCVAACTSVCD	Action=Svcd
		.1..		SCVAACTWAIT	Action=Wait
5	(5)	BITSTRING	1	SCVAACTION2	Actions
5	(5)	BITSTRING	1	*	Reserved
6	(6)	CHARACTER	2	*	Reserved

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAGTFID	
0	(0)	CHARACTER	4	SCVAGTFIDHD	Header
0	(0)	SIGNED	2	SCVAGTFIDLN	Entry length
2	(2)	UNSIGNED	1	SCVAGTFIDID	Entry id
3	(3)	CHARACTER	1	*	Reserved
4	(4)	CHARACTER	*	SCVAGTFIDDATA	
					Data - this part is passed to GTF
4	(4)	SIGNED	4	SCVAGTFIDNO	Number of Ids
8	(8)	CHARACTER	8	SCVAGTFIDENTRY	
				(*)	
					The actual IDs

SCVA Constants

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SCVAAPARM1	
0	(0)	CHARACTER	8	SCVAAPARM1HDR	
					Header
0	(0)	SIGNED	2	SCVAAPARM1LN	Entry length
2	(2)	UNSIGNED	1	SCVAAPARM1ID	Entry id
3	(3)	BITSTRING	1	SCVAAPARM1FLGS	
					flags
		1...		SCVAAPARM1SY	Symbolic asid qualifier
4	(4)	SIGNED	2	SCVAAPARM1NO	number of addresses in entry one for APARM1
6	(6)	CHARACTER	2	*	Reserved
8	(8)	CHARACTER	*	SCVAAPARM1AD	Address mapping

SCVA Constants

Len	Type	Value	Name	Description
4	DECIMAL	0	SCVASACU	SYMBOLIC ASID -- CURRENT
4	DECIMAL	1	SCVASAP	SYMBOLIC ASID -- PRIMARY
4	DECIMAL	2	SCVASAS	SYMBOLIC ASID -- SECONDARY
4	DECIMAL	3	SCVASAH	SYMBOLIC ASID -- HOME
4	DECIMAL	4	SCVASAXP	SYMBOLIC ASID -- EXPLICIT
4	DECIMAL	5	SCVASALL	SYMBOLIC ASID -- LOC LOCK
4	DECIMAL	6	SCVASAI	SYMBOLIC ASID -- I(NSTR)
4	DECIMAL	7	SCVASASA	SYMBOLIC ASID -- SA
4	DECIMAL	8	SCVAJOBNAMESPECIFIED	
				Jobname specified
4	DECIMAL	8	SCVASAMX	MAXIMUM SYMBOLIC VALUE
0	BIT	00	SCVAASCP	ASC-PRIMARY MODE
0	BIT	01	SCVAASCA	ASC-AR MODE
0	BIT	10	SCVAASCS	ASC-SECONDARY MODE
0	BIT	11	SCVAASCH	ASC-HOME MODE
1	DECIMAL	1	SCVAINE	END INDICATOR, CONSISTS ONLY OF ID
1	DECIMAL	2	SCVAINSL	STORAGE LOCATION, ID IS FOLLOWED BY A 4 BYTE HEX ADDRESS
1	DECIMAL	3	SCVAINDS	DISPLACEMENT (+ OR -), ID IS FOLLOWED BY A 2 BYTE HEX DISPLACEMENT (TWOS COMPLEMENT IF NEGATIVE)
1	DECIMAL	4	SCVAININ	24 BIT ADDRESSING MODE INDIRECTION (%), CONSISTS ONLY OF ID
1	DECIMAL	4	SCVAIN24	
1	DECIMAL	5	SCVAINRG	REGISTER, ID IS FOLLOWED BY A 1 BYTE HEX REGISTER NUMBER
1	DECIMAL	6	SCVAINQL	ADDRESS QUALIFIER, ID IS FOLLOWED BY A 1 BYTE ID(0-4) AND A 2 BYTE ASID. THE CODE FOR THE QUALIFIER IS CURRENT=00 PASID=01 SASID=02 HOME=03 OTHER=04 LLOC=05. IF the id equals ScvaJobnameSpecified then a jobname follows
1	DECIMAL	7	SCVAIN31	31 BIT ADDRESSING MODE INDIRECTION (?), CONSISTS ONLY OF ID
1	DECIMAL	8	SCVAING64	64-bit GPR. ID is followed by a one-byte hex register numer
1	DECIMAL	9	SCVAIN64	64 BIT ADDRESSING MODE INDIRECTION (!), CONSISTS ONLY OF ID
1	DECIMAL	10	SCVAINSL64	64 bit address element
1	DECIMAL	11	SCVAINBEA	Breaking Event Address
1	DECIMAL	12	SCVAINBPER	Beginning PER range
1	DECIMAL	12	SCVAINMAX	Maximum value, for arrays in IEAVTADR
4	DECIMAL	1	SCVADALP	LEFT PAREN IN DATA, THIS SCVADAEX IS FOLLOWED BY ANOTHER SCVADAEX
4	DECIMAL	2	SCVADARP	RIGHT PAREN IN DATA, THIS SCVADAEX IS FOLLOWED BY ANOTHER SCVADAEX
4	DECIMAL	3	SCVADAOR	OR IN DATA, THIS SCVADAEX IS FOLLOWED BY ANOTHER SCVADAEX
4	DECIMAL	4	SCVADAND	AND IN DATA, THIS SCVADAEX IS FOLLOWED BY ANOTHER SCVADAEX
4	DECIMAL	5	SCVADAIT	TRIPLET IN DATA, THIS SCVADAEX IS FOLLOWED BY SCVAIND AND SCVADA WHICH DEFINE THE TRIPLET
4	HEX	00020002	REMOTECOMPONENTTOKEN	
				Token used to identify SLIP to XCF
4	DECIMAL	0	SCVAREMOTEENTRYEND	
				End of list
4	DECIMAL	1	SCVAREMOTEENTRYLP	
				Left Paren
4	DECIMAL	2	SCVAREMOTEENTRYRP	
				Right Paren
4	DECIMAL	3	SCVAREMOTEENTRYKEY	

Len	Type	Value	Name	Description
				Some other keyword
4	DECIMAL	1	SCVAREMOTEKEYACTION	
4	DECIMAL	2	SCVAREMOTEKEYASIDLST	
4	DECIMAL	3	SCVAREMOTEKEYDSPNAME	
4	DECIMAL	4	SCVAREMOTEKEYJOBLIST	
4	DECIMAL	1	SCVAREMOTEKEYWORKJOB	
4	DECIMAL	5	SCVAREMOTEKEYLIST	
				Note that this must not have a value of 16, as that would match SumList
4	DECIMAL	6	SCVAREMOTEKEYSDATA	
4	DECIMAL	7	SCVAREMOTEKEYSYSLIST	
4	DECIMAL	8	SCVAREMOTEKEYASIDLSTCOPY	
4	DECIMAL	9	SCVAREMOTEKEYDSPNAMECOPY	
4	DECIMAL	10	SCVAREMOTEKEYLISTCOPY	
4	DECIMAL	11	SCVAREMOTEKEYSDATACOPY	
4	DECIMAL	12	SCVAREMOTEKEYSTRLIST	
				Strlist=
4	DECIMAL	13	SCVAREMOTEKEYSTRLISTCOPY	
				Strlist
4	DECIMAL	14	SCVAREMOTEKEYJOBLISTCOPY	
				Copy the local Joblist parm
4	DECIMAL	15	SCVAREMOTEKEYIDGROUP	
				Group id key
4	DECIMAL	16	SCVAREMOTEKEYLISTJOBNAME	
				Same as ScvaRemoteKeyList except that the addresses are jobnames qualified - this is required because downlevel systems cannot handle jobname qualification
4	DECIMAL	17	SCVAREMOTEKEYINCIDENTTOKEN	
				Dump incident token key
4	DECIMAL	18	SCVAREMOTEKEYDSPNAMEJOBNAME	
				Same as ScvaRemoteKeyDspname except that the addresses are jobname qualified - this is required because downlevel systems cannot handle jobname qualification
4	DECIMAL	18	SCVAMAXREMOTEKEY	
4	DECIMAL	254	SCVAREMOTEKEYCOND	
4	DECIMAL	255	SCVAREMOTEKEYUNCOND	
4	DECIMAL	1	SCVASYSLSYSTEMISADDR	
				When ScvaSysIIsAddr equals this value, then the system name is stored at a location whose address is to be resolved at match time
4	DECIMAL	32	SCVAMAXSYS	Maximum number of systems in a sysplex
4	DECIMAL	16	SCVAMAXDSSA	Maximum number of entries in a DSSA list
1	DECIMAL	1	SCVAIDL	LAST ENTRY ID
1	DECIMAL	2	SCVAIDCC	COMP ENTRY ID
1	DECIMAL	3	SCVAIDAS	ASID ENTRY ID
1	DECIMAL	4	SCVAIDJN	JOBNAME ID
1	DECIMAL	5	SCVAIDJS	JSPGM ID
1	DECIMAL	6	SCVAIDPV	PVTMOD ID
1	DECIMAL	7	SCVAIDLP	LPAMOD ID
1	DECIMAL	8	SCVAIDAD	ADDRESS ID
1	DECIMAL	9	SCVAIDMD	MODE ID
1	DECIMAL	10	SCVAIDER	ERRTYP ID
1	DECIMAL	11	SCVAIDML	MATCHLIM ID
1	DECIMAL	12	SCVAIDPL	PRCNTLIM ID
1	DECIMAL	13	SCVAIDRA	RANGE ID
1	DECIMAL	14	SCVAIDDA	DATA ID
1	DECIMAL	15	SCVAIDSD	SDATA ID
1	DECIMAL	16	SCVAIDSL	SUMLIST ID
1	DECIMAL	17	SCVAIDLS	LIST ID
1	DECIMAL	18	SCVAIDAL	ASIDLST ID
1	DECIMAL	19	SCVAIDTD	TRDATA ID
1	DECIMAL	20	SCVAIDSA	ASIDSA ID
1	DECIMAL	21	SCVAIDDP	DUMPID ID
1	DECIMAL	22	SCVAIDRC	REASON CODE ID
1	DECIMAL	23	SCVAIDNU	NUCMOD ID
1	DECIMAL	24	SCVAIDPA	PSWASC ID
1	DECIMAL	25	SCVAIDDN	DSPNAME ID
1	DECIMAL	26	SCVAIDDS	DSSA ID
1	DECIMAL	27	SCVAIDREMOTE	Remote ID
1	DECIMAL	28	SCVAIDST	Strlist ID
1	DECIMAL	29	SCVAIDJL	Joblist ID
1	DECIMAL	30	SCVAIDREFBEFOR	
				Refresh before id
1	DECIMAL	31	SCVAIDREFAFTER	
				Refresh after id
1	DECIMAL	32	SCVAIDMSG	MSGID ID

SCVA Cross Reference

Len	Type	Value	Name	Description
1	DECIMAL	33	SCVAIDGTFID	GTFID ID
1	DECIMAL	34	SCVAIDSTD	STDATA ID
1	DECIMAL	35	SCVAIDAEXIT	Aexit index
1	DECIMAL	36	SCVAIDTEXIT	Texit index
1	DECIMAL	37	SCVAIDAPARM1	AParm1 index
1	DECIMAL	37	SCVAIDMX	MAXIMUM ID
6	CHARACTER	*RC=4*	SCVAXXM4	ADDRESS NOT RESOLVED
7	CHARACTER	*A1>A2*	SCVAXXAE	FIRST ADDRESS IS GREATER THAN THE SECOND ADDRESS OF A PAIR
7	CHARACTER	*RB UN*	SCVAXXM1	RB LEVEL SPECIFIED IS UNAVAILABLE
9	CHARACTER	*ASID UN*	SCVAXXM2	ASID IS UNAVAILABLE
4	DECIMAL	10	SCVAMSGIDMAXLENGTH	Maximum allowable msgid length
4	DECIMAL	8	SCVAUSEREXITMAXLENGTH	Maximum length allowed for exit name
4	DECIMAL	0	STRLISTSUBPOOL	Subpool used to create the SprsStrl
4	DECIMAL	1024	STRLISTBUFADJ	Amount of storage to increase the new input buffer (SprsBufr) by

SCVA Cross Reference

Name	Hex Offset	Hex Value
SCVA	0	
SCVAACTHD	0	
SCVAACTID	2	
SCVAACTION	0	
SCVAACTIONS	4	
SCVAACTION1	4	
SCVAACTION2	5	
SCVAACTLN	0	
SCVAACTSVCD	4	80
SCVAACTWAIT	4	40
SCVAADD	4	
SCVAADDR	0	
SCVAADD1	4	
SCVAADD1HIGH	4	
SCVAADD1LOW	8	
SCVAADD2	C	
SCVAADD2HIGH	C	
SCVAADD2LOW	10	
SCVAADID	2	
SCVAADLN	0	
SCVAAEXIT	0	
SCVAAFLG	3	
SCVAAPARM1	0	
SCVAAPARM1AD	8	
SCVAAPARM1FLGS	3	
SCVAAPARM1HDR	0	
SCVAAPARM1ID	2	
SCVAAPARM1LN	0	
SCVAAPARM1NO	4	
SCVAAPARM1SY	3	80
SCVAAS	0	
SCVAASD	4	
SCVAASHD	0	
SCVAASID	2	
SCVAASLN	0	
SCVAASNO	3	
SCVACBID	0	
SCVACCB	3	
SCVACCD	7	
SCVACCID	2	
SCVACCLN	0	
SCVACCM	4	
SCVACCU	3	80
SCVACOID	2	
SCVACOLN	0	
SCVACOM	0	
SCVACOMP	0	
SCVADA	0	
SCVADAAD	0	02
SCVADAB	0	08
SCVADACC	0	04
SCVADACS	4	
SCVADACS1	8	
SCVADAD	C	
SCVADAEI	0	
SCVADAEQ	0	40
SCVADAERROR	A	
SCVADAERRORASIDINACTIVE	A	40
SCVADAERRORBADTRIPLETNUMBER	8	
SCVADAERRORDATAINACCESSIBLE	A	20
SCVADAERRORDATAWRITEPROTECTED	A	10
SCVADAERRORFIRSTOPERAND	A	80
SCVADAEX	0	
SCVADAFL	3	
SCVADAF2	1	
SCVADAGT	0	20
SCVADAG64	0	01
SCVADAHD	0	
SCVADAID	2	
SCVADAIM	1	80
SCVADALN	0	
SCVADALT	0	10
SCVADAN	0	80
SCVADANO	4	
SCVADAOP	0	
SCVADASM	1	40
SCVADASY	3	80
SCVADATA	0	
SCVADATX	B	
SCVADAUN	6	
SCVADAV	E	
SCVADAVL	1	
SCVADAVM	6	
SCVADAVMZEROES	2	
SCVADAVM8	2	
SCVADAVZEROES	A	
SCVADAV8	A	
SCVADITOKENPTR	274	
SCVADJOBLISTPTR	278	
SCVADMP	0	
SCVADMTL	8	

Name	Hex Offset	Hex Value
SCVADNA	C	
SCVADNAS	1	
SCVADNCS	0	
SCVADNFL	9	
SCVADNHD	0	
SCVADNID	2	
SCVADNJOBNAME		
	1	
SCVADNL	0	
SCVADNLN	0	
SCVADNNM	9	
SCVADNNO	8	
SCVADNP1	4	
SCVADNSA	0	
SCVADNSY	9	80
SCVADNT1	0	
SCVADPAR	6C	
SCVADPC64S	12C	
SCVADPD	16	
SCVADPESAME	EC	
SCVADPGR	2C	
SCVADPG6	EC	
SCVADPHR	0	
SCVADPID	2	
SCVADPLISTDA	27C	
SCVADPLISTDHDR		
	27C	
SCVADPLK	3	
SCVADPLN	0	
SCVADPLS	1C	
SCVADPSDPL	1AC	
SCVADPSL	1C	
SCVADPSR	20	
SCVADPSW	1C	
SCVADPSW16	BC	
SCVADPTEXTPTR		
	4	
SCVADPTH	8	
SCVADPVA	9	
SCVADPXM	AC	
SCVADSA	9	
SCVADSAN	9	
SCVADSD	8	
SCVADSFL	4	
SCVADSHD	0	
SCVADSID	2	
SCVADSJOBNAME		
	9	
SCVADSLN	0	
SCVADSN	11	
SCVADSNO	3	
SCVADSPLIST	0	
SCVADSPLISTANENTRY		
	0	
SCVADSPLISTASID		
	6	
SCVADSPLISTENTRY		
	4	
SCVADSPLISTHEADER		
	0	
SCVADSPLISTJOBNAME		
	0	
SCVADSPLISTNAME		
	8	
SCVADSS	8	
SCVADSSA	0	
SCVADSSY	4	80
SCVAEABN	3	10
SCVAEDAT	3	02
SCVAEMCH	3	80
SCVAEMEM	3	08
SCVAEPIO	3	01
SCVAEPRG	3	40
SCVAERFL	3	

Name	Hex Offset	Hex Value
SCVAERFL1	4	
SCVAERID	2	
SCVAERLN	0	
SCVAERRT	0	
SCVAERST	3	20
SCVAESVC	3	04
SCVAETXPROG	4	80
SCVAEXITADDR	C	
SCVAEXITADDR	C	
SCVAEXITHD	0	
SCVAEXITHD	0	
SCVAEXITID	2	
SCVAEXITID	2	
SCVAEXITLN	0	
SCVAEXITLN	0	
SCVAEXITNAME	4	
SCVAEXITNAME	4	
SCVAGTFID	0	
SCVAGTFIDDATA		
	4	
SCVAGTFIDENTRY		
	8	
SCVAGTFIDHD	0	
SCVAGTFIDID	2	
SCVAGTFIDLN	0	
SCVAGTFIDNO	4	
SCVAHDR	0	
SCVAINAD	1	
SCVAIND	0	
SCVAINDL	0	
SCVAINID	0	
SCVAJL	0	
SCVAJLENTRY	8	
SCVAJLHD	0	
SCVAJLID	2	
SCVAJLJOB	8	
SCVAJLLN	0	
SCVAJLPLIST	4	
SCVAJLPLLEN	4	
SCVAJND	4	
SCVAJNID	2	
SCVAJNLN	0	
SCVAJOBN	0	
SCVALDCNT	8	
SCVALDENDHIGH		
	14	
SCVALDENDLOW	18	
SCVALDEND64	14	
SCVALDSTKN	0	
SCVALDSTRTHIGH		
	C	
SCVALDSTRTLOW		
	10	
SCVALDSTRT64	C	
SCVALD64	0	
SCVALD64ADDRESSPAIRS		
	C	
SCVALD64ENTRYHEADER		
	0	
SCVALD64HEADER		
	0	
SCVALD64HEADERTOTALLENGTH		
	0	
SCVALIST	0	
SCVALISTADS	0	
SCVALISTAD1HIGH		
	0	
SCVALISTAD1LOW		
	4	
SCVALISTAD164		
	0	
SCVALISTAD2HIGH		
	8	
SCVALISTAD2LOW		

Name	Hex Offset	Hex Value
	C	
SCVALISTAD264		
	8	
SCVALISTDEND	1C	
SCVALN	4	
SCVALSAE	D	
SCVALSCS	0	
SCVALSD	30	
SCVALSDWAP	28	
SCVALSESAMEREQUIRED		
	6	40
SCVALSFL	6	
SCVALSHD	0	
SCVALSID	2	
SCVALSLN	0	
SCVALSM1	14	
SCVALSM2	1B	
SCVALSM4	7	
SCVALSNO	4	
SCVALSSYMA	24	
SCVALSYM	6	80
SCVALT	2C	
SCVALTHD	2C	
SCVAMD_DYNLPA		
	1C	
SCVAMD_POSIX	1C	
SCVAMDA1	8	
SCVAMDA1_8	4	
SCVAMDA1_8_HIGH		
	4	
SCVAMDA1A	1C	
SCVAMDA2	10	
SCVAMDA2_8	C	
SCVAMDA2_8_HIGH		
	C	
SCVAMDA2A	20	
SCVAMDEP	3	80
SCVAMDFIXED	0	
SCVAMDFL	3	
SCVAMDID	2	
SCVAMDIS	4	40
SCVAMDLN	0	
SCVAMDNM	24	
SCVAMDO1	14	
SCVAMDO2	18	
SCVAMDPENDING		
	3	20
SCVAMDPENDING		
	3	20
SCVAMDPOSIXPATHNAME		
	24	
SCVAMDPOSIXPATHNAMELENGTH		
	1C	
SCVAMDPOSIXPATHNAMEUSED		
	3	40
SCVAMDVARIABLE		
	24	
SCVAMGL	5	04
SCVAMGSD	4	10
SCVAMGSP	4	20
SCVAMHME	5	01
SCVAMLID	2	
SCVAMLIM	0	
SCVAMLL	4	08
SCVAMLLN	0	
SCVAMLNO	4	
SCVAMLOK	5	02
SCVAMLSP	8	
SCVAMOC	3	
SCVAMOD	0	
SCVAMODE	0	
SCVAMOE	3	80
SCVAMOFL	4	
SCVAMOID	2	

Name	Hex Offset	Hex Value
SCVAMOLN	0	
SCVAMO1	4	
SCVAMO2	5	
SCVAMPK	5	08
SCVAMPP	5	40
SCVAMREC	5	80
SCVAMSGID	0	
SCVAMSGIDFLGS		
	3	
SCVAMSGIDHD	0	
SCVAMSGIDID	2	
SCVAMSGIDLN	0	
SCVAMSGIDQUOTED		
	3	80
SCVAMSGIDTEXT		
	6	
SCVAMSGIDTEXTLENGTH		
	4	
SCVAMSK	5	10
SCVAMSRB	4	02
SCVAMSS	5	20
SCVAMSUP	4	80
SCVAMTCB	4	01
SCVAMT1S	4	04
SCVAPAAR	3	40
SCVAPAFL	3	
SCVAPAH	3	10
SCVAPAID	2	
SCVAPALN	0	
SCVAPAP	3	80
SCVAPAS	3	20
SCVAPASC	0	
SCVAPLAC	10	
SCVAPLAL	10	
SCVAPLAR	14	
SCVAPLID	2	
SCVAPLIM	0	
SCVAPLLN	0	
SCVAPLSC	4	
SCVAPLSP	3	
SCVAPLST	8	
SCVAPLTL	8	
SCVAPLTR	C	
SCVAP1	4	
SCVAP2	8	
SCVARANGE	0	
SCVARANGEAD	18	
SCVARANGEEVALUATED		
	8	
SCVARANGEFL	3	
SCVARANGEHEADER		
	0	
SCVARANGEID	2	
SCVARANGELN	0	
SCVARANGENO	4	
SCVARANGESAUSED		
	3	40
SCVARANGESY	3	80
SCVARANGE1	8	
SCVARANGE1HIGH		
	8	
SCVARANGE1LOW		
	C	
SCVARANGE2	10	
SCVARANGE2HIGH		
	10	
SCVARANGE2LOW		
	14	
SCVARCB	3	
SCVARCID	2	
SCVARCLN	0	
SCVARCM	4	
SCVARCSP	8	
SCVAREAS	0	

Name	Hex Offset	Hex Value
SCVAREMIDGROUP	0	
SCVAREMIDGROUPHD	0	
SCVAREMIDGROUPID	2	
SCVAREMIDGROUPLEN	0	
SCVAREMIDGROUPVAL	4	
SCVAREMOTE	0	
SCVAREMOTE#	4	
SCVAREMOTECOND	6	80
SCVAREMOTEENTRY	0	
SCVAREMOTEENTRYID	0	
SCVAREMOTEENTRYKEYVAL	1	
SCVAREMOTEHD	0	
SCVAREMOTEID	2	
SCVAREMOTEINFO	8	
SCVAREMOTELN	0	
SCVAREMSDUMPTOKEN	0	
SCVAREMSDUMPTOKENHD	0	
SCVAREMSDUMPTOKENID	2	
SCVAREMSDUMPTOKENLEN	0	
SCVAREMSDUMPTOKENVAL	4	
SCVAREMSPRSSTRL	8	
SCVAREMSPRSSTRLLEN	4	
SCVAREMSTRLID	2	
SCVAREMSTRLIST	0	
SCVAREMSTRLISTHD	0	
SCVAREMSTRLLN	0	
SCVAS	0	
SCVASA	0	
SCVASAD	8	
SCVASAFL	4	
SCVASAHD	0	
SCVASAID	2	
SCVASAJOBNAME	9	
SCVASALN	0	
SCVASANO	3	
SCVASANU	5	02
SCVASAPS	4	80
SCVASASID	1	
SCVASASY	4	80
SCVASCPL	6	10
SCVASCS	0	
SCVASCSA	5	80
SCVASD	E	
SCVASDAT	0	
SCVASDE1	6	
SCVASDFL	4	
SCVASDID	2	
SCVASDLN	0	
SCVASD1	4	
SCVASD2	5	
SCVASFL	D	
SCVASGRS	6	80

Name	Hex Offset	Hex Value
SCVASHD	0	
SCVASID	2	
SCVASLALET	4	
SCVASLCNT	8	
SCVASLEND	20	
SCVASLENDHIGH	18	
SCVASLENDLOW	1C	
SCVASLEND64	18	
SCVASLN	0	
SCVASLPA	4	02
SCVASLQUAL	C	
SCVASLQUALIFIERISANALET	C	40
SCVASLQUALIFIERISASTOKEN	C	80
SCVASLSQ	4	08
SCVASLSTKN	0	
SCVASLSTRTHIGH	10	
SCVASLSTRTLOW	14	
SCVASLSTRT64	10	
SCVASL64	0	
SCVASL64ADDRESSPAIRS	10	
SCVASL64ENTRYHEADER	0	
SCVASL64HEADER	0	
SCVASL64HEADERRESTOFDATA	8	
SCVASL64HEADERTOTALLENGTH	0	
SCVASNA	9	
SCVASNAP	5	08
SCVASNO	C	
SCVASNSD	5	10
SCVASNSQ	5	04
SCVASNUC	4	20
SCVASPRSSTRL	8	
SCVASPRSSTRLLEN	4	
SCVASPSA	4	40
SCVASRGN	4	04
SCVASSA	8	
SCVASSDP	5	20
SCVASSQA	4	10
SCVASSWA	5	40
SCVASSYMB	0	
SCVAST	0	
SCVASTDATA	0	
SCVASTDATAAD	8	
SCVASTDATAFL	3	
SCVASTDATAHEADER	0	
SCVASTDATAID	2	
SCVASTDATALN	0	
SCVASTDATANO	4	
SCVASTDATASAUSED	3	40
SCVASTDATASY	3	80
SCVASTRLIST	0	
SCVASTRLISTHD	0	
SCVASTRLISTID	2	
SCVASTRLISTLN	0	
SCVASTRT	4	01
SCVASWLM	6	02
SCVASXES	6	08
SCVASYM	D	80
SCVASYSL#	3	

SCVA Cross Reference

Name	Hex Offset	Hex Value
SCVASYSLADDR	7	
SCVASYSLADDRDATA	6	
SCVASYSLADDRHD	0	
SCVASYSLADDRLEN	4	
SCVASYSLADDRS	304	
SCVASYSLADDRSTR	10	
SCVASYSLGRPNAME	4	
SCVASYSLHD	0	
SCVASYSLID	2	
SCVASYSLISADDR	C	
SCVASYSLIST	0	
SCVASYSLISTADDRESS	0	
SCVASYSLLN	0	
SCVASYSLMEMNAME	C	
SCVASYSLSYS	4	
SCVASYSLSYSNAME	4	
SCVASYSLSYSTEMS	4	
SCVATDAD	3	20
SCVATDB	3	
SCVATDD	8	
SCVATDHD	0	
SCVATDID	2	
SCVATDLN	0	
SCVATDNO	4	
SCVATDRG	3	40
SCVATDST	3	80
SCVATDSY	3	10
SCVATEXIT	0	
SCVATRD	0	
SCVAVA	8	
SCVAWORK	0	
SCVAWORK#	3	
SCVAWORKENTRIES	4	
SCVAWORKHD	0	
SCVAWORKID	2	
SCVAWORKJ	8	
SCVAWORKJFIXED	4	
SCVAWORKJKEY	4	
SCVAWORKJLN	6	
SCVAWORKJOB	8	
SCVAWORKLN	0	

SCVT Information

SCVT Programming Interface information

Programming Interface information

SCVT

ONLY the following fields are part of the programming interface information:

- SCVTCCVT
- SCVTMCA

End of Programming Interface information

SCVT Heading Information

Common Name: Secondary Communication Vector Table
Macro ID: IHASCVT
DSECT Name: SCVTSECT
Owning Component: Common Macros (SC101)
Eye-Catcher ID: None
Storage Attributes: Key: key 0
Residency: Nucleus
Size: 224 bytes
Created by: IEASCVT
Pointed to by: CVTABEND field of the CVT data area
Serialization: None required
Function: Used by non-nucleus-resident routines to refer to routines used by the Supervisor, by ABEND, and other program components.

SCVT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SCVTSECT	
0	(0)	ADDRESS	4	SCVTPGTM	"V(IEAQPGTM)" - ADDR OF EOT TIMER PURGE ROUTINE.
4	(4)	ADDRESS	4	SCVTPGWR	- ADDRESS OF WTO/WTOR RESOURCE MANAGER. INITIALLY CONTAINS ADDRESS OF BR 14. CHANGED TO IEECVPRG (MODULE IEAVMED2) BY COMMUNICATIONS TASK INITIALIZATION (IEAVVINT). MDC013
8	(8)	ADDRESS	4	SCVTDSPA	- ANCHOR FOR SYSTEM/370 SNA/DS DISTRIBUTION SERVICES PRODUCT.
12	(C)	SIGNED	2	SCVTBR14	- A BR 14 INSTRUCTION. RETURN TO CALLER
14	(E)	SIGNED	2	SCVTVLFC	- COUNT OF THE NUMBER OF TIMES VLF IS STARTED OWNERSHIP: VLF. SERIALIZATION: NONE
16	(10)	ADDRESS	4		- ** SCVTTERAS FIELD UNUSED IN OS/VS2 RELEASE 2 ** @(DCR560)
20	(14)	ADDRESS	4	SCVTBWTO	"V(IEABRWTO)" - ADDRESS OF BRANCH-ENTRY WTO AND WTOR. MUST BE ADDRESSABLE IN 24-BIT MODE.
24	(18)	ADDRESS	4	SCVTBDOM	"V(IEABRDOM)" - ADDRESS OF BRANCH-ENTRY DOM. MUST BE ADDRESSABLE IN 24-BIT MODE.
28	(1C)	ADDRESS	4	SCVTRMBR	"V(RMBRANCH)" - ADDR OF REGMAIN BRANCH ENTRY
32	(20)	ADDRESS	4	SCVTCHSI	- POINTER TO THE IOS CHANNEL SUBSYSTEM INFORMATION BLOCK
36	(24)	ADDRESS	4	SCVTRACE	- ADDR OF POINTER TO TRACE ROUTINE
40	(28)	ADDRESS	4	SCVTHSCS	- ADDRESS OF SLIH FOR HPPI EXTERNAL INTERRUPTS. OWNERSHIP: HPPI SOFTWARE.
44	(2C)	ADDRESS	4	SCVTSORM	- ADDRESS OF SDOM RESOURCE MANAGER (COFMSORM). OWNERSHIP: VLF. SERIALIZATION: NONE.
48	(30)	ADDRESS	4	SCVTLFRM	"V(FMBRANCH)" - LIST FORMAT FREEMAIN BRANCH ENT PT
52	(34)	ADDRESS	4	SCVTSDOB	- ADDRESS OF SHARED DATA OBJECT MANAGER BLOCK. OWNERSHIP: VLF. SERIALIZATION: CS.
56	(38)	ADDRESS	4	SCVTMCA	- ANCHOR FOR GLOBAL MVS MESSAGE SERVICE DATA AREA. OWNERSHIP: MVS MESSAGE SERVICE. SERIALIZATION: LOCAL AND CMS LOCKS.
60	(3C)	ADDRESS	4	SCVTNETV	- NETVIEW CONTROL STRUCTURE ANCHOR. OWNERSHIP: NETVIEW. SERIALIZATION: NONE.
64	(40)	ADDRESS	4	SCVTRPTR	- ADDR OF TRACE TABLE POINTERS YM2703
68	(44)	ADDRESS	4	SCVTGMBR	"V(GMBRANCH)" - LIST FORMAT GETMAIN BRANCH ENTRY POINT ICB445
72	(48)	ADDRESS	4	SCVTRG2	- ENTRY POINT ADDRESS OF IEAVTRG2, RTM2 24 BIT ADDRESSING MODE INTERFACE ROUTINE. @(DCR854)
76	(4C)	ADDRESS	4	SCVTRG2B	- ADDRESS OF SECOND ENTRY POINT IN IEAVTRG2, RTM2 24 BIT ADDRESSING MODE INTERFACE ROUTINE. @(DCR854)
80	(50)	ADDRESS	4	SCVTRTXT	"V(IEECB867)" ADDRESS OF MODULE IEECB867. MODULE PROVIDES CALLER WITH THE MESSAGE TEXT ASSOCIATED WITH THE RETURN CODE PROVIDED BY SVC DUMP WHEN IT FAILED TO TAKE A DUMP. OWNERSHIP: SVC DUMP.
84	(54)	SIGNED	4	SCVTAMSP	- ADDRESS OF SYSTEM AVAILABILITY MANAGER(SAM) WHEN SAM IS ACTIVE OWNERSHIP: SAM SERIALIZATION: NONE
88	(58)	ADDRESS	4	SCVTASRS	"V(ASRGLTAB)" POINTER TO SYMPTOM RECORDS GLOBAL TABLE
92	(5C)	ADDRESS	4	SCVTSYMR	"V(ASRSERVM)" ADDRESS OF THE SYMREC RESOURCE MANAGER
96	(60)	ADDRESS	4	SCVTDOPX	"V(IEAVTRDX)" ADDRESS OF IEAVTRDX ROUTINE INVOKED BY SETRP WHEN DUMPOPX IS SPECIFIED OWNERSHIP: RTM
100	(64)	ADDRESS	4	SCVTCPLS	"V(IGVCPLST)" ADDRESS OF VSM CELL POOL LIST SERVICE. OWNERSHIP: VSM
104	(68)	ADDRESS	4	SCVTDIV	"V(ITVDIB)" POINTER TO THE DIV INFORMATION BLOCK
108	(6C)	ADDRESS	4	SCVTDIVM	"V(ITVGTERM)" POINTER TO THE DIV RESOURCE MANAGER
112	(70)	ADDRESS	4	SCVTCTR1	- RESERVED
116	(74)	ADDRESS	4	SCVTCTAB	- COMPONENT TRACE ANCHOR BLOCK. OWNERSHIP: COMPONENT TRACE. SERIALIZATION: ENQ/DEQ.
120	(78)	ADDRESS	4	SCVTRXLQ	- ADDR OF RECOVERY EXTENT LIST
124	(7C)	ADDRESS	4	SCVTCGT	- POINTER TO IHACGT SERIALIZATION: CS.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
128	(80)	ADDRESS	4	SCVTJOPN	"V(IDAVJOPN)" ADDR OF THE IDAVJOPN RTN SERIALIZATION: N/A
132	(84)	ADDRESS	4	SCVTSVCT	"V(SVCTABLE)" - ORIGIN OF SVC TABLE MDC005
136	(88)	ADDRESS	4	SCVTSVCR	"V(IEAVSVCR)" - ADDRESS OF SVC UPDATE RECORDING TABLE
140	(8C)	ADDRESS	4	SCVTTQE	"V(IEATSELM)" - ADDR OF TSO SUBSYSTEM'S TQE
144	(90)	ADDRESS	4	SCVTTTRM	- TSO TASK TERMINATION RESOUCE MANAGER
148	(94)	ADDRESS	4	SCVTSTAT	"V(IGC07902)" - ADDR OF SVC STATUS ROUTINE ICB339
152	(98)	ADDRESS	4	SCVTQCBR	"V(QCBRANCH)" - BRANCH ENTRY POINT TO GETMAIN/ FREEMAIN QUICKCELL ROUTINE ICB339
156	(9C)	ADDRESS	4	SCVTVLFB	- VLF COMMON DATA BLOCK ADDRESS. OWNERSHIP: VLF. SERIALIZATION: CS.
160	(A0)	ADDRESS	4	SCVTHSCT	- ADDRESS OF HPPI SOFTWARE DATA AREA. OWNERSHIP: HPPI SOFTWARE.
164	(A4)	ADDRESS	4	SCVTPTRM	"V(IARGTERM)" - ADDRESS OF PAGING SUPERVISOR ROUTINE (IARGTERM) TO QUIESCE ASYNCHRONOUS OR SYNCHRONOUS PAGING-IN, FIX, OR MIGRATION REQUESTS.
168	(A8)	ADDRESS	4	SCVTDFCS	- ANCHOR FOR SMSX GLOBAL CONTROL STRUCTURE. OWNERSHIP: DFP SERIALIZATION: NONE
172	(AC)	ADDRESS	4	SCVTPIQE	"V(IEADQIQE)" - ADDR OF RESIDENT SUBROUTINE IN EOT TO REMOVE IQE'S FROM ASYNCHRONOUS EXIT QUEUE ICB378
176	(B0)	ADDRESS	4	SCVTGAMP	- POINTER TO GAM CONTROL BLOCKS. OWNERSHIP: GRAPHICS ACCESS METHOD (GAM). SERIALIZATION: COMPARE AND SWAP.
180	(B4)	ADDRESS	4	SCVTGAMS	- POINTER TO GAM SQA AREA. OWNERSHIP: GRAPHICS ACCESS METHOD (GAM). SERIALIZATION: COMPARE AND SWAP.
184	(B8)	ADDRESS	4	SCVTCCVT	- POINTER TO THE CRYPTOGRAPHY CVT. OWNERSHIP: INTEGRATED CRYPTOGRAPHIC SERVICE FACILITY/MVS (ICSF/MVS) SERIALIZATION: NONE.
188	(BC)	ADDRESS	4	SCVTDIS	"V(IEAVTDIS)" ENTRY POINT OF MEMTERM DISABLE/ENABLE SERVICE. OWNERSHIP: RTM. SERIALIZATION: N/A
192	(C0)	BITSTRING	8	SCVTR0C0	RESERVED.
200	(C8)	ADDRESS	4	SCVTQOPN	"V(IGGQJOPN)" ADDR OF THE IGGQJOPN RTN SERIALIZATION: N/A
204	(CC)	ADDRESS	4	SCVTDLFB	- ANCHOR TO THE DLF CONTROL BLOCK OWNERSHIP: DLF. SERIALIZATION: CS.
208	(D0)	BITSTRING	16	SCVTR0D0	RESERVED.

SCVT Cross Reference

Name	Hex Offset	Hex Value
SCVTAMSP	54	0
SCVTASRS	58	
SCVTBDOM	18	
SCVTBR14	C	
SCVTBWTO	14	
SCVTCCVT	B8	
SCVTCGT	7C	
SCVTCHSI	20	
SCVTCPLS	64	
SCVTCTAB	74	
SCVTCTR1	70	
SCVTDFCS	A8	
SCVTDIS	BC	
SCVTDIV	68	
SCVTDIVM	6C	
SCVTDLFB	CC	
SCVTDOPX	60	
SCVTDSPA	8	
SCVTGAMP	B0	
SCVTGAMS	B4	
SCVTGMBR	44	
SCVTHSCS	28	
SCVTHSCT	A0	
SCVTJOPN	80	
SCVTLFRM	30	
SCVTMCA	38	
SCVTNETV	3C	
SCVTPGTM	0	
SCVTPGWR	4	
SCVTPIQE	AC	
SCVTPTRM	A4	
SCVTQCBR	98	
SCVTQOPN	C8	
SCVTRACE	24	
SCVTRG2	48	

Name	Hex Offset	Hex Value
SCVTRG2B	4C	
SCVTRMBR	1C	
SCVTRPTR	40	
SCVTRTXT	50	
SCVTRXLQ	78	
SCVTR0C0	C0	0
SCVTR0D0	D0	0
SCVTSDOB	34	
SCVTSECT	0	
SCVTSORM	2C	
SCVTSTAT	94	
SCVTSVCR	88	
SCVTSVCT	84	
SCVTSYMR	5C	
SCVTTQE	8C	
SCVTTTRM	90	
SCVTVLFB	9C	
SCVTVLFC	E	0

SCWA Information

SCWA Heading Information

Common Name: Supervisor Control Work Area
Macro ID: IHASCWA
DSECT Name: SCWA, SCWA1, SCWA2, SCWA3
Owning Component: Supervisor Control (SC1C5)
Eye-Catcher ID: SCWA
Offset: 0
Length: 4
Storage Attributes: Subpool: 239
Key: 0
Residency: Above 16M
Size: 12288 bytes
Created by: IEAVNIP0, IEEVCPRA
Pointed to by: PSASCWA field of the PSA data area
WSACSCWA field of the WSAVT data area
Serialization: Disablement
Function: Generates mapping macro for the Supervisor Control Work Save Area. There is one SCWA per Processor. The programs which have save areas must serialize the usage of their respective areas. The save areas reside in fetch protected 31-bit storage.

SCWA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4064	SCWA	SUPERVISOR CONTROL WORK AREA
0	(0)	CHARACTER	4	SCWASCWA	SCWA ACRONYM
4	(4)	BITSTRING	4	SCWFLAG	
4	(4)	BITSTRING	1	SCWFLAG1	FLAGS. SERIALIZED BY DISABLEMENT.
		1...		SCWSARP	IF THIS FLAG IS ON AT ENTRY TO IEAVESAR, IEAVESAR HAS BEEN RECURSIVELY ENTERED.
		.1..		SCWELKRF	IF THIS FLAG IS ON, THEN IEAVELKR WAS PROCESSING DUE TO BEING CALLED DIRECTLY BY RTM. IEAVESAR USES THIS FLAG TO PREVENT RECURSIVE ENTRY INTO IEAVELKR.
		..1.		SCWLCRVT	IF THIS FLAG IS ON, THEN IEAVELCR WAS UNABLE TO REFRESH THE PSAATCVT POINTER TO VTAM'S CVT. IEAVELCR USES THIS FLAG TO PREVENT FILLING LOGREC WITH REDUNDANT SOFTWARE ERROR RECORDS.
		...1		SCWEGR	IF THIS FLAG IS ON, THEN IEAVEGR PROCESSING IS ACTIVE. IEAVESAR USES THIS FLAG TO PREVENT RECURSIVE ENTRY INTO IEAVEGR.
		 1111		*	RESERVED.
5	(5)	BITSTRING	3	*	RESERVED.
8	(8)	CHARACTER	72	SCWEPC1	PROGRAM FLIH SAVE AREA 1
80	(50)	CHARACTER	72	SCWELK1	SPIN LOCK MGR SAVE AREA 1
152	(98)	CHARACTER	72	SCWELK2	SPIN LOCK MGR SAVE AREA 2
224	(E0)	CHARACTER	64	SCWELK3	SPIN LOCK MGR SAVE AREA 3
288	(120)	CHARACTER	72	SCWEPC3	PROGRAM FLIH SAVE AREA 3
360	(168)	CHARACTER	8	*	Reserved
368	(170)	CHARACTER	64	SCWFRLK1	FREE LOCK REGISTERS
432	(1B0)	CHARACTER	72	SCWESAR	SUPERVISOR ANALYSIS ROUTER SAVE AREA.
504	(1F8)	CHARACTER	72	SCWESAR2	SECOND LEVEL SAVE AREA TO BE PASSED FROM THE ROUTINES CALLED BY IEAVESAR TO THE ROUTINES THEY CALL.
576	(240)	CHARACTER	16	SCWUSRR	WORK AREA FOR USERRDY.
592	(250)	CHARACTER	40	SCWVQVPL	QVPL USED BY IEAVEVRR.
632	(278)	CHARACTER	72	SCWEPC2	PROGRAM FLIH SAVE AREA 2
704	(2C0)	CHARACTER	128	SCWSIGP	IPC/IEAVESGP SAVE AREA
832	(340)	CHARACTER	96	SCWEDR	IPC/IEAVEDR SAVE AREA
928	(3A0)	CHARACTER	64	SCWELK4	WORK AREA FOR LOCK MANAGER (IEAVELK) - INCLUDES AREA FOR EXCESSIVE SPIN ROUTINE PARAMETER LIST
992	(3E0)	CHARACTER	56	SCWEINT	WORK AREA FOR INTERSECT SPIN ROUTINE (IEAVEINT) - INCLUDES AREA FOR EXCESSIVE SPIN ROUTINE PARAMETER LIST
1048	(418)	CHARACTER	160	*	Reserved
1208	(4B8)	CHARACTER	104	SCWERP	IPC/IEAVERP1 SAVE AREA
1312	(520)	CHARACTER	112	SCWEXS	IPC/IEAVEXS SAVE AREA
1424	(590)	CHARACTER	64	SCWSARWA	TEMPORARY WORKAREA FOR IEAVESAR AND THE ROUTINES WHICH RUN UNDER ITS CONTROL.
1488	(5D0)	CHARACTER	8	SCWR5D8	Reserved
1496	(5D8)	ADDRESS	4	SCWA1PTR	POINTER TO SCWA1
1500	(5DC)	ADDRESS	4	SCWA2PTR	POINTER TO SCWA2

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
1504	(5E0)	ADDRESS	4	SCWA3PTR	Address of SCWA3
1508	(5E4)	CHARACTER	4	SCWR5E4	Reserved
1512	(5E8)	CHARACTER	8	SCWESRT0	Stop/Reset small area
1520	(5F0)	CHARACTER	40	SCWESVR	WORKAREA FOR SVC FLIH RECOVERY (IEAVESVR)
1560	(618)	CHARACTER	72	SCWESVR1	SAVEAREA FOR SVC FLIH RECOVERY (IEAVESVR)
1632	(660)	CHARACTER	72	SCWEGRSV	SAVEAREA FOR GLOBAL RECOVERY (IEAVEGR)
1704	(6A8)	CHARACTER	72	SCWVJST	SAVEAREA PASSED TO VECTOR JOB STEP TIMING (IEAVVJST) BY JOB STEP TIMING (IEAVEJST)
1776	(6F0)	CHARACTER	496	SCWVSLIH	SAVEAREA FOR VECTOR SLIH (IEAVEVS)
2272	(8E0)	CHARACTER	216	SCWESRT	STOP/RESET SAVE AREA
2488	(9B8)	CHARACTER	88	SCWEBBR	WORK AREA FOR BIND BREAK (IEAVEBBR) INCLUDES AREA FOR EXCESSIVE SPIN ROUTINE PARAMETER LIST
2576	(A10)	CHARACTER	200	SCWEES	IPC/IEAVEES SAVE AREA
2776	(AD8)	CHARACTER	72	SCWELSM	GPR SAVE AREA USED WHEN CALLING LINKAGE STACK SERVICES
2848	(B20)	CHARACTER	72	SCWELSL	GPR SAVE AREA USED WHEN CALLING IEAVLSLC.
2920	(B68)	CHARACTER	72	SCWXAPM	GPR SAVE AREA USED BY IEAVEDS0 AND IEAVETCL WHEN CALLING IEAVXAPM.
2992	(BB0)	CHARACTER	120	SCWESTA	IEAVESTA WORK AREA
3112	(C28)	CHARACTER	344	SCWESLK1	SUSPEND LOCK MANAGER WORK AREA
3456	(D80)	CHARACTER	512	SCWELSIH	WORK AREA FOR IEAVLSIH
3968	(F80)	CHARACTER	8	SCWSGNL	WORK AREA FOR STATUS SIGNAL ROUTINE
3976	(F88)	CHARACTER	80	SCWESPN	SAVE AREA FOR IEAVESPN. Also contains room for saving registers by invoker of SPINLOOP.
4056	(FD8)	CHARACTER	8	SCWRFD8	RESERVED
Comment					
Moved not deleted					
End of Comment					
4064	(FE0)	CHARACTER	0	SCWEND	END OF SCWA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4112	SCWA1	SCWA EXTENSION
0	(0)	CHARACTER	400	SCW1LSWA	WORK AREA FOR IEAVLSLO, IEAVLSLR, IEAVLSGO, IEAVLSGR, IEAVLSIO, IEAVLSIR.
400	(190)	CHARACTER	256	SCW1LSLC	WORK AREA FOR IEAVLSLC 1@LMD
656	(290)	CHARACTER	16	SCW1ARPA	SUPERVISOR ANALYSIS ROUTER PARAMETER AREA.
672	(2A0)	CHARACTER	200	SCW1TTKN	WORK AREA FOR IEAVTTKN 1@LMD
872	(368)	CHARACTER	64	SCW1ELK5	IEAVELK RECURSIVE SAVE AREA.
936	(3A8)	CHARACTER	160	SCW1ELKB	WORKAREA FOR IEAVELKB
1096	(448)	CHARACTER	144	SCW1ESPM	SAVEAREA FOR SRB POOL MANAGER (IEAVESPM)
1240	(4D8)	CHARACTER	384	SCW1SSWA	WORK AREA FOR IEAVSRBR AND IEAVSRBP.
1624	(658)	CHARACTER	256	SCW1ENTE	Work Area for IEAVENTE
1880	(758)	CHARACTER	512	SCW1AFS	WORK AREA FOR IEAVEAFS AND IEAVASRB.
2392	(958)	CHARACTER	40	SCW1CPUF	WORK AREA FOR IEAVCPUF.
2432	(980)	CHARACTER	72	SCW1EPC4	PROGRAM FLIH SAVE AREA 4
2504	(9C8)	CHARACTER	192	SCW1R9C8	Reserved 1@LMD
2696	(A88)	CHARACTER	640	SCW1SETS	WORK AREA FOR IEAVSETS
3336	(D08)	CHARACTER	72	SCW1SYSE	SAVE AREA FOR USERRDY SYSEVENT
3408	(D50)	CHARACTER	64	SCW1EPDR	SAVE AREA FOR USE BY IEAVEPDR WHEN CALLING IEAVEDSR (was SCW2EPDR)
3472	(D90)	CHARACTER	128	SCW1LSCL	WORK AREA FOR IEAVLSCL
3600	(E10)	CHARACTER	256	SCW1ELCR	WORK AREA FOR IEAVELCR
3856	(F10)	CHARACTER	256	SCW1ESAR	WORK AREA FOR IEAVESAR.
4112	(1010)	CHARACTER	0	SCW1END	END OF SCWA1

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	7184	SCWA2	SCWA EXTENSION.
0	(0)	CHARACTER	72	SCW2WBCH	SAVE AREA FOR IEAVWBCH
72	(48)	CHARACTER	72	SCW2SRBX	SAVE AREA FOR IEAMSRBX
144	(90)	CHARACTER	72	SCW2ESC0	SAVE AREA FOR IEAVESC0
216	(D8)	CHARACTER	256	SCW2ESPN	WORK AREA FOR IEAVESPN. Must be same size as SCW3ESPN
472	(1D8)	CHARACTER	72	SCW2ESLK	SAVE AREA FOR IEAVESLK
544	(220)	CHARACTER	256	SCW2WPM	SAVE AREA FOR IEAVWPM
800	(320)	CHARACTER	72	SCW2ESC5	SAVE AREA FOR IEAVESC5
872	(368)	CHARACTER	896	SCW2SCHW	WORK AREA FOR IEAVSCHA
1768	(6E8)	CHARACTER	784	SCW2SRBM	WORK AREA FOR IEAVSRBF AND IEAVSRBO.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
Comment					
Moved and renamed, not deleted 1@LMD					
End of Comment					
2552	(9F8)	CHARACTER	1096	SCW2EDSR	WORK AREA FOR IEAVEDSR
3648	(E40)	CHARACTER	464	SCW2CHAP	WORK AREA FOR IEAVEAC0
4112	(1010)	CHARACTER	3072	SCW2FPS	WORK AREA FOR IEAVEFPS
7184	(1C10)	CHARACTER	0	SCW2END	END OF SCWA2

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	1352	SCWA3	SCWA EXTENSION.
0	(0)	CHARACTER	72	SCW3ESRT	Work area for IEAVESRT
72	(48)	CHARACTER	256	SCW3ESPN	IEAVESPN re-entry. Must be same size as SCW2ESPN
328	(148)	CHARACTER	256	SCW3ERI	IEAVERI work area
584	(248)	CHARACTER	768	SCW3ECPX	IEAVECPX work area
1352	(548)	CHARACTER	0	SCW3END	END OF SCWA3

SCWA Cross Reference

Name	Hex Offset	Hex Value
SCWA	0	
SCWASCWA	0	
SCWA1	0	
SCWA1PTR	5D8	
SCWA2	0	
SCWA2PTR	5DC	
SCWA3	0	
SCWA3PTR	5E0	
SCWEBBR	9B8	
SCWEDR	340	
SCWEES	A10	
SCWEGR	4	10
SCWEGRSV	660	
SCWEINT	3E0	
SCWELKRF	4	40
SCWELK1	50	
SCWELK2	98	
SCWELK3	E0	
SCWELK4	3A0	
SCWELSIH	D80	
SCWELSL	B20	
SCWELSM	AD8	
SCWEND	FE0	
SCWEPC1	8	
SCWEPC2	278	
SCWEPC3	120	
SCWERP	4B8	
SCWESAR	1B0	
SCWESAR2	1F8	
SCWESLK1	C28	
SCWESPN	F88	
SCWESRT	8E0	
SCWESRT0	5E8	
SCWESTA	BB0	
SCWESVR	5F0	
SCWESVR1	618	
SCWEXS	520	
SCWFLAG	4	
SCWFLAG1	4	
SCWFRLK1	170	
SCWLCRVT	4	20
SCWRFD8	FD8	
SCWR5D8	5D0	
SCWR5E4	5E4	
SCWSARP	4	80
SCWSARWA	590	
SCWSGNL	F80	
SCWSIGP	2C0	
SCWUSRR	240	
SCWVJST	6A8	
SCWVQVPL	250	
SCWVSLIH	6F0	
SCWXAPM	B68	
SCW1AFS	758	
SCW1ARPA	290	
SCW1CPUF	958	
SCW1ELCR	E10	
SCW1ELKB	3A8	
SCW1ELK5	368	
SCW1END	1010	
SCW1ENTE	658	
SCW1EPC4	980	
SCW1EPDR	D50	
SCW1ESAR	F10	
SCW1ESPM	448	
SCW1LSCL	D90	
SCW1LSLC	190	
SCW1LSWA	0	
SCW1R9C8	9C8	
SCW1SETS	A88	
SCW1SSWA	4D8	
SCW1SYSE	D08	
SCW1TTKN	2A0	
SCW2CHAP	E40	
SCW2EDSR	9F8	
SCW2END	1C10	
SCW2ESC0	90	
SCW2ESC5	320	
SCW2ESLK	1D8	
SCW2ESPN	D8	
SCW2FPS	1010	
SCW2SCHW	368	
SCW2SRBM	6E8	
SCW2SRBX	48	
SCW2WBCH	0	
SCW2WPM	220	
SCW3ECPX	248	
SCW3END	548	
SCW3ERI	148	
SCW3ESPN	48	
SCW3ESRT	0	

SDDSQ Information

SDDSQ Heading Information

Common Name: SDUMP DUMP DATA SET QUEUE
Macro ID: IHASDDSQ
DSECT Name: SDDSQ
Owning Component: SVC Dump (SCDMP)
Eye-Catcher ID: SDDS
Offset: 0
Length: 4
Storage Attributes: Main Storage: One per dump dataset
Subpool: 245
Key: 0
Residency: Above 16M
Size: DECIMAL 96, X'60' BYTES
Created by: IEECB926
Pointed to by: RTCTSDDS
SDDSQFWD
SDDSQBWD
Serialization: THE SDDSQ IS USED BY SDUMP, THE DUMPDS OPERATOR COMMAND AND THE DISPLAY DUMP OPERATOR COMMAND. THE ENTIRE SDDSQ IS SERIALIZED BETWEEN DUMPDS AND DISPLAY DUMP BY WAY OF ENQ ON (SYSIEA01 DMPDSENQ). SERIALIZATION BETWEEN SDUMP AND DUMPDS IS DONE ON A QUEUE ELEMENT BASIS BY WAY OF COMPARE AND SWAP ON THE SDDSQCS BIT.
Function: MAPS EACH ENTRY IN THE DUMP DATA SET QUEUE.

SDDSQ Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	96	SDDSQ	SDUMP DUMP DATA SET QUEUE
0	(0)	CHARACTER	4	SDDSQID	IDENTIFER=SDDS
4	(4)	ADDRESS	4	SDDSQFWD	POINTER TO NEXT ELEMENT - ZERO IF THIS IS THE LAST ENTRY
8	(8)	ADDRESS	4	SDDSQBWD	POINTER TO LAST ELEMENT - ZERO IF THIS IS THE FIRST ENTRY.
12	(C)	CHARACTER	4	SDDSQLWD	WORD USED IN COMPARE AND SWAP LOGIC TO SERIALIZE SDDSQ ENTRIES.
12	(C)	CHARACTER	3	SDDSQDSN	DUMP DATA SET NAME. A 2 EBCDIC DIGIT DASD DUMP DATA SET NUMBER.
12	(C)	CHARACTER	2	SDDSQDAN	DASD DUMP DATA SET NUMBER
14	(E)	CHARACTER	1	SDDSQBLN	BLANK TO FOLLOW DASD NAME
15	(F)	BITSTRING	1	SDDSQCTL	LOCK BITS
		1...		SDDSQCS	COMPARE AND SWAP BIT TURNED ON BY SDUMP OR DUMPDS WHEN THE SERIAL USE OF THE DUMP DATA SET IS REQUIRED.
		.1..		SDDSQBAD	1 - SDDSQ ENTRY IS BAD AND SHOULD NOT BE USED. SET BY IEAVTSDR TO TELL IEECB926 TO DELETE THIS SDDSQ ENTRY.
16	(10)	BITSTRING	1	SDDSQFLG	FLAGS
		1...		SDDSQDDS	DUMP DATA SET STATUS. 1 - FULL 0 - EMPTY
		.1..		SDDSQDYN	Automatic Allocation Dataset. 1 - Automatically allocated, 0 - Pre-allocated
		..1.		SDDSQRSC	Automatic Allocation Resource
		...1 1111		*	UNUSED
17	(11)	UNSIGNED	1	SDDSQTRKCALCBKS	As determined by TRKCALC
18	(12)	UNSIGNED	1	SDDSQNMB	DASD DATA SET NUMBER IN HEX
19	(13)	UNSIGNED	1	SDDSQBKS	Number of records in one block
20	(14)	UNSIGNED	1	SDDSQNCPLEVEL	NCP level for BSAM
21	(15)	CHARACTER	3	*	UNUSED
24	(18)	CHARACTER	4	*	UNUSED
28	(1C)	CHARACTER	8	SDDSQDDN	DDNAME GENERATED BY THE DYNAMIC ALLOCATION OF THE DUMP DATA SET IN DUMPDS PROCESSING. THE ALLOCATION IS ALWAYS TO THE IEECB926 TASK IN THE DUMPSRV ADDRESS SPACE
36	(24)	SIGNED	4	SDDSQDOM	MESSAGE ID FOR DOM OF IEA911E
40	(28)	CHARACTER	2	*	Unused
42	(2A)	UNSIGNED	1	SDDSQRESOURCETYPE	Type of automatic allocation resource - valid if SDDSQRSC=1
43	(2B)	UNSIGNED	1	SDDSQDSNAMELENGTH	Dataset name length
44	(2C)	CHARACTER	44	SDDSQDSNAME	Full dataset name
44	(2C)	CHARACTER	9	* (4294967299:562120480)	Array of class types
44	(2C)	UNSIGNED	1	SDDSQRESOURCENAMELENGTH	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
45	(2D)	CHARACTER	8	SDDSQRESOURCENAME	Length of automatic allocation resource name - valid if SDDSQRSC=1 Name of automatic allocation resource - valid if SDDSQRSC=1
88	(58)	CHARACTER	8	*	UNUSED.

SDDSQ Constants

Len	Type	Value	Name	Description
4	DECIMAL	100	SDDSQMAX	MAXIMUM NUMBER OF DUMP DATA SETS ALLOWED ON THE SDDSQ
4	DECIMAL	1	SDDSQSMS	Automatic allocation resource is an SMS storage class
4	DECIMAL	2	SDDSQVOL	Automatic allocation resource is a DASD volume serial number
4	DECIMAL	0	SDDSQNOCLASS	Index not identified
4	DECIMAL	1	SDDSQDATACLASS	Index of data class
4	DECIMAL	2	SDDSQMGMTCLASS	Index of management class
4	DECIMAL	3	SDDSQSTORCLASS	Index of storage class
4	DECIMAL	3	SDDSQVOLCLASS	Index of volume 'class'
4	DECIMAL	3	SDDSQMAXCLASS	Maximum class index

SDDSQ Cross Reference

Name	Hex Offset	Hex Value
SDDSQ	0	
SDDSQBAD	F	40
SDDSQBKS	13	
SDDSQBLN	E	
SDDSQBWD	8	
SDDSQCS	F	80
SDDSQCTL	F	
SDDSQDAN	C	
SDDSQDDN	1C	
SDDSQDDS	10	80
SDDSQDOM	24	
SDDSQDSN	C	
SDDSQDSNAME	2C	
SDDSQDSNAMELENGTH	2B	
SDDSQDYN	10	40
SDDSQFLG	10	
SDDSQFWD	4	
SDDSQID	0	
SDDSQLWD	C	
SDDSQNCPLEVEL	14	
SDDSQNMB	12	
SDDSQRESOURCENAME	2D	
SDDSQRESOURCENAMELENGTH	2C	
SDDSQRESOURCETYPE	2A	
SDDSQRSC	10	20
SDDSQTRKCALCBKS	11	

SDEPL Information

SDEPL Programming Interface information

Programming Interface information

SDEPL

The following field is **NOT** programming interface information:

- SDEPLDSP

End of Programming Interface information

SDEPL Heading Information

Common Name: SDUMP POST EXIT PARAMETER LIST
Macro ID: IHASDEPL
DSECT Name: SDEPL
Owning Component: SVC DUMP (SCDMP)
Eye-Catcher ID: SDEP
Offset: X'0'
Length: 4
Storage Attributes: Subpool: 252
Key: 0
Residency: BELOW THE 16M LINE
Size: 64 bytes
Created by: IEAVTSEP
Pointed to by: REGISTER 1 CONTAINS POINTER TO ADDRESS OF SDEPL
ON ENTRY TO POST DUMP EXITS CONTAINS ADDRESS
Serialization: NONE
Function: THIS MACRO IS THE MAPPING FOR THE SDUMP POST EXIT PARAMETER LIST BUILT BY IEAVTSEP AND PASSED TO USER WRITTEN AND IBM SUPPLIED POST DUMP EXITS.

SDEPL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDEPL	
0	(0)	X'80'	0	BIT0	"128"
0	(0)	X'40'	0	BIT1	"64"
0	(0)	X'20'	0	BIT2	"32"
0	(0)	X'10'	0	BIT3	"16"
0	(0)	X'8'	0	BIT4	"8"
0	(0)	X'4'	0	BIT5	"4"
0	(0)	X'2'	0	BIT6	"2"
0	(0)	X'1'	0	BIT7	"1"
0	(0)	CHARACTER	4	SDEPLCHA	EBCDIC IDENTIFIER
4	(4)	BITSTRING	1	SDEPLFLG	EXIT STATUS FLAGS
		1...		SDEPLEXE	"BIT0" BIT ON - ERROR OCCURRED IN PRECEDING EXIT
		.1..		SDEPLERR	"BIT1" BIT ON - ERROR OCCURRED IN ANY PREVIOUS EXIT
		..1.		SDEPLNOD	"BIT2" BIT ON - NO DUMP WAS TAKEN
5	(5)	CHARACTER	3	SDEPLRES	RESERVED
8	(8)	SIGNED	4	SDEPLHD	ADDRESS OF THE DUMP HEADER RECORD MAPPED BY THE AMDDATA MAPPING MACRO
12	(C)	SIGNED	4	SDEPLWA	ADDRESS OF EXIT WORK AREA (200 DECIMAL BYTES IN LENGTH)
16	(10)	SIGNED	4	SDEPLEXT	ADDRESS OF EXIT INTERFACE AREA
20	(14)	SIGNED	4	SDEPLEXL	LENGTH OF EXIT INTERFACE AREA
24	(18)	SIGNED	4	SDEPLDSP	POINTER TO THE DSPD
28	(1C)	CHARACTER	8	SDEPLJOB	Jobname requesting dump
36	(24)	CHARACTER	28		Reserved

SDEPL Cross Reference

Name	**Hex Offset**	**Hex Value**
BIT0	0	80
BIT1	0	40
BIT2	0	20
BIT3	0	10
BIT4	0	8
BIT5	0	4
BIT6	0	2
BIT7	0	1
SDEPL	0	
SDEPLCHA	0	
SDEPLDSP	18	
SDEPLERR	4	40
SDEPLEXE	4	80
SDEPLEXL	14	
SDEPLEXT	10	
SDEPLFLG	4	
SDEPLHD	8	
SDEPLJOB	1C	
SDEPLNOD	4	20
SDEPLRES	5	
SDEPLWA	C	

SDIR Information

SDIR Heading Information

Common Name: VSM SQAT Directory
Macro ID: IGVSDIR
DSECT Name: SDIR
Owning Component: Virtual Storage Manager (SC1CH)
Eye-Catcher ID: none
Storage Attributes: Subpool: nucleus
Key: 0
Residency: Above 16M line
Size: 88 bytes
Created by: n/a
Pointed to by: vcon (IGVTSDIR)
Serialization: None
Function: Directory for SQA SQATs and LSQA SQAT templates

SDIR Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	88	SDIR	SQAT TABLE DIRECTORY
0	(0)	ADDRESS	4	SDIR226	ADDRESS OF 226 SQAT
4	(4)	ADDRESS	4	SDIRE226	ADDRESS OF EXTENDED 226 SQAT
8	(8)	ADDRESS	4	SDIR239	ADDRESS OF 239 SQAT
12	(C)	ADDRESS	4	SDIRE239	ADDRESS OF EXTENDED 239 SQAT
16	(10)	ADDRESS	4	SDIR245	ADDRESS OF 245 SQAT
20	(14)	ADDRESS	4	SDIRE245	ADDRESS OF EXTENDED 245 SQAT
24	(18)	ADDRESS	4	SDIR247	ADDRESS OF 247 SQAT
28	(1C)	ADDRESS	4	SDIRE247	ADDRESS OF E247 SQAT
32	(20)	ADDRESS	4	SDIR248	ADDRESS OF 248 SQAT
36	(24)	ADDRESS	4	SDIRE248	ADDRESS OF E248 SQAT
40	(28)	ADDRESS	4	SDIR255	ADDRESS OF 255 SQAT
44	(2C)	ADDRESS	4	SDIRE255	ADDRESS OF EXTENDED 255 SQAT
48	(30)	SIGNED	4	SDIRGLBL	MINIMUM NUMBER OF DUMMY DFES FOR SQA
52	(34)	SIGNED	4	SDIRLOCL	MINIMUM NUMBER OF DUMMY DFES FOR LSQA
56	(38)	ADDRESS	4	SDIR239R64	ADDRESS OF 239 SQAT R64
60	(3C)	ADDRESS	4	SDIRE239R64	ADDRESS OF EXTENDED 239 SQAT R64
64	(40)	ADDRESS	4	SDIR245R64	ADDRESS OF 245 SQAT R64
68	(44)	ADDRESS	4	SDIRE245R64	ADDRESS OF EXTENDED 245 SQAT R64
72	(48)	ADDRESS	4	SDIR247R64	ADDRESS OF 247 SQAT R64
76	(4C)	ADDRESS	4	SDIRE247R64	ADDRESS OF E247 SQAT R64
80	(50)	ADDRESS	4	SDIR248R64	ADDRESS OF 248 SQAT R64
84	(54)	ADDRESS	4	SDIRE248R64	ADDRESS OF E248 SQAT R64
88	(58)	CHARACTER	0	SDIREND	END OF SDIR MAP

SDIR Cross Reference

Name	Hex Offset	Hex Value
SDIR	0	
SDIREND	58	
SDIRE226	4	
SDIRE239	C	
SDIRE239R64	3C	
SDIRE245	14	
SDIRE245R64	44	
SDIRE247	1C	
SDIRE247R64	4C	
SDIRE248	24	
SDIRE248R64	54	
SDIRE255	2C	
SDIRGLBL	30	
SDIRLOCL	34	
SDIR226	0	
SDIR239	8	
SDIR239R64	38	
SDIR245	10	
SDIR245R64	40	
SDIR247	18	
SDIR247R64	48	
SDIR248	20	
SDIR248R64	50	
SDIR255	28	

SDMPX Information

SDMPX Heading Information

Common Name: SDUMP PARAMETER LIST
Macro ID: IHASDMPX
DSECT Name: SDUMP
Owning Component: SVC Dump (SCDMP)
Eye-Catcher ID: None
Storage Attributes: Main Storage: One per dump request
Subpool: Any
Key: Any
Residency: Any
Size: DECIMAL 112, X'70' IF PLVER=1 OR NOT SPECIFIED
DECIMAL 128, X'80' IF PLVER=2
DECIMAL 184, X'B8' IF PLVER=3
Created by: IEECB866 and other dump requestors
Pointed to by: R1 on entry to IEAVAD00 and IEAVTSDX
RTCTSDPL for dump in progress
Serialization: CS on RTCTSDPL
Function: THIS IS THE MAPPING FOR THE PARAMETER LIST
PRODUCED BY ALL FORMS OF THE SDUMPX MACRO.

SDMPX Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	184	SDUMP	
0	(0)	CHARACTER	1	SDUFLAG0	FIRST BYTE OF FLAGS
		1...		SDUDCB	1=USER SUPPLIED DCB 0=USE A SYS1.DUMP DATA SET
		.1..		SDUBUF	1=DUMP 4K SQA BUFFER 0=BYPASS 4K SQA BUFFER
		..1.		SDUSTOR	1=STORAGE LIST SPECIFIED 0=NO STORAGE LIST
		...1		SDUHDR	1=USER DATA SPECIFIED 0=NO USER DATA
		.... 1...		SDUECB	1=ECB SPECIFIED 0=ECB NOT SPECIFIED
		.... .1..		SDUASID	1=SCHEDULE DUMP REQUEST ASID SPECIFIED 0= ASID NOT SPECIFIED
		.... ..1.		SDUQUIET	1=SET SYSTEM NON-DISPATCHABLE WHILE DUMPING GLOBAL STORAGE 0=MAINTAIN CURRENT SYSTEM STATUS
		1		SDUBRANH	1=BRANCH ENTRY 0=SVC 51 ENTRY
1	(1)	CHARACTER	1	SDUFLAG1	SECOND BYTE OF FLAGS
		1...		SDUDTYPE	1=SVC DUMP REQUEST
		1...		DUMPTYPE	1=SVC DUMP REQUEST
		.1..		SDUABEND	1=SYSMDUMP REQUEST
		..1.		SDUNEW	1=ENHANCED SVC DUMP REQ
		...1		SDUASLST	1=ASIDLST SPECIFIED
		.... 1...		SDUSULST	1=SUMLIST SPECIFIED
		.... .1..		SDUIGNCD	1=IGNORE CHNGDUMP PARAMETERS
		.... ..1.		SDUTSOXT	1=TSO USER EXTENSION IS PRESENT
		1		SDUSE3P	1=JBB1226 PARMLIST
2	(2)	CHARACTER	2	SDUSDATA	SDATA OPTION FLAGS
2	(2)	CHARACTER	1	SDUSDAT1	1ST BYTE OF OPTIONS
		1...		SDUALPSA	DUMP ALL PSA'S
		.1..		SDUPSA	DUMP CURRENT PSA
		..1.		SDUNUC	DUMP THE NUCLEUS
		...1		SDUSQA	DUMP SQA
		.... 1...		SDULSQA	DUMP LSQA
		.... .1..		SDURGN	DUMP RGN-PRIVATE AREA
		.... ..1.		SDULPA	DUMP LPA MOD. FOR RGN
		1		SDUTRT	DUMP TRACE DATA
3	(3)	CHARACTER	1	SDUSDAT2	SECOND BYTE SDATA FLGS
		1...		SDUCSA	DUMP CSA
		.1..		SDUSWA	DUMP SWA
		..1.		SDUSMDMP	DUMP SUMMARY DUMP DATA
		...1		SDUNSMDP	DON'T DUMP SUMMARY DUMP
		.... 1...		SDUNAPSA	DO NOT DUMP ALL PSA
		.... .1..		SDUNASQA	DO NOT DUMP SQA
		.... ..1.		SDUALNUC	DUMP ALL NUCLEUS
		1		SDUDEFS	DEFAULTS
4	(4)	ADDRESS	4	SDUDCBAD	ADDRESS USER SUPPLIED DCB
8	(8)	ADDRESS	4	SDUSTORA	ADDRESS OF STORAGE LIST
12	(C)	ADDRESS	4	SDUHDRAD	ADDRESS OF USER DATA
16	(10)	ADDRESS	4	SDUECBAD	ADDRESS USER SUPPLIED ECB
16	(10)	ADDRESS	4	SDUSRBAD	SRB
20	(14)	CHARACTER	4	SDUMASID	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
20	(14)	UNSIGNED	2	SDUCASID	CALLER'S ASID
22	(16)	UNSIGNED	2	SDUTASID	TARGET ASID OF SCHEDULE DUMP
24	(18)	ADDRESS	4	SDUASIDP	ADDRESS CALLERS ASIDLIST
28	(1C)	ADDRESS	4	SDUSUMLP	ADDRESS CALLERS SUMLIST
32	(20)	CHARACTER	8	SDUTUSID	TSO USER ID ASSOCIATED WITH THIS DUMP
32	(20)	ADDRESS	4	SDUSDDAT	ADDRESS OF SVC DUMP DATA AREAS MAPPED BY IHASDDAT - SET ON SYSMDUMP PATH ONLY
36	(24)	ADDRESS	4	SDUTDDAT	Transaction dump data area addr. Mapped by IHATDDAT. Set for transaction dumps only
40	(28)	CHARACTER	1	SDUFLAG2	BYTE OF SDUMP CONTROL FLAGS
		1...		SDULISTA	1=LISTA PARAMETER SPECIFIED
		.1..		SDUSLSTA	1=SUMLSTA KEYWORD SPECIFIED
		..1.		SDUSPEND	1= SUSPEND=YES SPECIFIED
		...1		SDUSUBPL	1=SUBPLST KEYWORD SPECIFIED
		.... 1...		SDUKEYS	1=KEYLIST KEYWORD SPECIFIED
		.... .1..		SDUAUTH	1=TRT REQUESTED FOR AN AUTHORIZED SYSMDUMP USER.
		.... ..1.		SDULSTD	LISTD
		1		SDUSLL	SUMLISTL
41	(29)	CHARACTER	1	SDUCNTL1	CONTROL FLAGS FOR IDENTIFYING NEW RELEASES OF SDUMP MACRO
		1...		SDUSP21	1=RELEASE HBB2102 PARMLIST
		.1..		SDUVRSNB	1=VERSION NUMBER PRESENT
		..1.		SDUPSWR	1=PSWREGS AREA SPECIFIED
		...1		SDUSYMR	1=SYMREC SPECIFIED
		.... 1...		SDUID	1=ID IS SPECIFIED
		.... .1..		SDUWRITE	1=ECB IS TO BE POSTED OR SRB SCHEDULED AFTER THE WRITE PHASE HAS COMPLETED 0=(DEFAULT) ECB IS TO BE POSTED OR SRB SCHEDULED AFTER THE CAPTURE PHASE HAS COMPLETED
		.... ..1.		SDUSTREQ	1=STRLIST SPECIFIED
		1		SDUSRB	
42	(2A)	CHARACTER	1	SDUTYPE	SPECIFIED TYPE PARAMETERS
42	(2A)	CHARACTER	1	SDUTYP1	FIRST BYTE OF TYPE OPTIONS
		1...		SDUTYPXM	1=TYPE XMEM SPECIFIED
		.1..		SDUTPXME	1=TYPE XMEME SPECIFIED
		..1.		SDUTPNOL	1=TYPE NOLOCAL SPECIFIED
		...1		SDUTPFRC	1=TYPE FAILRC SPECIFIED
		.... 111.		*	RESERVED
		1		SDUREMOT	Remote SDUMP
43	(2B)	BITSTRING	1	SDUVERSN	VERSION ID, 1=SP3.1.0 2=MDUMP, 3=SP5.1.0
44	(2C)	CHARACTER	4	SDUSDTA2	EXTENDED SDATA OPTIONS
44	(2C)	CHARACTER	2	SDUEXIT	SDATA OPTIONS EXIT ROUTINES
44	(2C)	CHARACTER	1	SDUEDAT1	SDATA OPTIONS FOR SUPPORTED COMPONENT EXIT ROUTINES
		1...		SDUGRSQ	GRSQ COMPONENT EXIT SPECIFIED
		.1..		SDUMSTRC	1=MASTER TRACE AND GTF GLOBAL EXITS TO GET CONTROL
		..1.		SDUSMSX	SMSX LOCAL EXIT
		...1		SDUCOUPL	SDATA COUPLE OPTIONS SPECIFIED
		.... 1...		SDUXES	SDATA XESDATA OPTION SPECIFIED
		.... .1..		SDUIOEX	1=IOS EXIT TO GET CONTROL
		.... ..1.		SDUWLM	1=WLM EXIT TO GET CONTROL
		1		SDURSM	1=RSM EXIT TO GET CONTROL
45	(2D)	CHARACTER	1	SDUEDAT2	2ND BYTE OF DUMP EXITS
		1...		SDUSLIP	1=SLIP EXIT TO GET CONTROL
		.1..		SDUOPENE	1=OE EXIT TO GET CONTROL
		..1.		SDUTSVCD	1=Tailored SVC Dump was specified
		...1		SDURTM	1=RTM exit to get control

Comment

ADDITIONAL SDUMP EXITS SHOULD BE ADDED BEFORE THIS COMMENT AND
THE FOLLOWING RESERVED BIT COUNT DECREMENTED.

End of Comment

		 1111		*	RESERVED
46	(2E)	BITSTRING	1	SDUSDAT3	RESERVED FOR SDATA OPTIONS
		1...		SDUNODEF	1=NODEFAULTS SDATA OPTION SPECIFIED
		.1..		SDUIO	1=IODATA INCLUDED IN DUMP
47	(2F)	BITSTRING	1	SDUSDAT4	RESERVED FOR SDATA OPTIONS
48	(30)	ADDRESS	4	SDUSPLST	SUBPOOL LIST ADDRESS
52	(34)	ADDRESS	4	SDUKYLST	KEY LIST ADDRESS
56	(38)	ADDRESS	4	SDURGPSA	POINTER TO SLIP REGS AND PSW TO BE PLACED IN THE DUMP HEADER RECORD.
60	(3C)	SIGNED	4	SDUDCBA	ALET FOR DCB PARAMETER
64	(40)	SIGNED	4	SDUSTRAL	ALET FOR STORAGE PARAMETER
68	(44)	SIGNED	4	SDUHDRA	ALET FOR HDR PARAMETER
72	(48)	SIGNED	4	SDUASDLA	ALET FOR ASIDLIST PARAMETER

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
76	(4C)	SIGNED	4	SDUSMLA	ALET FOR SUMLIST PARAMETER
80	(50)	SIGNED	4	SDUSBPLA	ALET FOR SUBPLIST PARAMETER
84	(54)	SIGNED	4	SDUKEYLA	ALET FOR KEYLIST PARAMETER
88	(58)	ADDRESS	4	SDULSTDP	LISTD or LIST64 Address
92	(5C)	SIGNED	4	SDULSTDA	ALET FOR LISTD or LIST64 Parm
96	(60)	ADDRESS	4	SDUSMLLP	SUMLISTL or SUMLST64 Address
100	(64)	SIGNED	4	SDUSMLLA	ALET FOR SUMLISTL or SUMLST64
104	(68)	ADDRESS	4	SDUPSWRP	PSWREGS AREA ADDRESS
108	(6C)	SIGNED	4	SDUPSWRA	PSWREGS AREA ALET
112	(70)	ADDRESS	4	SDUSYMAD	SYMREC ADDRESS
116	(74)	SIGNED	4	SDUSYMA	SYMREC ALET
120	(78)	ADDRESS	4	SDUIDAD	ID ADDRESS
124	(7C)	SIGNED	4	SDUIDA	ID ALET
128	(80)	ADDRESS	4	SDUSLADR	ADDRESS OF USER SUPPLIED STRLIST
132	(84)	SIGNED	4	SDUSLALT	ALET OF USER SUPPLIED STRLIST
136	(88)	ADDRESS	4	SDUITADR	Address of user-supplied Incident Token
140	(8C)	SIGNED	4	SDUITALT	ALET of user-supplied Incident Token
144	(90)	ADDRESS	4	SDURMADR	Address of user-supplied REMOTE information
148	(94)	SIGNED	4	SDURMALT	ALET of user-supplied REMOTE Information
152	(98)	ADDRESS	4	SDUPDADR	Address of user-supplied problem description information
156	(9C)	SIGNED	4	SDUPDALT	ALET of user-supplied problem description information
160	(A0)	ADDRESS	4	SDUJLADR	Address of user-supplied JOBLIST information
164	(A4)	SIGNED	4	SDUJLALT	ALET of user-supplied JOBLIST information
168	(A8)	ADDRESS	4	SDUDLADR	Address of user-supplied DSPLIST information
172	(AC)	SIGNED	4	SDUDLALT	ALET of user-supplied DSPLIST information
176	(B0)	CHARACTER	1	SDUFLAG3	BYTE FOR SDUMP CONTROL FLAGS
		1...		SDULST64	1=LIST64 in SDULSTDP and SDULSTDA
		.1..		SDUSL64	1=SUMLST64 in SDUSMLLP and SDUSMLLA
		..1.		SDUCMD	SDUMPX was invoked by DUMP command
		...1		SDUDFSPF	If 1, the DEFERTND keyword was specified via DUMP command
		.... 1...		SDUDEFER	If 1, the DEFERTND=YES was requested via DUMP command
177	(B1)	CHARACTER	7	*	RESERVED

SDMPX Cross Reference

Name	Hex Offset	Hex Value
DUMPTYPE	1	80
SDUABEND	1	40
SDUALNUC	3	02
SDUALPSA	2	80
SDUASDLA	48	
SDUASID	0	04
SDUASIDP	18	
SDUASLST	1	10
SDUAUTH	28	04
SDUBRANH	0	01
SDUBUF	0	40
SDUCASID	14	
SDUCMD	B0	20
SDUCNTL1	29	
SDUCOUPL	2C	10
SDUCSA	3	80
SDUDCB	0	80
SDUDCBA	3C	
SDUDCBAD	4	
SDUDEFER	B0	08
SDUDEFS	3	01
SDUDFSPF	B0	10
SDUDLADR	A8	
SDUDLALT	AC	
SDUDTYPE	1	80
SDUECB	0	08
SDUECBAD	10	
SDUEDAT1	2C	
SDUEDAT2	2D	
SDUEXIT	2C	
SDUFLAG0	0	
SDUFLAG1	1	
SDUFLAG2	28	
SDUFLAG3	B0	
SDUGRSQ	2C	80
SDUHDR	0	10
SDUHDRA	44	
SDUHDRAD	C	
SDUID	29	08
SDUIDA	7C	
SDUIDAD	78	
SDUIGNCD	1	04
SDUIO	2E	40
SDUIOEX	2C	04
SDUITADR	88	
SDUITALT	8C	
SDUJLADR	A0	
SDUJLALT	A4	
SDUKEYLA	54	
SDUKEYS	28	08
SDUKYLST	34	
SDULISTA	28	80
SDULPA	2	02
SDULSQA	2	08
SDULSTD	28	02
SDULSTDA	5C	
SDULSTDP	58	
SDULST64	B0	80
SDUMASID	14	
SDUMP	0	
SDUMSTRC	2C	40
SDUNAPSA	3	08
SDUNASQA	3	04
SDUNEW	1	20
SDUNODEF	2E	80
SDUNSMDP	3	10
SDUNUC	2	20
SDUOPENE	2D	40
SDUPDADR	98	
SDUPDALT	9C	
SDUPSA	2	40
SDUPSWR	29	20

SDMPX Cross Reference

Name	Hex Offset	Hex Value
SDUPSWRA	6C	
SDUPSWRP	68	
SDUQUIET	0	02
SDUREMOT	2A	01
SDURGN	2	04
SDURGPSA	38	
SDURMADR	90	
SDURMALT	94	
SDURSM	2C	01
SDURTM	2D	10
SDUSBPLA	50	
SDUSDATA	2	
SDUSDAT1	2	
SDUSDAT2	3	
SDUSDAT3	2E	
SDUSDAT4	2F	
SDUSDDAT	20	
SDUSDTA2	2C	
SDUSE3P	1	01
SDUSLADR	80	
SDUSLALT	84	
SDUSLIP	2D	80
SDUSLL	28	01
SDUSLSTA	28	40
SDUSL64	B0	40
SDUSMDMP	3	20
SDUSMLA	4C	
SDUSMLLA	64	
SDUSMLLP	60	
SDUSMSX	2C	20
SDUSPEND	28	20
SDUSPLST	30	
SDUSP21	29	80
SDUSQA	2	10
SDUSRB	29	01
SDUSRBAD	10	
SDUSTOR	0	20
SDUSTORA	8	
SDUSTRAL	40	
SDUSTREQ	29	02
SDUSUBPL	28	10
SDUSULST	1	08
SDUSUMLP	1C	
SDUSWA	3	40
SDUSYMA	74	
SDUSYMAD	70	
SDUSYMR	29	10
SDUTASID	16	
SDUTDDAT	24	
SDUTPFRC	2A	10
SDUTPNOL	2A	20
SDUTPXME	2A	40
SDUTRT	2	01
SDUTSOXT	1	02
SDUTSVCD	2D	20
SDUTUSID	20	
SDUTYPE	2A	
SDUTYPXM	2A	80
SDUTYP1	2A	
SDUVERSN	2B	
SDUVRSNB	29	40
SDUWLM	2C	02
SDUWRITE	29	04
SDUXES	2C	08

SDRSN Information

SDRSN Programming Interface information

Programming Interface information

SDRSN

End of Programming Interface information

SDRSN Heading Information

Common Name: SDUMP PARTIAL DUMP REASON CODE CONTROL BLOCK
Macro ID: IHASDRSN
DSECT Name: SDRSN
Owning Component: SVC Dump (SCDMP)
Eye-Catcher ID: None
Storage Attributes: Main Storage: One per dump
Subpool: Any
Key: Any
Residency: Any
Size: DECIMAL 16, X'10'
Created by: IEAVTSDI (SDWSDRSN)
IEAVTSDS (DPLSDRSN)
Dump requestor (SDSTPDRC)
Pointed to by: Overlay of SDWSDRSN, DPLSDRSN and SDSTPDRC fields
Serialization: NONE
Function: IHASDRSN is a mapping macro which maps the bits which SDUMP processing uses to indicate what occurred when it is determined that portions of the dump data may be inaccurate. The first word of SDRSN represents conditions which may occur while processing a Branch-Entry or Scheduled SVC dump. The modules most likely to have encountered the problem are IEAVTSDX, IEAVTSSD, IEAVTSSV, IEAVTSSM AND IEAVTSDB. (In storage, these flags are originally kept in RTSDSRSN field of IHARTSD. They are subsequently moved into the SDWSDRSN field of IHASDWRK.
An application may access this information by using the SRB parameter of the SDUMP(X) macro invocation. When the SRB routine receives control the status area mapped by IHASDST contains the SDSTPDRC field, which may then be mapped by this macro.

SDRSN Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDRSN	PARTIAL DUMP REASON CONTROL BLOCK
				Comment	
					Partial Dump Message SRDSN Word 0 field - vvvvvvvv
				End of Comment	
0	(0)	BITSTRING	4	SDRSCDMP (0)	REASON CODES FROM SCHEDULE DUMP PROCESSING
0	(0)	BITSTRING	1	SDRSCDM0 (0)	
		1...		SDRBASID	"X'80'" AN SRB COULD NOT BE SCHEDULED TO A REQUESTED ASID BECAUSE THE ASID DID NOT EXIST OR WAS IN THE PROCESS OF MEMORY TERMINATION.
		.1..		SDRNRSM4	"X'40'" NO SUMMARY DUMP COULD BE TAKEN BECAUSE SDUMP WAS UNABLE TO OBTAIN RSM SERIALIZATION. (HIERARCHY PROBLEM)
		..1.		SDRNRSM8	"X'20'" NO SUMMARY DUMP COULD BE TAKEN BECAUSE SDUMP WAS UNABLE TO OBTAIN RSM SERIALIZATION. (RSM CONTROL PROBLEM)
		...1		SDRNORSB	"X'10'" NO SUMMARY DUMP WAS TAKEN BECAUSE SDUMP WAS UNABLE TO OBTAIN A REAL STORAGE BUFFER FROM RSM. SET BY SSD.
		.... 1...		SDRSCHFR	"X'08'" RECOVERY ROUTINE SCHFRR IN IEAVTSDX RECEIVED CONTROL.
		.... .1..		SDRSUMFR	"X'04'" RECOVERY ROUTINE SUMFRR IN IEAVTSSD RECEIVED CONTROL.
		.... ..1.		SDRSSVFR	"X'02'" RECOVERY ROUTINE SSVFRR IN IEAVTSSV RECEIVED CONTROL.
1	(1)	BITSTRING	1	SDRSCDM1 (0)	
		.1..		SDRSBERR	"X'40'" AN ERROR OCCURRED IN THE STEAL BACK ROUTINE IEAVTSDS IN IEAVTSSD WHICH CAUSED THE SUMMARY DUMP TO BE LOST
		..1.		SDRVBFUL	"X'20'" SOME DATA NOT MOVED IS SET BY IEAVTSSM WHEN THE VIRTUAL STORAGE BUFFER FILLS UP DURING A SUSPEND SUMMARY DUMP.
		...1		SDRRBFUL	"X'10'" THE REAL STORAGE BUFFER FILLED UP AND NO MORE SUMMARY DUMP DATA COULD BE DUMPED. (SET BY IEAVTSSM)
		.... 1...		SDRBRTRC	"X'08'" THE TRACE TABLES COULD NOT BE CAPTURED IN SDX OR SD2 BECAUSE OF SOME FAILURE IN THE SNAPTRC SERVICE.
2	(2)	BITSTRING	1	SDRSCDM2 (0)	
		1...		SDRSSTUN	"X'80'" storage unavailable for >2G range collection
		.1..		SDRSUSTK	"X'40'" USERTOKEN access failed
		..1.		SDRSPSNC	"X'20'" some >2G private storage was not collected
		...1		SDRSERFP	"X'10'" extended range table filled while collecting >2G private
		.... 1...		SDRSERFC	"X'08'" extended range table filled while collecting >2G common (future)
		.... .111		SDRSM2RS	"X'07'" RESERVED
3	(3)	BITSTRING	1	SDRSCDM3	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					Partial Dump Message SRDSN Word 1 field - wwwwwwww
					End of Comment
4	(4)	BITSTRING	4	SDRSVCD1 (0)	FIRST WORD OF REASON CODES FOR THE SVC SDUMP PATH AND ALL MODULES CALLED FROM IEAVTSDT AND AFTER
4	(4)	BITSTRING	1	SDRSVCDB (0)	
		1...		SDRSDFRR	"X'80'" RECOVERY ROUTINE SDFRRRTN IN IEAVTSRR RECEIVED CONTROL.
		.1..		SDRESTAX	"X'40'" RECOVERY ROUTINE SDESTAEX IN IEAVTSRR RECEIVED CONTROL.
		..1.		SDRESTA1	"X'20'" RECOVERY ROUTINE DTESTAE1 IN IEAVTSDT RECEIVED CONTROL.
		...1		SDRDTFAL	"X'10'" DUMP TASK (IEAVTSDT) FAILED RECEIVED CONTROL.
		.... 1...		SDRSNPTR	"X'08'" SNAPTRC FAILED TO GET A SNAP SHOT OF THE TRACE TABLE.
		.... .1..		SDRSDBFR	"X'04'" RECOVERY ROUTINE SDBFRR IN IEAVTSDB RECEIVED CONTROL.
		.... ..1.		SDRESTA2	"X'02'" RECOVERY ROUTINE SDS_ESTAE IN IEAVTSDS RECEIVED CONTROL.
		1		SDRDSFAL	"X'01'" DUMP TASK (IEAVTSDS) FAILED AFTER IT RECEIVED CONTROL.
5	(5)	BITSTRING	1	SDRSVCDA (0)	
		1...		SDRTTSDR	"X'80'" A PARTIAL DUMP OCCURRED BECAUSE THE RESOURCE MANAGER CLEANED UP THE DUMP BECAUSE A TASK INVOLVED IN THE DUMP ABNORMALLY TERMINATED.
		.1..		SDRDESTA	"X'40'" RECOVERY ROUTINE DWTESTAE IN IEAVTDWT RECEIVED CONTROL.
		..1.		SDRMTSDR	"X'20'" A PARTIAL DUMP OCCURRED BECAUSE THE RESOURCE MANAGER CLEANED UP THE DUMP BECAUSE AN ADDRESS SPACE INVOLVED IN THE DUMP ABNORMALLY TERMINATED.
		...1		SDRDSSDR	"X'10'" A PARTIAL DUMP OCCURRED BECAUSE THE RESOURCE MANAGER CLEANED UP THE DUMP BECAUSE DUMPSRV ABNORMALLY TERMINATED.
		.... 1...		SDRHDROV	"X'08'" A PARTIAL DUMP OCCURRED BECAUSE IEAVTSDH DID NOT HAVE ENOUGH SPACE TO PLACE DATA IN THE DUMP HEADER
		.... .1..		SDRNOLCL	"X'04'" PARTIAL DUMP DUE TO PURGEDQ ISSUED AGAINST SRB WHICH WOULD TRIGGER TSDT TO COLLECT LOCAL STORAGE IN ONE OF THE ADDR SPACES IN THE DUMP
		.... ..1.		SDRPLCL	"X'02'" Partial dump due to emergency dump attempt of possibly hung address space. Only LSQA is gathered.
		1		SDRIASID	"X'01'" Partial dump of possibly hung address space found invalidated ASID during processing. Data may not be consistent
6	(6)	BITSTRING	1	SDRSVCDC (0)	
		1...		SDRDSPNA	"X'80'" Partial dump due to requested data space inaccessible since ALESERV ADD failed.
		.1..		SDRNOMORELOCAL	"X'40'" If = '1'B Indicates that SDUMP truncated because no more data spaces could be allocated for local address space data capture
		..1.		SDRGDFUL	"X'20'" The Global data space was full
7	(7)	BITSTRING	1	SDRSVCDD	
					Comment
					Partial Dump Message SRDSN Word 2 field - xxxxxxxx
					End of Comment
8	(8)	BITSTRING	4	SDRSVCD2 (0)	SECOND WORD OF REASON CODES FOR THE SVC SDUMP PATH AND ALL MODULES CALLED FROM IEAVTSDT AND AFTER
8	(8)	BITSTRING	1	SDRSVCDE (0)	
		1...		SDRSDGFL	"X'80'" IEAVTSDG FILLED THE ADDRESS RANGE TABLE SOME DATA WAS NOT ADDED TO THE RANGE TABLE
		.1..		SDRSDLFL	"X'40'" IEAVTSDL FILLED THE ADDRESS RANGE TABLE SOME DATA WAS NOT ADDED TO THE RANGE TABLE
		..1.		SDRSDHFL	"X'20'" IEAVTSDH FILLED THE ADDRESS RANGE TABLE SOME DATA WAS NOT ADDED TO THE RANGE TABLE
		...1		SDRSDIOE	"X'10'" IEAVTSDO HAD AN I/O ERROR WRITING TO THE DUMP DATASET.
		.... 1...		SDRSDFUL	"X'08'" IEAVTSDO - THE DUMP DATASET IS FULL
		.... .1..		SDRSDWER	"X'04'" IEAVTSDW - ERROR WRITING SUMMARY DUMP
		.... ..1.		SDRSNTRC	"X'02'" IEAVTSDM - ERROR OBTAINING TRACE DATA
		1		SDRSRBER	"X'01'" ERROR OCCURRED IN IEAVTSRB
9	(9)	BITSTRING	1	SDRSVCDF (0)	
		1...		SDREXITE	"X'80'" ERROR OCCURRED IN A EXIT SOME DATA NOT INCLUDE IN DUMP

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.1..		SDRSDEOD	"X'40'" ERROR OCCURRED WHILE WRITING END OF DATA (EOD) RECORD ON DUMP DATASET
		..1.		SDRSDT2F	"X'20'" IEAVTSD2 OR IEAVTSD3 FILLED RANGE TABLE TWO, SOME RANGES WERE NOT ADDED
		...1		SDRSD3ER	"X'10'" ERROR OCCURRED IN IEAVTSD3
		.... 1...		SDRSD4ER	"X'08'" ERROR OCCURRED IN IEAVTSD4
		.... .1..		SDRLSTDF	"X'04'" ERROR OCCURRED WHILE PROCESSING LISTD SPECIFIED DATA SPACES
		.... ..1.		SDRSDT1F	"X'02'" IEAVTSD2 OR IEAVTSD3 FILLED RANGE TABLE ONE, SOME RANGES WERE NOT ADDED
		1		SDRSPMX	"X'01'" IF = '1'B INDICATES THAT SDUMP TRUNCATED BECAUSE MAXSPACE LIMIT HAS BEEN REACHED
10	(A)	BITSTRING	1	SDRSVCDG (0)	
		1...		SDRSQAFL	"X'80'" IEAVTVSM FILLED THE SDUMP ADDRESS RANGE TABLE WHILE PROCESSING SQA PARAMETER - SOME DATA WILL NOT BE AVAILABLE IN THE DUMP
		.1..		SDRCSAFL	"X'40'" IEAVTVSM FILLED THE SDUMP ADDRESS RANGE TABLE WHILE PROCESSING CSA PARAMETER - SOME DATA WILL NOT BE AVAILABLE IN THE DUMP
		..1.		SDRGSPFL	"X'20'" IEAVTVSM FILLED THE SDUMP ADDRESS RANGE TABLE WHILE PROCESSING GLOBAL SUBPOOLS - SOME DATA WILL NOT BE AVAILABLE IN THE DUMP
		...1		SDRLSQAF	"X'10'" IEAVTVSM FILLED THE SDUMP ADDRESS RANGE TABLE WHILE PROCESSING LSQA PARAMETER - SOME DATA WILL NOT BE AVAILABLE IN THE DUMP
		.... 1...		SDRRGNFL	"X'08'" IEAVTVSM FILLED THE SDUMP ADDRESS RANGE TABLE WHILE PROCESSING RGN PARAMETER - SOME DATA WILL NOT BE AVAILABLE IN THE DUMP
		.... .1..		SDRLSPFL	"X'04'" IEAVTVSM FILLED THE SDUMP ADDRESS RANGE TABLE WHILE PROCESSING PRIVATE SUBPOOLS - SOME DATA WILL NOT BE AVAILABLE IN THE DUMP
		.... ..1.		SDRSWAFL	"X'02'" IEAVTVSM FILLED THE SDUMP ADDRESS RANGE TABLE WHILE PROCESSING SWA PARAMETER - SOME DATA WILL NOT BE AVAILABLE IN THE DUMP
		1		SDRSD3GL	"X'01'" SD3 COULD NOT DUMP SOME RANGES OF COMMON STORAGE
11	(B)	BITSTRING	1	SDRDATSP (0)	Byte containing indicators of data space problems
		1...		SDREDATS	"X'80'" Bit used to indicate that either the Exit data space could not be created or the Exit data space was created, but could not obtain an alet to access the data space. Either one of the previous two events could cause the following data to be missing from an SVC dump: Early Global Exit data, Late Global Exit data and the System trace table. If processing a synchronous SVC dump the following data may be missing from the dump in addition to the data mention above: Local Exit data, One-Time-Only Exit data, and Console Loop Trace data
		.1..		SDRSDATS	"X'40'" Bit used to indicate that either the Summary dump data space could not be created or the Summary dump data space was created, but alet was not obtained to access the data space. Either one of the previous two events will cause Summary dump to be absent from the SVC dump.
		..1.		SDRLDATS	"X'20'" Bit used to indicate that either the Local data space could not be created or the Local data space was created, but an alet was not obtained to access the data space. Either one of the previous two events will cause private storage to be absent from the SVC dump.
		...1		SDRGDATS	"X'10'" Bit used to indicate that either the Global data space could not be created or the Global data space was created, but an alet was not obtained to access the data space. Either one of the previous two events will cause Global storage to be absent from the SVC dump.
		.... 1...		SDRXDATS	"X'08'" SVC Dump could not create or could not use data spaces which are required to process the STRLIST request. Some data will not be included in the dump.
		.... .1..		SDR_RANGE_EXT_FULL	"X'04'" IEAVTSDL filled the range table extension, some data was not added to the table
		.... ..1.		SDREXDSF	"X'02'" Bit used to indicate that exit dataspace is full
		1		SDRAUXSH	"X'01'" SVC Dump was truncated because SRM detected that there was a critical auxiliary storage shortage condition at the time of the dump

Comment

Partial Dump Message SRDSN Word 3 field - zzzzzzzz

End of Comment

12	(C)	BITSTRING	4	SDRSVCD3 (0)	THIRD WORD OF REASON CODES FOR THE SVC SDUMP PATH AND ALL MODULES CALLED FROM IEAVTSDT AND AFTER

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
12	(C)	BITSTRING	1	SDRSDBND (0)	BYTE CONTAINING PARTIAL DUMP FLAGS SET BY IEAVTSDB AS THE NON-DISPATCHABILITY TIMER DIE PROCESSOR. IF ANY OF THESE FLAGS ARE ON WHEN IEAVTSDC GETS CONTROL, THEN MESSAGE IEA911E WILL BE ISSUED WITH SPECIAL MESSAGE TEXT
		1...		SDRSDISP	"X'80'" IEAVTSDB HAS RESET THE SYSTEM DISPATCHABLE WHEN SDUMP HAS EITHER HUNG OR TERMINATED WITHOUT RESETTING THE SYSTEM
		.1..		SDRTDISP	"X'40'" IEAVTSDB HAS RESET THE TASKS OF AN ADDRESS SPACE INVOLVED IN THE DUMP WHEN SDUMP HAS EITHER HUNG OR TERMINATED WITHOUT RESETTING THE TASKS
		..1.		SDRRESET	"X'20'" IEAVTSDB HAS RESET THE UNIT OF WORK STOPPED BY IEAVTSSD (AS PART OF SUSPEND SUMMARY DUMP PROCESSING) WHEN SDUMP HAS EITHER HUNG OR TERMINATED WITHOUT DOING THE RESET
		...1		SDRSRSET	"X'10'" Because SDUMP appears to be hung, IEAVTSDB has released serialization for all serialized structures specified in the STRLIST
		.... 1...		SDRRLRSB	"X'08'" Because SDUMP appears to be hung, IEAVTSDB has released real storage buffers containing summary dump records
		.... .1..		SDRSNDSP	"X'04'" The system has been reset to be dispatchable because the duration of the system being set non-dispatchable exceeded the maximum time interval.
13	(D)	BITSTRING	1	SDRSTRL (0)	Partial dump bits relating to STRLIST processing
		1...		SDRSTRFF	"X'80'" Facility not available. Some structures will not be dumped.
		.1..		SDRSTRSF	"X'40'" The structure is not available 1) Structure failure detected and the structure cannot be accessed OR 2) Structure is not allocated
		..1.		SDRSTRNS	"X'20'" No facility dump space allocated or no facility dump space is available because it is being used to hold structure dump tables for other structure dumps
		...1		SDRSTRLU	"X'10'" Possible error in STRLIST parameter list: 1) Structure does not exist in policy 2) Structure type is not compatible with range options 3) Lock structure was requested - SDUMP does not support lock structures
		.... 1...		SDRSTRLE	"X'08'" Some or all of the STRLIST could not be processed
		.... .1..		SDRSTRRS	"X'04'" Structure dump serialization was released before all the data was captured. Note that if serialization was released during capture of the entry data, this bit will be set only when some entry data was requested serialized but was captured after serialization was released.
		.... ..1.		SDRSTRRC	"X'02'" Recovery received control while processing STRLIST
		1		SDRSTRPS	"X'01'" SVC Dump was unable to continue processing a structure requested in the STRLIST parameter list. This may be because the structure dump was deleted by the operator via the SETXCF FORCE command.
14	(E)	BITSTRING	1	SDRSVCDK (0)	More flags
		1...		SDRREMOT	"X'80'" Recovery received control while building the remote SDUMP signal(s)
15	(F)	BITSTRING	1	SDRSVCDL	More flags
15	(F)	X'10'	0	SDRSN_LEN	"*-SDRSN"

SDRSN Cross Reference

Name	Hex Offset	Hex Value
SDR_RANGE_EXT_FULL		
	B	4
SDRAUXSH	B	1
SDRBASID	0	80
SDRBRTRC	1	8
SDRCSAFL	A	40
SDRDATSP	B	
SDRDESTA	5	40
SDRDSFAL	4	1
SDRDSPNA	6	80
SDRDSSDR	5	10
SDRDTFAL	4	10
SDREDATS	B	80
SDRESTAX	4	40
SDRESTA1	4	20
SDRESTA2	4	2
SDREXDSF	B	2
SDREXITE	9	80
SDRGDATS	B	10
SDRGDFUL	6	20
SDRGSPFL	A	20
SDRHDROV	5	8
SDRIASID	5	1
SDRLDATS	B	20
SDRLSPFL	A	4
SDRLSQAF	A	10
SDRLSTDF	9	4
SDRMTSDR	5	20
SDRNOLCL	5	4
SDRNOMORELOCAL		
	6	40
SDRNORSB	0	10
SDRNRSM4	0	40
SDRNRSM8	0	20
SDRPLCL	5	2
SDRRBFUL	1	10
SDRREMOT	E	80
SDRRESET	C	20
SDRRGNFL	A	8
SDRRLRSB	C	8
SDRSBERR	1	40
SDRSCDMP	0	
SDRSCDM0	0	
SDRSCDM1	1	
SDRSCDM2	2	
SDRSCDM3	3	
SDRSCHFR	0	8
SDRSDATS	B	40
SDRSDBFR	4	4
SDRSDBND	C	
SDRSDEOD	9	40
SDRSDFRR	4	80
SDRSDFUL	8	8
SDRSDGFL	8	80
SDRSDHFL	8	20
SDRSDIOE	8	10

SDRSN Cross Reference

Name	Hex Offset	Hex Value
SDRSDISP	C	80
SDRSDLFL	8	40
SDRSDT1F	9	2
SDRSDT2F	9	20
SDRSDWER	8	4
SDRSD3ER	9	10
SDRSD3GL	A	1
SDRSD4ER	9	8
SDRSERFC	2	8
SDRSERFP	2	10
SDRSM2RS	2	7
SDRSN	0	
SDRSN_LEN	F	10
SDRSNDSP	C	4
SDRSNPTR	4	8
SDRSNTRC	8	2
SDRSPMX	9	1
SDRSPSNC	2	20
SDRSQAFL	A	80
SDRSRBER	8	1
SDRSRSET	C	10
SDRSSTUN	2	80
SDRSSVFR	0	2
SDRSTRFF	D	80
SDRSTRL	D	
SDRSTRLE	D	8
SDRSTRLU	D	10
SDRSTRNS	D	20
SDRSTRPS	D	1
SDRSTRRC	D	2
SDRSTRRS	D	4
SDRSTRSF	D	40
SDRSUMFR	0	4
SDRSUSTK	2	40
SDRSVCDA	5	
SDRSVCDB	4	
SDRSVCDC	6	
SDRSVCDD	7	
SDRSVCDE	8	
SDRSVCDF	9	
SDRSVCDG	A	
SDRSVCDK	E	
SDRSVCDL	F	
SDRSVCD1	4	
SDRSVCD2	8	
SDRSVCD3	C	
SDRSWAFL	A	2
SDRTDISP	C	40
SDRTTSDR	5	80
SDRVBFUL	1	20
SDRXDATS	B	8

SDST Information

SDST Programming Interface information

Programming Interface information

SDST

End of Programming Interface information

SDST Heading Information

Common Name: SVC DUMP Status Area
Macro ID: IHASDST
DSECT Name: SDSTATUS
Owning Component: SVC Dump (SCDMP)
Eye-Catcher ID: None
Storage Attributes: Subpool: Caller Specified
Key: Caller Determined
Data Space: None
Residency: any,any
Size: 24 bytes or 72 bytes
Created by: Caller
Pointed to by: Caller
Serialization: None. The storage is owned by the caller.
Function: The SVC DUMP Status Area is used by SVC DUMP to communicate with the caller of SDUMP(X) who specified SRB= on the invocation of the SDUMP(X) macro. The Return Code, the the NO-DUMP Reason Code, and the Partial DUMP Reason Codes are mapped in this macro.

SDST Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDSTATUS	,
0	(0)	ADDRESS	4	SDST (0)	Force fullword alignment
0	(0)	SIGNED	2	SDSTLEN	Length of SDSTATUS as filled in by the caller
2	(2)	SIGNED	1	SDSTRETC	SDUMP return code: 0 - Complete DUMP, 4 - Partial DUMP, 8 - No DUMP,
3	(3)	SIGNED	1	SDSTNDRC	NO-DUMP Reason Code
4	(4)	CHARACTER	16	SDSTPDRC	Reason Code for a Partial DUMP. This is a string of flags that is mapped by IHASDRSN.
20	(14)	CHARACTER	16		Reserved
36	(24)	CHARACTER	44	SDSTDSN	Dump dataset name
80	(50)	CHARACTER	4		Reserved
84	(54)	SIGNED	4	SDSTEND (0)	End of SDSTATUS
84	(54)	X'54'	0	SDSTDSNL	"SDSTEND-SDST" Length of the SDSTATUS with DSN
84	(54)	X'18'	0	SDSTLENC	"24" Length of SDSTATUS area w/o DSN

SDST Cross Reference

Name	Hex Offset	Hex Value
SDST	0	
SDSTATUS	0	
SDSTDSN	24	
SDSTDSNL	54	54
SDSTEND	54	
SDSTLEN	0	
SDSTLENC	54	18
SDSTNDRC	3	
SDSTPDRC	4	
SDSTRETC	2	

SDUMP Information

SDUMP Heading Information

Common Name: SDUMP PARAMETER LIST
Macro ID: IHASDUMP
DSECT Name: SDUMP
Owning Component: SVC Dump (SCDMP)
Eye-Catcher ID: None
Storage Attributes: Main Storage: One per dump request
Subpool: Any
Key: Any
Residency: Any
Size: DECIMAL 68, X'44' IF PLISTVER=1 OR NOT SPECIFIED
DECIMAL 128, X'80' IF PLISTVER=2
DECIMAL 184, X'B8' IF PLISTVER=3
Created by: IEECB866 and other dump requestors
Pointed to by: R1 on entry to IEAVAD00 and IEAVTSDX
RTCTSDPL for dump in progress
Serialization: CS on RTCTSDPL
Function: THIS IS THE MAPPING FOR THE PARAMETER LIST
PRODUCED BY ALL FORMS OF THE SDUMP MACRO.

SDUMP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDUMP	, SDUMPPTR SDUMP PARAMETER LIST
0	(0)	X'80'	0	BIT0	"128"
0	(0)	X'40'	0	BIT1	"64"
0	(0)	X'20'	0	BIT2	"32"
0	(0)	X'10'	0	BIT3	"16"
0	(0)	X'8'	0	BIT4	"8"
0	(0)	X'4'	0	BIT5	"4"
0	(0)	X'2'	0	BIT6	"2"
0	(0)	X'1'	0	BIT7	"1"
0	(0)	BITSTRING	1	SDUFLAG0	FIRST BYTE OF FLAGS
		1...		SDUDCB	"BIT0" 1=USER SUPPLIED DCB 0=USE OF SYS1.DUMP DATA SET
		.1..		SDUBUF	"BIT1" 1=DUMP 4K SQA BUFFER 0=BYPASS 4K SQA BUFFER
		..1.		SDUSTOR	"BIT2" 1=STORAGE LIST SPECIFIED 0=NO STORAGE LIST
		...1		SDUHDR	"BIT3" 1=USER DATA SPECIFIED 0=NO USER DATA
		.... 1...		SDUECB	"BIT4" 1=ECB SPECIFIED 0=ECB NOT SPECIFIED
		.... .1..		SDUASID	"BIT5" 1=SCHEDULE DUMP REQUEST ASID SPECIFIED 0=ASID NOT SPECIFIED
		.... ..1.		SDUQUIET	"BIT6" 1=SET SYSTEM NON-DISPATCHABLE WHILE DUMPING GLOBAL STORAGE 0=MAINTAIN CURRENT SYSTEM STATUS
		1		SDUBRANH	"BIT7" 1=BRANCH ENTRY 0=SVC 51 ENTRY
1	(1)	BITSTRING	1	SDUFLAG1	SECOND BYTE OF FLAGS
		1...		SDUDTYPE	"BIT0" 1=SVC DUMP REQUEST @L1A
		1...		DUMPTYPE	"BIT0" 1=SVC DUMP REQUEST
		.1..		SDUABEND	"BIT1" 1=SYSMDUMP REQUEST
		..1.		SDUNEW	"BIT2" 1=ENHANCED SVC DUMP REQUEST
		...1		SDUASLST	"BIT3" 1=ASIDLST SPECIFIED
		.... 1...		SDUSULST	"BIT4" 1=SUMLIST SPECIFIED
		.... .1..		SDUIGNCD	"BIT5" 1=IGNORE CHNGDUMP OPTIONS
		.... ..1.		SDUTSOXT	"BIT6" 1=TSO USER EXTENSION PRESENT
		1		SDUSE3P	"BIT7" 1=JBB1226 PARMLIST
2	(2)	BITSTRING	2	SDUSDATA (0)	SDATA OPTION FLAGS
2	(2)	BITSTRING	1	SDUSDAT1	FIRST BYTE OF SDATA FLAGS
		1...		SDUALPSA	"BIT0" DUMP ALL PSA'S IN SYSTEM
		.1..		SDUPSA	"BIT1" DUMP THE CURRENT PSA
		..1.		SDUNUC	"BIT2" DUMP THE NUCLEUS
		...1		SDUSQA	"BIT3" DUMP SQA
		.... 1...		SDULSQA	"BIT4" DUMP LSQA
		.... .1..		SDURGN	"BIT5" DUMP REGION (PRIVATE AREA)
		.... ..1.		SDULPA	"BIT6" DUMP ACTIVE LPA MOD. FOR RGN
		1		SDUTRT	"BIT7" DUMP TRACE TABLE / GTF BUFFERS
3	(3)	BITSTRING	1	SDUSDAT2	SECOND BYTE OF SDATA FLAGS
		1...		SDUCSA	"BIT0" DUMP CSA
		.1..		SDUSWA	"BIT1" DUMP SWA FOR REGION
		..1.		SDUSMDMP	"BIT2" SUMMARY DUMP REQUESTED
		...1		SDUNSMDP	"BIT3" DO NOT DUMP SUMMARY DUMP
		.... 1...		SDUNAPSA	"BIT4" DO NOT DUMP ALL PSA

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... .1..		SDUNASQA	"BIT5" DO NOT DUMP SQA
		.... ..1.		SDUALNUC	"BIT6" DUMP ALL NUCLEUS AREAS
		1		SDUDEFS	"BIT7" USE DEFAULTS
4	(4)	ADDRESS	4	SDUDCBAD	ADDRESS USER SUPPLIED DCB
8	(8)	ADDRESS	4	SDUSTORA	ADDRESS OF LISTA OR STORAGE RANGES
12	(C)	ADDRESS	4	SDUHDRAD	ADDRESS OF USER DATA
16	(10)	ADDRESS	4	SDUSRBAD (0)	ADDRESS OF USER SUPPLIED SRB
16	(10)	ADDRESS	4	SDUECBAD	ADDRESS USER SUPPLIED ECB
20	(14)	ADDRESS	4	SDUMASID (0)	SCHEDULE DUMP ASIDS
20	(14)	ADDRESS	2	SDUCASID	CALLERS ASID
22	(16)	ADDRESS	2	SDUTASID	TARGET ASID OF SCHEDULE DUMP
24	(18)	ADDRESS	4	SDUASIDP	ADDRESS CALLERS ASID LIST
28	(1C)	ADDRESS	4	SDUSUMLP	ADDRESS CALLERS SUMMARY LIST
32	(20)	CHARACTER	8	SDUTUSID (0)	TSO USER ID THIS DUMP
32	(20)	ADDRESS	4	SDUSDDAT	ADDRESS OF SVC DUMP DATA AREAS MAPPED BY IHASDDAT - SET ON SYSMDUMP PATH ONLY
36	(24)	ADDRESS	4	SDUTDDAT	Transaction dump data area addr. Mapped by IHATDDAT. Set for transaction dumps only
40	(28)	BITSTRING	1	SDUFLAG2	BYTE OF SDUMP CONTROL FLAGS
		1...		SDULISTA	"BIT0" 1=LISTA PARAMETER SPECIFIED 0=LISTA PARAMETER NOT SPECIFIED
		.1..		SDUSLSTA	"BIT1" 1=SUMLSTA PARAMETER SPECIFIED 0=SUMLSTA PARAMETER NOT SPECIFIED
		..1.		SDUSPEND	"BIT2" 1=SUSPEND=YES PARAMETER SPECIFIED 0=SUSPEND=NO OF PARAMETER LEFT OFF
		...1		SDUSUBPL	"BIT3" SPECIFIES PARAMETER SUBPLST POINTER IS IN FIELD SDUSPLST
		.... 1...		SDUKEYS	"BIT4" SPECIFIES PARAMETER KEYLIST POINTER IS IN FIELD SDUKYLST
		.... .1..		SDUAUTH	"BIT5" TRT REQUESTED FOR AN AUTHORIZED SYSMDUMP USER. SET BY ABDUMP
		Comment			
		BIT6 RESERVED FOR MVS/ESA BIT7 RESERVED FOR MVS/ESA			
		End of Comment			
41	(29)	BITSTRING	1	SDUCNTL1	CONTROL BYTE USED TO SPECIFY NEW RELEASES TO DETERMINE PARAMETER LIST LENGTH
		1...		SDUSP21	"BIT0" RELEASE HBB2102 PARAMETER LIST
		.1..		SDUVRSNB	"BIT1" 1=VERSION BIT PRESENT
		..1.		SDUPSWR	"BIT2" 1=PSWREGS SPECIFIED
		...1		SDUSYMR	"BIT3" 1=SYMREC SPECIFIED
		.... 1...		SDUID	"BIT4" 1=ID/IDAD SPECIFIED
		Comment			
		EQU BIT5 RESERVED FOR MVS/ESA			
		End of Comment			
		.... ..1.		SDUSTREQ	"BIT6" 1=STRLIST SPECIFIED
		1		SDUSRB	"BIT7" ON, SRB KEYWORD WAS SPECIFIED
42	(2A)	BITSTRING	1	SDUTYPE (0)	BYTE DESCRIBING TYPE PARAMETER
42	(2A)	BITSTRING	1	SDUTYP1	FIRST BYTE OF TYPE PARAMETERS
		1...		SDUTYPXM	"BIT0" 1=TYPE XMEM SPECIFIED 0=TYPE XMEM NOT SPECIFIED
		.1..		SDUTPXME	"BIT1" 1=TYPE XMEME SPECIFIED 0=TYPE XMEME NOT SPECIFIED
		..1.		SDUTPNOL	"BIT2" 1=TYPE NOLOCAL SPECIFIED 0=TYPE NOLOCAL NOT SPECIFIED
		...1		SDUTPFRC	"BIT3" 1=TYPE FAILRC SPECIFIED 0=TYPE FAILRC NOT SPECIFIED
43	(2B)	BITSTRING	1	SDUVERSN	VERSION NUMBER
44	(2C)	BITSTRING	4	SDUSDTA2 (0)	EXTENDED SDATA OPTIONS
44	(2C)	BITSTRING	2	SDUEXIT (0)	EXIT ROUTINE OPTIONS
44	(2C)	BITSTRING	1	SDUEDAT1	SDATA OPTIONS FOR EXIT ROUTINES
		1...		SDUGRSQ	"BIT0" 1=GRSQ SDATA OPTION WAS SPECIFIED
		.1..		SDUMSTRC	"BIT1" 1=MASTER TRACE OPTION AND GTF EXIT OPTION SPECIFIED
		..1.		SDUSMSX	"BIT2" 1=SMSX LOCAL EXIT
		...1		SDUCOUPL	"BIT3" 1=COUPLE - FOR SDUMPX ONLY
		.... 1...		SDUXES	"BIT4" 1=XESDATA - FOR SDUMPX ONLY
		.... .1..		SDUIOEX	"BIT5" 1=IOS GLOBAL EXIT WILL BE INVOKED
		.... ..1.		SDUWLM	"BIT6" 1=WLM SDATA OPTION WAS SPECIFIED
		1		SDURSM	"BIT7" 1=RSM SDATA OPTION WAS SPECIFIED
45	(2D)	BITSTRING	1	SDUEDAT2	2ND SDATA EXIT BYTE
		1...		SDUSLIP	"BIT0" 1=SLIP SDATA OPTION WAS SPECIFIED
		.1..		SDUOPENE	"BIT1" 1=OE SDATA OPTION WAS SPECIFIED
		..1.		SDUTSVCD	"BIT2" 1=TAILORED SVC DUMP WAS SPECIFIED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
46	(2E)	BITSTRING	1	SDUSDAT3	MORE SDATA OPTIONS
		1...		SDUNODEF	"BIT0" 1=NODEFAULTS
		.1..		SDUIO	"BIT1" 1=I/O AREAS
47	(2F)	BITSTRING	1	SDUSDAT4	MORE SDATA OPTIONS
48	(30)	ADDRESS	4	SDUSPLST	ADDRESS OF SUBPOOL LIST
52	(34)	ADDRESS	4	SDUKYLST	ADDRESS OF KEYLIST
56	(38)	ADDRESS	4	SDURGPSA	ADDRESS OF SLIP REGS/PSW FOR THE DUMP HEADER RECORD
60	(3C)	ADDRESS	4	(2)	RESERVED

SDUMP Cross Reference

Name	Hex Offset	Hex Value
BIT0	0	80
BIT1	0	40
BIT2	0	20
BIT3	0	10
BIT4	0	8
BIT5	0	4
BIT6	0	2
BIT7	0	1
DUMPTYPE	1	80
SDUABEND	1	40
SDUALNUC	3	2
SDUALPSA	2	80
SDUASID	0	4
SDUASIDP	18	
SDUASLST	1	10
SDUAUTH	28	4
SDUBRANH	0	1
SDUBUF	0	40
SDUCASID	14	
SDUCNTL1	29	
SDUCOUPL	2C	10
SDUCSA	3	80
SDUDCB	0	80
SDUDCBAD	4	
SDUDEFS	3	1
SDUDTYPE	1	80
SDUECB	0	8
SDUECBAD	10	
SDUEDAT1	2C	
SDUEDAT2	2D	
SDUEXIT	2C	
SDUFLAG0	0	
SDUFLAG1	1	
SDUFLAG2	28	
SDUGRSQ	2C	80
SDUHDR	0	10
SDUHDRAD	C	
SDUID	29	8
SDUIGNCD	1	4
SDUIO	2E	40
SDUIOEX	2C	4
SDUKEYS	28	8
SDUKYLST	34	
SDULISTA	28	80
SDULPA	2	2
SDULSQA	2	8
SDUMASID	14	
SDUMP	0	
SDUMSTRC	2C	40
SDUNAPSA	3	8
SDUNASQA	3	4
SDUNEW	1	20
SDUNODEF	2E	80
SDUNSMDP	3	10
SDUNUC	2	20
SDUOPENE	2D	40
SDUPSA	2	40
SDUPSWR	29	20
SDUQUIET	0	2
SDURGN	2	4
SDURGPSA	38	
SDURSM	2C	1
SDUSDATA	2	
SDUSDAT1	2	
SDUSDAT2	3	
SDUSDAT3	2E	
SDUSDAT4	2F	
SDUSDDAT	20	
SDUSDTA2	2C	
SDUSE3P	1	1
SDUSLIP	2D	80
SDUSLSTA	28	40
SDUSMDMP	3	20
SDUSMSX	2C	20
SDUSPEND	28	20
SDUSPLST	30	
SDUSP21	29	80
SDUSQA	2	10
SDUSRB	29	1
SDUSRBAD	10	
SDUSTOR	0	20
SDUSTORA	8	
SDUSTREQ	29	2
SDUSUBPL	28	10
SDUSULST	1	8
SDUSUMLP	1C	
SDUSWA	3	40
SDUSYMR	29	10
SDUTASID	16	
SDUTDDAT	24	
SDUTPFRC	2A	10
SDUTPNOL	2A	20
SDUTPXME	2A	40
SDUTRT	2	1
SDUTSOXT	1	2
SDUTSVCD	2D	20
SDUTUSID	20	
SDUTYPE	2A	
SDUTYPXM	2A	80
SDUTYP1	2A	
SDUVERSN	2B	
SDUVRSNB	29	40
SDUWLM	2C	2
SDUXES	2C	8

SDWA Information

SDWA Programming Interface information

Programming Interface information

SDWA

ONLY the following fields are part of the programming interface information:

- SDWAABCC
- SDWAABTM
- SDWAAEC1
- SDWAAEC2
- SDWAARER
- SDWAARGU
- SDWAARSV
- SDWAASCB
- SDWACID
- SDWACIDB
- SDWACLUP
- SDWACOMP
- SDWACOMU
- SDWACRC
- SDWACRGS
- SDWACTS
- SDWAEC1
- SDWAEC2
- SDWAEPA
- SDWAERRB
- SDWAERRC
- SDWAFIOB
- SDWAFMID
- SDWAFPRX
- SDWAGRSV
- SDWAG64
- SDWAIDNT
- SDWAIOFS
- SDWALSLV
- SDWAMABD
- SDWAMCHK
- SDWAMCIV
- SDWAMLVL
- SDWANAME
- SDWANRBE
- SDWAOCRC
- SDWAPARM
- SDWAPCHK
- SDWAPGIO
- SDWAPSWU
- SDWAPSW16
- SDWARA
- SDWAREGU
- SDWARELEASECODE
- SDWARFXM
- SDWARKEY
- SDWARPIV
- SDWARRL
- SDWARTAM
- SDWASABC
- SDWASC
- SDWASDRC
- SDWASFLG
- SDWASRSV
- SDWASRVP
- SDWASTAE
- SDWASVCD
- SDWASVCE
- SDWATEAR
- SDWATEXC
- SDWATYPE
- SDWAVRIV
- SDWAXFLG
- SDWAXPAD

End of Programming Interface information

SDWA Heading Information

Common Name: SYSTEM DIAGNOSTIC WORK AREA
Macro ID: IHASDWA
DSECT Name: SDWA, SDWARC1, SDWARC2, SDWARC3, SDWARC4, SDWARC5, SDWAPTS, SDWANRC1, SDWANRC2, SDWANRC3
Owning Component: RECOVERY TERMINATION MANAGER (SCRTM)
Eye-Catcher ID: SDWA
Offset: X'293'
Length: 5
Storage Attributes: Subpool: 0 OR 230 OR 239
Key: TCB KEY FOR SUBPOOL 0, OTHERWISE KEY 0
Residency: Above or below the 16M line, depending on the recovery routine that the SDWA is provided for
Size: Variable, depending on which which extensions are provided with the SDWA.
Created by: GLOBAL SDWAS ARE PREALLOCATED, GETMAINED SDWAS ARE OBTAINED BY IEAVTR1S, TASK MODE SDWAS ARE OBTAINED BY IEAVTRT2 AND IEAVTAS1
Pointed to by: REGISTER 1 UPON ENTRY TO AN FRR, REGISTER 1 UPON ENTRY TO AN ESTAE-TYPE RECOVERY ROUTINE IF REGISTER 0 DOES NOT CONTAIN 12 (X'0C').
ADJACENT TO EACH SUPER FRR STACK (GLOBAL SDWA)
RT1WRTCA FIELD OF THE RT1W DATA AREA
RT1WSD24 FIELD OF THE RT1W DATA AREA
RT1WSD31 FIELD OF THE RT1W DATA AREA
RTM2RTCA AND RTM2SDW2 FIELD OF THE RTM2WA DATA AREA FOR TASK MODE SDWAS
Serialization: GLOBAL SDWA - PHYSICALLY DISABLED OR GLOBALLY LOCKED
GETMAINED SDWA - SRB MODE, LOCALLY LOCKED, OR NONE
Function: THE SDWA PROVIDES FOR COMMUNICATION BETWEEN THE RTM AND FRRS OR ESTAE-TYPE RECOVERY ROUTINES. IT CONTAINS DATA ABOUT THE ORIGINAL ERROR AND ALSO CONCERNING ACTIONS OF PREVIOUSLY ENTERED RECOVERY ROUTINES. THE SDWA IS ALSO KNOWN AS THE RTCA.
THE SDWA IS USED FOR RECORDING ERRORS IN LOGREC AS SOFTWARE RECORDS.

SDWA Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWA	, SDWAPTR
0	(0)	ADDRESS	4	SDWAPARM	- PARAMETER LIST ADDRESS IF (E)STAE MACRO SPECIFIED PARAM OPTION OR 0. FOR FRRS THIS IS THE ADDRESS OF THE 6 WORD PARM AREA RETURNED BY THE SETFRR MACRO WHEN THE PARMAD KEYWORD IS SPECIFIED ON THE SETFRR FOR AN ARR, THIS IS THE ADDRESS OF THE 8-BYTES OF THE MSTA AREA. FOR ESTAEX, THIS IS THE ADDRESS OF 8-BYTES CONTAINING THE ADDRESS AND ALET OF THE PARAMETER LIST. For a recovery routine that gets control in AMODE 64, it is the address of 8-bytes that contains the 64-bit address of the parameter list.
4	(4)	ADDRESS	4	SDWAFIOB (0)	- ADDRESS OF PURGE I/O REQUEST LIST (PIRL) OR 0 IF HALT I/O IS REQUESTED ON ENTRY TO RETRY ROUTINE FOR (E)STAE.
4	(4)	BITSTRING	4	SDWAABCC (0)	- ABEND COMPLETION CODE ON ENTRY TO EXIT ROUTINE.
4	(4)	BITSTRING	1	SDWACMPF	- FLAG BITS IN COMPLETION CODE.
		1...		SDWAREQ	"X'80'" - ON, SYSABEND/SYSMDUMP/SYSUDUMP DUMP TO BE GIVEN. SET IF DUMP=YES REQUESTED ON ABEND, CALLRTM OR SETRP MACRO.
		.1..		SDWASTEP	"X'40'" - ON, JOBSTEP TO BE TERMINATED. SET IF STEP OPTION SPECIFIED ON ABEND MACRO.
		...1		SDWASTCC	"X'10'" - ON, DON'T STORE COMPLETION CODE. NOT USED IN OS/VS2 R2.
		.... .1..		SDWARCF	"X'04'" - ON, REASON CODE IN SDWACRC IS VALID
5	(5)	BITSTRING	3	SDWACMPC	- SYSTEM COMPLETION CODE (FIRST 12 BITS) AND USER COMPLETION CODE (SECOND 12 BITS).
8	(8)	CHARACTER	8	SDWACTL1 (0)	- BC MODE PSW AT TIME OF ERROR NOT INITIALIZED FOR FRRS.
8	(8)	BITSTRING	1	SDWACMKA	- CHANNEL INTERRUPT MASKS.
		1111 111.		SDWAIOA	"X'FE'" - I/O INTERRUPTS (ALL ZEROS OR ALL ONES).
		1		SDWAEXTA	"X'01'" - EXTERNAL INTERRUPT.
9	(9)	BITSTRING	1	SDWAMWPA	- PSW KEY AND 'M-W-P'.
		1111		SDWAKEYA	"X'F0'" - PSW KEY.
		.... .1..		SDWAMCKA	"X'04'" - MACHINE CHECK INTERRUPT.
		.... ..1.		SDWAWATA	"X'02'" - WAIT STATE.
		1		SDWASPVA	"X'01'" - SUPERVISOR/PROBLEM-PROGRAM MODE.
10	(A)	CHARACTER	2	SDWAINTA	- INTERRUPT CODE (LAST 2 BYTES OF INTERRUPT CODE IF I/O INTERRUPT).
12	(C)	BITSTRING	1	SDWAPMKA	- INSTRUCTION LENGTH CODE, CONDITION CODE, AND PROGRAM MASKS.
		11..		SDWAILA	"X'C0'" - INSTRUCTION LENGTH CODE.

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		..11		SDWACCA	"X'30'" - LAST CONDITION CODE.
		.... 1...		SDWAFPA	"X'08'" - FIXED-POINT OVERFLOW.
		.... .1..		SDWADOA	"X'04'" - DECIMAL OVERFLOW.
		.... ..1.		SDWAEUA	"X'02'" - EXPONENT UNDERFLOW.
		1		SDWASGA	"X'01'" - SIGNIFICANCE.
13	(D)	ADDRESS	3	SDWANXTA	- ADDRESS OF NEXT INSTRUCTION TO BE EXECUTED.
16	(10)	CHARACTER	8	SDWACTL2 (0)	- BC MODE PSW FROM LAST PRB ON RB CHAIN. ZERO FOR FRRS.
16	(10)	BITSTRING	1	SDWACMKP	- CHANNEL INTERRUPT MASKS.
		1111 111.		SDWAIOP	"X'FE'" - I/O INTERRUPTS (ALL ZEROS OR ALL ONES).
		1		SDWAEXTP	"X'01'" - EXTERNAL INTERRUPT.
17	(11)	BITSTRING	1	SDWAMWPP	- PSW KEY AND 'M-W-P'.
		1111		SDWAKEYP	"X'F0'" - PSW KEY.
		.... .1..		SDWAMCKP	"X'04'" - MACHINE CHECK INTERRUPT.
		.... ..1.		SDWAWATP	"X'02'" - WAIT STATE.
		1		SDWASPVP	"X'01'" - SUPERVISOR/PROBLEM-PROGRAM MODE.
18	(12)	CHARACTER	2	SDWAINTP	- INTERRUPT CODE (LAST 2 BYTES OF INTERRUPT CODE IF I/O INTERRUPT).
20	(14)	BITSTRING	1	SDWAPMKP	- INSTRUCTION LENGTH CODE, CONDITION CODE, AND PROGRAM MASKS.
		11..		SDWAILP	"X'C0'" - INSTRUCTION LENGTH CODE.
		..11		SDWACCP	"X'30'" - LAST CONDITION CODE.
		.... 1...		SDWAFPP	"X'08'" - FIXED-POINT OVERFLOW.
		.... .1..		SDWADOP	"X'04'" - DECIMAL OVERFLOW.
		.... ..1.		SDWAEUP	"X'02'" - EXPONENT UNDERFLOW.
		1		SDWASGP	"X'01'" - SIGNIFICANCE.
21	(15)	ADDRESS	3	SDWANXTP	- ADDRESS OF NEXT INSTRUCTION TO BE EXECUTED.
24	(18)	CHARACTER	64	SDWAGRSV (0)	- General purpose registers at time of error. When the error was a program interrupt within transactional execution, these are the regs at the time of the program interrupt within the transaction.
24	(18)	SIGNED	4	SDWAGR00	- GPR 0.
28	(1C)	SIGNED	4	SDWAGR01	- GPR 1.
32	(20)	SIGNED	4	SDWAGR02	- GPR 2.
36	(24)	SIGNED	4	SDWAGR03	- GPR 3.
40	(28)	SIGNED	4	SDWAGR04	- GPR 4.
44	(2C)	SIGNED	4	SDWAGR05	- GPR 5.
48	(30)	SIGNED	4	SDWAGR06	- GPR 6.
52	(34)	SIGNED	4	SDWAGR07	- GPR 7.
56	(38)	SIGNED	4	SDWAGR08	- GPR 8.
60	(3C)	SIGNED	4	SDWAGR09	- GPR 9.
64	(40)	SIGNED	4	SDWAGR10	- GPR 10.
68	(44)	SIGNED	4	SDWAGR11	- GPR 11.
72	(48)	SIGNED	4	SDWAGR12	- GPR 12.
76	(4C)	SIGNED	4	SDWAGR13	- GPR 13.
80	(50)	SIGNED	4	SDWAGR14	- GPR 14.
84	(54)	SIGNED	4	SDWAGR15	- GPR 15.
88	(58)	CHARACTER	8	SDWANAME (0)	- IF PROBLEM PROGRAM MODE NAME OF ABENDING PROGRAM, OR ZERO IF NO NAME IS AVAILABLE. ZERO IF NOT RUNNING UNDER AN RB
88	(58)	ADDRESS	4	SDWARBAD	- RB ADDRESS OF ABENDING PROGRAM (IF SUPERVISOR MODE PROGRAM RUNNING UNDER AN RB)
92	(5C)	BITSTRING	4		- CONTAINS ZEROS IF SUPERVISOR MODE PROGRAM RUNNING UNDER AN RB OR IF PROGRAM NOT RUNNING UNDER AN RB
96	(60)	ADDRESS	4	SDWAEPA	- ENTRY POINT ADDRESS OF ABENDING PROGRAM. ZERO IF NOT RUNNING UNDER AN RB
100	(64)	ADDRESS	4	SDWAIOBR	- POINTER TO SDWAFIOB FIELD, OR 0 IF NO RETRY, OR 0 IF HALT I/O IS REQUESTED FOR (E)STA EXITS. ZERO FOR FRRS ICB377
104	(68)	CHARACTER	8	SDWAEC1 (0)	Extended control PSW at time of error(abend). When the error was a program interrupt within transactional execution, this is the PSW at the time of the program interrupt within the transaction.
104	(68)	BITSTRING	1	SDWAEMK1	INTERRUPT INFORMATION MASKS
		.1..		SDWAPER1	"X'40'" ON,PROGRAM EVENT RECORDING INTERRUPTS CAN OCCUR OFF, PROGRAM EVENT RECORDING INTERRUPTS CANNOT OCCUR
		.... .1..		SDWATRM1	"X'04'" ON,ADDRESS TRANSLATION ACTIVE
		.... ..1.		SDWAIO1	"X'02'" OFF,I/0 INTERRUPTION CAN NOT OCCUR ON,I/0 INTERRUPTIONS CAN OCCUR SUBJECT TO TO CHANNEL MASK BITS IN CONTROL REGS 2 AND 3
		1		SDWAEXT1	"X'01'" OFF,EXTERNAL INTERRUPTION CANNOT OCCUR ON,EXTERNAL INTERRUPTIONS CAN OCCUR SUBJECT TO EXTERNAL SUBCLASS MASK BITS OF CONTROL REG 0
105	(69)	BITSTRING	1	SDWAMWP1	PSW KEY AND 'M-W-P'
		1111		SDWAKEY1	"X'F0'" PSW KEY
		.... 1...		SDWAECT1	"X'08'" EXTENDED CONTROL MODE BIT

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... .1..		SDWAMCK1	"X'04'" OFF,MACHINE CHECK CANNOT OCCUR ON,MACHINE CHECK DUE TO SYSTEM DAMAGE AND INSTRUCTION-PROCESSING DAMAGE CAN OCCUR OTHER MACHINE CHECKS SUBJECT TO MASK BITS IN CONTROL REGISTER 14
		.... ..1.		SDWAWAT1	"X'02'" ON,CPU IN WAIT STATE
		1		SDWAPGM1	"X'01'" ON,PROBLEM STATE OFF, SUPERVISOR STATE
106	(6A)	BITSTRING	1	SDWAINT1	CONDITION CODE AND PROGRAM MASK
		11..		SDWAASCM	"X'C0'" ADDRESS SPACE CONTROL MODE BITS 00 - PRIMARY MODE 01 - ACCESS REGISTER MODE 10 - SECONDARY MODE 11 - HOME SPACE MODE
		1...		SDWAS1	"X'80'" ADDRESS SPACE SELECTION BIT
		..11		SDWACC1	"X'30'" CONDITION CODE
		.... 1...		SDWAFPO1	"X'08'" FIXED POINT OVERFLOW
		.... .1..		SDWADEC1	"X'04'" DECIMAL OVERFLOW
		.... ..1.		SDWAEXP1	"X'02'" EXPONENT UNDERFLOW
		1		SDWASGN1	"X'01'" SIGNIFICANCE
107	(6B)	BITSTRING	1		RESERVED
108	(6C)	SIGNED	4	SDWANXT1 (0)	ADDRESS OF NEXT INSTRUCTION TO BE EXECUTED. Bit 31 could be on due to - bad address - address in SDWAPSW16 above 2G (note that an address above 2G will not always result in Bit 31 being turned on)
108	(6C)	BITSTRING	1	SDWAAMF1	ADDRESSING MODE FLAG
		1...		SDWAMOD1	"X'80'" ADDRESSING MODE OF THE NEXT INSTRUCTION TO BE EXECUTED.
109	(6D)	CHARACTER	3	SDWAADD1	INSTRUCTION ADDRESS
112	(70)	CHARACTER	8	SDWAAEC1 (0)	ADDITIONAL EC MODE INFORMATION
112	(70)	CHARACTER	1		RESERVED
113	(71)	BITSTRING	1	SDWAILC1	INSTRUCTION LENGTH CODE FOR PSW DEFINED BY SDWAEC1/SDWAPSW16
		.... .11.		SDWAIL1	"X'06'" ILC
114	(72)	CHARACTER	2	SDWAINC1 (0)	INTERRUPT CODE. IF PROGRAM CHECK OCCURRED THE SUBFIELDS ARE FURTHER DIVIDED
114	(72)	BITSTRING	1	SDWAIC1H	High byte of PI code
		.... ..1.		SDWAPTX1	"X'02'" Program interrupt during transactional execution
115	(73)	BITSTRING	1	SDWAICD1	8 BIT INTERRUPT CODE IF PROGRAM CHECK
		1...		SDWAIPR1	"X'80'" PER INTERRUPT OCCURRED
		.1..		SDWAIMC1	"X'40'" MONITOR CALL INTERRUPT OCCURRED
		..11 1111		SDWAIPC1	"X'3F'" AN UNSOLICITED PROGRAM CHECK HAS OCCURRED
116	(74)	ADDRESS	4	SDWATRAN	VIRTUAL ADDRESS CAUSING TRANSLATION EXCEPTION
119	(77)	BITSTRING	1	SDWADXC	Data exception code when program interrupt code 7
120	(78)	CHARACTER	8	SDWAEC2 (0)	EXTENDED CONTROL PSW FROM THE RB LEVEL OR LINKAGE STACK LEVEL WHICH CREATED THE ESTAE EXIT AT THE TIME IT LAST INCURRED AN INTERRUPT OR 0 FOR ESTAI. OR PSW USED TO GIVE FRR CONTROL
120	(78)	BITSTRING	1	SDWAEMK2	INTERRUPT INFORMATION MASKS
		.1..		SDWAPER2	"X'40'" ON,PROGRAM EVENT RECORDING INTERRUPTS CAN OCCUR OFF,PROGRAM EVENT RECORDING INTERRUPTS CANNOT OCCUR
		.... .1..		SDWATRM2	"X'04'" ON,ADDRESS TRANSLATION ACTIVE
		.... ..1.		SDWAIO2	"X'02'" OFF,I/0 INTERRUPTION CANNOT OCCUR ON,I/0 INTERRUPTIONS CAN OCCUR SUBJECT TO TO CHANNEL MASK BITS IN CONTROL REGS 2 AND 3
		1		SDWAEXT2	"X'01'" OFF,EXTERNAL INTERRUPTION CANNOT OCCUR ON,EXTERNAL INTERRUPTIONS CAN OCCUR SUBJECT TO EXTERNAL SUBCLASS MASK BITS OF CONTROL REG 0
121	(79)	BITSTRING	1	SDWAMWP2	PSW KEY AND 'M-W-P'
		1111		SDWAKEY2	"X'F0'" PSW KEY
		.... 1...		SDWAECT2	"X'08'" EXTENDED CONTROL MODE BIT
		.... .1..		SDWAMCK2	"X'04'" OFF,MACHINE CHECK CANNOT OCCUR ON,MACHINE CHECK DUE TO SYSTEM DAMAGE AND INSTRUCTION-PROCESSING DAMAGE CAN OCCUR OTHER MACHINE CHECKS SUBJECT TO MASK BITS IN CONTROL REGISTER 14
		.... ..1.		SDWAWAT2	"X'02'" ON,CPU IN WAIT STATE
		1		SDWAPGM2	"X'01'" ON,PROBLEM STATE OFF, SUPERVISOR STATE
122	(7A)	BITSTRING	1	SDWAINT2	CONDITION CODE AND PROGRAM MASK
		1...		SDWAS2	"X'80'" ADDRESS SPACE SELECTION BIT
		..11		SDWACC2	"X'30'" CONDITION CODE
		.... 1...		SDWAFPO2	"X'08'" FIXED POINT OVERFLOW
		.... .1..		SDWADEC2	"X'04'" DECIMAL OVERFLOW
		.... ..1.		SDWAEXP2	"X'02'" EXPONENT UNDERFLOW
		1		SDWASGN2	"X'01'" SIGNIFICANCE
123	(7B)	BITSTRING	1		RESERVED
124	(7C)	SIGNED	4	SDWANXT2 (0)	ADDRESS OF NEXT INSTRUCTION TO BE EXECUTED Bit 31 could be on due to - bad address - address in SDWAPSW16 above 2G (note that an address above 2G will not always result in Bit 31 being turned on)
124	(7C)	BITSTRING	1	SDWAAMF2	ADDRESSING MODE FLAG

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1...		SDWAMOD2	"X'80'" ADDRESSING MODE OF THE NEXT INSTRUCTION TO BE EXECUTED.
125	(7D)	CHARACTER	3	SDWAADD2	INSTRUCTION ADDRESS
128	(80)	CHARACTER	8	SDWAAEC2 (0)	ADDITIONAL EC MODE INFORMATION
128	(80)	CHARACTER	1		RESERVED
129	(81)	BITSTRING	1	SDWAILC2	INSTRUCTION LENGTH CODE FOR PSW DEFINED BY SDWAEC2
		.... .11.		SDWAIL2	"X'06'" ILC
130	(82)	CHARACTER	2	SDWAINC2 (0)	INTERRUPT CODE. IF PROGRAM CHECK OCCURRED THE SUBFIELDS ARE FURTHER DIVIDED
130	(82)	BITSTRING	1	SDWAIC2H	High byte of PI code
		.... ..1.		SDWAPTX2	"X'02'" Program interrupt during transactional execution
131	(83)	BITSTRING	1	SDWAICD2	8 BIT INTERRUPT CODE IF PROGRAM CHECK
		1...		SDWAIPR2	"X'80'" PER INTERRUPT OCCURRED
		.1..		SDWAIMC2	"X'40'" MONITOR CALL INTERRRUPT OCCURRED
		..11 1111		SDWAIPC2	"X'3F'" AN UNSOLICITED PROGRAM CHECK HAS OCCURRED
132	(84)	ADDRESS	4	SDWATRN2	VIRTUAL ADDRESS CAUSING TRANSLATION EXCEPTION
136	(88)	CHARACTER	64	SDWASRSV (0)	GENERAL PURPOSE REGISTERS OF THE RB LEVEL OR LINKAGE STACK LEVEL WHICH CREATED THE ESTAE EXIT AT THE TIME IT LAST INCURRED AN INTERRUPT OR 0 FOR ESTAI FOR FRRS INITIALIZED TO REGISTERS AT TIME OF ERROR. THIS REGISTER AREA IS USED TO UPDATE REGISTER CONTENTS FOR RETRY IF REQUESTED
136	(88)	SIGNED	4	SDWASR00	GPR 0.
140	(8C)	SIGNED	4	SDWASR01	GPR 1.
144	(90)	SIGNED	4	SDWASR02	GPR 2.
148	(94)	SIGNED	4	SDWASR03	GPR 3.
152	(98)	SIGNED	4	SDWASR04	GPR 4.
156	(9C)	SIGNED	4	SDWASR05	GPR 5.
160	(A0)	SIGNED	4	SDWASR06	GPR 6.
164	(A4)	SIGNED	4	SDWASR07	GPR 7.
168	(A8)	SIGNED	4	SDWASR08	GPR 8.
172	(AC)	SIGNED	4	SDWASR09	GPR 9.
176	(B0)	SIGNED	4	SDWASR10	GPR 10.
180	(B4)	SIGNED	4	SDWASR11	GPR 11.
184	(B8)	SIGNED	4	SDWASR12	GPR 12.
188	(BC)	SIGNED	4	SDWASR13	GPR 13.
192	(C0)	SIGNED	4	SDWASR14	GPR 14.
196	(C4)	SIGNED	4	SDWASR15	GPR 15.
200	(C8)	CHARACTER	4	SDWAIDNT (0)	SDWA IDENTIFICATION ATTRIBUTES
200	(C8)	CHARACTER	1	SDWASPID	SUBPOOL ID OF STORAGE CONTAINING THIS SDWA
201	(C9)	CHARACTER	3	SDWALNTH	LENGTH OF THIS SDWA IN BYTES
204	(CC)	CHARACTER	28	SDWAMCH (0)	CONTAINS MACHINE CHECK DATA IF SDWAMCHK IS ON
204	(CC)	CHARACTER	8	SDWASTCK (0)	BEGINNING AND ENDING STORAGE CHECK ADDRESSES. FILLED IN DUE TO STORAGE ERROR (SDWASCK) OR A KEY FAILURE (SDWASKYF). THESE ADDRESSES ARE VALID ONLY IF INDICATED BY THE SDWASRVL FLAG.
204	(CC)	ADDRESS	4	SDWASCKB	BEGINNING VIRTUAL ADDRESS OF STORAGE CHECK
208	(D0)	ADDRESS	4	SDWASCKE	ENDING VIRTUAL ADDRESS OF STORAGE CHECK
212	(D4)	BITSTRING	2	SDWAMCHI (0)	ADDITIONAL MCH INFORMATION FLAGS
212	(D4)	BITSTRING	1	SDWAMCHS	MCH FLAG BYTE
		1...		SDWASRVL	"X'80'" ON,STORAGE ADDRESSES SUPPLIED (SDWASTCK, SDWARFSA) ARE VALID. On z/Architecture system, SDWARFSA contains only the low word of the FSA. SDWARFSE contains the 8-byte FSA.
		.1..		SDWARCDF	"X'40'" ON,MACHINE CHECK RECORD NOT RECORDED
		..1.		SDWATSVL	"X'20'" ON,TIME STAMP IS VALID
		...1		SDWAINVP	"X'10'" ON,STORAGE IS RECONFIGURED, PAGE IS INVALIDATED
		.... 1...		SDWARSRC	"X'08'" ON,STORAGE RECONFIGURATION (SDWARSR1,SDWARSR2) STATUS AVAILABLE.
		.... .1..		SDWARSRF	"X'04'" ON,STORAGE RECONFIGURATION NOT ATTEMPTED. (SDWARSR1 AND SDWARSR2 ARE INVALID)
		.... ..1.		SDWAVRIV	"X'02'" ON, INDICATES VECTOR REGISTERS ARE UNPREDICTABLE
		1		SDWAARGU	"X'01'" ON, INDICATES ACCESS REGISTERS ARE UNPREDICTABLE
213	(D5)	BITSTRING	1	SDWAMCHD	INPUT INFORMATION TO RECOVERY ROUTINE CONCERNING A MACHINE CHECK ERROR
		1...		SDWASKYF	"X'80'" ON,STORAGE KEY FAILURE
		.1..		SDWAREGU	"X'40'" ON,GENERAL PURPOSE REGISTER CONTENTS AT TIME OF MACHINE CHECK UNPREDICTABLE
		..1.		SDWAPSWU	"X'20'" ON,PSW AND/OR CONTROL REGISTERS AT TIME OF MACHINE CHECK UNPREDICTABLE
		...1		SDWASCK	"X'10'" ON,INDICATES STORAGE DATA CHECK
		.... 1...		SDWAACR	"X'08'" ON,INDICATES ACR REQUEST
		.... .1..		SDWAINSF	"X'04'" ON,INSTRUCTION FAILURE
		.... ..1.		SDWAFPRX	"X'02'" ON,CONTENTS OF FLOATING POINT REGISTERS AT TIME MACHINE CHECK ARE UNPREDICTABLE

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		1		SDWATERR	"X'01'" ON,TIMER ERROR - CAUSES ENTRY TO RECOVERY ROUTINES ONLY IF LOGOUT FAILED.
214	(D6)	CHARACTER	2	SDWACPID	ID OF OF FAILING CPU CAUSING ACR
216	(D8)	BITSTRING	1	SDWARSR1	ADDITIONAL STORAGE FRAME ERROR INDICATORS AS RETURNED FROM REAL STORAGE RECONFIGURATION.
		..1.		SDWAPREF	"X'20'" PREFERRED FRAME
		...1		SDWAVRCN	"X'10'" V = R CANDIDATE - CAN GO OFFLINE
		.... 1...		SDWANSWP	"X'08'" LONG-TERM NON-SWAPPABLE ADDRESS SPACE
		.... .1..		SDWANSWA	"X'04'" NON-SWAPPABLE ADDRESS SPACE
		.... ..1.		SDWAMSER	"X'02'" STORAGE ERROR ALREADY SET IN FRAME.
		1		SDWACHNG	"X'01'" CHANGE INDICATOR WAS ON IN FRAME.
217	(D9)	BITSTRING	1	SDWARSR2	ADDITIONAL STORAGE ERROR INDICATORS.
		1...		SDWAOFLN	"X'80'" FRAME OFFLINE OR SCHEDULED TO GO OFFLINE IF SDWAINTC IS ON
		.1..		SDWAINTC	"X'40'" INTERCEPT THE FRAME IS SCHEDULED TO GO OFFLINE, OR THE FRAME HAS INCURRED A STORAGE ERROR, OR IS V=R INTERCEPTED
		..1.		SDWASPER	"X'20'" STORAGE ERROR PERMANENT ON FRAME.
		...1		SDWANUCL	"X'10'" FRAME CONTAINS PERMANENT RESIDENT STORAGE, I.E. NUCLEUS.
		.... 1...		SDWAFSQA	"X'08'" FRAME IN SQA
		.... .1..		SDWAFLSQ	"X'04'" FRAME IN LSQA
		.... ..1.		SDWAPGFX	"X'02'" FRAME IS PAGE FIXED
		1		SDWAVEQR	"X'01'" FRAME IS VIRTUAL = REAL
218	(DA)	BITSTRING	1	SDWAMCHO	OTHER MACHINE CHECK FLAGS
		1...		SDWASKPR	"X'80'" SKIP RECORDING REQUESTED BY MACHINE CHECK
219	(DB)	CHARACTER	1		RESERVED
220	(DC)	ADDRESS	4	SDWARFSA	REAL STORAGE FAILING ADDRESS (VALID ONLY IF INDICATED BY SDWASRVL)
224	(E0)	CHARACTER	8	SDWATIME	TIME STAMP OF ASSOCIATED MACHINE CHECK RECORD
232	(E8)	BITSTRING	4	SDWAFLGS (0)	INPUT FLAGS DESCRIBING REASONS AND CONDITIONS FOR ENTERING A RECOVERY EXIT ROUTINE.
232	(E8)	BITSTRING	1	SDWAERRA	ERROR TYPE CAUSING ENTRY TO RECOVERY EXIT
		1...		SDWAMCHK	"X'80'" ON INDICATES MACHINE CHECK
		.1..		SDWAPCHK	"X'40'" ON INDICATES PROGRAM CHECK
		..1.		SDWARKEY	"X'20'" ON INDICATES CONSOLE RESTART KEY WAS DEPRESSED
		...1		SDWASVCD	"X'10'" ON INDICATES TASK ISSUED SVC 13
		.... 1...		SDWAABTM	"X'08'" ON INDICATES SYSTEM FORCED SVC 13(I.E.ABTERM)
		.... .1..		SDWASVCE	"X'04'" ON,INDICATES AN SVC WAS ISSUED BY A LOCKED OR SRB ROUTINE
		.... ..1.		SDWATEXC	"X'02'" ON,INDICATES AN UNRECOVERABLE TRANSLATION FAILURE
		1		SDWAPGIO	"X'01'" ON,INDICATES A PAGE I/O ERROR
		1		SDWASTRM	"X'01'" ON,INDICATES AN RTM1 SERVICE ROUTINE (SUCH AS IEAVTSR1 PROCESSING ITERM OR IEAVTRTM PROCESSING STERM) SCHEDULED RTM1 TO CONTINUE PROCESSING AS AN SVC ERROR (BY PUTTING SVC 13 IN THE PSW TO BE DISPATCHED).
233	(E9)	BITSTRING	1	SDWAERRB	ADDITIONAL ERROR ENTRY INFORMATION
		1...		SDWAPDIP	"X'80'" ON INDICATES THAT THIS TASK WAS PARALLEL DETACHED - RECOVERY ROUTINES FOR OTHER TASKS IN THIS ADDRESS SPACE MAY BE EXECUTING AT THE SAME TIME AS THIS RECOVERY ROUTINE
		.1..		SDWANMFS	"X'40'" Not My Fault Summary -- indicates that this abend was received asynchronously (from an external source). SDWANMFS may be examined as an alternative to checking individual abend codes when deciding whether to capture failure documentation or retry since when it is on the abend generally will not have been the fault of the program that received it. SDWANMFS is available to Estae-type recovery and EUT FRRs running under TCBs. It is set when any of the following abend indicators have been set: SDWAABTM - ABTERM indicator (note that Cancel and Detach are always ABTERMs) SDWAMABD - This TCB has been detached by RTM after its mother task abended SDWASRBM - An SRB has abended and percolated to this TCB SDWAIRB - An IRB has interrupted this TCB and abended SDWAMCHK - A Machine Check has occurred SDWARKEY - A Restart was received SDWACTS - An abend was Converted To Step
		..1.		SDWASRBT	"X'20'" On, indicates that this abend was issued via CALLRTM TYPE=SRBTERM
		...1		SDWASRBS	"X'10'" On - this SDWA was allocated for an SRB Off - this SDWA was allocated for a TCB SDWASRBS is only valid when SDWASVAL is on
		.... 1...		SDWATYP1	"X'08'" ON TYPE 1 SVC IN CONTROL AT TIME OF ERROR
		.... .1..		SDWAENRB	"X'04'" ON ENABLED RB IN CONTROL AT TIME OF ERROR
		.... ..1.		SDWALDIS	"X'02'" ON A LOGICALLY OR PHYSICALLY DISABLED ROUTINE WAS IN CONTROL AT THE TIME OF ERROR.
		1		SDWASRBM	"X'01'" ON SYSTEM IN SRB MODE AT TIME OF ERROR
234	(EA)	BITSTRING	1	SDWAERRC	ADDITIONAL ERROR ENTRY INFORMATION
		1...		SDWASTAF	"X'80'" ON INDICATES A PREVIOUS (E)STA OR FRR EXIT FAILED.
		.1..		SDWASTAI	"X'40'" ON A (E)STAI EXIT PREVIOUSLY RECEIVED CONTROL

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		..1.		SDWAIRB	"X'20'" ON AN IRB PRECEDED THE RB THAT IS ASSOCIATED WITH THIS EXIT
		...1		SDWAPERC	"X'10'" ON THIS RECOVERY ROUTINE IS BEING PERCOLATED TO
		.... 1...		SDWAEAS	"X'08'" ON INDICATES A LOWER LEVEL EXIT HAS RECOGNIZED AN ERROR AND PROVIDED SERVICEABILITY INFORMATION
		.... .1..		SDWASKIP	"X'04'" ON INDICATES FRRS WERE SKIPPED
		.... ..1.		SDWALCL	"X'02'" ON IND ENTRY AS A LOCAL RESOURCE MGR
		1		SDWAGLBL	"X'01'" ON IND ENTRY AS A GLOBAL RESOURCE MGR
235	(EB)	BITSTRING	1	SDWAERRD	ADDITIONAL ERROR ENTRY INFORMATION
		1...		SDWACLUP	"X'80'" ON INDICATES RECOVERY EXIT ONLY TO CLEANUP AND NOT RETRY (IF ESTA EXIT AND 33E COMPLETION CODE THE DUMP IS TAKEN AFTER ENTRY TO THE RECOVERY ROUTINE,IF THE COMPLETION CODE IS OTHER THAN 33E AND IT IS AN ESTA EXIT THE DUMP IS TAKEN BEFORE ENTRY TO THE RECOVERY ROUTINE)
		.1..		SDWANRBE	"X'40'" ON RB ASSOCIATED WITH THIS ESTA EXIT WAS NOT IN CONTROL AT TIME OF ERROR NEVER ON FOR FRRS
		..1.		SDWASTAE	"X'20'" ON THIS ESTA EXIT HAS BEEN ENTERED FOR A PREVIOUS ABEND NEVER ON FOR FRRS
		...1		SDWACTS	"X'10'" ON,THIS TASK WAS NOT IN CONTROL AT TIME OF ERROR BUT A TASK WITHIN THE SAME JOBSTEP TREE REQUESTED A 'STEP' ABEND. ONLY 'ON' IF SDWACLUP IS 'ON'
		.... 1...		SDWAMABD	"X'08'" ON,THIS TASK WAS NOT IN CONTROL AT TIME OF ERROR BUT AN ANCESTOR OF THIS TASK HAS ABEND'ED ONLY 'ON' IF SDWACLUP IS 'ON'.
		.... .1..		SDWARPIV	"X'04'" ON, THE REGISTERS, PSW AND CONTROL REGISTERS AT TIME OF ERROR ARE UNAVAILABLE
		.... ..1.		SDWAMCIV	"X'02'" ON,MACHINE CHECK ERROR INFORMATION IS UNAVAILABLE.
		1		SDWAERFL	"X'01'" ON,ERRORID INFORMATION AVAILABLE
236	(EC)	CHARACTER	2	SDWAFMID	ASID OF MEMORY IN WHICH ERROR OCCURRED. =0, IF THE MEMORY IS CURRENT NOT=0, IF OTHER MEMORY IS CURRENT FOR FRRS- IF THE VALUE IS NON ZERO THE FRR IS RECEIVING CONTROL IN THE MASTER SCHEDULER ADDRESS SPACE AND CANNOT ADDRESS THE PRIVATE AREA OF THE FAILING ADDRESS SPACE. FOR ESTA- IF THE VALUE IS NON ZERO ENTRY IS DUE TO CROSS MEMORY ABTERM.
238	(EE)	BITSTRING	1	SDWAIOFS	THIS IS THE CURRENT I/O STATUS (THE I/O PROCESSING REQUESTED BY THE FIRST (E)STA EXIT IS THE ONLY REQUEST HONORED)
		1...		SDWAIOQR	"X'80'" ON,I/O FOR FAILING PROGRAM HAS BEEN QUIESCED AND IS RESTOREABLE
		.1..		SDWAIOHT	"X'40'" ON,I/O FOR FAILING PROGRAM HAS BEEN HALTED AND IS NOT RESTOREABLE
		..1.		SDWANOIO	"X'20'" ON,FAILING PROGRAM HAS NO I/O OUTSTANDING
		...1		SDWANIOP	"X'10'" ON,USER REQUESTED NO I/O PROCESSING
239	(EF)	CHARACTER	1	SDWACPUI	Low order byte of the error logical CPU id. IBM recommends using the 2-byte logical CPU id in SDWA2CID.
240	(F0)	ADDRESS	4	SDWARTYA (0)	ADDRESS OF RETRY ROUTINE
240	(F0)	BITSTRING	1	SDWARTYF	ADDRESSING MODE INDICATOR BYTE
		1...		SDWAAMOD	"X'80'" This bit is never looked at. The AMODE of the retry is determined by other information
241	(F1)	ADDRESS	3		LOW THREE ORDER BYTES OF RETRY ADDRESS
244	(F4)	ADDRESS	4	SDWARECA	ADDRESS OF VARIABLE RECORDING AREA WITHIN SDWA
248	(F8)	CHARACTER	4	SDWACPUA (0)	ADDRESS OF CPU HOLDING RESOURCE WHICH CAUSES VALID SPIN ON CURRENT CPU - USED WITH RESTART KEY ERROR TYPE.IF THIS FIELD IS VALIDLY FILLED IN BY AN FRR THE FRRS MAINLINE PROGRAM WILL BE RESUMED AT THE NEXT SEQUENTIAL INSTRUCTION. NOT VALID FOR ESTAE EXITS.
248	(F8)	CHARACTER	2		RESERVED
250	(FA)	SIGNED	2	SDWALCPU	LOGICAL ADDRESS OF CPU HOLDING RESOURCE
252	(FC)	BITSTRING	4	SDWAPARQ (0)	FLAGS SET BY RECOVERY ROUTINE TO REQUEST FURTHER PROCESSING ACTION
252	(FC)	BITSTRING	1	SDWARCDE	RETURN CODE FROM RECOVERY ROUTINE TO INDICATE RETRY OR TERMINATION
252	(FC)	X'0'	0	SDWACWT	"0" 0 ,CONTINUE WITH TERMINATION. THIS INDICATION IMPLIES PERCOLATION
252	(FC)	X'4'	0	SDWARETY	"4" 4 ,RETRY USING RETRY ADDRESS IN SDWARTYA FIELD
252	(FC)	X'10'	0	SDWAPSTI	"16" 16,PREVENT FURTHER (E)STAI PROCESSING
253	(FD)	BITSTRING	1	SDWAACF2	FLAGS TO INDICATE ADDITIONAL PROCESSING REQUESTS
		1...		SDWARCRD	"X'80'" ON,RECORDING REQUESTED
		.1..		SDWARFXM	"X'40'" ON,RETRY TO FULLXM AT TIME OF FRR SET. CAN BE USED BY BY MODE=PRIMARY FRRS
		..1.		SDWASPIN	"X'20'" ON,PROGRAM INTERRUPTED VIA THE RESTART KEY WAS IN A VALID SPIN(SET BY THE SETRP MACRO WHEN CPU ADRESS IS SPECIFIED ALONG WITH THE CPU ADDRESS IN SDWACPUA FIELD TO ALLOW RESTART OF THE ALTERNATE CPU)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		...1		SDWARERR	"X'10'" ON,RETRY USING THE CROSS MEMORY ADDRESSING ENVIRONMENT AT THE TIME OF THE ERROR. OFF,RETRY USING THE CROSS MEMORY ADDR ENV ON ENTRY TO THE FRR.
		.... 1...		SDWAUPRG	"X'08'" ON,UPDATED REGISTERS STARTING WITH SDWASR00 ARE TO BE USED FOR RETRY
		.... .1..		SDWAFREE	"X'04'" ON, SDWA (RTCA) TO BE FREED PRIOR TO RETRY. ONLY VALID FOR ESTA TYPE EXITS.
		.... ..1.		SDWASERP	"X'02'" ON,SERIALIZE PERCOLATION (USED WHEN AN SRB MODE FRR PERCOLATES SERIALLY TO A RELATED TASK)
		1		SDWACML	"X'01'" ON,FREE THE CROSS MEMORY LOCAL LOCK
254	(FE)	BITSTRING	1	SDWAACF3	FLAGS INDICATING SOME GLOBAL LOCKS TO BE FREED. ONLY VALID FOR FRRS. OTHER LOCKS ARE INDICATED AT SDWAFLK1 AND SDWAFLK2.
		1...		SDWAFRSX	"X'80'" ON, THE RSM CROSS MEMORY CLASS LOCK
		.1..		SDWAFRSA	"X'40'" ON, THE RSM ADDRESS SPACE CLASS LOCK
		..1.		SDWAFVSP	"X'20'" ON, THE VSM PAGE LOCK
		...1		SDWADISP	"X'10'" ON,THE DISPATCHER LOCK
		.... 1...		SDWAASMP	"X'08'" ON,THE ASM CLASS LOCK Z40WPXH
		.... .1..		SDWASALL	"X'04'" ON, THE SALLOC LOCK
		.... ..1.		SDWAIPRG	"X'02'" ON, THE IOSYNCH LOCK
		1		SDWAFRSD	"X'01'" ON, THE RSM DATA SPACE LOCK
255	(FF)	BITSTRING	1	SDWAACF4	ADDITIONAL LOCKS TO BE FREED, OR ADDITIONAL PROCESSING FOR FRRS
		1...		SDWAIUCB	"X'80'" ON, FREE IOSUCB LOCK
		.... 1...		SDWATADB	"X'08'" RESERVED LOCK Z40WPXH
		.... .1..		SDWAOPTM	"X'04'" ON, FREE SYSTEM RESOURCES MGR(SRM) LOCK
		.... ..1.		SDWACMS	"X'02'" ON, FREE CMS LOCK
		1		SDWAFLLK	"X'01'" ON, FREE LOCAL LOCK
256	(100)	CHARACTER	32	SDWALKWA (0)	LOCK AREA
256	(100)	CHARACTER	32	SDWALKWS (0)	LOCKWORDS REQUIRED TO FREE GLOBAL LOCKS ONLY USED FOR FRRS
256	(100)	ADDRESS	4	SDWALRSD	LOCKWORD FOR THE RSM DATA SPACE LOCK
260	(104)	ADDRESS	4	SDWAIULW	LOCKWORD FOR THE IOSUCB LOCK
264	(108)	ADDRESS	4		LOCKWORD - RESERVED
268	(10C)	ADDRESS	4	SDWAIPLW	LOCKWORD FOR THE IOSYNCH LOCK
272	(110)	ADDRESS	4	SDWAAPLW	LOCKWORD FOR THE ASM CLASS LOCK Z40WPXH
276	(114)	ADDRESS	4		RESERVED
280	(118)	ADDRESS	4		RESERVED
284	(11C)	ADDRESS	4	SDWATALW	LOCKWORD - RESERVED Z40WPXH
288	(120)	CHARACTER	2	SDWAASID	ASID FOR LOGREC DEBUGGING (HOME ASID)
290	(122)	CHARACTER	2	SDWASEQ#	ERRORID SEQUENCE NUMBER
292	(124)	CHARACTER	24	SDWARECP (0)	RECORDING PARAMETERS (MODULE,CSECT AND RECOVERY ROUTINE NAMES-RESPECTIVELY)
292	(124)	CHARACTER	8	SDWAMODN	THE LOAD MODULE NAME INVOLVED IN THE ERROR (SUPPLIED BY THE RECOVERY ROUTINE)
300	(12C)	CHARACTER	8	SDWACSCT	THE CSECT (MICROFICHE) NAME INVOLVED IN THE ERROR (SUPPLIED BY THE RECOVERY ROUTINE)
308	(134)	CHARACTER	8	SDWAREXN	THE RECOVERY ROUTINE (MICROFICHE) NAME HANDLING THE ERROR (SUPPLIED BY THE RECOVERY ROUTINE)
316	(13C)	ADDRESS	4	SDWADPLA	POINTER TO DUMP PARAMETER LIST RESIDING IN SDWA
320	(140)	CHARACTER	8	SDWASNPA (0)	SNAP PARAMETER LIST FLAGS
320	(140)	CHARACTER	4	SDWADUMP (0)	DUMP CHARACTERISTICS
320	(140)	CHARACTER	1	SDWADPID	ID OF DUMP REQUESTED
321	(141)	BITSTRING	1	SDWADPFS	DUMP FLAGS
		1...		SDWADPT	"X'80'" ALWAYS OFF, INDICATES SNAP DUMP REQUEST
		.1..		SDWADLST	"X'40'" ALWAYS ON, INDICATES OS/VS2 REL. 2 DUMP PARAMETER LIST SUPPLIED USED BY RTM TO INDICATE DUMP OPTIONS ARE AVAILABLE IN THE SDWA
		..1.		SDWAENSN	"X'20'" ON,ENHANCED DUMP OPTIONS
		.... ..1.		SDWASLST	"X'02'" ON,STORAGE LISTS SUPPLIED FOR DUMP
322	(142)	BITSTRING	1	SDWADPF2	DUMP FLAGS 2
		1...		SDWADVS3	"X'80'" ON, STORAGE RANGES IN SDWADSR, OFF, STORAGE RANGES IN SDWADPSL
		.... 1...		SDWAXLST	"X'08'" ON,DATA SPACE STORAGE LISTS SUPPLIED FOR DUMP
		.... .1..		SDWALVL2	"X'04'" ON, MVS/SP2.1 VERSION OF SNAP PARMLIST
		.... ..1.		SDWASUBL	"X'02'" ON, SUBPOOL LIST SUPPLIED
323	(143)	CHARACTER	1		RESERVED
324	(144)	CHARACTER	4	SDWADDAT (0)	SDATA AND PDATA OPTIONS
324	(144)	CHARACTER	2	SDWASDAT (0)	SDATA OPTIONS
324	(144)	BITSTRING	1	SDWASDA0	SDATA OPTIONS FLAG ONE
		1...		SDWANUC	"X'80'" DISPLAY NUCLEUS
		.1..		SDWASQA	"X'40'" DISPLAY SQA
		..1.		SDWALSQA	"X'20'" DISPLAY LSQA
		...1		SDWASWA	"X'10'" DISPLAY SWA

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... 1...		SDWAGTF	"X'08'" DISPLAY GTF INCORE TRACE TABLE
		.... .1..		SDWACBS	"X'04'" FORMAT AND DISPLAY CONTROL BLOCKS
		.... ..1.		SDWAQQS	"X'02'" FORMAT AND DISPLAY QCBS/QELS
		1		SDWADM	"X'01'" FORMAT DATA MGT CONTROL BLOCKS
325	(145)	BITSTRING	1	SDWASDA1	SDATA OPTIONS
		1...		SDWAIO	"X'80'" FORMAT I/O SUPERVISOR CONTROL BLOCKS
		.1..		SDWAERR	"X'40'" FORMAT ERROR CONTROL BLOCKS
		...1		SDWASUM	"X'10'" PROVIDE SUMMARY DUMP
		.... 1...		SDWAALLN	"X'08'" DISPLAY ENTIRE VIRTUAL NUCLEUS
326	(146)	BITSTRING	1	SDWAPDAT	PDATA OPTIONS
		1...		SDWADSAS	"X'80'" DISPLAY SAVE AREAS
		.1..		SDWADSAH	"X'40'" DISPLAY SAVE AREA HEADER
		..1.		SDWADREG	"X'20'" DISPLAY REGISTERS
		...1		SDWATLPA	"X'10'" DISPLAY LPA MODULES OF TASK
		.... 1...		SDWATJPA	"X'08'" DISPLAY JPA MODULES OF TASK
		.... .1..		SDWADPSW	"X'04'" DISPLAY PSW
		.... ..1.		SDWAUSPL	"X'02'" DISPLAY USER SUBPOOLS
327	(147)	BITSTRING	1		RESERVED
328	(148)	CHARACTER	36	SDWADPSA (0)	DUMP RANGES AREA. Note the last 4 bytes of this area is a substructure of the dump ranges area, but it should not be part of the dump ranges area. However, for compatibility reasons, no changes were made to size of the SDWADPSA area.
328	(148)	CHARACTER	32	SDWADPSL (0)	DUMP STORAGE LISTS (MAX 4 RANGES AVAILABLE)
328	(148)	ADDRESS	4	SDWAFRM1	BEGINNING ADDRESS FOR STORAGE RANGE 1
332	(14C)	ADDRESS	4	SDWATO1	ENDING ADDRESS FOR STORAGE RANGE 1
336	(150)	ADDRESS	4	SDWAFRM2	BEGINNING ADDRESS FOR STORAGE RANGE2
340	(154)	ADDRESS	4	SDWATO2	ENDING ADDRESS FOR STORAGE RANGE 2
344	(158)	ADDRESS	4	SDWAFRM3	BEGINNING ADDRESS FOR STORAGE RANGE 3
348	(15C)	ADDRESS	4	SDWATO3	ENDING ADDRESS FOR STORAGE RANGE 3
352	(160)	ADDRESS	4	SDWAFRM4	BEGINNING ADDRESS FOR STORAGE RANGE 4
356	(164)	ADDRESS	4	SDWATO4	ENDING ADDRESS FOR STORAGE RANGE 4
360	(168)	SIGNED	2	SDWA2CID	2-byte ERRORID logical CPU id. See SDWADPSA comment.
362	(16A)	SIGNED	2		Reserved. See SDWADPSA comment. SDWAVERI IS TO INDICATE THE VERSION OF THE SDWA VIA A NUMBER IN THE SDWAVID
364	(16C)	CHARACTER	4	SDWAVERI (0)	SDWA VERSION INDICATOR
364	(16C)	CHARACTER	2	SDWAVERF	FFFF INDICATES VID FIELD IS VALID
366	(16E)	CHARACTER	2	SDWAVID	VERSION INDICATOR, EXPLAINED AS FOLLOWS: 1, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT RELEASE 2 LEVEL 2, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT VERSION 2 RELEASE 1 LEVEL 3, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT JBB2110 LEVEL. 4, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT HBB3310 LEVEL. 5, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT HBB4410 LEVEL. 6, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT HBB5510 LEVEL. 7, indicates the SDWA is at an OS/390 R10 HBB7703 level *and* that it contains the SDWARC4 extension 8, indicates the SDWA is at a z/OS R7 HBB7720 level *and* that it contains the SDWARC4 extension 9, indicates the SDWA is at a z/OS V2R1 HBB7790 level *and* that it contains the SDWARC5 extension
366	(16E)	X'1'	0	SDWAVS3	"1" 1, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT RELEASE 2 LEVEL
366	(16E)	X'2'	0	SDWAVS4	"2" 2, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT VERSION 2 RELEASE 1 LEVEL
366	(16E)	X'3'	0	SDWAVS5	"3" 3, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT JBB2110 LEVEL.
366	(16E)	X'4'	0	SDWAVS6	"4" 4, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT HBB3310 LEVEL.
366	(16E)	X'5'	0	SDWAVS7	"5" 5, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT HBB4410 LEVEL.
366	(16E)	X'6'	0	SDWAVS8	"6" 6, INDICATES THE SDWA IS AT AN MVS/SYSTEM PRODUCT HBB5510 LEVEL.
366	(16E)	X'7'	0	SDWAVS9	"7" 7, indicates the SDWA is at an OS/390 R10 HBB7703 level *and* that it contains the SDWARC4 extension
366	(16E)	X'8'	0	SDWAVS10	"8" 8, indicates the SDWA is at a z/OS R7 HBB7720 level *and* that it contains the SDWARC4 extension
366	(16E)	X'9'	0	SDWAVS11	"9" 9, indicates the SDWA is at a z/OS V2R1 HBB7790 level *and* that it contains the SDWARC5 extension
366	(16E)	X'9'	0	SDWAVSN	"9" 9, indicates the SDWA is at a z/OS V2R1 HBB7790 level *and* that it contains the SDWARC5 extension IF THE VALUE OF THIS EQUATE IS CHANGED, ADD A NEW SDWAVSX WHERE X IS THE NUMBER THAT FOLLOWS THE LAST SDWAVSX FIELD, AND MAKE THE EQUATE EQUAL TO SDWAVSN. ALSO DOCUMENT THE VALUE IN SDWAVID AS ABOVE. REFER TO MODULE IEAVTFRO FOR INSTRUCTIONS ON WHICH MODULES NEED TO CHANGE AND RECOMPILE
368	(170)	ADDRESS	4	SDWAXPAD	ADDR OF THE EXTENSION POINTERS (SDWAPTRS)

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
372	(174)	CHARACTER	12	SDWAXM (0)	CROSS MEMORY INFORMATION
372	(174)	CHARACTER	8	SDWACRGS (0)	CONTROL REGISTERS 3 AND 4
372	(174)	CHARACTER	4	SDWACR3 (0)	CONTROL REGISTER 3
372	(174)	CHARACTER	2	SDWAKM	KEY MASK
374	(176)	CHARACTER	2	SDWASCND	ASID OF THE SECONDARY ADDR SPACE -SASID
376	(178)	CHARACTER	4	SDWACR4 (0)	CONTROL REGISTER 4
376	(178)	CHARACTER	2	SDWAAX	AUTHORIZATION INDEX
378	(17A)	CHARACTER	2	SDWAPRIM	ASID OF THE PRIMARY ADDR SPACE -PASID
380	(17C)	ADDRESS	4	SDWACMLA	ADDRESS OF ASCB OF CML TO BE FREED
384	(180)	CHARACTER	8	SDWACOMU	FRR TO ESTAE COMMUNICATION BUFFER
392	(188)	ADDRESS	4	SDWACOMP	THIS WORD IS PROVIDED FOR COMMUNICATION OF ADDITIONAL RECOVERY DATA ON A PER COMPONENT BASIS (FOR OS/VS2 RELEASE 2 THIS FIELD IS ONLY USED BY DATA MANAGER)
396	(18C)	CHARACTER	4	SDWAERTM	ERRORID TIME STAMP
400	(190)	CHARACTER	264	SDWARA (0)	VARIABLE RECORDING AREA PREFIXED BY A TWO BYTE LENGTH FIELD OF AREA, A ONE BYTE FLAG FIELD, AND A ONE BYTE FIELD WITH LENGTH OF USER SUPPLIED RECORDING INFORMATION
400	(190)	CHARACTER	2	SDWAVRAL	LENGTH OF VARIABLE RECORDING AREA
402	(192)	BITSTRING	1	SDWADPVA	BITS THAT DEFINE DATA IN VARIABLE AREA
		1...		SDWAHEX	"X'80'" SDWAVRA DATA TO BE PRINTED BY EREP IN HEX
		.1..		SDWAEBC	"X'40'" SDWAVRA DATA TO BE PRINTED BY EREP IN EBCDIC
		..1.		SDWAVRAM	"X'20'" SDWAVRA DATA IS IN THE FORMAT MAPPED BY THE VRAMAP DSECT (IHAVRA MACRO)
403	(193)	BITSTRING	1	SDWAURAL	LENGTH OF USER SUPPLIED INFORMATION IN THE VARIABLE RECORDING AREA (ZEROED BEFORE EACH RECOVERY ROUTINE IS INVOKED)
404	(194)	CHARACTER	255	SDWAVRA	VARIABLE RECORDING AREA
659	(293)	CHARACTER	5	SDWAID	CONTAINS 'SDWA ' AS ID
664	(298)	DBL WORD	8	SDWAEND (0)	END OF NON-EXTENDED SDWA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWARC1	, RECORDABLE EXTENSION, BASED ON SDWASRVP
0	(0)	CHARACTER	456	SDWASERV (0)	ADDITIONAL COMPONENT SERVICE DATA
0	(0)	CHARACTER	118	SDWARC1Z (0)	CAN BE ZEROED ON PERCOLATION.
0	(0)	CHARACTER	5	SDWACID	COMPONENT ID OF THE COMPONENT INVOLVED IN THE ERROR (FOR EXAMPLE, SC1CR)
5	(5)	CHARACTER	23	SDWASC	NAME OF THE SUBCOMPONENT AND THE MODULE SUBFUNCTION INVOLVED IN THE ERROR
28	(1C)	CHARACTER	16	SDWAMLVL (0)	LEVEL OF THE MODULE INVOLVED IN THE ERROR
28	(1C)	CHARACTER	8	SDWAMDAT	ASSEMBLY DATE OF THE MODULE INVOLVED IN THE ERROR
36	(24)	CHARACTER	8	SDWAMVRS	VERSION OF THE MODULE - PTF OR PRODUCT NUMBER
44	(2C)	CHARACTER	4	SDWACRC (0)	This field contains the abend reason code that was set via an ABEND, CALLRTM, or SETRP macro. It is valid only if SDWARCF is set on. For SRBTERM abends (see SDWASRBT), the high order bit of the reason code, when on, indicates that the issuer felt that an SVCDUMP was not necessary for this abend. Note that this field should not be confused with the return code that some programs place into register 15 before issuing an abend.
44	(2C)	SIGNED	4	SDWAHRC	HEXADECIMAL DECLARE FOR SDWACRC
48	(30)	CHARACTER	8	SDWARRL	ENTRY POINT LABEL OF THE RECOVERY ROUTINE THAT FILLED IN THIS SDWA
56	(38)	CHARACTER	4	SDWACIDB	THE COMPONENT ID BASE (PREFIX) NUMBER, SUCH AS 5741.
60	(3C)	SIGNED	1	SDWASDRC	SVCDUMP STATUS INDICATOR (FOR USE BY SDUMP)
61	(3D)	CHARACTER	1	SDWACCRC	FLAGS FOR COMPCODE AND REASON CODE
		1...		SDWACCF	"X'80'" =1, IF RECOVERY EXIT ALTERED COMPCODE
		.1..		SDWAREAF	"X'40'" =1, IF RECOVERY EXIT ALTERED REASON CODE
62	(3E)	BITSTRING	1	SDWARETF	SDWA RETRY FLAGS
		1...		SDWART15	"X'80'" ON, SET REGISTER 15 ON RETRY TO THE VALUE IN SDWASR15. OFF, SET REGISTER 15 ON RETRY TO THE RETRY ADDRESS, REGARDLESS OF THE VALUE IN SDWASR15. ONLY VALID FOR FRRS
		.1..		SDWAREMR	"X'40'" ON, REMOVE RECOVERY ROUTINE ON RETRY OFF, DONT REMOVE RECOVERY ROUTINE ON RETRY
		..1.		SDWAFRLK	"X'20'" ON, FREE LOCKS ON A RETRY WHOSE BIT SETTINGS IN THE SDWA HAVE BEEN TURNED ON OFF, DO NOT FREE ANY LOCKS ON A RETRY
		...1		SDWAUP64	"X'10'" If on, use the 64-bit GPRs for setting the retry regs. Only valid when SDWARC4 extension exists.
		.... 1...		SDWAKEAX	"X'08'" ON, when retrying keep the current EAX rather than resetting EAX to time-of-FRR-set
63	(3F)	BITSTRING	1	SDWATYPE	TYPE OF RECOVERY ROUTINE THAT RTM GAVE CONTROL TO: 0 - NO RECOVERY WAS SET UP 1 - FRR 2 - NON FRR (ESTAE, ESTAI, ETC.) 3 - ARR (ASSOCIATED RECOVERY ROUTINE)

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
63	(3F)	X'0'	0	SDWANREC	"0" NO RECOVERY WAS SET UP
63	(3F)	X'1'	0	SDWATFRR	"1" FRR WAS GIVEN CONTROL
63	(3F)	X'2'	0	SDWATEST	"2" ESTAE/I/X WAS GIVEN CONTROL
63	(3F)	X'3'	0	SDWATARR	"3" ARR WAS GIVEN CONTROL
64	(40)	CHARACTER	4	SDWAHLHI	Copy of PSAHLHI (Highest Lock Held Indicator) at the time of error, minus any locks that have been released by FRR recovery routines that ran before the current recovery routine. This field was always '0' on entry to Estae-type recovery routines prior to HBB7770
68	(44)	CHARACTER	4	SDWASUPR	Copy of PSASUPER (Supervisor Control Word) at the time of error, minus any bits that have been turned off by FRR recovery routines that ran before the current recovery routine. This field was always '0' on entry to Estae-type recovery routines prior to HBB7770
72	(48)	CHARACTER	4	SDWASPN	Copy of LCCASPIN (Processor Spinning Indicators) at the time of error, minus any spins that have been resolved by FRR recovery routines that ran before the current recovery routine. This field was always '0' on entry to Estae-type recovery routines prior to HBB7770
76	(4C)	CHARACTER	4	SDWAEADR	FRR OR ESTAE RECOVERY ROUTINE ADDRESS. =0 IF NO FRR EXISTED WHEN RTM1 RECIEVED CONTROL TO PROCESS A SYSTEM MODE ERROR.
80	(50)	CHARACTER	24	SDWAFRRE	IF FRR EXISTS: COPY OF FRR PARAMETER AREA FROM THE CURRENT FRR STACK ENTRY ON ENTRY TO AN ESTAE: 0
104	(68)	CHARACTER	4	SDWASDRN	SDUMP REASON FLAGS FOR TAKING PARTIAL DUMP. SEE RTSDSRSN FIELD IN RTSD. ADDITIONS TO SDWARC1 - 40 BYTES
108	(6C)	CHARACTER	10	SDWADAEW (0)	STRUCTURE FOR DAE INFO
108	(6C)	CHARACTER	8	SDWADAET	DAE STATUS FLAGS MAPPED BY ADYDSTAT
116	(74)	CHARACTER	2	SDWAOCUR	NUMBER OF OCCURRENCES OF THIS PROBLEM. IF 0 THEN DAE DID NOT CHECK FOR DUPLICATE OCCURRENCES. IF 1 THEN THIS IS THE FIRST OCCURRENCE OF THIS PROBLEM. IF GREATER THAN 1 THEN THIS IS THE COUNT OF HOW MANY TIMES THIS DUMP REQUEST HAS OCCURRED WHILE DAE WAS ACTIVE.
118	(76)	CHARACTER	34	SDWARC1P (0)	THIS PART IS TO BE PRESERVED ON PERCOLATION AND NOT ZEROED
118	(76)	CHARACTER	6	SDWAPGTA (0)	CONTAINING STRUCTURE AS IN LCCAPGTA
118	(76)	CHARACTER	2	SDWAASI1	ADDRESS SPACE ID OF TASK FOR PURGEDQ
120	(78)	SIGNED	4	SDWATCB	ADDRESS OF TCB FOR PURGEDQ
124	(7C)	CHARACTER	28	SDWART12 (0)	USED FOR FILLING IN EED'S
124	(7C)	CHARACTER	12	SDWAFAIN	12 BYTES OF INSTRUCTION STREAM AS DETERMINED BY THE ADDRESS IN THE PSW AT THE TIME OF FAILURE. 6 BYTES BEFORE AND AFTER.
136	(88)	SIGNED	4	SDWAASCB	ADDRESS OF ASCB FOR FAILING ADDRESS SPACE.
140	(8C)	SIGNED	4	SDWAASST	ADDRESS OF ADDRESS SPACE SEGMENT TABLE.
144	(90)	SIGNED	4	SDWASABC (0)	ORIGINAL COMPLETION CODE.
144	(90)	CHARACTER	1	SDWAOABF	FLAGS IN COMPLETION CODE REGISTER.
		1...		SDWAOREQ	"X'80'" ORIGINAL VALUE OF SDWAREQ
		.1..		SDWAOSTP	"X'40'" ORIGINAL VALUE OF SDWASTEP
					Comment
					EQU X'20' USED FOR MEMTERM REQUESTS
					End of Comment
		...1		SDWAOSTC	"X'10'" ORIGINAL VALUE OF SDWASTCC
					Comment
					EQU X'08' USED BY RTM2 FOR NORMAL END-OF-TASK
					End of Comment
		.... .1..		SDWAORCF	"X'04'" VALID REASON CODE IN SDWAOCRC.
					Comment
					EQU X'02' RESERVED EQU X'01' RESERVED
					End of Comment
145	(91)	CHARACTER	3	SDWAOCMP	COMPLETION CODE.
148	(94)	SIGNED	4	SDWAOCRC	ORIGINAL REASON CODE FROM SDWACRC AT ENTRY TO RECOVERY PROCESSING. ONLY VALID IF SDWAORCF IS SET ON.

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					SDWARC1 FIELDS FROM THIS POINT ON ARE PRESERVED ON RTM1 FRR PERCOLATION AND ARE ZEROED ON RTM2 ESTAE PERCOLATION.
					End of Comment
152	(98)	CHARACTER	64	SDWACRER (0)	CONTROL REGISTERS AT TIME OF ERROR. THESE VALUES ARE NOT RESTORED ON RETRY.
152	(98)	SIGNED	4	SDWACRE0	CONTROL REGISTER 0
156	(9C)	SIGNED	4	SDWACRE1	CONTROL REGISTER 1
160	(A0)	SIGNED	4	SDWACRE2	CONTROL REGISTER 2
164	(A4)	SIGNED	4	SDWACRE3	CONTROL REGISTER 3
168	(A8)	SIGNED	4	SDWACRE4	CONTROL REGISTER 4
172	(AC)	SIGNED	4	SDWACRE5	CONTROL REGISTER 5
176	(B0)	SIGNED	4	SDWACRE6	CONTROL REGISTER 6
180	(B4)	SIGNED	4	SDWACRE7	CONTROL REGISTER 7
184	(B8)	SIGNED	4	SDWACRE8	CONTROL REGISTER 8
188	(BC)	SIGNED	4	SDWACRE9	CONTROL REGISTER 9
192	(C0)	SIGNED	4	SDWACREA	CONTROL REGISTER 10
196	(C4)	SIGNED	4	SDWACREB	CONTROL REGISTER 11
200	(C8)	SIGNED	4	SDWACREC	CONTROL REGISTER 12
204	(CC)	SIGNED	4	SDWACRED	CONTROL REGISTER 13
208	(D0)	SIGNED	4	SDWACREE	CONTROL REGISTER 14
212	(D4)	SIGNED	4	SDWACREF	CONTROL REGISTER 15
216	(D8)	CHARACTER	64	SDWAARER (0)	ACCESS REGISTERS AT TIME OF ERROR
216	(D8)	SIGNED	4	SDWAARE0	ACCESS REGISTER 0
220	(DC)	SIGNED	4	SDWAARE1	ACCESS REGISTER 1
224	(E0)	SIGNED	4	SDWAARE2	ACCESS REGISTER 2
228	(E4)	SIGNED	4	SDWAARE3	ACCESS REGISTER 3
232	(E8)	SIGNED	4	SDWAARE4	ACCESS REGISTER 4
236	(EC)	SIGNED	4	SDWAARE5	ACCESS REGISTER 5
240	(F0)	SIGNED	4	SDWAARE6	ACCESS REGISTER 6
244	(F4)	SIGNED	4	SDWAARE7	ACCESS REGISTER 7
248	(F8)	SIGNED	4	SDWAARE8	ACCESS REGISTER 8
252	(FC)	SIGNED	4	SDWAARE9	ACCESS REGISTER 9
256	(100)	SIGNED	4	SDWAAREA	ACCESS REGISTER 10
260	(104)	SIGNED	4	SDWAAREB	ACCESS REGISTER 11
264	(108)	SIGNED	4	SDWAAREC	ACCESS REGISTER 12
268	(10C)	SIGNED	4	SDWAARED	ACCESS REGISTER 13
272	(110)	SIGNED	4	SDWAAREE	ACCESS REGISTER 14
276	(114)	SIGNED	4	SDWAAREF	ACCESS REGISTER 15
280	(118)	CHARACTER	64	SDWAARSV (0)	ACCESS REGISTERS OF THE RB LEVEL AND LINKAGE STACK LEVEL WHICH CREATED THE ESTAE EXIT AT THE TIME IT LAST INCURRED AN INTERRUPT OR 0 FOR ESTAI. FOR FRRS INITIALIZED TO REGISTERS AT TIME OF ERROR. THIS REGISTER AREA IS USED TO UPDATE REGISTER CONTENTS FOR RETRY IF REQUESTED.
280	(118)	SIGNED	4	SDWAARS0	ACCESS REGISTER 0
284	(11C)	SIGNED	4	SDWAARS1	ACCESS REGISTER 1
288	(120)	SIGNED	4	SDWAARS2	ACCESS REGISTER 2
292	(124)	SIGNED	4	SDWAARS3	ACCESS REGISTER 3
296	(128)	SIGNED	4	SDWAARS4	ACCESS REGISTER 4
300	(12C)	SIGNED	4	SDWAARS5	ACCESS REGISTER 5
304	(130)	SIGNED	4	SDWAARS6	ACCESS REGISTER 6
308	(134)	SIGNED	4	SDWAARS7	ACCESS REGISTER 7
312	(138)	SIGNED	4	SDWAARS8	ACCESS REGISTER 8
316	(13C)	SIGNED	4	SDWAARS9	ACCESS REGISTER 9
320	(140)	SIGNED	4	SDWAARSA	ACCESS REGISTER 10
324	(144)	SIGNED	4	SDWAARSB	ACCESS REGISTER 11
328	(148)	SIGNED	4	SDWAARSC	ACCESS REGISTER 12
332	(14C)	SIGNED	4	SDWAARSD	ACCESS REGISTER 13
336	(150)	SIGNED	4	SDWAARSE	ACCESS REGISTER 14
340	(154)	SIGNED	4	SDWAARSF	ACCESS REGISTER 15
344	(158)	CHARACTER	64	SDWADUCT	DISPATCHABLE UNIT CONTROL TABLE
408	(198)	BITSTRING	1	SDWATEAR	TRANSLATION EXCEPTION ACCESS REGISTER NUMBER in bits 4-7. The first 4 bits might not be 0.
		 1111		SDWATEAN	"X'0F'" Actual bits for AR number
409	(199)	BITSTRING	1	SDWAXFLG	EXTENDED FLAG AREA
		1...		SDWAINTF	"X'80'" ON, SDWAEC2, SDWASRSV, AND SDWAARSV ARE FROM A LINKAGE STACK ENTRY
		.1..		SDWATEAV	"X'40'" ON, SDWATRAN CONTAINS A VALID ADDRESS
		..1.		SDWATEIV	"X'20'" ON, SDWATRAN CONTAINS A VALID ASID
		...1		SDWAESTX	"X'10'" ON, IF SDWATYPE = SDWATEST, THE RECOVERY ROUTINE WAS ESTABLISHED VIA ESTAEX

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		.... 1...		SDWATEPC	"X'08'" ON, SDWATRAN CONTAINS A VALID PC number
		.... .1..		SDWATIRR	"X'04'" On, if SDWATYPE = SDWATARR, the recovery routine was established via IEAARR
		.... ..1.		SDWASVAL	"X'02'" On, the state of SDWASRBS is valid
		1		SDWARELEASECODEVALID	"X'01'" On, indicates that the abended RB level was interrupted for RTM processing after it had been Released but before it could regain control, and that SdwaReleaseCode contains its Release code. This bit (and SdwaReleaseCode) is propagated upon percolation to another recovery routine
410	(19A)	BITSTRING	1	SDWASFLG	SUBSPACE FLAG AREA
		1...		SDWASVLD	"X'80'" ON IF SUBSPACE INFORMATION AT TIME OF ERROR (SDWASTKN AND SDWASNM) IS AVAILABLE AND VALID
		.1..		SDWASSA	"X'40'" ON IF A SUBSPACE WAS ACTIVE AT TIME OF ERROR
		.... ..1.		SDWABSA	"X'02'" Indicates that Reduced Authority (set via the BSA instruction) is in effect.
		1		SDWASSRS	"X'01'" TURNED ON BY AN ESTAE-TYPE RECOVERY ROUTINE TO INDICATE THAT RTM SHOULD RESTORE ITS ORIGINAL SUBSPACE ENVIRONMENT IF IT RECURSES
411	(19B)	BITSTRING	1	SDWAARCH	Copy of FLCARCH
		1		SDWAZARC	"X'01'" Copy of PSAZARCH
		1		SDWAESAM	"X'01'" Copy of PSAZARCH
412	(19C)	CHARACTER	8	SDWAPRM2 (0)	ARR MSTA AREA COPY
412	(19C)	CHARACTER	4	SDWAMST1 (0)	1ST WORD OF MSTA AREA
412	(19C)	CHARACTER	4	SDWAPCEP	PC ESTAE PARAM VALUE
416	(1A0)	CHARACTER	4	SDWAMST2 (0)	2ND WORD OF MSTA AREA
416	(1A0)	CHARACTER	4	SDWAPCEA	PC ESTAE PARAM ALET VALUE
420	(1A4)	SIGNED	4	SDWALSED	PTR TO LINKAGE STK ENTRY (CR15)
424	(1A8)	CHARACTER	4	SDWACLSE	Copy of PSACLHSE (Locks Held String Extension) at the time of error, minus any locks that have been released by FRR recovery routines that ran before the current recovery routine. This field was always '0' on entry to Estae-type recovery routines prior to HBB7770
428	(1AC)	BITSTRING	2	SDWALSLV	FOR RETRY: NUMBER OF BAKR ENTRIES PAST TIME-OF-SET TO SET LINKAGE STACK ON RETRY. IGNORED FOR FRR IF RETRY=ERROR
430	(1AE)	BITSTRING	1	SDWARTAM	Retry Amode: 0 = "normal", 1 = AMODE 24, 2 = AMODE 31, 3 = AMODE 64
430	(1AE)	X'0'	0	SDWARASR	"0" Retry using default AMODE system rules
430	(1AE)	X'1'	0	SDWARA24	"1" Retry to AMODE 24 specifically
430	(1AE)	X'2'	0	SDWARA31	"2" Retry to AMODE 31 specifically
430	(1AE)	X'3'	0	SDWARA64	"3" Retry to AMODE 64 specifically
431	(1AF)	CHARACTER	1		RESERVED
432	(1B0)	CHARACTER	8	SDWASTKN	STOKEN OF THE SUBSPACE AT TIME OF ERROR - VALID ONLY IF SDWASVLD IS ON, NOT AVAILABLE FOR FRRS
440	(1B8)	CHARACTER	8	SDWASNM	NAME OF THE SUBSPACE AT TIME OF ERROR - VALID ONLY IF SDWASVLD IS ON, NOT AVAILABLE FOR FRRS
448	(1C0)	CHARACTER	8	SDWASNAM	Name of the SYSTEM that this record was created on. This is the same system name that is used in a SYSPLEX.

Comment

THE LENGTH MUST BE UPDATED IF ADDITIONS ARE MADE HERE

End of Comment

456	(1C8)	DBL WORD	8	SDWASEND (0)	END OF SERV EXTENSION OF SDWA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWARC2	, POINTED TO BY SDWAXIOM
0	(0)	CHARACTER	16	SDWAIOMA (0)	ADDITIONAL IO MACHINE CHECK DATA
0	(0)	CHARACTER	8	SDWARFSE (0)	FSA
0	(0)	CHARACTER	4	SDWARFSH	High half of FSA (zero pre-z/Architecture)
4	(4)	CHARACTER	4	SDWARFSL	Low half of FSA
8	(8)	CHARACTER	8	SDWAMCIC	MACHINE CHECK INTERRUPT CODE
16	(10)	DBL WORD	8	SDWAIEND (0)	END OF SDWAIOMA EXTENSION OF SDWA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWARC3	, POINTED TO BY SDWAXLCK
0	(0)	CHARACTER	32	SDWAFLCK (0)	ADDITIONAL FRELOCK DATA
0	(0)	BITSTRING	1	SDWAFLK1	FLAGS INDICATING WHAT LOCKS ARE TO BE FREED
		1...		SDWAFCPU	"X'80'" ON, FREE THE CPU LOCK
		.... 1...		SDWAFRSM	"X'08'" ON, FREE THE RSM LOCK
		.... .1..		SDWAFTRC	"X'04'" ON, FREE THE TRACE LOCK

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... ..1.		SDWAIOCB	"X'02'" ON, THE IOS LOCK
1	(1)	BITSTRING	1	SDWAFLK2	FLAGS INDICATING WHAT LOCKS ARE TO BE FREED
		...1		SDWAFRSC	"X'10'" ON, FREE THE RSM COMMON CLASS LOCK
		.... 1...		SDWAFRSG	"X'08'" ON, FREE THE RSM GLOBAL CLASS LOCK
		.... .1..		SDWAFVSF	"X'04'" ON, FREE THE VSM FIX LOCK
		.... ..1.		SDWAFASG	"X'02'" ON, FREE THE ASM GLOBAL CLASS LOCK
		1		SDWAFRSS	"X'01'" ON, FREE THE RSM STEAL CLASS LOCK
2	(2)	CHARACTER	4	SDWAFLKE (0)	FLAGS INDICATING LOCKS TO BE FREED
2	(2)	CHARACTER	1	SDWAFLE1	FLAGS FOR LOCKS TO BE FREED IN FIRST BYTE OF EXTENSION
		1...		SDWABLSD	"X'80'" ON, FREE THE BMFLSD LOCK
		.1..		SDWAXDS	"X'40'" ON, FREE THE XCFDS LOCK
		..1.		SDWAXRES	"X'20'" ON, FREE THE XCFRES LOCK
		...1		SDWAXQ	"X'10'" ON, FREE THE XCFQ LOCK
		.... 1...		SDWAESET	"X'08'" ON, FREE THE ETRSET LOCK
		.... .1..		SDWAIXSC	"X'04'" ON, FREE THE IXLSCH LOCK
		.... ..1.		SDWAIXSR	"X'02'" ON, FREE THE IXLSHR LOCK
		1		SDWAIXDS	"X'01'" ON, FREE THE IXLDS LOCK
3	(3)	CHARACTER	1	SDWAFLE2	FLAGS FOR LOCKS TO BE FREED IN SECOND BYTE OF EXTENSION
		1...		SDWAIXSH	"X'80'" ON, FREE THE IXLSHELL LOCK
		.1..		SDWAULUT	"X'40'" ON, FREE THE IOSULUT LOCK
		..1.		SDWAIXRE	"X'20'" ON, FREE THE IXLREQST LOCK
		...1		SDWAWLMR	"X'10'" On, free the WLMRES lock
		.... 1...		SDWAWLMQ	"X'08'" On, free the WLMQ lock
		.... .1..		SDWACNTX	"X'04'" On, free the CONTEXT lock
		.... ..1.		SDWARGSV	"X'02'" On, free the REGSRV lock
		1		SDWASSD	"X'01'" On, free the SSD lock
4	(4)	CHARACTER	1	SDWAFLE3	FLAGS FOR LOCKS TO BE FREED IN THIRD BYTE OF EXTENSION
		1...		SDWAGRSI	"X'80'" On, free the GRSINT lock
		.1..		SDWASLK1	"X'40'" On, free the HCWPSLK1 lock
		..1.		SDWANLK1	"X'20'" On, free the HCWPNLK1 lock
		...1		SDWAOLK1	"X'10'" On, free the HCWIOLK1 lock
		.... 1...		SDWAXLK1	"X'08'" On, free the HCWPXLK1 lock
		.... .1..		SDWARLK3	"X'04'" On, free the HCWDRLK3 lock
		.... ..1.		SDWARLK2	"X'02'" On, free the HCWDRLK2 lock
		1		SDWARLK1	"X'01'" On, free the HCWDRLK1 lock
5	(5)	CHARACTER	1	SDWAFLE4	FLAGS FOR LOCKS TO BE FREED IN FOURTH BYTE OF EXTENSION
		1...		SDWASRME	"X'80'" On, free the SRMENQ lock
6	(6)	CHARACTER	2		RESERVED
8	(8)	SIGNED	4	SDWALRSG	LOCKWORD ADDR FOR THE RSMGL LOCK
12	(C)	SIGNED	4	SDWALASG	LOCKWORD ADDR FOR THE ASMGL LOCK
16	(10)	SIGNED	4	SDWALRSS	LOCKWORD ADDR FOR THE RSMST LOCK
20	(14)	SIGNED	4	SDWALRSX	LOCKWORD ADDR FOR THE RSMXM LOCK
24	(18)	SIGNED	4	SDWALRSA	LOCKWORD ADDR FOR THE RSMAD LOCK
28	(1C)	SIGNED	4	SDWALRSC	LOCKWORD ADDR FOR THE RSMCM LOCK
32	(20)	DBL WORD	8	SDWALEND (0)	END OF SDWAFLCK EXTENSION OF SDWA

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	0	SDWARC4	, Pointed to by SDWAXEME
0	(0)	CHARACTER	128	SDWAG64 (0)	64-bit GPRs - initially contains registers at the Time Of Error, also used for retry. When the error was a program interrupt within transactional execution, these are the regs at the time of the program interrupt within the transaction.
0	(0)	DBL WORD	8	SDWAG6400	Register 0
8	(8)	DBL WORD	8	SDWAG6401	Register 1
16	(10)	DBL WORD	8	SDWAG6402	Register 2
24	(18)	DBL WORD	8	SDWAG6403	Register 3
32	(20)	DBL WORD	8	SDWAG6404	Register 4
40	(28)	DBL WORD	8	SDWAG6405	Register 5
48	(30)	DBL WORD	8	SDWAG6406	Register 6
56	(38)	DBL WORD	8	SDWAG6407	Register 7
64	(40)	DBL WORD	8	SDWAG6408	Register 8
72	(48)	DBL WORD	8	SDWAG6409	Register 9
80	(50)	DBL WORD	8	SDWAG6410	Register 10
88	(58)	DBL WORD	8	SDWAG6411	Register 11
96	(60)	DBL WORD	8	SDWAG6412	Register 12
104	(68)	DBL WORD	8	SDWAG6413	Register 13
112	(70)	DBL WORD	8	SDWAG6414	Register 14
120	(78)	DBL WORD	8	SDWAG6415	Register 15
128	(80)	CHARACTER	1		Reserved
129	(81)	CHARACTER	3	SDWARELEASECODE	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
					Release code when the abended RB level was interrupted for RTM processing after it had been Released but before it could regain control. This field is valid only when SdwaReleaseCodeValid is on
132	(84)	CHARACTER	4		Reserved
136	(88)	CHARACTER	64	SDWAG64H	High order halves of 64-bit GPRs in G64 When the error was a program interrupt within transactional execution, these are the regs at the time of the program interrupt within the transaction.
200	(C8)	CHARACTER	128	SDWAC64S (0)	z/Architecture CRs at time of error
200	(C8)	CHARACTER	8	SDWAC640	z/Architecture CR0 at time of error
208	(D0)	CHARACTER	8	SDWAC641	z/Architecture CR1 at time of error
216	(D8)	CHARACTER	8	SDWAC642	z/Architecture CR2 at time of error
224	(E0)	CHARACTER	16	SDWAC64_XM (0)	z/Architecture CR3/CR4 at time of error
224	(E0)	CHARACTER	8	SDWAC643 (0)	z/Architecture CR3 at time of error
224	(E0)	CHARACTER	4		
228	(E4)	CHARACTER	2	SDWAC643_KM	Key Mask
230	(E6)	CHARACTER	2	SDWAC643_SASID	Secondary ASID
232	(E8)	CHARACTER	8	SDWAC644 (0)	z/Architecture CR4 at time of error
232	(E8)	CHARACTER	4		
236	(EC)	CHARACTER	2	SDWAC644_AX	Authorization index
238	(EE)	CHARACTER	2	SDWAC644_PASID	Primary ASID
240	(F0)	CHARACTER	8	SDWAC645	z/Architecture CR5 at time of error
248	(F8)	CHARACTER	8	SDWAC646	z/Architecture CR6 at time of error
256	(100)	CHARACTER	8	SDWAC647	z/Architecture CR7 at time of error
264	(108)	CHARACTER	8	SDWAC648	z/Architecture CR8 at time of error
272	(110)	CHARACTER	8	SDWAC649	z/Architecture CR9 at time of error
280	(118)	CHARACTER	8	SDWAC64A	z/Architecture CRA at time of error
288	(120)	CHARACTER	8	SDWAC64B	z/Architecture CRB at time of error
296	(128)	CHARACTER	8	SDWAC64C	z/Architecture CRC at time of error
304	(130)	CHARACTER	8	SDWAC64D	z/Architecture CRD at time of error
312	(138)	CHARACTER	8	SDWAC64E	z/Architecture CRE at time of error
320	(140)	CHARACTER	8	SDWAC64F	z/Architecture CRF at time of error
328	(148)	CHARACTER	8	SDWATRNE	8-byte TEA
336	(150)	CHARACTER	8	SDWABEA	Breaking Event Address
344	(158)	CHARACTER	16	SDWAPSW16	16-byte PSW analog of SDWAEC1. When the error was a program interrupt within transactional execution, this is the PSW at the time of the program interrupt within the transaction.
360	(168)	DBL WORD	8	SDWAEEND (0)	End of 64-bit extension of the SDWA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWARC5	, Pointed to by SDWAXRC5
0	(0)	CHARACTER	128	SDWATXG64	Transaction abort G64. 64-bit GPRs 0-15 in order. This data is valid only when bits SDWAPCHK and SDWAPTX1 are on, indicating that the program interrupt occurred while within transactional execution
128	(80)	CHARACTER	16	SDWATXPSW16	Transaction abort PSW. This data is valid only when bits SDWAPCHK and SDWAPTX1 are on, indicating that the program interrupt occurred while within transactional execution
144	(90)	DBL WORD	8	SDWA5END (0)	End of SDWARC5

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWAPTRS	, POINTED TO BY SDWAXPAD
0	(0)	ADDRESS	4	SDWADSRP	ADDR DUMP STORAGE RANGES PTR. - SDWANRC1
4	(4)	ADDRESS	4	SDWASRVP	ADDR ADDITIONAL COMP SERV DATA - SDWARC1
8	(8)	ADDRESS	4	SDWAXIOM	ADDR OF I/O MACHINE CHECK AREA - SDWARC2
12	(C)	ADDRESS	4	SDWAXSPL	ADDR OF STORAGE SUBPOOLS AREA - SDWANRC2
16	(10)	ADDRESS	4	SDWAXLCK	ADDR ADDITIONAL FRELOCK DATA - SDWARC3
20	(14)	ADDRESS	4	SDWADSPP	DATA SPACE STORAGE RANGES POINTER - SDWANRC3
24	(18)	ADDRESS	4	SDWAXEME	Addr 64-bit information - SDWARC4
28	(1C)	ADDRESS	4	SDWAXRC5	Addr SDWARC5
32	(20)	DBL WORD	8	SDWAPEND (0)	END OF PTRS EXTENSION OF SDWA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWANRC1	, NONRECORDABLE EXTENSION, BASED SDWADSRP
0	(0)	CHARACTER	240	SDWADSR	DUMP STORAGE RANGES

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
240	(F0)	DBL WORD	8	SDWAREND (0)	END OF DSR EXTENSION OF SDWA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWANRC2	, POINTED TO BY SDWAXSPL
0	(0)	X'7'	0	SDWASPMX	"7" MAX NUMBER OF SUBPOOLS ON DUMPOPT
0	(0)	CHARACTER	16	SDWASPLE (0)	UP TO 7 SUBPOOLS OF STORAGE TO BE DUMPED BY ABDUMP
0	(0)	SIGNED	2	SDWASPLN	NUMBER OF SUBPOOLS TO BE DUMPED
2	(2)	SIGNED	2	SDWASPLS (7)	IDS OF SUBPOOLS TO BE DUMPED
16	(10)	DBL WORD	8	SDWASEN (0)	END OF SDWASPLS EXTENSION OF SDWA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWANRC3	, DUMPOPX EXTENSION, POINTED TO BY SDWADSPP
0	(0)	CHARACTER	240	SDWADXSL (0)	LIST OF DUMPOPX RANGES
0	(0)	CHARACTER	16	SDWADXSR (15)	DUMPOPX RANGE (UP TO 15)
240	(F0)	DBL WORD	8	SDWADEND (0)	
240	(F0)	X'F'	0	SDWADXMX	"15" UP TO 15 DATA SPACE STORAGE RANGES MAY BE SPECIFIED
240	(F0)	X'298'	0	SDWALEN	"SDWAEND-SDWA" LENGTH OF SDWA
240	(F0)	X'20'	0	SDWAPLEN	"SDWAPEND-SDWAPTRS" LENGTH OF PTRS EXTENSION
240	(F0)	X'F0'	0	SDWARLEN	"SDWAREND-SDWANRC1" LENGTH OF DSR EXTENSION
240	(F0)	X'1C8'	0	SDWACLEN	"SDWASEND-SDWARC1" LENGTH OF SERV EXTENSION
240	(F0)	X'10'	0	SDWAILEN	"SDWAIEND-SDWARC2" LENGTH OF IOMA EXTENSION
240	(F0)	X'20'	0	SDWALLEN	"SDWALEND-SDWARC3" LENGTH OF FRELOCK EXTENSION
240	(F0)	X'10'	0	SDWASPL	"SDWASEN-SDWANRC2" LENGTH OF SUBPOOL EXTENSION
240	(F0)	X'F0'	0	SDWADLEN	"SDWADEND-SDWANRC3" LENGTH OF EXTENSION FOR DATA SPACE RANGES
240	(F0)	X'1F0'	0	SDWANLNS	"SDWARLEN+SDWASPL+SDWADLEN" Non-recordable extensions
240	(F0)	X'168'	0	SDWAELEN	"SDWAEEND-SDWARC4" Length of z/Architecture extension
240	(F0)	X'90'	0	SDWARC5L	"SDWA5END-SDWARC5" Length of SDWARC5
240	(F0)	X'3F0'	0	SDWARLNS	"SDWACLEN+SDWAILEN+SDWALLEN+SDWAELEN+SDWARC5L"
240	(F0)	X'688'	0	SDWAMLNP	"SDWALEN+SDWACLEN+SDWAILEN+SDWALLEN+SDWAELEN+SDWARC5L"
240	(F0)	X'6A8'	0	SDWAMLEN	"SDWAMLNP+SDWAPLEN"

Comment

Total length of SDWA with only recordable
extensions. This is a super stack SDWA.

End of Comment

240	(F0)	X'898'	0	SDWATLEN	"SDWAMLEN+SDWANLNS"

Comment

Total length of SDWA with all extensions
This is a normal stack SDWA

End of Comment

240	(F0)	X'6A0'	0	SDWAOLEN	"SDWATLEN-SDWAELEN-SDWARC5L"

Comment

Total length of SDWA with all extensions except RC4
and RC5.
This is an RTM2 below-16M SDWA

End of Comment

240	(F0)	X'7B8'	0	SDWASLEN	"SDWAMLEN+272"

Comment

Getmain length for super stack FRR SDWA

End of Comment

240	(F0)	X'9A8'	0	SDWAFLEN	"SDWASLEN+SDWANLNS"

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
					Comment
		Getmain length for normal stack FRR SDWA			
					End of Comment
240	(F0)	X'0'	0	SDWANOPR	"0" THIS FIELD IS ONLY DEFINED IN ASSEMBLER VERSION OF THE SDWA. ITS PURPOSE IS TO FLAG INCOMPATIBLE USE OF SETRP AND SDWA.

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SDWA	,

SDWA Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
SDWA	0		SDWAASCM	6A	C0
SDWA	0		SDWAASID	120	
SDWAABCC	4		SDWAASI1	76	
SDWAABTM	E8	8	SDWAASMP	FE	8
SDWAACF2	FD		SDWAASST	8C	
SDWAACF3	FE		SDWAAX	178	
SDWAACF4	FF		SDWABEA	150	
SDWAACR	D5	8	SDWABLSD	2	80
SDWAADD1	6D		SDWABSA	19A	2
SDWAADD2	7D		SDWACBS	144	4
SDWAAEC1	70		SDWACCA	C	30
SDWAAEC2	80		SDWACCF	3D	80
SDWAALLN	145	8	SDWACCP	14	30
SDWAAMF1	6C		SDWACCRC	3D	
SDWAAMF2	7C		SDWACC1	6A	30
SDWAAMOD	F0	80	SDWACC2	7A	30
SDWAAPLW	110		SDWACHNG	D8	1
SDWAARCH	19B		SDWACID	0	
SDWAAREA	100		SDWACIDB	38	
SDWAAREB	104		SDWACLEN	F0	1C8
SDWAAREC	108		SDWACLSE	1A8	
SDWAARED	10C		SDWACLUP	EB	80
SDWAAREE	110		SDWACMKA	8	
SDWAAREF	114		SDWACMKP	10	
SDWAARER	D8		SDWACML	FD	1
SDWAARE0	D8		SDWACMLA	17C	
SDWAARE1	DC		SDWACMPC	5	
SDWAARE2	E0		SDWACMPF	4	
SDWAARE3	E4		SDWACMS	FF	2
SDWAARE4	E8		SDWACNTX	3	4
SDWAARE5	EC		SDWACOMP	188	
SDWAARE6	F0		SDWACOMU	180	
SDWAARE7	F4		SDWACPID	D6	
SDWAARE8	F8		SDWACPUA	F8	
SDWAARE9	FC		SDWACPUI	EF	
SDWAARGU	D4	1	SDWACRC	2C	
SDWAARSA	140		SDWACREA	C0	
SDWAARSB	144		SDWACREB	C4	
SDWAARSC	148		SDWACREC	C8	
SDWAARSD	14C		SDWACRED	CC	
SDWAARSE	150		SDWACREE	D0	
SDWAARSF	154		SDWACREF	D4	
SDWAARSV	118		SDWACRER	98	
SDWAARS0	118		SDWACRE0	98	
SDWAARS1	11C		SDWACRE1	9C	
SDWAARS2	120		SDWACRE2	A0	
SDWAARS3	124		SDWACRE3	A4	
SDWAARS4	128		SDWACRE4	A8	
SDWAARS5	12C		SDWACRE5	AC	
SDWAARS6	130		SDWACRE6	B0	
SDWAARS7	134		SDWACRE7	B4	
SDWAARS8	138		SDWACRE8	B8	
SDWAARS9	13C		SDWACRE9	BC	
SDWAASCB	88		SDWACRGS	174	

SDWA Cross Reference

Name	Hex Offset	Hex Value
SDWACR3	174	
SDWACR4	178	
SDWACSCT	12C	
SDWACTL1	8	
SDWACTL2	10	
SDWACTS	EB	10
SDWACWT	FC	0
SDWAC64_XM	E0	
SDWAC64A	118	
SDWAC64B	120	
SDWAC64C	128	
SDWAC64D	130	
SDWAC64E	138	
SDWAC64F	140	
SDWAC64S	C8	
SDWAC640	C8	
SDWAC641	D0	
SDWAC642	D8	
SDWAC643	E0	
SDWAC643_KM	E4	
SDWAC643_SASID	E6	
SDWAC644	E8	
SDWAC644_AX	EC	
SDWAC644_PASID	EE	
SDWAC645	F0	
SDWAC646	F8	
SDWAC647	100	
SDWAC648	108	
SDWAC649	110	
SDWADAET	6C	
SDWADAEW	6C	
SDWADDAT	144	
SDWADEC1	6A	4
SDWADEC2	7A	4
SDWADEND	F0	
SDWADISP	FE	10
SDWADLEN	F0	F0
SDWADLST	141	40
SDWADM	144	1
SDWADOA	C	4
SDWADOP	14	4
SDWADPFS	141	
SDWADPF2	142	
SDWADPID	140	
SDWADPLA	13C	
SDWADPSA	148	
SDWADPSL	148	
SDWADPSW	146	4
SDWADPT	141	80
SDWADPVA	192	
SDWADREG	146	20
SDWADSAH	146	40
SDWADSAS	146	80
SDWADSPP	14	
SDWADSR	0	
SDWADSRP	0	
SDWADUCT	158	
SDWADUMP	140	
SDWADVS3	142	80
SDWADXC	77	
SDWADXMX	F0	F
SDWADXSL	0	
SDWADXSR	0	
SDWAEADR	4C	
SDWAEAS	EA	8
SDWAEBC	192	40
SDWAECT1	69	8
SDWAECT2	79	8
SDWAEC1	68	
SDWAEC2	78	
SDWAEEND	168	
SDWAELEN	F0	168
SDWAEMK1	68	
SDWAEMK2	78	
SDWAEND	298	
SDWAENRB	E9	4
SDWAENSN	141	20
SDWAEPA	60	
SDWAERFL	EB	1
SDWAERR	145	40
SDWAERRA	E8	
SDWAERRB	E9	
SDWAERRC	EA	
SDWAERRD	EB	
SDWAERTM	18C	
SDWAESAM	19B	1
SDWAESET	2	8
SDWAESTX	199	10
SDWAEUA	C	2
SDWAEUP	14	2
SDWAEXP1	6A	2
SDWAEXP2	7A	2
SDWAEXTA	8	1
SDWAEXTP	10	1
SDWAEXT1	68	1
SDWAEXT2	78	1
SDWAFAIN	7C	
SDWAFASG	1	2
SDWAFCPU	0	80
SDWAFIOB	4	
SDWAFLCK	0	
SDWAFLEN	F0	9A8
SDWAFLE1	2	
SDWAFLE2	3	
SDWAFLE3	4	
SDWAFLE4	5	
SDWAFLGS	E8	
SDWAFLKE	2	
SDWAFLK1	0	
SDWAFLK2	1	
SDWAFLLK	FF	1
SDWAFLSQ	D9	4
SDWAFMID	EC	
SDWAFPA	C	8
SDWAFPO1	6A	8
SDWAFPO2	7A	8
SDWAFPP	14	8
SDWAFPRX	D5	2
SDWAFREE	FD	4
SDWAFRLK	3E	20
SDWAFRM1	148	
SDWAFRM2	150	
SDWAFRM3	158	
SDWAFRM4	160	
SDWAFRRE	50	
SDWAFRSA	FE	40
SDWAFRSC	1	10
SDWAFRSD	FE	1
SDWAFRSG	1	8
SDWAFRSM	0	8
SDWAFRSS	1	1
SDWAFRSX	FE	80
SDWAFSQA	D9	8
SDWAFTRC	0	4
SDWAFVSF	1	4
SDWAFVSP	FE	20
SDWAGLBL	EA	1
SDWAGRSI	4	80
SDWAGRSV	18	
SDWAGR00	18	
SDWAGR01	1C	
SDWAGR02	20	
SDWAGR03	24	
SDWAGR04	28	
SDWAGR05	2C	
SDWAGR06	30	

Name	Hex Offset	Hex Value
SDWAGR07	34	
SDWAGR08	38	
SDWAGR09	3C	
SDWAGR10	40	
SDWAGR11	44	
SDWAGR12	48	
SDWAGR13	4C	
SDWAGR14	50	
SDWAGR15	54	
SDWAGTF	144	8
SDWAG64	0	
SDWAG64H	88	
SDWAG6400	0	
SDWAG6401	8	
SDWAG6402	10	
SDWAG6403	18	
SDWAG6404	20	
SDWAG6405	28	
SDWAG6406	30	
SDWAG6407	38	
SDWAG6408	40	
SDWAG6409	48	
SDWAG6410	50	
SDWAG6411	58	
SDWAG6412	60	
SDWAG6413	68	
SDWAG6414	70	
SDWAG6415	78	
SDWAHEX	192	80
SDWAHLHI	40	
SDWAHRC	2C	
SDWAICD1	73	
SDWAICD2	83	
SDWAIC1H	72	
SDWAIC2H	82	
SDWAID	293	
SDWAIDNT	C8	
SDWAIEND	10	
SDWAILA	C	C0
SDWAILC1	71	
SDWAILC2	81	
SDWAILEN	F0	10
SDWAILP	14	C0
SDWAIL1	71	6
SDWAIL2	81	6
SDWAIMC1	73	40
SDWAIMC2	83	40
SDWAINC1	72	
SDWAINC2	82	
SDWAINSF	D5	4
SDWAINTA	A	
SDWAINTC	D9	40
SDWAINTF	199	80
SDWAINTP	12	
SDWAINT1	6A	
SDWAINT2	7A	
SDWAINVP	D4	10
SDWAIO	145	80
SDWAIOA	8	FE
SDWAIOBR	64	
SDWAIOCB	0	2
SDWAIOFS	EE	
SDWAIOHT	EE	40
SDWAIOMA	0	
SDWAIOP	10	FE
SDWAIOQR	EE	80
SDWAIO1	68	2
SDWAIO2	78	2
SDWAIPC1	73	3F
SDWAIPC2	83	3F
SDWAIPLW	10C	
SDWAIPRG	FE	2
SDWAIPR1	73	80
SDWAIPR2	83	80
SDWAIRB	EA	20
SDWAIUCB	FF	80
SDWAIULW	104	
SDWAIXDS	2	1
SDWAIXRE	3	20
SDWAIXSC	2	4
SDWAIXSH	3	80
SDWAIXSR	2	2
SDWAKEAX	3E	8
SDWAKEYA	9	F0
SDWAKEYP	11	F0
SDWAKEY1	69	F0
SDWAKEY2	79	F0
SDWAKM	174	
SDWALASG	C	
SDWALCL	EA	2
SDWALCPU	FA	
SDWALDIS	E9	2
SDWALEN	F0	298
SDWALEND	20	
SDWALKWA	100	
SDWALKWS	100	
SDWALLEN	F0	20
SDWALNTH	C9	
SDWALRSA	18	
SDWALRSC	1C	
SDWALRSD	100	
SDWALRSG	8	
SDWALRSS	10	
SDWALRSX	14	
SDWALSED	1A4	
SDWALSLV	1AC	
SDWALSQA	144	20
SDWALVL2	142	4
SDWAMABD	EB	8
SDWAMCH	CC	
SDWAMCHD	D5	
SDWAMCHI	D4	
SDWAMCHK	E8	80
SDWAMCHO	DA	
SDWAMCHS	D4	
SDWAMCIC	8	
SDWAMCIV	EB	2
SDWAMCKA	9	4
SDWAMCKP	11	4
SDWAMCK1	69	4
SDWAMCK2	79	4
SDWAMDAT	1C	
SDWAMLEN	F0	6A8
SDWAMLNP	F0	688
SDWAMLVL	1C	
SDWAMODN	124	
SDWAMOD1	6C	80
SDWAMOD2	7C	80
SDWAMSER	D8	2
SDWAMST1	19C	
SDWAMST2	1A0	
SDWAMVRS	24	
SDWAMWPA	9	
SDWAMWPP	11	
SDWAMWP1	69	
SDWAMWP2	79	
SDWANAME	58	
SDWANIOP	EE	10
SDWANLK1	4	20
SDWANLNS	F0	1F0
SDWANMFS	E9	40
SDWANOIO	EE	20
SDWANOPR	F0	0
SDWANRBE	EB	40
SDWANRC1	0	
SDWANRC2	0	
SDWANRC3	0	
SDWANREC	3F	0

Name	Hex Offset	Hex Value
SDWANSWA	D8	4
SDWANSWP	D8	8
SDWANUC	144	80
SDWANUCL	D9	10
SDWANXTA	D	
SDWANXTP	15	
SDWANXT1	6C	
SDWANXT2	7C	
SDWAOABF	90	
SDWAOCMP	91	
SDWAOCRC	94	
SDWAOCUR	74	
SDWAOFLN	D9	80
SDWAOLEN	F0	6A0
SDWAOLK1	4	10
SDWAOPTM	FF	4
SDWAORCF	90	4
SDWAOREQ	90	80
SDWAOSTC	90	10
SDWAOSTP	90	40
SDWAPARM	0	
SDWAPARQ	FC	
SDWAPCEA	1A0	
SDWAPCEP	19C	
SDWAPCHK	E8	40
SDWAPDAT	146	
SDWAPDIP	E9	80
SDWAPEND	20	
SDWAPERC	EA	10
SDWAPER1	68	40
SDWAPER2	78	40
SDWAPGFX	D9	2
SDWAPGIO	E8	1
SDWAPGM1	69	1
SDWAPGM2	79	1
SDWAPGTA	76	
SDWAPLEN	F0	20
SDWAPMKA	C	
SDWAPMKP	14	
SDWAPREF	D8	20
SDWAPRIM	17A	
SDWAPRM2	19C	
SDWAPSTI	FC	10
SDWAPSWU	D5	20
SDWAPSW16	158	
SDWAPTRS	0	
SDWAPTX1	72	2
SDWAPTX2	82	2
SDWAQQS	144	2
SDWARA	190	
SDWARASR	1AE	0
SDWARA24	1AE	1
SDWARA31	1AE	2
SDWARA64	1AE	3
SDWARBAD	58	
SDWARCDE	FC	
SDWARCDF	D4	40
SDWARCF	4	4
SDWARCRD	FD	80
SDWARC1	0	
SDWARC1P	76	
SDWARC1Z	0	
SDWARC2	0	
SDWARC3	0	
SDWARC4	0	
SDWARC5	0	
SDWARC5L	F0	90
SDWAREAF	3D	40
SDWARECA	F4	
SDWARECP	124	
SDWAREGU	D5	40
SDWARELEASECODE	81	
SDWARELEASECODEVALID	199	1
SDWAREMR	3E	40
SDWAREND	F0	
SDWAREQ	4	80
SDWARERR	FD	10
SDWARETF	3E	
SDWARETY	FC	4
SDWAREXN	134	
SDWARFSA	DC	
SDWARFSE	0	
SDWARFSH	0	
SDWARFSL	4	
SDWARFXM	FD	40
SDWARGSV	3	2
SDWARKEY	E8	20
SDWARLEN	F0	F0
SDWARLK1	4	1
SDWARLK2	4	2
SDWARLK3	4	4
SDWARLNS	F0	3F0
SDWARPIV	EB	4
SDWARRL	30	
SDWARSRC	D4	8
SDWARSRF	D4	4
SDWARSR1	D8	
SDWARSR2	D9	
SDWARTAM	1AE	
SDWARTYA	F0	
SDWARTYF	F0	
SDWART12	7C	
SDWART15	3E	80
SDWASABC	90	
SDWASALL	FE	4
SDWASC	5	
SDWASCK	D5	10
SDWASCKB	CC	
SDWASCKE	D0	
SDWASCND	176	
SDWASDAT	144	
SDWASDA0	144	
SDWASDA1	145	
SDWASDRC	3C	
SDWASDRN	68	
SDWASEN	10	
SDWASEND	1C8	
SDWASEQ#	122	
SDWASERP	FD	2
SDWASERV	0	
SDWASFLG	19A	
SDWASGA	C	1
SDWASGN1	6A	1
SDWASGN2	7A	1
SDWASGP	14	1
SDWASKIP	EA	4
SDWASKPR	DA	80
SDWASKYF	D5	80
SDWASLEN	F0	7B8
SDWASLK1	4	40
SDWASLST	141	2
SDWASNAM	1C0	
SDWASNM	1B8	
SDWASNPA	140	
SDWASPER	D9	20
SDWASPID	C8	
SDWASPIN	FD	20
SDWASPL	F0	10
SDWASPLE	0	
SDWASPLN	0	
SDWASPLS	2	
SDWASPMX	0	7
SDWASPN	48	
SDWASPVA	9	1
SDWASPVP	11	1
SDWASQA	144	40

Name	Hex Offset	Hex Value
SDWASRBM	E9	1
SDWASRBS	E9	10
SDWASRBT	E9	20
SDWASRME	5	80
SDWASRSV	88	
SDWASRVL	D4	80
SDWASRVP	4	
SDWASR00	88	
SDWASR01	8C	
SDWASR02	90	
SDWASR03	94	
SDWASR04	98	
SDWASR05	9C	
SDWASR06	A0	
SDWASR07	A4	
SDWASR08	A8	
SDWASR09	AC	
SDWASR10	B0	
SDWASR11	B4	
SDWASR12	B8	
SDWASR13	BC	
SDWASR14	C0	
SDWASR15	C4	
SDWASSA	19A	40
SDWASSD	3	1
SDWASSRS	19A	1
SDWASTAE	EB	20
SDWASTAF	EA	80
SDWASTAI	EA	40
SDWASTCC	4	10
SDWASTCK	CC	
SDWASTEP	4	40
SDWASTKN	1B0	
SDWASTRM	E8	1
SDWASUBL	142	2
SDWASUM	145	10
SDWASUPR	44	
SDWASVAL	199	2
SDWASVCD	E8	10
SDWASVCE	E8	4
SDWASVLD	19A	80
SDWASWA	144	10
SDWAS1	6A	80
SDWAS2	7A	80
SDWATADB	FF	8
SDWATALW	11C	
SDWATARR	3F	3
SDWATCB	78	
SDWATEAN	198	F
SDWATEAR	198	
SDWATEAV	199	40
SDWATEIV	199	20
SDWATEPC	199	8
SDWATERR	D5	1
SDWATEST	3F	2
SDWATEXC	E8	2
SDWATFRR	3F	1
SDWATIME	E0	
SDWATIRR	199	4
SDWATJPA	146	8
SDWATLEN	F0	898
SDWATLPA	146	10
SDWATO1	14C	
SDWATO2	154	
SDWATO3	15C	
SDWATO4	164	
SDWATRAN	74	
SDWATRM1	68	4
SDWATRM2	78	4
SDWATRNE	148	
SDWATRN2	84	
SDWATSVL	D4	20
SDWATXG64	0	
SDWATXPSW16	80	

Name	Hex Offset	Hex Value
SDWATYPE	3F	
SDWATYP1	E9	8
SDWAULUT	3	40
SDWAUPRG	FD	8
SDWAUP64	3E	10
SDWAURAL	193	
SDWAUSPL	146	2
SDWAVEQR	D9	1
SDWAVERF	16C	
SDWAVERI	16C	
SDWAVID	16E	
SDWAVRA	194	
SDWAVRAL	190	
SDWAVRAM	192	20
SDWAVRCN	D8	10
SDWAVRIV	D4	2
SDWAVSN	16E	9
SDWAVS10	16E	8
SDWAVS11	16E	9
SDWAVS3	16E	1
SDWAVS4	16E	2
SDWAVS5	16E	3
SDWAVS6	16E	4
SDWAVS7	16E	5
SDWAVS8	16E	6
SDWAVS9	16E	7
SDWAWATA	9	2
SDWAWATP	11	2
SDWAWAT1	69	2
SDWAWAT2	79	2
SDWAWLMQ	3	8
SDWAWLMR	3	10
SDWAXDS	2	40
SDWAXEME	18	
SDWAXFLG	199	
SDWAXIOM	8	
SDWAXLCK	10	
SDWAXLK1	4	8
SDWAXLST	142	8
SDWAXM	174	
SDWAXPAD	170	
SDWAXQ	2	10
SDWAXRC5	1C	
SDWAXRES	2	20
SDWAXSPL	C	
SDWAZARC	19B	1
SDWA2CID	168	
SDWA5END	90	

SDWORK Information

SDWORK Heading Information

Common Name: SVC DUMP WORK AREA
Macro ID: IHASDWRK
DSECT Name: SDWORK
Owning Component: SVC Dump (SCDMP)
Eye-Catcher ID: SDW1
Offset: 0
Length: 4
Storage Attributes: Main Storage: One per system
Subpool: 227
Key: 0
Residency: Below the 16M line
Size: 376 bytes
Created by: IEAVTSDI
Pointed to by: RTCTSDWK
Serialization: RTCTSDPL FIELD (ONE ACCESS AT A TIME)
Function: THE SVC DUMP WORK AREA IS USED BY SVC DUMP DURING ITS PROCESSING TO CONTAIN POINTERS AND DATA AREAS WHICH ARE USED BY MORE THAN ONE SVC DUMP MODULE.

SDWORK Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	408	SDWORK	SVC DUMP WORK AREA
0	(0)	CHARACTER	40	SDPASS	AREA WHICH IS USED TO PASS INFORMATION FROM ONE SVC DUMP TO ANOTHER. THIS AREA IS NOT CLEARED AT THE START OF A DUMP.
0	(0)	CHARACTER	4	SDID	SDWORK ACRONYM
4	(4)	CHARACTER	4	*	RESERVED
8	(8)	SIGNED	2	*	RESERVED
10	(A)	SIGNED	2	*	Reserved
12	(C)	CHARACTER	8	SDWNDSPB	TIME JUST BEFORE SYSTEM SET NON-DISPTACHABLE
20	(14)	CHARACTER	8	SDWNDSPE	TIME JUST AFTER SYSTEM SET NON-DISPATCHABLE
28	(1C)	UNSIGNED	4	SDSDSECB	SDS wait for next dump request ECB
32	(20)	UNSIGNED	4	SDWPSAPOOL	PSA copy buffer cpool ID
36	(24)	UNSIGNED	4	SDWWRKPOOL	SRB workarea cpool ID
40	(28)	CHARACTER	368	SDCLEAR	AREA WHICH IS CLEARED AT THE START OF EVERY DUMP
40	(28)	CHARACTER	24	SDWSES	Data related to dumping the STRLIST
40	(28)	UNSIGNED	4	SDWSSRB#	Number of STRLIST capture SRBs scheduled. This count is decremented as each SRB completes
44	(2C)	UNSIGNED	4	SDWSCECB	STRLIST structure capture complete ECB
48	(30)	BITSTRING	4	SDWSESF	Flags
		1...		SDWOSSS	Set by SDS indicating at least one SRB was scheduled to capture structure data
52	(34)	ADDRESS	4	SDWSR14	Save R14 in SDSNOCAP
56	(38)	SIGNED	4	SDWLOOPI	LoopIndex in SDSNOCAP
60	(3C)	ADDRESS	4	SDWSASCB	ASCB address of the address space waiting on SDWSCECB
64	(40)	CHARACTER	36	*	Reserved
100	(64)	CHARACTER	8	SDWSSTOK	STOKEN of subspace
108	(6C)	SIGNED	4	SDWSALET	Alet of subspace
108	(6C)	ADDRESS	4	SDWDUCTR	DUCT real address
112	(70)	CHARACTER	4	SDFLAGS	FLAG BYTES
112	(70)	BITSTRING	1	SDFLAGS1	FIRST FLAG BYTE
		1...		SDRTM2TR	1=IEAVTSDM IS CALLED TO LOCATE TRACE TABLE OFF RTM2WA AND IF SYSMDUMP AND AN UNAUTHORIZED REQUESTOR COMPRESS TRACE DATA
		.1..		SDLOCPSW	PSW POINTS TO LOCAL AREA
		..1.		SDWCDSD	0=SCOPE(ALL) DATA SPACES HAVE NOT BEEN DUMPED YET 1=SCOPE(ALL) DATA SPACES DUMPED
		...1		SDWCLSDC	SDC CLEANUP INDICATOR
		.... 1...		SDWDWTAT	1=IEAVTDWT has been ATTACHed
		.... .1..		SDWDWTP	1=IEAVTDWT has been POSTed
		.... ..1.		SDWGLCAP	Global capture in progress
		1		*	RESERVED
113	(71)	BITSTRING	1	SDFLAGS2	
		1...		SDWUPROB	SYSM Problem pgm state flag
114	(72)	CHARACTER	1	SDFLAGS3	
		1...		SDWWRTST	Let TSCC write the SDUMP statistics to the exit collection area
115	(73)	CHARACTER	1	SDFLAGS4	RESERVED
116	(74)	ADDRESS	4	SDWDPL	POINTER TO THE DPL

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
120	(78)	SIGNED	2	SDWTINDX	Save number of 1/2 second intervals spent waiting for an SRB to be dispatched. ENQ must be held to update this field
122	(7A)	CHARACTER	2	*	RESERVED
124	(7C)	CHARACTER	4	*	RESERVED
128	(80)	CHARACTER	4	*	RESERVED
132	(84)	ADDRESS	4	SDPDEP	POINTER TO COPY OF THE HEADER RECORD GETMAINED FOR POST EXIT PROCESSING
136	(88)	UNSIGNED	4	SDWCURMX	Value of MAXSPACE when MAXSPACE limit is reached
140	(8C)	SIGNED	4	SDWIOCNT	Used to indicate that SDUMP is actively capturing data into the data spaces. This is not an exact count of the number of pages captured.
140	(8C)	ADDRESS	4	SDWMNPTR	Pointer to an SQA-storage I/O counter for ABDUMP
144	(90)	ADDRESS	4	SDWIOAAD	I/O AREA ADDRESS FOR CALLS TO IEAVTSVO
148	(94)	UNSIGNED	4	SDSD2ECB	IEAVTSD2 processing complete ECB
152	(98)	ADDRESS	4	SDSUBGLB	POINTER TO GLOBAL SUBPOOL LIST
156	(9C)	ADDRESS	4	SDSUBLCL	POINTER TO LOCAL SUBPOOL LIST
160	(A0)	ADDRESS	4	SDSDWAPT	POINTER TO SDWA
164	(A4)	ADDRESS	4	SDTTCH2	Pointer to the trace table of the SNAPTRC which was issued when the system was reset to dispatchable prematurely
168	(A8)	ADDRESS	4	SDTRACRC	RETURN CODE PASSED BACK BY TRACE MACROS
172	(AC)	ADDRESS	4	SDTTCH	POINTER TO TRACE TABLE TO DUMP TTCH USED BY IEAVTSDM
176	(B0)	ADDRESS	4	SDTRCLEN	LENGTH OF TRACE TABLE
180	(B4)	SIGNED	4	SDRETCOD	RETURN CODE FROM DUMP ROUTINE
180	(B4)	CHARACTER	2	*	RESERVED
182	(B6)	UNSIGNED	1	SDNODUMP	NO DUMP REASON CODE RETURNED TO CALLER
183	(B7)	UNSIGNED	1	SDRETURN	SVC DUMP RETURN CODE INDICATING COMPLETE, PARTIAL, OR NO DUMP CONDITION
184	(B8)	CHARACTER	16	SDWSDRSN	SDUMP REASON CODES MAPPED BY IHASDRSN
200	(C8)	UNSIGNED	1	SDWSDRC	NO DUMP REASON CODES
201	(C9)	UNSIGNED	1	SDWEXITT	Exit type for SDREXITE
202	(CA)	UNSIGNED	2	SDWBLKCT	NUMBER OF RECORDS TO BE OUTPUTED PER EXCP
204	(CC)	UNSIGNED	2	SDWRINDX	CURRENT RECORD NUMBER USED TO INDEX INTO SDWBUFR, SDWCCW AND SDWIDAW
206	(CE)	UNSIGNED	2	SDWBINDX	CURRENT BUFFER INDEX USED TO INDEX INTO SDWBUFAD, SDWCCWAD AND SDWIDWAD. THE VALUE IS ALWAYS 0 OR 1. (SDWBINDX + 1) IS USED TO INDEX THE CURRENT BUFFER AND (2 - SDWBINDX) IS USED TO INDEX THE OTHER BUFFER
208	(D0)	UNSIGNED	4	SDSRBTHR	SRB threshold
212	(D4)	UNSIGNED	4	SDWDMPID	Unique number for PRDSEQ
212	(D4)	BITSTRING	3	SDWDMPTN	Bits 7-30 from time of dump
215	(D7)	BITSTRING	1	SDWDMPSN	Sequence number from RTSDDNUM
216	(D8)	UNSIGNED	4	SDCSCECB	COMMON-STORAGE-COPIED ECB
220	(DC)	UNSIGNED	4	SDSTRECB	SYSTEM-TRACE-COMPLETE ECB
224	(E0)	UNSIGNED	4	SDSRBCNT	COUNT OF SRB'S IN USE
228	(E4)	UNSIGNED	4	SDD00ECB	ECB FOR SDS TO POST D00 TO FINISH DUMPING PROCESS.
232	(E8)	ADDRESS	4	SDSDSWAS	SAVEAREA FOR SDS WORK AREA
236	(EC)	ADDRESS	4	SDRNGPT2	RANGE POINTER FOR USE BY SRBS
240	(F0)	BITSTRING	2	SDWECFP	IEAVTSCC FOOTPRINTS
		1...		SDWECSYS	SYSTEM DISPATCHABLE FOOTPRINT
		.11.		*	Reserved
		...1		SDWECFTT	FREE TRACE TABLE FOOTPRINT
		.... 1...		SDWECEOD	END OF DATA FOOTPRINT
		.... .1..		*	Reserved
		.... ..1.		SDWECWTO	Issue message IEA794I
		1		SDWECSD1	CALL TO IEAVTSD1 FOOTPRINT
241	(F1)	1...		SDWECECB	ECB POST/SRB SCHEDULE FOOTPRINT
		.1..		SDWECBFT	RESET THRESHOLD FOOTPRINT
		..1.		SDWMAXSP	WHEN ON INDICATES THAT IEAVTSCC has written on the MAXSPACE reached message
		...1 1111		*	Reserved
242	(F2)	BITSTRING	2	SDWEFLGS	STATUS FLAGS USED BY IEAVTSDC TO CLEANUP
		1...		SDWEINIO	SET ON WHEN DOING I/O, WHEN RECOVERY GETS CONTROL ON INDICATES THE LAST WRITE WAS UNSUCCESSFUL
		.1..		SDWE1RCD	AT LEAST ONE RECORD WRITTEN SUCCESSFULLY TO DUMP DATASET
		..1.		SDWESDGF	IEAVTSDG IS PAGEFIXED
		...1		SDWESSEF	IEAVTSSE IS PAGEFIXED
		.... 1...		SDWEVSMF	IEAVTVSM IS PAGEFIXED
		.... .1..		*	RESERVED
244	(F4)	CHARACTER	8	*	Reserved
252	(FC)	CHARACTER	2	SDSDWAAS	ASID OF SDWA
254	(FE)	UNSIGNED	1	SDWPARMC	REASON CODE FOR PARAMETER LIST VALIDATION
255	(FF)	BITSTRING	1	SDWUKEY	SYSMDUMP User Key
		1111		SDWKBITS	SYSMDUMP User Key bits
256	(100)	ADDRESS	8	SDWHVIRTLASTENT	Ptr to used High Virtual Range Table range

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
256	(100)	UNSIGNED	4	SDWHVIRTLASTENTHI	High fullword
260	(104)	ADDRESS	4	SDWHVIRTLASTENT31	31 bit pointer
264	(108)	CHARACTER	4	*	Reserved
268	(10C)	ADDRESS	4	SDSDWANX	CONTENTS OF SDWANXT1
272	(110)	CHARACTER	8	SDWCSTOK	When ECB specified - STOKEN of callers space. When SRB specified STOKEN of SRBASCB space (may be 0)
280	(118)	CHARACTER	8	SDWDTIME	Time of dump (used by SD2 to generate unique dump id for PRDSEQ)
280	(118)	UNSIGNED	4	SDWDTIMH	First word of time of dump
288	(120)	CHARACTER	28	SDWGDATA	Global storage dataspace data
288	(120)	CHARACTER	4	*	Flags
		1...		SDWGCREA	Dataspace created
		.1..		SDWGPACC	Dataspace added to PASN access list
288	(120)	BITSTRING	3	*	Reserved
292	(124)	ADDRESS	4	SDWGDSOR	Dataspace origin
296	(128)	BITSTRING	8	SDWGSTOK	Dataspace STOKEN
304	(130)	UNSIGNED	4	SDWGSECS	Number of DRPX Data Sections
308	(134)	ADDRESS	4	SDWGDNDS	Address of next available DRPX Data Section
312	(138)	CHARACTER	4	SDWGALET	Datapsace ALET
316	(13C)	CHARACTER	24	SDWDSPPL	Map DSPCALL parm list
316	(13C)	CHARACTER	1	SDWDSLVL	Level indicator-DSPCALL
317	(13D)	UNSIGNED	1	SDWDSRQT	Request type
318	(13E)	BITSTRING	1	SDWDSATR	Dataspace attributes
319	(13F)	BITSTRING	1	SDWDSZR0	Key of data space
		1111		SDWDSPKY	Storage protection key
		.... 1...		SDWDFPRO	Fetch protect bit
		.... .111		*	
320	(140)	BITSTRING	8	SDWDSSTK	STOKEN
328	(148)	ADDRESS	4	SDWDSPTR	Address of ASCB or ASTE
332	(14C)	CHARACTER	8	SDWDSDSN	Data space name
340	(154)	ADDRESS	4	SDWUCBAD	Ucb pointer. Note - It will be extracted from DEB and saved in this field by IEAVTSPR. Later it will be copied to DPL by IEAVTSXS so that it can be used to gain addressability to UCB once DUMPSRV serilization is lost.
344	(158)	CHARACTER	16	SDWCMSV	CMSET Savearea
360	(168)	CHARACTER	16	SDWXMSAV	CMSET Savearea 2
376	(178)	ADDRESS	8	SDTTCHBUF	POINTER TO TRACE TABLE TO DUMP TTCH BUFFERS USED BY IEAVTSDM
384	(180)	UNSIGNED	8	SDTRCBUFOBJS	NUMBER OF TRACE TABLE MEMORY OBJECTS
392	(188)	CHARACTER	16	*	RESERVED

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4168	SDOUTBUF	
0	(0)	CHARACTER	4168	*	SVC DUMP OUTPUT BUFFER

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	32	SDWDDRNG	LISTD ENTRY
0	(0)	ADDRESS	4	SDWDLDBA	BEGINNING ADDRESS
4	(4)	ADDRESS	4	SDWDLDEA	ENDING ADDRESS
		1...		LDSRANGE	ON - INDICATES LAST RANGE FOR THIS DATA SPACE
8	(8)	BITSTRING	8	SDWDSTOK	STOKEN
16	(10)	ADDRESS	4	SDWDRAA	POINTER TO REAL ASTE ADDRESS
20	(14)	CHARACTER	8	SDWDDSN	DATA SPACE NAME
28	(1C)	ADDRESS	4	SDWDOWNA	OWNING ASCB WHEN SYSMDUMP OR NEXT STOKEN ENTRY ASSOCIATED WITH A DIFFERENT ASID WHEN SDUMP

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	40	SDW_LIST64_ENTRY	LIST64 Entry
0	(0)	CHARACTER	8	SDW_LIST64_START_ADDR	Range start address
0	(0)	UNSIGNED	4	SDW_LIST64_STARTHH_WORD	Uppper 4 bytes
4	(4)	ADDRESS	4	SDW_LIST64_STARTLH_ADDR	Lower 4-bytes of the 64-bit address

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		1...		SDW_LIST64_STARTLH_HIBIT	Hi order bit
4	(4)	BITSTRING	3	*	
8	(8)	CHARACTER	8	SDW_LIST64_END_ADDR	Range end address
8	(8)	UNSIGNED	4	SDW_LIST64_ENDHH_WORD	Upper 4 bytes
12	(C)	ADDRESS	4	SDW_LIST64_ENDLH_ADDR	Lower 4-bytes of the 64-bit address
		1...		SDW_LIST64_ENDLH_HIBIT	Hi order bit
12	(C)	BITSTRING	3	*	
16	(10)	BITSTRING	8	SDW_LIST64_STOKEN	Stoken
24	(18)	ADDRESS	4	SDW_LIST64_ASTE_ADDR	Real storage address of the Aste
28	(1C)	CHARACTER	8	SDW_DATASPACE_NAME	DataSpace name
36	(24)	ADDRESS	4	SDW_OWNINGASCB	Owning ASCB when Sysmdump or next stoken entry associated with a different asid when SDUMP

SDWORK Constants

Len	Type	Value	Name	Description
4	CHARACTER	SDW1	SDWRKID	CONTROL BLOCK IDENTIFIER TO BE USED WITH SDID FIELD
4	HEX	FFFFFFFF	SDW_BAD_ALET	Bad ALET constant
8	CHAR HEX	FFFFFFFFFFFFFFFF	SDW_L64_DELIMITER	Value that immediately follows a LIST64-style list of ranges in the LISTD table

SDWORK Cross Reference

Name	Hex Offset	Hex Value
LDSRANGE	4	80
SDCLEAR	28	
SDCSCECB	D8	
SDD00ECB	E4	
SDFLAGS	70	
SDFLAGS1	70	
SDFLAGS2	71	
SDFLAGS3	72	
SDFLAGS4	73	
SDID	0	
SDLOCPSW	70	40
SDNODUMP	B6	
SDOUTBUF	0	
SDPASS	0	
SDPDEP	84	
SDRETCOD	B4	
SDRETURN	B7	
SDRNGPT2	EC	
SDRTM2TR	70	80
SDSDSECB	1C	
SDSDSWAS	E8	
SDSDWAAS	FC	
SDSDWANX	10C	
SDSDWAPT	A0	
SDSD2ECB	94	
SDSRBCNT	E0	
SDSRBTHR	D0	
SDSTRECB	DC	
SDSUBGLB	98	
SDSUBLCL	9C	
SDTRACRC	A8	
SDTRCBUFOBJS	180	
SDTRCLEN	B0	
SDTTCH	AC	
SDTTCHBUF	178	
SDTTCH2	A4	
SDW_DATASPACE_NAME	1C	
SDW_LIST64_ASTE_ADDR	18	
SDW_LIST64_END_ADDR	8	
SDW_LIST64_ENDHH_WORD	8	
SDW_LIST64_ENDLH_ADDR	C	
SDW_LIST64_ENDLH_HIBIT	C	80
SDW_LIST64_ENTRY	0	
SDW_LIST64_START_ADDR	0	
SDW_LIST64_STARTHH_WORD	0	
SDW_LIST64_STARTLH_ADDR	4	
SDW_LIST64_STARTLH_HIBIT	4	80
SDW_LIST64_STOKEN	10	
SDW_OWNINGASCB	24	
SDWBINDX	CE	
SDWBLKCT	CA	
SDWCDSD	70	20
SDWCLSDC	70	10
SDWCMSV	158	
SDWCSTOK	110	
SDWCURMX	88	
SDWDDRNG	0	
SDWDDSN	14	
SDWDFPRO	13F	08
SDWDLDBA	0	
SDWDLDEA	4	

Name	Hex Offset	Hex Value
SDWDMPID	D4	
SDWDMPSN	D7	
SDWDMPTN	D4	
SDWDOWNA	1C	
SDWDPL	74	
SDWDRAA	10	
SDWDSATR	13E	
SDWDSDSN	14C	
SDWDSLVL	13C	
SDWDSPKY	13F	F0
SDWDSPPL	13C	
SDWDSPTR	148	
SDWDSRQT	13D	
SDWDSSTK	140	
SDWDSTOK	8	
SDWDSZR0	13F	
SDWDTIME	118	
SDWDTIMH	118	
SDWDUCTR	6C	
SDWDWTAT	70	08
SDWDWTP	70	04
SDWECBFT	F1	40
SDWECECB	F1	80
SDWECEOD	F0	08
SDWECFP	F0	
SDWECFTT	F0	10
SDWECSD1	F0	01
SDWECSYS	F0	80
SDWECWTO	F0	02
SDWEFLGS	F2	
SDWEINIO	F2	80
SDWESDGF	F2	20
SDWESSEF	F2	10
SDWEVSMF	F2	08
SDWEXITT	C9	
SDWE1RCD	F2	40
SDWGALET	138	
SDWGCREA	120	80
SDWGDATA	120	
SDWGDNDS	134	
SDWGDSOR	124	
SDWGLCAP	70	02
SDWGPACC	120	40
SDWGSECS	130	
SDWGSTOK	128	
SDWHVIRTLASTENT	100	
SDWHVIRTLASTENTHI	100	
SDWHVIRTLASTENT31	104	
SDWIOAAD	90	
SDWIOCNT	8C	
SDWKBITS	FF	F0
SDWLOOPI	38	
SDWMAXSP	F1	20
SDWMNPTR	8C	
SDWNDSPB	C	
SDWNDSPE	14	
SDWORK	0	
SDWOSSS	30	80
SDWPARMC	FE	
SDWPSAPOOL	20	
SDWRINDX	CC	
SDWSALET	6C	
SDWSASCB	3C	
SDWSCECB	2C	
SDWSDRC	C8	
SDWSDRSN	B8	
SDWSES	28	
SDWSESF	30	
SDWSR14	34	
SDWSSRB#	28	
SDWSSTOK	64	
SDWTINDX	78	
SDWUCBAD	154	
SDWUKEY	FF	
SDWUPROB	71	80
SDWWRKPOOL	24	
SDWWRTST	72	80
SDWXMSAV	168	

SETXPL Information

SETXPL Heading Information

Common Name: Extended Parameter List for the SET Keyword
Macro ID: IEEZB831
DSECT Name: SETPARMS
Owning Component: System Command - SVC 34 (SC1B8)
Eye-Catcher ID: None
Storage Attributes: Residency: IEEMB811 dynamic storage
Size: 24 bytes plus a variable number of 2-byte fields at offset 24.
Created by: IEEMB811 - SET Keyword Scanner module
Pointed to by: Normal MVS linkage as seventh parameter
Serialization: None
Function: It is passed as the seventh parameter to the SET Keyword Processors and contains the new parameters that must be passed to the Processors.

SETXPL Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	SETPARMS	Parm list for Keyword Processors
0	(0)	UNSIGNED	1	SETPVRSN	Version Level
1	(1)	BITSTRING	1	SETPFLG1	Flags
		1...		SETPFLST	'L' option is requested to obtain list of parmlib statements as they processed
		.1..		SETPFLNP	Resources obtained
		..11 1111		*	Reserved
2	(2)	BITSTRING	2	SETPRSVD	Reserved
4	(4)	UNSIGNED	4	SETPCNID	4-byte Console ID
8	(8)	CHARACTER	8	SETPCART	Command and Response Token
16	(10)	ADDRESS	4	SETPLSTP	Pointer to the begining of a list of suffix values in NIP time format (xx,...xx)' or (xx)'@L2A
20	(14)	SIGNED	4	SETPSFXN	Number of operand entires in SETPSFXT table
24	(18)	CHARACTER	2	SETPSFXT (*)	Table contains operands (suffix names specified on keyword)

SETXPL Constants

Len	Type	Value	Name	Description
1	DECIMAL	1	SETPSP41	Version Level - HBB4410
1	DECIMAL	2	SETPSP42	Version Level - HBB4420
1	DECIMAL	2	SETPVERS	Version Level - Current
1	DECIMAL	2	SETPSFXL	Length of each entry. This must be changed if KEYnDLM in IEEMB876 is changed.

SETXPL Cross Reference

Name	Hex Offset	Hex Value
SETPARMS	0	
SETPCART	8	
SETPCNID	4	
SETPFLG1	1	
SETPFLNP	1	40
SETPFLST	1	80
SETPLSTP	10	
SETPRSVD	2	
SETPSFXN	14	
SETPSFXT	18	
SETPVRSN	0	

SGTE Information

SGTE Heading Information

Common Name: SEGMENT TABLE ENTRY
Macro ID: IARSGTE
DSECT Name: SGTE
Owning Component: Real Storage Manager (SC1CR)
Eye-Catcher ID: None
Storage Attributes: Main Storage: Yes
Virtual Storage: Yes
Subpool: N/A
Key: 0
Data Space: Yes
Residency: Above 16Meg Virtual for address spaces Anywhere for Data spaces
Size: 4 bytes
Created by: RSM
Pointed to by: RABSGT field of the RAB Data Area
Serialization: Varies
Function: Maps a Segment Table Entry

SGTE Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	4	SGTE	
0	(0)	CHARACTER	4	SGTALL	
0	(0)	CHARACTER	3	SGTPTRSA	REAL ADDRESS OF PGT
0	(0)	SIGNED	2	SGTINDEX	INDEX TO SLT FOR THIS SEGMENT
0	(0)	CHARACTER	1	*	RESERVED
1	(1)	CHARACTER	1	SGTSKEY	STORAGE KEY VALUE FOR PAGES
		1111 1...		SGTSKEY5	STORAGE KEY 5 BITS
		1111		SGTKEY	KEY FOR ALL PAGES IN SEGMENT
		.... 1...		SGTFPROT	FETCH PROTECT ALL PAGES IF ON
		.... .111		*	RESERVED
2	(2)	BITSTRING	1	SGTFLGS1	SGTE FLAGS (IF SEGMENT INVALID)
		11..		SGTTYPNO	INVALID SEGMENT TYPE NUMBER
		..11 11..		*	RESERVED
		.... ..1.		SGTPGTBL	THE PGT FOR THIS SEGMENT HAS BEEN BUILT
		1		SGTANYWH	PAGES MAY BE BACKED ANYWHERE

Comment

NOTE - THE ABOVE THREE BYTES ARE VALID ONLY IF SGTINV=1. THE
BYTES ARE USED TO FORM THE PGT'S REAL ADDRESS IF SGTINV=0

End of Comment

3	(3)	CHARACTER	1	SGTBYTE	FLAGS PLUS PGT LENGTH
		11..		*	RESERVED. THESE TWO BITS ARE USED TO FORM THE PGT'S REAL ADDRESS IF SGTINV=0
		1...		SGTASSOC	VDAC SPACE MAY NEED TO BE ASSOCIATED IN THIS SEGMENT
		.1..		SGTMPEA	THERE EXIST MPES ASSOCIATED WITH THIS SEGMENT.
		..1.		SGTINV	SEGMENT IS INVALID
		...1		SGTCOM	SEGMENT IS A COMMON AREA SEG
		 1111		SGTPTL	(PGT SIZE/64)-1

SGTE Constants

Len	Type	Value	Name	Description
Comment				
CONSTANTS USED TO DEFINE SGTE TYPE				
End of Comment				
0	BIT	00	SGTTYP0	TYPE 0 - ALL PAGES IN THE SEGMENT ARE IN A NON-GETMAINED STATE
0	BIT	01	SGTTYP1	TYPE 1 - ALL PAGES IN SEGMENT ARE GETMAINED WITH THE SAME KEY, FETCH PROTECTION, AND BACK ANYWHERE INDICATION
0	BIT	10	SGTTYP2	TYPE 2 - NOTHING HAS CHANGED IN THE SEGMENT SINCE THE SEGMENT WAS MADE INVALID EXCEPT POSSIBLY THE STORAGE KEY
0	BIT	11	SGTTYP3	TYPE 3 - THE SGTE POINTS TO A SECOND LEVEL TABLE (SLT)

SGTE Cross Reference

Name	Hex Offset	Hex Value
SGTALL	0	
SGTANYWH	2	01
SGTASSOC	3	80
SGTBYTE	3	
SGTCOM	3	10
SGTE	0	
SGTFLGS1	2	
SGTFPROT	1	08
SGTINDEX	0	
SGTINV	3	20
SGTKEY	1	F0
SGTMPEA	3	40
SGTPGTBL	2	02
SGTPTL	3	0F
SGTPTRSA	0	
SGTSKEY	1	
SGTSKEY5	1	F8
SGTTYPNO	2	C0

SHDR Information

SHDR Heading Information

Common Name: SLIP Header
Macro ID: IHASHDR
DSECT Name: SHDR, SHDRX
Owning Component: SLIP (SCSLP)
Eye-Catcher ID: SHDR - for SHDR, SHDX - for SHDRX
Offset: 0 for both SHDR and SHDRX
Length: 4 for both SHDR and SHDRX
Storage Attributes: Subpool: 245 for both SHDR and SHDRX
Key: 0 for both SHDR and SHDRX
Residency: BELOW for SHDR, ANY for SHDRX
Size: SHDR - 208 bytes
SHDRX - 2624 bytes
Created by: IEECB905 when the first SLIP trap is set.
Pointed to by: CVTRTMS field of the CVT data area
Serialization: Compare & Swap, Compare Double & Swap
Function: The SHDR is the head of the PER and non-PER SCE chains. It points to the first and last elements in the SCE chains.

SHDR Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	208	SHDR	
0	(0)	CHARACTER	4	SHDRCBID	CONTROL BLOCK ID = SHDR
4	(4)	ADDRESS	4	SHDRPFC	IEAVTSLP USE COUNT
8	(8)	CHARACTER	8	SHDRCTFW	TO DELETE ENTRIES,MUST CDS
8	(8)	ADDRESS	4	SHDRCTR	PROCESSOR SERIALIZATION CTR
12	(C)	ADDRESS	4	SHDRFWD	FORWARD PTR TO SCE CHAIN
16	(10)	ADDRESS	4	SHDRBKWD	BACKWARD PTR TO SCE CHAIN
20	(14)	CHARACTER	4	SHDRFLCS	Label to CS flags
20	(14)	BITSTRING	1	SHDRFLGS	FLAGS
		1...		*	RESERVED
		.1..		SHDRDELP	DELETE IS PENDNG ON PREV SCE
		..1.		SHDRCRTN	COMMUNICATION RTN IS ACTIVE
		...1		SHDRSRBR	RESCHEDULE GLOBAL SRB
		.... 1...		SHDRPERI	ENABLED PER IGNORE TRAPS or subtrap action traps WITH THE SAME PER EVENT (IF, SA,SB) AS DEFINED IN THE ENABLED NON-IGNORE PER TRAP EXIST BETWEEN THE ENABLED NON-IGNORE PER TRAP AND THE END OF THE CHAIN
		.... .1..		SHDRM415	IEA415I MSG FLAG
		.... ..1.		SHDRM422	IEA422I MSG FLAG
		1		SHDRENABLEDMSGIDTRAPSEXIST	
21	(15)	BITSTRING	1	SHDRFLG2	FLAGS
		1...		SHDRPSTM	IND TO POST THE MESSAGE RTN, SET AND TESTED BY IEAVTSLP
		.1..		SHDRGCFC	IND GETCELL-FREECELL PROBLEM HAS OCCURRED
		..1.		SHDRPVTP	PER TRAP IS PVTMOD
		...1		SHDRPVTA	PVTMOD PER TRAP IS ACTIVE (ADDRESSES DETERMINED). ONLY VALID WITH SHDRPVTP
		.... 1...		SHDRPVLP	PVTMOD PER TRAP IS WAITING FOR LOAD OR VIRTUAL FETCH
		.... .1..		SHDRSTDP	DISABLED PER TRAP, ACTION = STRACE
		.... ..1.		SHDRCHNE	CHAINING ERROR IN SCE CHAIN
		1		SHDRAEXITDEF	SLIPACTIONEXIT dynamic exit defined
22	(16)	ADDRESS	2	SHDRIDCT	CTR FOR GENERATING SCE ID
24	(18)	ADDRESS	4	SHDRPROC	SLIP PROCESSOR IEAVTSLP
28	(1C)	ADDRESS	4	SHDRPER	ENABLED NON-IGNORE PER TRAP
32	(20)	ADDRESS	4	SHDRSRTN	PER SELECT RTN (IEAVTJBN)
36	(24)	ADDRESS	4	SHDRPERR	PER TRAPS SCVA RANGE ENTRY
40	(28)	ADDRESS	4	SHDRPERA	PER TRAPS SCVA ASID ENTRY
44	(2C)	ADDRESS	4	SHDRPERJ	PER TRAPS SCVA JOBNAME ENTRY
48	(30)	CHARACTER	4	SHDRSEQ	SEQUENCE WORD
48	(30)	CHARACTER	2	*	RESERVED
50	(32)	CHARACTER	2	SHDRSEQA	ASID WHEN LOCAL RTN OWNS SEQ WORD
52	(34)	ADDRESS	4	SHDRSRB	GLOBAL SRB FOR PER
		1...		SHDRSRBA	AVAILABILITY FLAG (ON=AVAIL)
56	(38)	ADDRESS	4	SHDRPOST	SRB FOR VTSLP TO POST THE COMMUNICATION RTN
		1...		SHDRPSTA	AVAILABILITY FLAG (ON=AVAIL)
60	(3C)	CHARACTER	4	SHDRCPID	SRB CELL POOL ID
64	(40)	CHARACTER	4	SHDRECB	MESSAGE ECB

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
68	(44)	CHARACTER	4	SHDRGECB	ECB FOR IEAVTGLB TO ISSUE PGFIX
72	(48)	CHARACTER	4	SHDRLECB	ECB FOR IEAVTLCL TO ISSUE PGFIX
76	(4C)	CHARACTER	12	SHDRCREG	PARM AREA FOR UPDATING THE PER CONTROL REGS
76	(4C)	CHARACTER	4	SHDRCR9	CONTROL REGISTER 9
76	(4C)	CHARACTER	1	SHDRC9	EVENT MASK BYTE
		1...		SHDRC9SB	SUCCESSFUL BRANCH
		.1..		SHDRC9IF	INSTRUCTION FETCH
		..1.		SHDRC9SA	STORAGE ALTERATION
		...1		*	RESERVED
		.... 1...		SHDRC9SS	SA STURA
		.... .1..		SHDRC9ZAD	Zero Address Detection
		11		*	SPACER
77	(4D)	CHARACTER	1	SHDRC9B1	
		1...		SHDRC9S2	PER 2 SB
		.1..		*	RESERVED
		..1.		SHDRC9SE	SA SELECTION
80	(50)	CHARACTER	8	SHDRCRS	PER RANGE
80	(50)	CHARACTER	4	SHDRCR10	PER STARTING ADDRESS TO BE LOADED INTO CONTROL REG 10
84	(54)	CHARACTER	4	SHDRCR11	PER ENDING ADDRESS TO BE LOADED INTO CONTROL REG 11
88	(58)	ADDRESS	4	SHDRCMS1	ADDRESS OF THE CMSET SET ENTRY POINT
92	(5C)	ADDRESS	4	SHDRCMR1	ADDRESS OF THE CMSET RESET WITH AUTH CHECK ENTRY POINT
96	(60)	ADDRESS	4	SHDRCMR2	ADDRESS OF THE CMSET RESET WITHOUT AUTH CHECK ENTRY POINT
100	(64)	ADDRESS	4	SHDRSSHP	ADDRESS OF THE SPACE SWITCH HANDLER (IEAVTSSH)
104	(68)	CHARACTER	8	SHDRSSTM	AMOUNT OF TIME TO EXECUTE THE SPACE SWITCH PROCESSING FOR SLIP
112	(70)	ADDRESS	4	SHDRSS1P	ADDRESS OF THE SPACE SWITCH HANDLER ENTRY POINT FOR CMSET INTERCEPTS (IEAVTSS1)
116	(74)	ADDRESS	4	SHDRPCDE	ADDRESS OF CDE FOR PVTMOD PER TRAP
120	(78)	ADDRESS	4	SHDRPVL1	ADDRESS OF EXIT FROM CONTENTS SUPERVISOR WHEN CDE IS PUT ONTO JOB PACK QUEUE
124	(7C)	ADDRESS	4	SHDRPVD1	ADDRESS OF EXIT FROM CONTENTS SUPERVISOR WHEN CDE IS TAKEN OFF JOB PACK QUEUE
128	(80)	CHARACTER	8	SHDRPVMN	PVTMOD PER MODULE NAME
136	(88)	UNSIGNED	2	SHDRPVAS	PVTMOD PER ASID
138	(8A)	BITSTRING	1	SHDRPVFL	PVTMOD PER FLAGS
		1...		SHDRPVTL	IF ON, PRIVATE MODULE IS LOCAL
		.1..		SHDRPVTG	IF ON, PRIVATE MODULE IS GLOBAL
		..1.		SHDRPOSIXPATHNAMEUSED	Indicate POSIX pathname is specified
		...1 1111		*	RESERVED
139	(8B)	BITSTRING	1	SHDRFLAG	MORE FLAGS
		1...		SHDRSTRC	IF ON, ACTION = STRACE, PER TRAP
		.1..		SHDRSTFP	IF ON, ACTION = STRACE, FAST PATH
		..1.		SHDRSTSB	IF ON, ACTION = STRACE, PER SB
		...1 1111		*	RESERVED
140	(8C)	ADDRESS	4	SHDRPASC	PVTMOD PER ASCB ADDR
144	(90)	ADDRESS	4	SHDRPTCB	PVTMOD PER TCB ADDRESS
148	(94)	ADDRESS	4	SHDRPVR1	ADDRESS OF EXIT FROM VIRTUAL FETCH END OF TASK, END OF MEMORY RESOURCE MANAGERS
152	(98)	CHARACTER	4	SHDRSTID	ACTION=STRACE, ID OF TRAP
156	(9C)	SIGNED	4	SHDRMLC	ACTION=STRACE, CURRENT MATCHLIM
160	(A0)	UNSIGNED	2	SHDRMLT	ACTION=STRACE, TOTAL MATCHLIM DEFAULT
162	(A2)	CHARACTER	2	*	RESERVED
164	(A4)	ADDRESS	4	SHDRPERFIRST	For an active dynamic PER activation chain, this points to the first trap on the chain. For a single active PER trap this is the same as ShdrPer
168	(A8)	CHARACTER	8	SHDRCTF2	TO DELETE ENTRIES,MUST CDS. THIS QUEUE IS OF NON-PER SCE'S
168	(A8)	ADDRESS	4	SHDRCTR2	SERIALIZATION COUNTER
172	(AC)	ADDRESS	4	SHDRFWD2	FORWARD PTR TO SCE
176	(B0)	ADDRESS	4	SHDRBWD2	BACKWARD PTR TO SCE
180	(B4)	ADDRESS	4	SHDRIDQ	QUEUE OF IDS WITH ASSOCIATED SCE'S
184	(B8)	ADDRESS	4	SHDRIDQL	END OF ID QUEUE
188	(BC)	ADDRESS	4	SHDRWMSG	ADDRESS OF DISABLED CONSOLE MESSAGE AREA
		1...		SHDRWALK	Lock bit (C/S)
192	(C0)	ADDRESS	4	SHDRXADR	ADDRESS OF SHDR EXTENSION
196	(C4)	ADDRESS	4	SHDRCMS2	ADDRESS OF THE CMSET SET DIE=YES ENTRY POINT
200	(C8)	ADDRESS	4	SHDRCMR3	ADDRESS OF THE CMSET RESET CMSET WITH AUTH CHECK DIE=YES ENTRY POINT
204	(CC)	ADDRESS	4	SHDRCMR4	ADDRESS OF THE CMSET RESET WITHOUT AUTH CHECK DIE=YES ENTRY POINT 1@PAD ROUND UP LENGTH TO 8
208	(D0)	CHARACTER	0	*	End of SHDR

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
0	(0)	STRUCTURE	2984	SHDRX	SHDR EXTENSION, ABOVE 16M
0	(0)	CHARACTER	4	SHDRXID	SHDR EXTENSION ID
4	(4)	ADDRESS	4	SHDRSYNV	A=SYNCSVCD VCONS
		1...		SHDRSDIU	1 => SYNCSVCD AREA IN USE
8	(8)	ADDRESS	4	SHDRRCTX	ADDRESS OF IEAVTSLO
12	(C)	ADDRESS	4	SHDRADR	ADDRESS OF IEAVTADR
16	(10)	ADDRESS	4	SHDRGLB	ADDRESS OF IEAVTGLB
20	(14)	ADDRESS	4	SHDRCECB	ECB FOR COMMAND PROCESSOR IEECB941 TO USE FOR PGFIX
24	(18)	CHARACTER	4	SHDRXCSWORD	Work for CS
24	(18)	SIGNED	2	SHDRXLEN	LENGTH OF SHDRX
26	(1A)	CHARACTER	1	SHDRXFLGS1	Flags
		1...		SHDRXPERDEACTIVATIONINPROGRESS	Indicates a PER trap is being deactivated. This bit is turned on by the command processor for disablement or deletion of a PER trap and by the action processor for disablement via matchlim/prcntlim. If it is on control will not enter IEAVTSLD. It remains on until all of the PER workareas are freed by IEAVTGLB
27	(1B)	UNSIGNED	1	SHDRXPOSIXPATHNAMELENGTH	Length of specified pathname
28	(1C)	ADDRESS	4	SHDRXSSB	ADDRESS OF IEAVTSSB
32	(20)	ADDRESS	4	SHDRXIEAVTPVG@	Address of IEAVTPVG
36	(24)	ADDRESS	4	SHDRXPLP@	Address of IEAVTPLP
40	(28)	CHARACTER	3	*	RESERVED
43	(2B)	BITSTRING	1	SHDRXFLAGS2	Flags serialized by SLIP sequence word
		1...		SHDRXLPAMODORLPAEP	
		.1..		SHDRXMSGIDQUOTED	MSGID is quoted
44	(2C)	CHARACTER	8	*	RESERVED
52	(34)	CHARACTER	452	SHDRSYND	A=SYNCSVCD AREA
52	(34)	CHARACTER	40	SHDRSDSP	STOP PARAMETER LIST
92	(5C)	CHARACTER	12	SHDRSDOA	STOP/RESET OUTPUT AREA
104	(68)	CHARACTER	44	SHDRSDS1	SRB 1
148	(94)	CHARACTER	44	*	Reserved
192	(C0)	CHARACTER	48	SHDRSDP1	SRB 1 PARAMETER AREA
192	(C0)	CHARACTER	32	SHDRSDSD	RESERVED FOR SDUMP
224	(E0)	CHARACTER	4	SHDRSDAS	TARGET ASCB
240	(F0)	CHARACTER	72	SHDRSDSA	SAVE AREA
312	(138)	CHARACTER	24	SHDRSDRP	RESET PARAMETER LIST
336	(150)	CHARACTER	72	SHDRSDWA	WORK AREA
408	(198)	CHARACTER	24	*	Reserved
432	(1B0)	CHARACTER	72	*	RESERVED
504	(1F8)	CHARACTER	1160	SHDRXWMAOLD	This area used to be here. For ease of expansion, it is now just pointed to. This means that these 1160 bytes can be remapped.
1664	(680)	CHARACTER	44	SHDRXSRB	SRB POINTED TO BY SHDRPOST
1708	(6AC)	SIGNED	4	SHDRXAS#	NUMBER ASIDSA ASIDS
1712	(6B0)	CHARACTER	9	SHDRXASA (4294967312:562158632)	ASIDSA ASIDS/Jobnames
1712	(6B0)	BITSTRING	1	SHDRXAFLGS	
		1...		SHDRXAJOBNAMESPECIFIED	Jobname specified
1713	(6B1)	CHARACTER	8	SHDRXAJOBNAME	Jobname
1713	(6B1)	CHARACTER	2	SHDRXAASID	ASID
1856	(740)	SIGNED	4	SHDRXDS#	NUMBER DSSA SPACES
1860	(744)	CHARACTER	17	SHDRXDSA (4294967312:562159944)	DSSA SPACES
1860	(744)	BITSTRING	1	SHDRXDFLGS	
		1...		SHDRXDJOBNAMESPECIFIED	Jobname was specified
1861	(745)	CHARACTER	8	SHDRXDJOBNAME	Jobname
1861	(745)	CHARACTER	2	SHDRXDASID	ASID
1869	(74D)	CHARACTER	8	SHDRXDNM	NAME
2132	(854)	CHARACTER	236	SHDRXWST	A=WAIT Status area. This area is serialized by the high order bit of SHDRWMSG
2132	(854)	CHARACTER	28	SHDRXWRK	X'40C' area
2132	(854)	UNSIGNED	1	SHDRXWTY	Trap type
2133	(855)	CHARACTER	2	SHDRXWCP	Logical Cpu
2135	(857)	CHARACTER	1	*	Reserved
2136	(858)	ADDRESS	4	SHDRXWG@	Address of GRs
2140	(85C)	ADDRESS	4	SHDRXWP@	Address of ARs
2144	(860)	ADDRESS	4	SHDRXWV@	Address of Variable
2148	(864)	ADDRESS	4	SHDRXWC@	Address of CRs
2152	(868)	ADDRESS	4	SHDRXWA@	Address of ARs

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
2156	(86C)	ADDRESS	4	SHDRXWG64H@	Address of G64H
2160	(870)	CHARACTER	4	SHDRXWID	Trap ID
2164	(874)	CHARACTER	64	SHDRXWGR	GPRs 0-15
2228	(8B4)	CHARACTER	8	SHDRXWPS	PSW
2236	(8BC)	CHARACTER	52	*	
2288	(8F0)	CHARACTER	8	SHDRXWCR	Control regs 3,4
2296	(8F8)	CHARACTER	64	SHDRXWAR	ARs 0-15
2360	(938)	CHARACTER	8	SHDRXWPE	PER code when needed
2368	(940)	CHARACTER	240	SHDRWAL4	Area for SL4
2368	(940)	CHARACTER	44	SHDRWAS1	SRB to IEAVTSL4
2412	(96C)	CHARACTER	4	*	RESERVED
2416	(970)	CHARACTER	40	SHDRWASP	STOP PARAMETER LIST
2456	(998)	CHARACTER	12	SHDRWAOA	STOP/RESET OUTPUT AREA
2468	(9A4)	CHARACTER	72	SHDRWASA	SAVE AREA
2540	(9EC)	CHARACTER	24	SHDRWARP	RESET PARAMETER LIST
2564	(A04)	CHARACTER	4	*	Reserved
2568	(A08)	CHARACTER	40	SHDRWAP1	SL4 parmarea
2568	(A08)	ADDRESS	4	SHDRWASC	SCE address
2572	(A0C)	BITSTRING	1	*	
		1...		SHDRWAPE	1 => PER
		.1..		SHDRWARS	1 => Reset needed
		..1.		SHDRWARESCHEDULE	
					1 => Reschedule of IEAVTSL4
2573	(A0D)	CHARACTER	3	*	Reserved
2576	(A10)	CHARACTER	32	SHDRSDUMPTOKEN	
					Token to be passed to SDUMP
2576	(A10)	CHARACTER	8	SHDRIXCSSMOTIME	
					Time when the IXCSSMO was invoked
2608	(A30)	SIGNED	4	SHDRXSLDCOUNT	
					Counter to serialize the setting of the control regs in IEAVTGLB/IEAVTPVT with IEAVTSLD. When IEAVTSLD is in control, the value is '80000000'X which locks out IEAVTGLB/IEAVTPVT. The dispatcher lock serializes IEAVTGLB with IEAVTPVT
2612	(A34)	ADDRESS	4	SHDRXLASTPERFIRST	
					Last value that ShdrPerFirst was set to
2616	(A38)	ADDRESS	4	SHDRXLCL	Address of IEAVTLCL
2620	(A3C)	ADDRESS	4	SHDRXLCL1	Address of LPERRMTR in IEAVTLCL
2624	(A40)	CHARACTER	64	SHDRXWST2	More A=WAIT Status area. This area is serialized by the high order bit of SHDRWMSG
2624	(A40)	CHARACTER	64	SHDRXWG64H	G64H
2688	(A80)	CHARACTER	16	SHDRXCGS	ESAME PER range
2688	(A80)	CHARACTER	8	SHDRXCG10	PER STARTING ADDRESS TO BE LOADED INTO CONTROL REG 10
2688	(A80)	ADDRESS	4	SHDRXCG10HIGH	
2692	(A84)	ADDRESS	4	SHDRXCG10LOW	
2696	(A88)	CHARACTER	8	SHDRXCG11	PER ENDING ADDRESS TO BE LOADED INTO CONTROL REG 11
2696	(A88)	ADDRESS	4	SHDRXCG11HIGH	
2700	(A8C)	ADDRESS	4	SHDRXCG11LOW	
2704	(A90)	ADDRESS	4	SHDRXVTSMG	Address of the slip msg exit for SVC WTO
2708	(A94)	ADDRESS	4	SHDRXVTSM1	Address of the slip msg exit for branch entry WTO
2712	(A98)	UNSIGNED	2	SHDRXMSGIDLENGTH	
					Length of msgid - used when only 1 msgid trap exists
2714	(A9A)	CHARACTER	10	SHDRXMSGIDTEXT	
					Text of the msgid - used when only 1 msgid trap exists
2724	(AA4)	ADDRESS	4	SHDRXPVTEP@	Entry point address of the named module/alias whether PVTMOD or PVTEP
2728	(AA8)	ADDRESS	4	SHDRXPVTEND@	End address of the extent corresponding to the address in PvtEP@
2732	(AAC)	CHARACTER	4	*	Unused
2736	(AB0)	CHARACTER	136	SHDRXPVTXTLST	
					The complete extent list for PVTMOD PER or PVTEP PER, of the named module/alias
2872	(B38)	CHARACTER	80	SHDRXPOSIXPATHNAME	
					Buffer for posix pathnames
2952	(B88)	ADDRESS	4	SHDRXEXITSLIPAREAPTR	
					Added pointer for SLIP area needed for saving regs and a dynamic area for the user exit on a userexit SLIP PER trap.
2956	(B8C)	CHARACTER	4	*	
2960	(B90)	CHARACTER	8	SHDRXAEXITINFO@	
					AexitInfo address (IHASLZWA)
2968	(B98)	CHARACTER	8	*	
2976	(BA0)	CHARACTER	8	SHDRXPERACTIVATIONTIME	
					STCK value when PER trap was activated
2984	(BA8)	CHARACTER	0	*	End of SHDR

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	24	SHDRSDV	V-CONS FOR SYNCSVCD
0	(0)	ADDRESS	4	SHDRVS1E	POINTER-DEFINED EPA
4	(4)	ADDRESS	4	SHDRVS1F	POINTER-DEFINED FRRA
8	(8)	ADDRESS	4	SHDRVS1R	POINTER-DEFINED RMTRA
12	(C)	ADDRESS	4	SHDRVS2E	POINTER-DEFINED EPA
16	(10)	ADDRESS	4	SHDRVS2F	POINTER-DEFINED FRRA
20	(14)	ADDRESS	4	SHDRVS2R	POINTER-DEFINED RMTRA

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	*	EXITSLIPAREA	
0	(0)	CHARACTER	1024	USEREXITAREAARRAY (*)	
0	(0)	CHARACTER	296	EXITSLIPWORKAREA	
0	(0)	STRUCTURE IsA(IHA_TUEPARMS)	28	EXITPLIST	
0	(0)	ADDRESS	4	UEDYNAREAPTR	dynamic area ptr
4	(4)	ADDRESS	4	UESCEPTR	SCE ptr
8	(8)	CHARACTER	8	UEFLAGS	
		1...		UEDRIVENASATESTEXIT	called for test
		.1..		UEDRIVENASAACTIONEXIT	called for action
16	(10)	CHARACTER	8	UEAPARM1@	Aparm1 address
24	(18)	ADDRESS	4	UESLWAADDR	SLWA address
28	(1C)	ADDRESS	4	EXITPLISTPTR	
32	(20)	ADDRESS	4	EXITSAVEREG1	Rename from SaveReg1
36	(24)	ADDRESS	4	EXITSAVEREG13	Rename from SaveReg
40	(28)	ADDRESS	4	EXITSAVEREG14	Rename from SaveReg
296	(128)	CHARACTER	216	EXITSAVEAREA	
512	(200)	CHARACTER	512	EXITDYNAREA	

SHDR Constants

Len	Type	Value	Name	Description
4	DECIMAL	1024	KEXITSLIPAREASIZE	
4	DECIMAL	512	KSLIPUSEREXITAUTOAREASIZE	
4	DECIMAL	216	KEXITSAVEAREASIZE	Save area size for user exit
4	DECIMAL	296	KEXITSLIPWORKSIZE	Work area size used for the parmlist and saving R1,R13, and R14. Size of 296 is the remainder area of 512 minus the save area size.

Comment

THE FOLLOWING CONSTANTS IDENTIFY THE OWNER OF THE SHDRSEQ WORD WHICH IS USED TO SERIALIZE ACCESS TO THE SCE CHAIN.

End of Comment

4	CHARACTER	CMD	SHDRSEQC	SHDRSEQ OWNED BY THE CMD PROC IEECB905
4	CHARACTER	DSP	SHDRSEQD	SHDRSEQ OWNED BY THE DISPLAY RTN IEECB907
4	CHARACTER	GLB	SHDRSEQG	SHDRSEQ OWNED BY THE SLIP GLOBAL PER ACT/DEACT RTN IEAVTGLB
4	CHARACTER	SLX	SHDRSEQX	ShdrSeq owned by Slip dump exit
4	CHARACTER	L	SHDRSEQL	SHDRSEQ OWNED BY THE SLIP LOCAL PER ACT/DEACT RTN IEAVTLCL (NOTE: LAST 2 CHAR CONTAIN THE ASID OF THE ADDR SPACE IN WHICH IEAVTLCL IS EXECUTING)
4	DECIMAL	1160	SHDRWMSZ	SIZE OF WAIT STATE MESSAGE / WORK AREA. SERIALIZED BY RESTART LOCK WORD AND HIGH ORDER (USE-) BIT OF SHDRWMSG

SHDR Cross Reference

Len	Type	Value	Name	Description
		Comment		
		THE FOLLOWING ARE CONSTANTS THAT SHOULD BE USED WHEN SETTING BITS IN THE SHDR VIA THE CS INSTRUCTION. THE BIT WHICH EACH OF THE FOLLOWING MASKS SETS IS GIVEN IN THE COMMENT ON THAT LINE. THE SHDRX... FORM IS FOR SETTING THE BIT ON AND THE SHDRY... FORM IS FOR SETTING THE BIT OFF.		
		End of Comment		
4	HEX	40000000	SHDRXDEL	SHDRDELP
4	HEX	BFFFFFFF	SHDRYDEL	SHDRDELP
4	HEX	20000000	SHDRXCRN	SHDRCRTN
4	HEX	DFFFFFFF	SHDRYCRN	SHDRCRTN
4	HEX	10000000	SHDRXRBR	SHDRSRBR
4	HEX	EFFFFFFF	SHDRYRBR	SHDRSRBR
4	HEX	08000000	SHDRXPRI	SHDRPERI
4	HEX	F7FFFFFF	SHDRYPRI	SHDRPERI
4	HEX	04000000	SHDRX415	SHDRM415
4	HEX	FBFFFFFF	SHDRY415	SHDRM415
4	HEX	02000000	SHDRX422	SHDRM422
4	HEX	FDFFFFFF	SHDRY422	SHDRM422
4	HEX	00800000	SHDRXPST	SHDRPSTM
4	HEX	FF7FFFFF	SHDRYPST	SHDRPSTM
4	HEX	00400000	SHDRXFC	SHDRGCFC
4	HEX	FFBFFFFF	SHDRYFC	SHDRGCFC
4	HEX	00200000	SHDRXPVP	SHDRPVTP
4	HEX	FFDFFFFF	SHDRYPVP	SHDRPVTP
4	HEX	00100000	SHDRXPVA	SHDRPVTA
4	HEX	FFEFFFFF	SHDRYPVA	SHDRPVTA
4	HEX	00080000	SHDRXPLP	SHDRPVLP
4	HEX	FFF7FFFF	SHDRYPLP	SHDRPVLP
4	HEX	00040000	SHDRXSTD	SHDRSTDP
4	HEX	FFFBFFFF	SHDRYSTD	SHDRSTDP
4	HEX	00020000	SHDRXCHN	SHDRCHNE
4	HEX	FFFDFFFF	SHDRYCHN	SHDRCHNE
4	DECIMAL	80	SHDRPOSIXPATHNAMEMAXLEN	Max length of posix pathname Test for PLX 2.1
4	NUMB HEX	0000006F	SHDRMSGABENDCODE	
4	NUMB HEX	0006F000	SHDRMSGABENDCODECHECK	
4	DECIMAL	8	SHDRMSGABENDREASON	
4	DECIMAL	336	SHDRMSGDYNSIZE	
8	CHAR HEX	FFFFFFFFFFBADBAD	VTADRBADBASECON	It is vital that bit 32 of this constant be "1" so that the ESA390 test in IEAVTADR will properly identify the "no base" condition */
4	DECIMAL	1	VTADRENABLED	Function code to tell IEAVTADR that the caller is enabled
4	DECIMAL	2	VTADRSCAN	Function code to tell IEAVTADR just to scan and not convert the indirect address
4	DECIMAL	3	VTADRISITINDIRECT	Function code to tell IEAVTADR just to scan to see if this is an indirect address
4	DECIMAL	4	VTADRSCANTONEXTADRQUALIFIER	Function code to tell IEAVTADR just to scan to to the next address qualifier

SHDR Cross Reference

Name	Hex Offset	Hex Value
EXITDYNAREA	200	
EXITPLIST	0	
EXITPLISTPTR	1C	
EXITSAVEAREA	128	
EXITSAVEREG1	20	
EXITSAVEREG13	24	
EXITSAVEREG14	28	
EXITSLIPAREA	0	
EXITSLIPWORKAREA	0	
SHDR	0	
SHDRADR	C	
SHDRAEXITDEF	15	01
SHDRBKWD	10	
SHDRBWD2	B0	
SHDRCBID	0	
SHDRCECB	14	
SHDRCHNE	15	02
SHDRCMR1	5C	
SHDRCMR2	60	
SHDRCMR3	C8	
SHDRCMR4	CC	
SHDRCMS1	58	
SHDRCMS2	C4	
SHDRCPID	3C	
SHDRCREG	4C	
SHDRCRS	50	
SHDRCRTN	14	20

Name	Hex Offset	Hex Value
SHDRCR10	50	
SHDRCR11	54	
SHDRCR9	4C	
SHDRCTFW	8	
SHDRCTF2	A8	
SHDRCTR	8	
SHDRCTR2	A8	
SHDRC9	4C	
SHDRC9B1	4D	
SHDRC9IF	4C	40
SHDRC9SA	4C	20
SHDRC9SB	4C	80
SHDRC9SE	4D	20
SHDRC9SS	4C	08
SHDRC9S2	4D	80
SHDRC9ZAD	4C	04
SHDRDELP	14	40
SHDRECB	40	
SHDRENABLEDMSGIDTRAPSEXIST		
	14	01
SHDRFLAG	8B	
SHDRFLCS	14	
SHDRFLGS	14	
SHDRFLG2	15	
SHDRFWD	C	
SHDRFWD2	AC	
SHDRGCFC	15	40
SHDRGECB	44	
SHDRGLB	10	
SHDRIDCT	16	
SHDRIDQ	B4	
SHDRIDQL	B8	
SHDRIXCSSMOTIME		
	A10	
SHDRLECB	48	
SHDRMLC	9C	
SHDRMLT	A0	
SHDRM415	14	04
SHDRM422	14	02
SHDRPASC	8C	
SHDRPCDE	74	
SHDRPER	1C	
SHDRPERA	28	
SHDRPERFIRST	A4	
SHDRPERI	14	08
SHDRPERJ	2C	
SHDRPERR	24	
SHDRPFC	4	
SHDRPOSIXPATHNAMEUSED		
	8A	20
SHDRPOST	38	
SHDRPROC	18	
SHDRPSTA	38	80
SHDRPSTM	15	80
SHDRPTCB	90	
SHDRPVAS	88	
SHDRPVD1	7C	
SHDRPVFL	8A	
SHDRPVLP	15	08
SHDRPVL1	78	
SHDRPVMN	80	
SHDRPVR1	94	
SHDRPVTA	15	10
SHDRPVTG	8A	40
SHDRPVTL	8A	80
SHDRPVTP	15	20
SHDRRCTX	8	
SHDRSDAS	E0	
SHDRSDIU	4	80
SHDRSDOA	5C	
SHDRSDP1	C0	
SHDRSDRP	138	
SHDRSDSA	F0	
SHDRSDSD	C0	
SHDRSDSP	34	
SHDRSDS1	68	
SHDRSDUMPTOKEN		
	A10	
SHDRSDV	0	
SHDRSDWA	150	
SHDRSEQ	30	
SHDRSEQA	32	
SHDRSRB	34	
SHDRSRBA	34	80
SHDRSRBR	14	10
SHDRSRTN	20	
SHDRSSHP	64	
SHDRSSTM	68	
SHDRSS1P	70	
SHDRSTDP	15	04
SHDRSTFP	8B	40
SHDRSTID	98	
SHDRSTRC	8B	80
SHDRSTSB	8B	20
SHDRSYND	34	
SHDRSYNV	4	
SHDRVS1E	0	
SHDRVS1F	4	
SHDRVS1R	8	
SHDRVS2E	C	
SHDRVS2F	10	
SHDRVS2R	14	
SHDRWALK	BC	80
SHDRWAL4	940	
SHDRWAOA	998	
SHDRWAPE	A0C	80
SHDRWAP1	A08	
SHDRWARESCHEDULE		
	A0C	20
SHDRWARP	9EC	
SHDRWARS	A0C	40
SHDRWASA	9A4	
SHDRWASC	A08	
SHDRWASP	970	
SHDRWAS1	940	
SHDRWMSG	BC	
SHDRX	0	
SHDRXAASID	6B1	
SHDRXADR	C0	
SHDRXAEXITINFO@		
	B90	
SHDRXAFLGS	6B0	
SHDRXAJOBNAME		
	6B1	
SHDRXAJOBNAMESPECIFIED		
	6B0	80
SHDRXAS#	6AC	
SHDRXASA	6B0	
SHDRXCGS	A80	
SHDRXCG10	A80	
SHDRXCG10HIGH		
	A80	
SHDRXCG10LOW	A84	
SHDRXCG11	A88	
SHDRXCG11HIGH		
	A88	
SHDRXCG11LOW	A8C	
SHDRXCSWORD	18	
SHDRXDASID	745	
SHDRXDFLGS	744	
SHDRXDJOBNAME		
	745	
SHDRXDJOBNAMESPECIFIED		
	744	80
SHDRXDNM	74D	
SHDRXDS#	740	
SHDRXDSA	744	
SHDRXEXITSLIPAREAPTR		

SHDR Cross Reference

Name	Hex Offset	Hex Value
	B88	
SHDRXFLAGS2	2B	
SHDRXFLGS1	1A	
SHDRXID	0	
SHDRXIEAVTPVG@		
	20	
SHDRXLASTPERFIRST		
	A34	
SHDRXLCL	A38	
SHDRXLCL1	A3C	
SHDRXLEN	18	
SHDRXLPAMODORLPAEP		
	2B	80
SHDRXMSGIDLENGTH		
	A98	
SHDRXMSGIDQUOTED		
	2B	40
SHDRXMSGIDTEXT		
	A9A	
SHDRXPERACTIVATIONTIME		
	BA0	
SHDRXPERDEACTIVATIONINPROGRESS		
	1A	80
SHDRXPLP@	24	
SHDRXPOSIXPATHNAME		
	B38	
SHDRXPOSIXPATHNAMELENGTH		
	1B	
SHDRXPVTEND@	AA8	
SHDRXPVTEP@	AA4	
SHDRXPVTXTLST		
	AB0	
SHDRXSLDCOUNT		
	A30	
SHDRXSRB	680	
SHDRXSSB	1C	
SHDRXVTSMG	A90	
SHDRXVTSM1	A94	
SHDRXWA@	868	
SHDRXWAR	8F8	
SHDRXWC@	864	
SHDRXWCP	855	
SHDRXWCR	8F0	
SHDRXWG@	858	
SHDRXWGR	874	
SHDRXWG64H	A40	
SHDRXWG64H@	86C	
SHDRXWID	870	
SHDRXWMAOLD	1F8	
SHDRXWP@	85C	
SHDRXWPE	938	
SHDRXWPS	8B4	
SHDRXWRK	854	
SHDRXWST	854	
SHDRXWST2	A40	
SHDRXWTY	854	
SHDRXWV@	860	
UEAPARM1@	10	
UEDRIVENASAACTIONEXIT		
	8	40
UEDRIVENASATESTEXIT		
	8	80
UEDYNAREAPTR	0	
UEFLAGS	8	
UESCEPTR	4	
UESLWAADDR	18	
USEREXITAREAARRAY		
	0	

SIOT Information

SIOT Programming Interface information

Programming Interface information

SIOT

ONLY the following fields are part of the programming interface information:

- SCTDDNAM
- SCTSBYT1
- SCTSBYT2
- SCTSBYT3
- SCTSBYT4
- SCTSDISP
- SCTUTYPE
- SIOTDEVT
- SIOTSMSM
- SIOUBYT3

End of Programming Interface information

SIOT Heading Information

Common Name: Step Input/Output Table
Macro ID: IEFASIOT
DSECT Name: INDMSIOT
Owning Component: Interpreter (SC1B9)
Eye-Catcher ID: 'SIOT'
Offset: -4 (SWA prefix)
Length: 4 bytes
Storage Attributes: Subpool: 236 or 237 (SWA), 241 for MSTR address space
Key: 1
Residency: Any
Size: 174 bytes
FREQUENCY = One per specified or generated DD statement.
Created by: Interpreter and Dynamic Allocation
Pointed to by: - AWARSAV1 field (pointer) of the IATYAWA data area (IATUX32)
Serialization: None
Function: Contains information concerning a data definition (DD)
JCL statement

SIOT Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0		
0	(0)	DBL WORD	8	(0)	
0	(0)	X'0'	0	INDMSIOT	"*" STEP I-O TABLE
0	(0)	CHARACTER	3	SIOTDSKA	SVA OF THIS SIOT
3	(3)	CHARACTER	1	SIOTTYPE	TABLE ID OF SIOT =3
3	(3)	X'3'	0	SIOTID	"3"
3	(3)	X'7'	0	DSNID	"7"
4	(4)	CHARACTER	8	SCTDDNAM	THE DDNAME FROM THE DD CARD
12	(C)	CHARACTER	8	SIOTDEST	USER ID ENABLING SYSOUT TO BE ROUTED VIA JCL
20	(14)	CHARACTER	2	SCTUSADD	INTERNAL NUMBER OF THE DD STATEMENT FOR WHICH UNIT AFFINITY IS SPECIFIED IN THIS DD STATEMENT
20	(14)	X'14'	0	SIOTUNAF	"SCTUSADD"
22	(16)	CHARACTER	2	SIODSNTE	OFFSET INTO DSNT FOR DCB REFERENCE TO A DATA SET
24	(18)	CHARACTER	2	SIOTVLSP	VOL SEP DD NO.
26	(1A)	CHARACTER	2	SIOTAFID	AFFINITY ASSOCIATION ID WITH MULTI-UNIT/GENERIC REQUEST
28	(1C)	CHARACTER	4	SCTPSIOT	SVA OF NEXT SIOT IN CHAIN
32	(20)	CHARACTER	4	SCTPJFCB	SVA OF JFCB
36	(24)	CHARACTER	3	SIOTVRSB	SVA OF SIOT FOR VOLREF OR SUBALLOCATE
39	(27)	CHARACTER	1	SIOTOTUN	TOTAL NUMBER OF UNITS USED FOR THIS SIOT
40	(28)	CHARACTER	2	SIOTREFN	DD NUMBER OF INTRA STEP
42	(2A)	CHARACTER	1		Reserved
43	(2B)	CHARACTER	1	SIOTBYT1	MVM INDICATORS
43	(2B)	X'80'	0	SIOTOCKP	"128" BIT 0 - DATA SET OPEN AT LAST CHECKPOINT
43	(2B)	X'40'	0	SIOTHOLD	"64" BIT 1 - SYSOUT DATA SET TO BE PLACED ON HOLD QUEUE.
43	(2B)	X'20'	0	SIOVAMDS	"32" BIT 2 - VAM DATA SET
43	(2B)	X'10'	0	SIODUNAL	"16" BIT 3 - DATA SET HAS BEEN DYNAMICALLY UNALLOCATED
43	(2B)	X'8'	0	SIOTDADR	"8" BIT 4 - DADSM IS REQUIRED
43	(2B)	X'4'	0	SIODADSM	"4" BIT 5 - DADSM WAS SUCCESSFUL
43	(2B)	X'2'	0	SIOTALCD	"2" BIT 6 - THIS SIOT IS COMPLETELY ALLOCATED
43	(2B)	X'1'	0	SIOTDDNT	"1" BIT 7 - IN TSO, COMMAND PROCESSOR MUST PUT DDNAME IN DDNT
44	(2C)	CHARACTER	2	SCTDDINO	INTERNAL NUMBER OF THE DD STATEMENT
46	(2E)	CHARACTER	1	SIOTBYT3	ALLOCATION INDICATOR BYTE
46	(2E)	X'80'	0	SIOALIAS	"128" BIT 0 - ALIAS EXISTS FOR THIS DATA SET
46	(2E)	X'40'	0	SIOCDEVT	"64" BIT 1 - DEVICE TYPE FOR THIS DATA SET OBTAINED FROM CATALOG
46	(2E)	X'20'	0	SIOTJES3	"32" BIT 2 - DEVICES FOR THIS ALLOCATION SELECTED BY JES3
46	(2E)	X'10'	0	S3400OFF	"16" BIT 3 - INITALIZE S3400DSP TO OFF
46	(2E)	X'8'	0	SIOTDSID	"8" BIT 4 - ON FOR DSID KEYWORD
46	(2E)	X'4'	0	SIOUCVTD	"4" BIT 5 - On when unit name conversion has been done. Also refer to SIOUCNVT.
46	(2E)	X'2'	0	SIOTDSOP	"2" BIT 6 - On when the dataset has been opened.
46	(2E)	X'1'	0	SIOTHLD	"1" BIT 7 - HOLD= WAS SPECIFIED FOR THIS ALLOCATION
47	(2F)	CHARACTER	1	SIOTTSTC	INDICATORS FOR TIME SHARING AND TCAM
47	(2F)	X'80'	0	SIOTINFC	"128" BIT 0 - SIOT INF CODE INDICATOR
47	(2F)	X'40'	0	SIOTTERM	"64" BIT 1 - TSO TERMINAL BIT - DD TERM=TS PARAMETER SET BY IEFVDA

Comment

EQU 32 BIT 2 - RESERVED

End of Comment

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
47	(2F)	X'10'	0	SIOTSSGP	"16" BIT 3 - GROUP SUBSYSTEM REQUEST(SUBSYS)
47	(2F)	X'8'	0	SIOTSSMG	"8" BIT 4 - SUBSYSTEM ERROR MSG INDICATOR
47	(2F)	X'4'	0	SIOTTRKM	"4" BIT 5 - XB609 SETS FOR AB427 WHEN DYNAMIC
47	(2F)	X'2'	0	SIOTDSNM	"2" BIT 6 - SYSOUT DSDR FOUND ON CHECKPOINT DATA SET
47	(2F)	X'1'	0	SIOTQNAM	"1" BIT 7 - FOR TCAM USE =1 IF QNAME= ON DD STATEMENT. SET BY IEFVDA, TESTED BY ALLOCATION
48	(30)	CHARACTER	1	SCTSPOOL	INTERNAL NO. OF POOL DD
49	(31)	CHARACTER	1	SCTVOLCT	NUMBER OF VOLUMES FOR THIS DATASET
50	(32)	CHARACTER	2		Reserved
52	(34)	CHARACTER	1	SIOTBYT0	EXTENDED ALLOCATION INDICATORS
52	(34)	X'80'	0	SIOTSSDS	"128" BIT 0 - DATA SET WILL BE PROCESSED BY A SUBSYSTEM
52	(34)	X'40'	0	SIOTDYAL	"64" BIT 1 - DATA SET DYNAMICALLY ALLOCATED
52	(34)	X'20'	0	SIOTCRIJ	"32" BIT 2 - Data set created in this job
52	(34)	X'10'	0	SIOTWOWO	"16" BIT 3 - Who Owns, We Own flag. When on, it indicates that this is a Dynamic Allocation that is Demanding an ATL device that is already allocated to this job. This flag is set by IEFAB452 and then checked by IEFAB42B and IEFAB424 to avoid failing the subject Request.
52	(34)	X'8'	0	SIOTDDSP	"8" BIT 4 - Value in SCTSDISP was defaulted
52	(34)	X'4'	0	SIOTNOPV	"4" BIT 5 - USE ATTRIBUTE OF UCB HAS BEEN MADE PRIVATE
52	(34)	X'2'	0	SIOTPUPV	"2" BIT 6 - USE ATT. OF UCB CHANGED FROM PUB TO PRIVATE
52	(34)	X'1'	0	SIOTDDMP	"1" BIT 7 - This Dynamically Allocated or Unallocated SIOT requested message processing. - This flag will be set by both IEFDB414 IEFDB4A1 and will mirror S99EWTP. - It will be checked by IEFAB4EE (Allocate) and used by IEFAB4A2 (DISPosition) to determine if the messages for a particular Dynamic Allocation or Unallocation should be buffered or left for Dynamic message processing to handle.
53	(35)	CHARACTER	1	SCTNMBUT	THE NUMBER OF UNITS FOR THE DATA SET
54	(36)	CHARACTER	1	SIOTVLCT	VALUE OF SPECIFIED VOL COUNT(= JFCBVLCT)
55	(37)	CHARACTER	1	SCTSDISP	SCHEDULER DISPOSITION OF THE DATA SET (AT THE END OF THE STEP OR JOB) INDICATORS
55	(37)	X'80'	0	SIOTRETN	"128" BIT 0 - RETAIN BIT
55	(37)	X'40'	0	S3400DSP	"64" BIT 1 - FOR DISP. PROCESSING OF DATA SET ON ASPEN DEVICE
55	(37)	X'20'	0	PRIVATE	"32" BIT 2 - PRIVATE VOLUME
55	(37)	X'20'	0	SIOTPRIV	"32" BIT 2 - PRIVATE VOLUME
55	(37)	X'10'	0	SIOTPASS	"16" BIT 3 - PASS THE DATA SET
55	(37)	X'8'	0	SIOTKEEP	"8" BIT 4 - KEEP THE DATA SET
55	(37)	X'4'	0	SIOTDLET	"4" BIT 5 - DELETE DATA SET
55	(37)	X'2'	0	SIOTCTLG	"2" BIT 6 - CATALOG THE DATA SET
55	(37)	X'1'	0	SIOTUNCT	"1" BIT 7 - UNCATALOG THE DATA SET
56	(38)	CHARACTER	1	SCTSBYT1	INDICATOR BYTE NUMBER 1
56	(38)	X'80'	0	SCTDUMMY	"128" BIT 0 - DUMMY DATA SET
56	(38)	X'40'	0	SCTSYSIN	"64" BIT 1 - SYSIN DATA SET
56	(38)	X'20'	0	SIOTCCAT	"32" BIT 2 - BLANK DD NAME - CONCATENATION
56	(38)	X'10'	0	SIOTGDSN	"16" BIT 3 - GENERATED DATA SET NAME
56	(38)	X'8'	0	SIOTQDSN	"8" BIT 4 - QUALIFIED DATA SET IS SPECIFIED
56	(38)	X'4'	0	SCTPARLM	"4" BIT 5 - PARALLEL MOUNT INDICATOR
56	(38)	X'2'	0	SCTUNAFF	"2" BIT 6 - UNIT AFFINITY
56	(38)	X'1'	0	SIOTJSCT	"1" BIT 7 - SIOT IS ASSOCIATED WITH A JOBCAT OR STEPCAT
57	(39)	CHARACTER	1	SCTSBYT2	INDICATOR BYTE NUMBER 2
57	(39)	X'80'	0	SIOCLUNL	"128" BIT 0 - CLOSE SHOULD DYNAMICALLY UNALLOCATE DATA SET
57	(39)	X'40'	0	SIOTCATL	"64" BIT 1- DATA SET IS A CATALOG
57	(39)	X'20'	0	SCTVOLAF	"32" BIT 2 - VOLUME AFFINITY
57	(39)	X'10'	0	SCTJOBLB	"16" BIT 3 - JOBLIB DD STMNT
57	(39)	X'8'	0	SCTUNLBD	"8" BIT 4 - UNLABELED
57	(39)	X'4'	0	SCTLABEL	"4" BIT 5 - NONSTANDARD LABEL
57	(39)	X'2'	0	SCTDEFER	"2" BIT 6 - DEFER MOUNTING
57	(39)	X'1'	0	SCTRECVD	"1" BIT 7 - RECEIVED DATA SET
58	(3A)	CHARACTER	1	SCTSBYT3	INDICATOR BYTE NUMBER 3
58	(3A)	X'80'	0	SCTDSNRF	"128" BIT 0 - VOLUME REFERENCE DSNAME PRESENT
58	(3A)	X'40'	0	SCTSYSNE	"64" BIT 1 - SYSIN EXPECTED (PROCEDURES ONLY)
58	(3A)	X'20'	0	SCTALCHK	"32" BIT 2 - THIS SIOT ALLOCATED AT LAST CHECKPOINT
58	(3A)	X'10'	0	SCTVREF	"16" BIT 3 - VOLUME REFERENCE IN STEP
58	(3A)	X'8'	0	SCTSYSOU	"8" BIT 4 SYSOUT IS SPECIFIED
58	(3A)	X'4'	0	SCTSNEW	"4" BIT 5 - NEW DATA SET
58	(3A)	X'2'	0	SCTSMOD	"2" BIT 6 - MODIFIED DATA SET
58	(3A)	X'1'	0	SCTSOLD	"1" BIT 7 - OLD DATA SET
59	(3B)	CHARACTER	1	SCTSBYT4	INDICATOR BYTE NUMBER 4
59	(3B)	X'80'	0	SCTSGDGS	"128" BIT 0 - SET BY R/O TO INDICATE GDG SINGLE
59	(3B)	X'40'	0	SIOTGDGA	"64" BIT 1 - THIS IS A GENERATED SIOT
59	(3B)	X'20'	0	SIOTAFF	"32" BIT 2 - SIOT AFFINITY INDICATOR
59	(3B)	X'10'	0	SIOTASCI	"16" BIT 3 - USASCII TAPE LABEL. SET BY IEFVDA,TESTED BY IEFWA000
59	(3B)	X'8'	0	SIOTSTEP	"8" BIT 4 - STEP PROCESSED
59	(3B)	X'4'	0	SIOTVAFF	"4" BIT 5 - INTRA-STEP VOLUME AFFINITY
59	(3B)	X'2'	0	SIOTIPDI	"2" BIT 6 - DATA SET IS IN PDI

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
59	(3B)	X'1'	0	SIOTOMN	"1" BIT 7 - 1 = OLD OR MODIFIED DATA SET 0 = NEW DATA SET
60	(3C)	CHARACTER	8	SCTUTYPE (0)	UNIT TYPE
60	(3C)	CHARACTER	4	SIOTDEVT (0)	DEVICE TYPE
60	(3C)	CHARACTER	1	SIOUBYT1	
61	(3D)	CHARACTER	1	SIOUBYT2	
62	(3E)	CHARACTER	1	SIOUBYT3	DEVICE CLASS
62	(3E)	X'80'	0	SIO3TAPE	"128" BIT 0 - TAPE DEVICE
62	(3E)	X'40'	0	SIO3COMM	"64" BIT 1 - COMMUNICATIONS DEVICE
62	(3E)	X'20'	0	SIO3DACC	"32" BIT 2 - DIRECT ACCESS DEVICE
62	(3E)	X'10'	0	SIO3DISP	"16" BIT 3 - GRAPHICS DEVICE
62	(3E)	X'8'	0	SIO3UREC	"8" BIT 4 - UNIT RECORD DEVICE
Comment					
EQU 4 BIT 5 - RESERVED EQU 2 BIT 6 - RESERVED EQU 1 BIT 7 - RESERVED					
End of Comment					
63	(3F)	CHARACTER	1	SIOUBYT4	
64	(40)	BITSTRING	4	SIOUCBA4 (0)	UCB address (4-bytes)
64	(40)	CHARACTER	1	SIOUCNVT	IF = X'FF' SIOUCBAD is an SVA, IF = X'00' Unit name conversion has been done. Also refer to flag SIOUCVTD.
65	(41)	CHARACTER	3	SIOUCBAD	3-byte UCB address or SVA for UNIT=AFF
68	(44)	CHARACTER	8	SCTOUTNM	THE SYSTEM OUTPUT PROGRAM NAME
76	(4C)	CHARACTER	4	SCTOUTNO	THE FORM NUMBER OF THE CARD OR PAPER STOCK TO BE USED WHEN THIS DATA SET IS PUNCHED OR PRINTED
80	(50)	CHARACTER	1	SCTOUTPN	THE SYSTEM OUTPUT CLASS NAME
81	(51)	CHARACTER	1	SIOTBYT4	SIOT INDICATORS BYTE 4
81	(51)	X'80'	0	SIOTPROT	"128" BIT 0 - PROTECT SPECIFIED ON DD
81	(51)	X'40'	0	SIOTRACD	"64" BIT 1 - PROTECT OK IF ALLOC TO DASD
81	(51)	X'20'	0	SIOTRACT	"32" BIT 2 - PROTECT OK IF ALLOC TO TAPE
81	(51)	X'10'	0	SIOTGPRV	"16" BIT 3 - INDICATES THAT SCTANAME CONTAINS AN SVA WHICH IS USED TO FIND A PREVIOUSLY GENERATED TEMPORARY DATASET NAME
81	(51)	X'8'	0	SIOTHIER	"8" BIT 4 - SIOT represents a hierarchical file
81	(51)	X'4'	0	SIOTDSQU	"4" BIT 5 - Reference via SJF Access routine
81	(51)	X'2'	0	SIOTUCNT	"2" BIT 6 - Reference via SJF Access routine
81	(51)	X'1'	0	SIOTVCNT	"1" BIT 7 - Reference via SJF Access routine
82	(52)	CHARACTER	1		Reserved
83	(53)	CHARACTER	1	SIOTSMS	STORAGE MANAGEMENT SUBSYSTEM INDICATORS
83	(53)	X'80'	0	SIOTSMSM	"128" BIT 0 - DATASET IS SMS MANAGED
83	(53)	X'40'	0	SIOTSMS1	"64" BIT 1 - Reserved SJF Access (SMS)
83	(53)	X'20'	0	SIOTSMS2	"32" BIT 2 - Reserved SJF Access (SMS)
83	(53)	X'10'	0	SIOTSMS3	"16" BIT 3 - Reserved SJF Access (SMS)
83	(53)	X'8'	0	SIOTSMS4	"8" BIT 4 - Reserved SJF Access (SMS)
83	(53)	X'4'	0	SIOTSMS5	"4" BIT 5 - Reserved SJF Access (SMS)
83	(53)	X'2'	0	SIOTSMS6	"2" BIT 6 - Reserved SJF Access (SMS)
83	(53)	X'1'	0	SIOTSMS7	"1" BIT 7 - Reserved SJF Access (SMS)
84	(54)	CHARACTER	4	SIOTSWB	SCHEDULER WORK BLOCK (SWB) STRUCTURE POINTER
88	(58)	CHARACTER	4	SIOTASCT (0)	SVA STRUCTURE OF STEP CONTROL TABLE (SCT) FOR THIS STEP
88	(58)	CHARACTER	1		RESERVED
89	(59)	CHARACTER	3	SIOTSCT	SVA OF SCT FOR THIS STEP
92	(5C)	CHARACTER	1	SIOTALTD	CONDITIONAL DISPOSITION
92	(5C)	X'80'	0	SIOTREDT	"128" BIT 0 - Tape redirected to SMS DASD
92	(5C)	X'40'	0	SIOTOPEN	"64" BIT 1 - Data set opened in step
92	(5C)	X'20'	0	SIOJCATS	"32" BIT 2 - JOB CAT SWITCH USED ONLY BY INTERPRETER WHEN READING IN COPIES OF CONCATENATED JOBCAT SIOTS
92	(5C)	X'10'	0	SIOTNPRV	"16" BIT 3 -THIS BIT IS SET AT RESTART TIME TO INDICATE THAT THIS DD IS NON-PRIVATE EVEN THOUGH IT MAY NOW APPEAR TO BE PRIVATE
92	(5C)	X'8'	0	SIOTAKEP	"8" BIT 4 - KEEP DATA SET IF ABEND
92	(5C)	X'4'	0	SIOTADEL	"4" BIT 5 - DELETE DATA SET IF ABEND
92	(5C)	X'2'	0	SIOTACAT	"2" BIT 6 - CATALOG DATA SET IF ABNORNAL TERMINATION
92	(5C)	X'1'	0	SIOTAUNC	"1" BIT 7 - UNCATALOG DATA SET IF ABNORMAL TERMINATION
93	(5D)	CHARACTER	3	SIOTSSWA	SVA OF SSWA
96	(60)	CHARACTER	1	SIOTOUTC	NUMBER OF SYSOUT COPIES TO BE PRINTED
97	(61)	CHARACTER	1	SIOTBYT5	SIOT INDICATOR BYTE 5
97	(61)	X'80'	0	SIOTDEFC	"128" BIT 0 - INDICATE DEFAULT COPIES WAS SET
97	(61)	X'40'	0	SIOTDCLA	"64" BIT 1 - INDICATE DEFAULT SYSOUT WAS SET
97	(61)	X'20'	0	SIOTDDST	"32" BIT 2 - INDICATE DEFAULT DEST WAS SET
97	(61)	X'10'	0	SIOTADUN	"16" BIT 3 - INDICATE DEFAULT UNITS - SET BY DYNALLOC
97	(61)	X'8'	0	SIOTDYNU	"8" BIT 4 - Indicates to SMS that the unit parameter in SCTUTYPE is the Dynamic Allocation default unit from the Device Allocation Defaults Table

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
97	(61)	X'4'	0	SIOTOVES	"4" BIT 5 - When set, it indicates this SIOT is associated with an 'overriding' esoteric. It is set by IEFAB464 and used by IEFAB4A2, IEFAB464, and IEFAB490.
97	(61)	X'2'	0	SIOTBLKD	"2" BIT 6 - USER SPECIFIED BLKSIZE KEYWORD VALUE, SO ALLOCATION SHOULD NOT OVERWRITE WITH VALUE IN MODEL DSCB
97	(61)	X'1'	0	SIOTWTRN	"1" BIT 7 - INDICATE PROGRAM NAME IS A WRITER NAME
98	(62)	CHARACTER	1		Reserved
99	(63)	CHARACTER	3	SIOTXSVA	SIOT Extension block (SIOTX) SVA
102	(66)	CHARACTER	1	SIOTSMSF	STORAGE MANAGEMENT SUBSYSTEM INDICATORS
102	(66)	X'80'	0	SIOTUSEQ	"128" BIT 0 - Reserved SJF Access (SMS)
102	(66)	X'40'	0	SIOTULAB	"64" BIT 1 - Reserved SJF Access (SMS)
102	(66)	X'20'	0	SIOTUTRT	"32" BIT 2 - Reserved SJF Access (SMS)
102	(66)	X'10'	0	SIOTTRTD	"16" BIT 3 - Reserved SJF Access (SMS)
102	(66)	X'8'	0	SIOTKEYD	"8" BIT 4 - Reserved SJF Access (SMS)
102	(66)	X'4'	0	SIOTRESL	"4" BIT 5 - Reserved SJF Access (SMS)
102	(66)	X'2'	0	SIOTDUPV	"2" BIT 6 - Duplicate Volume support for JES3 Updated via SJF key '8530'X
102	(66)	X'1'	0	SIOTSHNR	"1" BIT 7 - SMSHONOR (DALSMSHR) is coded on UNIT- honor UNITNAME for the SMS tape lib request
103	(67)	CHARACTER	1	SIOTBYT2	MVM INDICATOR BYTE
103	(67)	X'80'	0	SIOTDMND	"128" BIT 0 - INDICATES SPECIFIC UNIT REQ MADE
103	(67)	X'40'	0	SIOTDSPD	"64" BIT 1 - DISP FOR THIS DATA SET HAS BEEN PROCESSED
103	(67)	X'20'	0	SIOTGALL	"32" BIT 2 - SIOT IS PART OF GDG ALL REQUEST
103	(67)	X'10'	0	SIOTCALC	"16" BIT 3 - DATA SET CATLGD WHEN ALLOC'D
103	(67)	X'8'	0	SIOTCNEW	"8" BIT 4 - ORIG ALLOC'D STAT OF NEW CONVRTD
103	(67)	X'4'	0	SIOTCVOL	"4" BIT 5 - SIOT REPRESENTS AN OS CVOL
103	(67)	X'2'	0	SIOTSACP	"2" Bit 6 - When on, indicates that message cells pointed to by SIOTAMSG for this request have been moved to the Allocation 'SC1B4 AB490' Cell Pool. - This flag is set in IEFAB490 and then checked and reset in IEFDB4A1 when the cells are FREEd.
103	(67)	X'1'	0	SIOTPTTS	"1" BIT 7 - When on, indicates that we are 'Processing This Tape Siot' in Recovery Allocation. - This flag is set in IEFAB48A, checked in IEFAB489 and is never reset.
104	(68)	CHARACTER	4	SIOTSSNM	NAME OF SUBSYSTEM TO PROCESS DATASET
108	(6C)	CHARACTER	4	SIOTSTMT	JCL STATEMENT NUMBER CORRESPONDING TO THIS DD STATEMENT
112	(70)	ADDRESS	4	SIOTSIOX	SIOT Extension block (SIOTX) virtual address
116	(74)	CHARACTER	4	SIOTAMSG	- Pointer to the first Allocation message cell in any one of the following message Cell Pools, 'IGDMCSCG MSG CELL POOL', or 'ALLOCATION ERROR MESSAGE', or 'SC1B4 AB490'. - If SIOTSACP is on, the message cell(s) for this request have been moved from either of of the first two Cell Pools into the 'SC1B4 AB490' Cell Pool.
120	(78)	CHARACTER	2		Reserved
122	(7A)	CHARACTER	8	SCTANAME (0)	&NAME FROM DSNAME=
122	(7A)	CHARACTER	2		DEFINES THE REST OF THE SCTANAME FIELD
124	(7C)	CHARACTER	3	SIOPGSVA	THIS FORM CONTAINS THE SVA OF A JFCB WITH A PREVIOUSLY GENERATED TEMPORARY DATASET NAME.
127	(7F)	CHARACTER	3		DEFINES THE REST OF THE SCTANAME FIELD
130	(82)	CHARACTER	2	SIOTRSNC	ERROR CODE
132	(84)	CHARACTER	4	SIOTDDWA	Virtual address of the IEFZDDWA which is valid only during allocation
136	(88)	CHARACTER	4	SIOTEDLP	EDL POINTER
140	(8C)	ADDRESS	4	SIOTDDIB	Pointer to DDIB block used by Allocation to contain the path name for a hierarchical file (OpenMVS path name)
144	(90)	CHARACTER	4	SIOTPSVA	SVA OF PASSING SIOT
148	(94)	CHARACTER	4	SIOTETIO	ETIOT ENTRY
152	(98)	CHARACTER	4	SIOTNPTR	VIRTUAL ADDRESS OF NEXT SIOT
156	(9C)	CHARACTER	4	SJFCBPTR	VIRTUAL ADDRESS OF JFCB
160	(A0)	CHARACTER	4	SIOTJFX	VIRTUAL ADDRESS OF JFCBX
164	(A4)	CHARACTER	4	SIOTVMVP	VOLUME MNT AND VERIFY REQUEST
168	(A8)	CHARACTER	2		Reserved
170	(AA)	CHARACTER	2	SIOVDSNT	OFFSET INTO DSNT FOR VOL REF TO A DATA SET NAME
172	(AC)	CHARACTER	1	SIOVDSNL	LENGTH OF DATA SET NAME OF VOL REF TO A DATA SET NAME
173	(AD)	CHARACTER	1	SIODDSNL	LENGTH OF DATA SET NAME OF DCB REF TO A DATA SET NAME
174	(AE)	CHARACTER	6		TO MAKE 180(SIOT)
174	(AE)	X'AE'	0	SIOTLGTH	"174" LENGTH OF SIOT
180	(B4)	CHARACTER	4		HDR(INTERPRETER ONLY)
180	(B4)	X'1C'	0	JFCBID	"28"

SIOT Cross Reference

Name	Hex Offset	Hex Value
DSNID	3	7
INDMSIOT	0	0
JFCBID	B4	1C
PRIVATE	37	20
SCTALCHK	3A	20
SCTANAME	7A	
SCTDDINO	2C	
SCTDDNAM	4	
SCTDEFER	39	2
SCTDSNRF	3A	80
SCTDUMMY	38	80
SCTJOBLB	39	10
SCTLABEL	39	4
SCTNMBUT	35	
SCTOUTNM	44	
SCTOUTNO	4C	
SCTOUTPN	50	
SCTPARLM	38	4
SCTPJFCB	20	
SCTPSIOT	1C	
SCTRECVD	39	1
SCTSBYT1	38	
SCTSBYT2	39	
SCTSBYT3	3A	
SCTSBYT4	3B	
SCTSDISP	37	
SCTSGDGS	3B	80
SCTSMOD	3A	2
SCTSNEW	3A	4
SCTSOLD	3A	1
SCTSPOOL	30	
SCTSYSIN	38	40
SCTSYSNE	3A	40
SCTSYSOU	3A	8
SCTUNAFF	38	2
SCTUNLBD	39	8
SCTUSADD	14	
SCTUTYPE	3C	
SCTVOLAF	39	20
SCTVOLCT	31	
SCTVREF	3A	10
SIOALIAS	2E	80
SIOCDEVT	2E	40
SIOCLUNL	39	80
SIODADSM	2B	4
SIODDSNL	AD	
SIODSNTE	16	
SIODUNAL	2B	10
SIOJCATS	5C	20
SIOPGSVA	7C	
SIOTACAT	5C	2
SIOTADEL	5C	4
SIOTADUN	61	10
SIOTAFF	3B	20
SIOTAFID	1A	
SIOTAKEP	5C	8
SIOTALCD	2B	2
SIOTALTD	5C	
SIOTAMSG	74	
SIOTASCI	3B	10
SIOTASCT	58	
SIOTAUNC	5C	1
SIOTBLKD	61	2
SIOTBYT0	34	
SIOTBYT1	2B	
SIOTBYT2	67	
SIOTBYT3	2E	
SIOTBYT4	51	
SIOTBYT5	61	
SIOTCALC	67	10
SIOTCATL	39	40
SIOTCCAT	38	20
SIOTCNEW	67	8
SIOTCRIJ	34	20
SIOTCTLG	37	2
SIOTCVOL	67	4
SIOTDADR	2B	8
SIOTDCLA	61	40
SIOTDDIB	8C	
SIOTDDMP	34	1
SIOTDDNT	2B	1
SIOTDDSP	34	8
SIOTDDST	61	20
SIOTDDWA	84	
SIOTDEFC	61	80
SIOTDEST	C	
SIOTDEVT	3C	
SIOTDLET	37	4
SIOTDMND	67	80
SIOTDSID	2E	8
SIOTDSKA	0	
SIOTDSNM	2F	2
SIOTDSOP	2E	2
SIOTDSPD	67	40
SIOTDSQU	51	4
SIOTDUPV	66	2
SIOTDYAL	34	40
SIOTDYNU	61	8
SIOTEDLP	88	
SIOTETIO	94	
SIOTGALL	67	20
SIOTGDGA	3B	40
SIOTGDSN	38	10
SIOTGPRV	51	10
SIOTHIER	51	8
SIOTHLD	2E	1
SIOTHOLD	2B	40
SIOTID	3	3
SIOTINFC	2F	80
SIOTIPDI	3B	2
SIOTJES3	2E	20
SIOTJFX	A0	
SIOTJSCT	38	1
SIOTKEEP	37	8
SIOTKEYD	66	8
SIOTLGTH	AE	AE
SIOTNOPV	34	4
SIOTNPRV	5C	10
SIOTNPTR	98	
SIOTOCKP	2B	80
SIOTOMN	3B	1
SIOTOPEN	5C	40
SIOTOTUN	27	
SIOTOUTC	60	
SIOTOVES	61	4
SIOTPASS	37	10
SIOTPRIV	37	20
SIOTPROT	51	80
SIOTPSVA	90	
SIOTPTTS	67	1
SIOTPUPV	34	2
SIOTQDSN	38	8
SIOTQNAM	2F	1
SIOTRACD	51	40
SIOTRACT	51	20
SIOTREDT	5C	80
SIOTREFN	28	
SIOTRESL	66	4
SIOTRETN	37	80
SIOTRSNC	82	
SIOTSACP	67	2
SIOTSCT	59	
SIOTSHNR	66	1
SIOTSIOX	70	

Name	Hex Offset	Hex Value
SIOTSMS	53	
SIOTSMSF	66	
SIOTSMSM	53	80
SIOTSMS1	53	40
SIOTSMS2	53	20
SIOTSMS3	53	10
SIOTSMS4	53	8
SIOTSMS5	53	4
SIOTSMS6	53	2
SIOTSMS7	53	1
SIOTSSDS	34	80
SIOTSSGP	2F	10
SIOTSSMG	2F	8
SIOTSSNM	68	
SIOTSSWA	5D	
SIOTSTEP	3B	8
SIOTSTMT	6C	
SIOTSWB	54	
SIOTTERM	2F	40
SIOTTRKM	2F	4
SIOTTRTD	66	10
SIOTTSTC	2F	
SIOTTYPE	3	
SIOTUCNT	51	2
SIOTULAB	66	40
SIOTUNAF	14	14
SIOTUNCT	37	1
SIOTUSEQ	66	80
SIOTUTRT	66	20
SIOTVAFF	3B	4
SIOTVCNT	51	1
SIOTVLCT	36	
SIOTVLSP	18	
SIOTVMVP	A4	
SIOTVRSB	24	
SIOTWOWO	34	10
SIOTWTRN	61	1
SIOTXSVA	63	
SIOUBYT1	3C	
SIOUBYT2	3D	
SIOUBYT3	3E	
SIOUBYT4	3F	
SIOUCBAD	41	
SIOUCBA4	40	
SIOUCNVT	40	
SIOUCVTD	2E	4
SIOVAMDS	2B	20
SIOVDSNL	AC	
SIOVDSNT	AA	
SIO3COMM	3E	40
SIO3DACC	3E	20
SIO3DISP	3E	10
SIO3TAPE	3E	80
SIO3UREC	3E	8
SJFCBPTR	9C	
S3400DSP	37	40
S3400OFF	2E	10

SJACP Information

SJACP Heading Information

Common Name: SCHEDULER JCL FACILITY ACCESS FUNCTION PARAMETER LIST
Macro ID: IEFSJACP
DSECT Name: SJACP, SJACRQT
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: SJAC
Offset: 0
Length: 4
Storage Attributes: Subpool: Any
Key: Caller's key
Size: 80 (decimal)
Created by: Caller
Pointed to by: N/A
Serialization: None
Function: Mapping for the Scheduler JCL facility access function parameter list.

SJACP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	80	SJACP	SJF Access Function parm list
0	(0)	CHARACTER	4	SJACID	Identifier 'SJAC'
4	(4)	UNSIGNED	1	SJACVERS	Version number
5	(5)	CHARACTER	1	SJACFLAG	Control flags
		1...		SJACNREC	No recovery
		.1..		SJACNOCU	No cleanup
		..11 1111		*	Reserved
6	(6)	SIGNED	2	SJACLEN	Length of parameter list
8	(8)	ADDRESS	4	SJACSTOR	Local storage pointer or zero
12	(C)	SIGNED	4	SJACREAS	Reason code (returned)
16	(10)	CHARACTER	8	SJACTOKN	SJF token
24	(18)	CHARACTER	56	SJACFLDS	Used to zero parameter list
24	(18)	CHARACTER	1	SJACRQST	Request type
		1...		SJACUPD	Update
		.1..		SJACRET	Retrieve
		..1.		SJACFIND	Find
		...1 1111		*	Reserved
25	(19)	CHARACTER	1	SJACFUNC	Flag field
		1...		SJACSYST	System input
		.1..		SJACUNAU	Request is from an invoker whose caller is unauthorized
		..1.		SJACCNT	Continue processing after errors have occurred
		...1		SJACJRNL	Journaling requested
		.... 1...		SJACOSER	Serialization on swb use count is not required
		.... .111		*	Reserved
26	(1A)	UNSIGNED	2	SJACREQ#	Number of individual requests
28	(1C)	ADDRESS	4	SJACRPTR	Pointer to request table
32	(20)	CHARACTER	16	SJACCHID	SWB chain identification
32	(20)	CHARACTER	8	SJACVERB	Verb
40	(28)	CHARACTER	8	SJACLABL	Statement label
48	(30)	CHARACTER	20	SJACFNP	Parameters used for FIND requests only
48	(30)	CHARACTER	1	SJACFLG2	Function flag
		1...		SJACNEXT	Find next SWB processing
		.1..		SJACNJST	JOB token supplied
		..1.		SJACJBTK	JOB token build requested
		...1		SJACCSTK	Current Step token build requested
		 1111		*	Reserved
49	(31)	CHARACTER	1	SJACFUN1	FIND non-master scheduler flag byte
		1...		SJACJOB	Job level
		.1..		SJACCST	Current step level
		..1.		SJACST	Step level or procname and step
		...1 1111		*	Reserved
50	(32)	CHARACTER	2	SJACRSV0	Reserved
52	(34)	CHARACTER	8	SJACSTPN	Step name
60	(3C)	CHARACTER	8	SJACPRLB	Label on the proc statement
68	(44)	UNSIGNED	4	SJACSTMT	Statement number in hex (returned)
72	(48)	ADDRESS	4	SJACALT	Alternate SWA manager address
76	(4C)	CHARACTER	4	SJACRSV2	Reserved

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	16	SJACRQT (*)	Request table
0	(0)	CHARACTER	16	SJACENTY	Request table entry
0	(0)	SIGNED	4	SJACRSN	Reason code (returned)
4	(4)	CHARACTER	10	SJACINFO	Request table information
4	(4)	ADDRESS	4	SJACADDR	Address of area
8	(8)	SIGNED	2	SJACLNTH	Length of area
10	(A)	UNSIGNED	2	SJACKEY	SJF Key
12	(C)	UNSIGNED	1	SJACPARM	Parameter number
13	(D)	BITSTRING	1	SJACVALB	Keyword validity byte
		1...		SJACVLKY	Keyword associated with this parameter was specified by the user
		.111 1111		*	Reserved
14	(E)	UNSIGNED	2	SJACARLN	Actual returned length of area

SJACP Constants

Len	Type	Value	Name	Description
		Comment		
		ADDITIONAL DATA NEEDED FOR PARAMETER LIST		
		End of Comment		
4	CHARACTER	SJAC	SJACCID	Identifier
1	DECIMAL	1	SJACCVER	Version number

SJACP Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
SJACADDR	4		SJACSTOR	8	
SJACALT	48		SJACSTPN	34	
SJACARLN	E		SJACSYST	19	80
SJACCHID	20		SJACTOKN	10	
SJACCNT	19	20	SJACUNAU	19	40
SJACCST	31	40	SJACUPD	18	80
SJACCSTK	30	10	SJACVALB	D	
SJACENTY	0		SJACVERB	20	
SJACFIND	18	20	SJACVERS	4	
SJACFLAG	5		SJACVLKY	D	80
SJACFLDS	18				
SJACFLG2	30				
SJACFNP	30				
SJACFUNC	19				
SJACFUN1	31				
SJACID	0				
SJACINFO	4				
SJACJBTK	30	20			
SJACJOB	31	80			
SJACJRNL	19	10			
SJACKEY	A				
SJACLABL	28				
SJACLEN	6				
SJACLNTH	8				
SJACNEXT	30	80			
SJACNJST	30	40			
SJACNOCU	5	40			
SJACNREC	5	80			
SJACOSER	19	08			
SJACP	0				
SJACPARM	C				
SJACPRLB	3C				
SJACREAS	C				
SJACREQ#	1A				
SJACRET	18	40			
SJACRPTR	1C				
SJACRQST	18				
SJACRQT	0				
SJACRSN	0				
SJACRSV0	32				
SJACRSV2	4C				
SJACST	31	20			
SJACSTMT	44				

SJCLS Information

SJCLS Heading Information

Common Name: Scheduler JCL Facility JDT Class Definitions
Macro ID: IEFSJCLS
DSECT Name: SJCLS
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: N/A
Offset: N/A
Length: N/A
Storage Attributes: Subpool: N/A
Key: N/A
Residency: N/A
Size: 0
Created by: N/A
Pointed to by: N/A
Serialization: N/A
Function: Defines the JDT class names. A class is a subset of the keywords defined to a verb in the JDTs. A class is a logical grouping of the keywords of a JDT defined verb. A keyword is assigned to a class by a parameter on the JDT JDKEY macro. The names of the classes are defined in IEFSJCLS.

SJCLS Map

SJCLS Constants

Len	Type	Value	Name	Description
8	HEX	0000000000000000	SJCLALL	Identifies all keywords/keys are requested
8	CHARACTER	J2GROUP	SJCLJ2G	Identifies SJF keys used in JES2 output grouping
8	CHARACTER	SPOOL	SJCLSPOL	Identifies SJF keys that are defined to the JES's as spoolable
8	CHARACTER	USERGRP	SJCLUSER	Identifies SJF user-oriented defined keys

SJDLP Information

SJDLP Heading Information

Common Name: SCHEDULER JCL FACILITY DELETE SCHEDULER BLOCK (SWB) PARAMETER LIST
Macro ID: IEFSJDLP
DSECT Name: SJDLP
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: SJDL
Offset: 0
Length: 4
Storage Attributes: Subpool: Any
Key: Any
Residency: Any
Size: 32 bytes
Created by: Caller of SJFREQ REQUEST=DELETESWB
Pointed to by: On entry to SJF, register 1 points to a word that points to SJDLP
Serialization: None
Function: MAPPING FOR THE SCHEDULER JCL FACILITY DELETE SWB PARAMETER LIST.

SJDLP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	32	SJDLP	DELETE SWB PARAMETER LIST
0	(0)	CHARACTER	4	SJDLID	IDENTIFIER C'SJDL'
4	(4)	UNSIGNED	1	SJDLVERS	VERSION NUMBER
5	(5)	BITSTRING	1	SJDLFLAG	CONTROL FLAG BYTE
		1...		SJDLNREC	NO RECOVERY
		.1..		SJDLNOCU	NO CLEANUP
		..11 1111		*	RESERVED
6	(6)	SIGNED	2	SJDLLEN	LENGTH OF PARAMETER LIST
8	(8)	ADDRESS	4	SJDLSTOR	LOCAL STORAGE POINTER OR ZERO
12	(C)	SIGNED	4	SJDLREAS	REASON CODE
16	(10)	CHARACTER	4	SJDLRSV1	RESERVED
20	(14)	CHARACTER	8	SJDLTOKN	SWB CHAIN TOKEN (SEE NOTE ABOVE)
20	(14)	ADDRESS	4	SJDLANBK	ADDRESS OF CONTROL BLOCK FOR A JCL STATEMENT (JCT, SCT, SIOT OR SWB) OR THE ADDRESS OF A SWB CHAIN
24	(18)	ADDRESS	4	SJDLANCA	ADDRESS OF A WORD POINTING TO A SWB CHAIN OR ZERO
28	(1C)	CHARACTER	1	SJDLFUNC	FUNCTION FLAGS FOR DELETE
		1...		SJDLLDEL	LOGICALLY DELETE THE SWB CHAIN INDICATED BY THE TOKEN
		.111 1111		*	RESERVED
29	(1D)	CHARACTER	3	SJDLRSV2	RESERVED

SJDLP Constants

Len	Type	Value	Name	Description
Comment				
ADDITIONAL DATA NEEDED FOR PARAMETER LIST				
End of Comment				
4	CHARACTER	SJDL	SJDLCID	IDENTIFIER
1	DECIMAL	1	SJDLCVER	VERSION NUMBER

SJDLP Cross Reference

Name	Hex Offset	Hex Value
SJDLANBK	14	
SJDLANCA	18	
SJDLFLAG	5	
SJDLFUNC	1C	
SJDLID	0	
SJDLLDEL	1C	80
SJDLLEN	6	
SJDLNOCU	5	40
SJDLNREC	5	80
SJDLP	0	
SJDLREAS	C	
SJDLRSV1	10	
SJDLRSV2	1D	
SJDLSTOR	8	
SJDLTOKN	14	
SJDLVERS	4	

SJERP Information

SJERP Heading Information

Common Name: SJF ERASE SWB PARAMETER LIST
Macro ID: IEFSJERP
DSECT Name: SJERP
Owning Component: Scheduler services (BB131)
Eye-Catcher ID: SJER
Offset: 0
Length: 4
Storage Attributes: Subpool: Any
Key: Caller's key
Residency: Any
Size: 58 (decimal)
Created by: Caller of SJFREQ REQUEST=ERASE
Pointed to by: On entry to SJF, register 1 points to a word that points to SJERP
Serialization: None
Function: The parameter list identifies a SWB chain (via the token) containing the field to be erased, as well as the JDVT/VERB/KEYWORD/PARAMETER or JDVT/VERB/KEY/PARAMETER to be erased.

SJERP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	58	SJERP	Parameter list
0	(0)	CHARACTER	4	SJERID	Identifier 'SJER'
4	(4)	UNSIGNED	1	SJERVERS	Version number
5	(5)	BITSTRING	1	SJERFLAG	Control flags
		1...		SJERNREC	No recovery
		.1..		SJERNOCU	No cleanup
		..11 1111		*	Reserved
6	(6)	SIGNED	2	SJERLEN	Length of parameter list
8	(8)	SIGNED	4	SJERSTOR	Local storage pointer
12	(C)	SIGNED	4	SJERREAS	Reason code (returned)
16	(10)	CHARACTER	8	SJERTOKN	Token identifying SWB chain
16	(10)	ADDRESS	4	SJERANBK	
20	(14)	ADDRESS	4	SJERANCA	
24	(18)	BITSTRING	1	SJERFUNC	Function byte
		1...		SJERJOUR	Journalling requested
		.1..		SJERALL	Erase all subparameters
		..1.		SJERSUB	Erase all sublist data
		...1 1111		*	Reserved
25	(19)	CHARACTER	3	SJERRSV1	Reserved
28	(1C)	CHARACTER	8	SJERJDVT	JDVT name for keyword to erase
36	(24)	CHARACTER	8	SJERVERB	Verb of keyword to be erased
44	(2C)	CHARACTER	8	SJERKEYW	Keyword to be erased
52	(34)	UNSIGNED	2	SJERPARM	Parameter to be erased
54	(36)	UNSIGNED	2	SJERSUBL	Sublist element to be erased
56	(38)	UNSIGNED	2	SJERKEY	Key to be erased

SJERP Constants

Len	Type	Value		Name	Description
Comment					
Additional data needed for the Erase parameter list					
End of Comment					
4	CHARACTER	SJER		SJERCID	Parameter list acronym
1	DECIMAL		1	SJERCVER	Parameter list version

SJERP Cross Reference

Name	Hex Offset	Hex Value
SJERALL	18	40
SJERANBK	10	
SJERANCA	14	
SJERFLAG	5	
SJERFUNC	18	
SJERID	0	
SJERJDVT	1C	
SJERJOUR	18	80
SJERKEY	38	
SJERKEYW	2C	
SJERLEN	6	
SJERNOCU	5	40
SJERNREC	5	80
SJERP	0	
SJERPARM	34	
SJERREAS	C	
SJERRSV1	19	
SJERSTOR	8	
SJERSUB	18	20
SJERSUBL	36	
SJERTOKN	10	
SJERVERB	24	
SJERVERS	4	

SJFNP Information

SJFNP Heading Information

Common Name: Scheduler JCL Facility Find SWB Parameter List
Macro ID: IEFSJFNP
DSECT Name: SJFNP
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: SJFN
Offset: 0
Length: 4
Storage Attributes: Subpool: Callers
Key: Callers
Size: 72 bytes
Created by: Caller
Pointed to by: Caller sets up Register 1 pointing to a word which points to SJFNP.
Serialization: None
Function: Maps the input for the Scheduler JCL Facility Find SWB routine.

SJFNP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	72	SJFNP	FIND SWB PARAMETER LIST
0	(0)	CHARACTER	4	SJFNID	IDENTIFIER C'SJFN'
4	(4)	UNSIGNED	1	SJFNVERS	VERSION NUMBER
5	(5)	BITSTRING	1	SJFNFLAG	CONTROL FLAG BYTE
		1...		SJFNNREC	NO RECOVERY
		.1..		SJFNNOCU	NO CLEANUP
		..11 1111		*	RESERVED
6	(6)	SIGNED	2	SJFNLEN	LENGTH OF PARAMETER LIST
8	(8)	ADDRESS	4	SJFNSTOR	LOCAL STORAGE POINTER OR ZERO
12	(C)	SIGNED	4	SJFNREAS	REASON CODE
16	(10)	CHARACTER	56	SJFNINFO	PARAMETER INFORMATION
16	(10)	BITSTRING	1	SJFNFLG2	
		1...		SJFNNEXT	FIND NEXT SWB PROCESSING
		.1..		SJFNCNTL	SEARCH FOR A STATEMENT WITHIN A CONTROL GROUP
		..1.		SJFNSASP	STARTING ADDRESS SPECIFIED
		...1		SJFNOSER	SERIALIZATION ON SWB USE COUNT IS TO BE BYPASSED WHEN ON
		.... 1...		SJFNRSWB	INDICATES THAT RETURNSWB WILL BE ISSUED FOR INPUT TOKEN
		.... .111		*	RESERVED
17	(11)	BITSTRING	2	SJFNIDSW	IDENTIFY THE SWB TO BE FOUND
17	(11)	BITSTRING	1	SJFNFUN1	NON-MASTER SCHEDULER FLAG BYTE
		1...		SJFNJOB	JOB LEVEL
		.1..		SJFNCST	CURRENT STEP LEVEL
		..1.		SJFNST	STEP LEVEL OR PROC AND STEP
		...1 1111		*	RESERVED
18	(12)	BITSTRING	1	SJFNFUN2	MASTER SCHEDULER FLAG BYTE
		1...		SJFNMSTJ	JOB LEVEL
		.1..		SJFNMSTS	STEP LEVEL
		..11 1111		*	RESERVED
19	(13)	BITSTRING	1	SJFNFLG3	
		1...		SJFNJST	JOB TOKEN INDICATOR
		.111 1111		*	RESERVED
20	(14)	CHARACTER	8	SJFNSTPN	STEPNAME-REQUIRED IF SJFNST ON
28	(1C)	CHARACTER	16	SJFNCHID	SWB CHAIN IDENTIFICATION
28	(1C)	CHARACTER	8	SJFNVERB	VERB (OPTIONAL IF NOT DD)
36	(24)	CHARACTER	8	SJFNLABL	STATEMENT LABEL (OPTIONAL)
44	(2C)	CHARACTER	8	SJFNTOKN	SWB CHAIN TOKEN
44	(2C)	ADDRESS	4	SJFNANBK	ADDRESS OF CONTROL BLOCK FOR A JCL STATEMENT (JCT, SCT, SIOT OR SWB) OR THE ADDRESS OF A SWB CHAIN
48	(30)	ADDRESS	4	SJFNANCA	ADDRESS OF A WORD POINTING TO A SWB CHAIN OR ZERO
52	(34)	CHARACTER	8	SJFNCNLB	LABEL ON THE CNTL STATEMENT
60	(3C)	CHARACTER	8	SJFNPRLB	LABEL ON THE PROC STATEMENT (OPTIONAL)
68	(44)	UNSIGNED	4	SJFNSTMT	STATEMENT NUMBER RETURNED IN HEXADECIMAL

SJFNP Constants

Len	Type	Value		Name	Description
		Comment			
	ADDITIONAL DATA NEEDED FOR PARAMETER LIST				
		End of Comment			
4	CHARACTER	SJFN		SJFNCID	IDENTIFIER
1	DECIMAL		2	SJFNCVER	VERSION NUMBER

SJFNP Cross Reference

Name	Hex Offset	Hex Value
SJFNANBK	2C	
SJFNANCA	30	
SJFNCHID	1C	
SJFNCNLB	34	
SJFNCNTL	10	40
SJFNCST	11	40
SJFNFLAG	5	
SJFNFLG2	10	
SJFNFLG3	13	
SJFNFUN1	11	
SJFNFUN2	12	
SJFNID	0	
SJFNIDSW	11	
SJFNINFO	10	
SJFNJOB	11	80
SJFNJST	13	80
SJFNLABL	24	
SJFNLEN	6	
SJFNMSTJ	12	80
SJFNMSTS	12	40
SJFNNEXT	10	80
SJFNNOCU	5	40
SJFNNREC	5	80
SJFNOSER	10	10
SJFNP	0	
SJFNPRLB	3C	
SJFNREAS	C	
SJFNRSWB	10	08
SJFNSASP	10	20
SJFNST	11	20
SJFNSTMT	44	
SJFNSTOR	8	
SJFNSTPN	14	
SJFNTOKN	2C	
SJFNVERB	1C	
SJFNVERS	4	

SJGEP Information

SJGEP Heading Information

Common Name: SJF GET SWB PARAMETER LIST
Macro ID: IEFSJGEP
DSECT Name: SJGEP
Owning Component: Scheduler JCL Facility (BB131
Eye-Catcher ID: SJGE
Offset: 0
Length: 4
Storage Attributes: Subpool: Caller
Key: Caller
Size: 44 bytes
Created by: Caller
Pointed to by: Caller sets up Register 1 pointing to a word which points to SJFNP.
Serialization: None
Function: Maps the input to the Scheduler JCL Facility Get SWB routine.

SJGEP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	44	SJGEP	
0	(0)	CHARACTER	4	SJGEID	IDENTIFIER
4	(4)	UNSIGNED	1	SJGEVERS	VERSION NUMBER
5	(5)	BITSTRING	1	SJGEFLAG	CONTROL FLAGS
		1...		SJGENREC	NO RECOVERY
		.1..		SJGENOCU	NO CLEANUP
		..11 1111		*	RESERVED
6	(6)	SIGNED	2	SJGELEN	LENGTH OF PARAMETER LIST
8	(8)	ADDRESS	4	SJGESTOR	LOCAL STORAGE POINTER
12	(C)	SIGNED	4	SJGEREAS	RESULT REASON CODE (RETURNED)
16	(10)	CHARACTER	8	SJGETOKN	SWB TOKEN
16	(10)	ADDRESS	4	SJGEANBK	ADDRESS OF CONTROL BLOCK FOR A JCL STATEMENT (JCT, SCT, SIOT OR SWB) OR THE ADDRESS OF A SWB CHAIN
20	(14)	ADDRESS	4	SJGEANCA	ADDRESS OF A WORD POINTING TO A SWB CHAIN OR ZERO
24	(18)	BITSTRING	2	SJGEQUAL	BIT QUALIFIERS FOR KEYWORD SELECTION
24	(18)	BITSTRING	1	SJGEPOSI	ATTRIBUTES REQUESTED
		1...		SJGESPL	KEYWORDS TO BE SPOOLED
		.111 1111		*	RESERVED
25	(19)	BITSTRING	1	SJGENEGA	ATTRIBUTES NOT REQUESTED
		1...		SJGENSPL	KEYWORDS NOT TO BE SPOOLED
		.111 1111		*	RESERVED
26	(1A)	SIGNED	2	SJGERSV2	RESERVED
28	(1C)	ADDRESS	4	SJGESWBA	ADDRESS OF AREA TO COPY THE KEYWORD DATA
32	(20)	SIGNED	2	SJGEALEN	LENGTH OF KEYWORD DATA AREA
34	(22)	SIGNED	2	SJGERSV4	RESERVED
36	(24)	CHARACTER	8	SJGEJDVT	JDVT NAME

SJGEP Constants

Len	Type	Value		Name	Description
Comment					
ADDITIONAL DATA NEEDED FOR THE GET SWB PARAMETER LIST					
End of Comment					
4	CHARACTER	SJGE		SJGECID	IDENTIFIER
1	DECIMAL		1	SJGECVER	CURRENT VERSION NUMBER

SJGEP Cross Reference

Name	Hex Offset	Hex Value
SJGEALEN	20	
SJGEANBK	10	
SJGEANCA	14	
SJGEFLAG	5	
SJGEID	0	
SJGEJDVT	24	
SJGELEN	6	
SJGENEGA	19	
SJGENOCU	5	40
SJGENREC	5	80
SJGENSPL	19	80
SJGEP	0	
SJGEPOSI	18	
SJGEQUAL	18	
SJGEREAS	C	
SJGERSV2	1A	
SJGERSV4	22	
SJGESPL	18	80
SJGESTOR	8	
SJGESWBA	1C	
SJGETOKN	10	
SJGEVERS	4	

SJKEY Information

SJKEY Programming Interface information

Programming Interface information

SJKEY

ONLY the following fields are part of the programming interface information:

- SJKYACDE
- SJKYAVGR
- SJKYCNTL
- SJKYDACL
- SJKYDSNT
- SJKYFDAT
- SJKYJBYT
- SJKYJCRD
- SJKYJENV
- SJKYJLIN
- SJKYJPAG
- SJKYKEYO
- SJKYLIKE
- SJKYMGCL
- SJKYOUTP
- SJKYPATH
- SJKYPMDE
- SJKYPNDS
- SJKYPOPT
- SJKYRECO
- SJKYREFD
- SJKYRLS
- SJKYSECM
- SJKYSEGM
- SJKYSPIN
- SJKYSTCL

End of Programming Interface information

SJKEY Heading Information

Common Name: Scheduler JCL Facility (SJF) Key Constants
Macro ID: IEFSJKEY
DSECT Name: n/a
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: n/a
Offset: n/a
Length: n/a
Storage Attributes: Virtual Storage: included in module's dynamic area
Size: n/a
Created by: n/a
Pointed to by: n/a
Serialization: None
Function: Provides keys for SJF defined JCL keywords

SJKEY Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0		
0	(0)	SIGNED	4	SJKY (0)	
0	(0)	CHARACTER	8	SJVBDD	
0	(0)	BITSTRING	0	SJKYACDE	"X'8001'" ACCODE
0	(0)	BITSTRING	0	SJKYOUTP	"X'8002'" OUTPUT 1 20
				Comment	
				Include new DD keys needed by SVC 99 callers SJF DD ALLOCATION KEYS	
				End of Comment	
0	(0)	BITSTRING	0	SJKYCNTL	"X'8003'" CNTL
0	(0)	BITSTRING	0	SJKYSTCL	"X'8004'" STORCLAS
0	(0)	BITSTRING	0	SJKYMGCL	"X'8005'" MGMTCLAS
0	(0)	BITSTRING	0	SJKYDACL	"X'8006'" DATACLAS
0	(0)	BITSTRING	0	SJKYRECO	"X'800B'" RECORG
				Comment	
				Values for RECORG keyword	
				End of Comment	
		1...		SJVLROKS	"X'80'" KS - Key sequence
		.1..		SJVLROES	"X'40'" ES - Entry sequence
		..1.		SJVLRORR	"X'20'" RR - Relative record
		...1		SJVLROLS	"X'10'" LS - Linear space
0	(0)	BITSTRING	0	SJKYKEYO	"X'800C'" KEYOFF
0	(0)	BITSTRING	0	SJKYREFD	"X'800D'" REFDD
0	(0)	BITSTRING	0	SJKYSECM	"X'800E'" SECMODEL
				Comment	
				Value for GENERIC option of SECMODEL (parameter #2)	
				End of Comment	
		1...		SJVLGENR	"X'80'" Generic option
0	(0)	BITSTRING	0	SJKYLIKE	"X'800F'" LIKE
0	(0)	BITSTRING	0	SJKYAVGR	"X'8010'" AVGREC
				Comment	
				Values for AVGREC keyword	
				End of Comment	
		1...		SJVLARUN	"X'80'" U - units (ie times 1)
		.1..		SJVLARKI	"X'40'" K - kilo (ie times 1000)
		..1.		SJVLARME	"X'20'" M - Mega (ie times 1 million)
0	(0)	BITSTRING	0	SJKYDSNT	"X'8012'" DSNTYPE

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					Values for DSNTYPE keyword
					End of Comment
		1...		SJVLDTLI	"X'80'" LIBRARY
		.1..		SJVLDTPD	"X'40'" PDS
		..1.		SJVLPIPE	"X'20'" PIPE
		...1		SJVLHFSI	"X'10'" HFS
		.... 1...		SJVLEXR	"X'08'" EXTREQ
		.... .1..		SJVLEXP	"X'04'" EXTPREF
		.... ..1.		SJVLBASC	"X'02'" BASIC
		1		SJVLLARG	"X'01'" LARGE
0	(0)	BITSTRING	0	SJKYSPIN	"X'8013'" SPIN
					Comment
					Values for SPIN keyword
					End of Comment
		1...		SJVLSPUN	"X'80'" UNALLOC
		.1..		SJVLSPNO	"X'40'" NO
0	(0)	BITSTRING	0	SJKYSEGM	"X'8014'" SEGMENT
0	(0)	BITSTRING	0	SJKYPATH	"X'8017'" PATH
0	(0)	BITSTRING	0	SJKYPOPT	"X'8018'" PATHOPTS
					Comment
					Values for PATHOPTS keyword
					End of Comment
0	(0)	BITSTRING	0	SJVLSYNC	"X'00000100'" OSYNC
		11..		SJVLCEXL	"X'000000C0'" OCREXCL
		1...		SJVLCREA	"X'00000080'" OCREAT
		.1..		SJVLEXCL	"X'00000040'" OEXCL
		..1.		SJVLNOCT	"X'00000020'" ONOCTTY
		...1		SJVLTRUN	"X'00000010'" OTRUNC
		.... 1...		SJVLAPPE	"X'00000008'" OAPPEND
		.... .1..		SJVLNBLK	"X'00000004'" ONONBLOCK
		11		SJVLRDWR	"X'00000003'" ORDWR
		.... ..1.		SJVLRDON	"X'00000002'" ORDONLY
		1		SJVLWDON	"X'00000001'" OWRONLY
0	(0)	BITSTRING	0	SJKYPMDE	"X'8019'" PATHMODE
					Comment
					Values for PATHMODE keyword
					End of Comment
0	(0)	BITSTRING	0	SJVLSUID	"X'00000800'" SISUID
0	(0)	BITSTRING	0	SJVLSGID	"X'00000400'" SISGID
0	(0)	BITSTRING	0	SJVLRUSR	"X'00000100'" SIRUSR
		1...		SJVLWUSR	"X'00000080'" SIWUSR
		.1..		SJVLXUSR	"X'00000040'" SIXUSR
0	(0)	BITSTRING	0	SJVLRWXU	"X'000001C0'" SIRWXU
		..1.		SJVLRGRP	"X'00000020'" SIRGRP
		...1		SJVLWGRP	"X'00000010'" SIWGRP
		.... 1...		SJVLXGRP	"X'00000008'" SIXGRP
		..11 1...		SJVLRWXG	"X'00000038'" SIRWXG
		.... .1..		SJVLROTH	"X'00000004'" SIROTH
		.... ..1.		SJVLWOTH	"X'00000002'" SIWOTH
		1		SJVLXOTH	"X'00000001'" SIXOTH
		.... .111		SJVLRWXO	"X'00000007'" SIRWXO
0	(0)	BITSTRING	0	SJKYPNDS	"X'801A'" PATHDISP - Normal Disposition
0	(0)	BITSTRING	0	SJKYPCDS	"X'801B'" PATHDISP - Conditional Disposition
					Comment
					Values for PATHDISP keyword
					End of Comment
		.... 1...		SJVLKEEP	"X'08'" KEEP

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... .1..		SJVLDELE	"X'04'" DELETE
0	(0)	BITSTRING	0	SJKYRLS	"X'801C'" RLS - Record Level Sharing
				Comment	
				Values for RLS keyword	
				End of Comment	
		1...		SJVLNRI	"X'80'" NRI
		.1..		SJVLCR	"X'40'" CR
		..1.		SJVLCRE	"X'20'" CRE
0	(0)	BITSTRING	0	SJKYFDAT	"X'801D'" FILEDATA - file organization
				Comment	
				Values for FILEDATA keyword	
				End of Comment	
		1...		SJVLBIN	"X'80'" BINARY
		.1..		SJVLTEXT	"X'40'" TEXT
		..1.		SJVLREC	"X'20'" RECORD
0	(0)	BITSTRING	0	SJKYLGST	"X'801F'" LGSTREAM
0	(0)	BITSTRING	0	SJKYDCCS	"X'8020'" CCSID
0	(0)	BITSTRING	0	SJKYBSLM	"X'8022'" BLKSZLIM
0	(0)	BITSTRING	0	SJKYKYL1	"X'8023'" KEYLABL1
0	(0)	BITSTRING	0	SJKYKYL2	"X'8024'" KEYLABL2
0	(0)	BITSTRING	0	SJKYKYC1	"X'8025'" KEYENCD1
				Comment	
				Values for KEYENCD1 keyword	
				End of Comment	
		11.1 ..11		SJVLKE1L	"X'D3'" L - Label encoding
		11.. 1...		SJVLKE1H	"X'C8'" H - Hash encoding
0	(0)	BITSTRING	0	SJKYKYC2	"X'8026'" KEYENCD2
				Comment	
				Values for KEYENCD2 keyword	
				End of Comment	
		11.1 ..11		SJVLKE2L	"X'D3'" L - Label encoding
		11.. 1...		SJVLKE2H	"X'C8'" H - Hash encoding
0	(0)	BITSTRING	0	SJKYEATT	"X'8028'" EATTR
				Comment	
				Values for EATTR keyword	
				End of Comment	
		1		SJVLEATN	"X'01'" 0000 0001b - NO
		.... ..1.		SJVLEATO	"X'02'" 0000 0010b - OPT
0	(0)	BITSTRING	0	SJKYFRVL	"X'8029'" FREEVOL
				Comment	
				Values for FREEVOL keyword	
				End of Comment	
		1		SJVLFRVE	"X'01'" 0000 0001b - END
		.... ..1.		SJVLFRVV	"X'02'" 0000 0010b - EOV
0	(0)	BITSTRING	0	SJKYSPI2	"X'802A'" SPIN second parm, SPIN INTERVAL
0	(0)	BITSTRING	0	SJKYSYML	"X'802B'" SYMLIST ON DD
0	(0)	BITSTRING	0	SJKYDSNV	"X'802C'" DSNTYPE version
0	(0)	BITSTRING	0	SJKYMAXG	"X'802D'" MAXGENS - Requires APAR OA42358
0	(0)	BITSTRING	0	SJKYGDGO	"X'802E'" GDGORDER - GDG-all concatenation order
				Comment	
				Values for GDGORDER keyword	
				End of Comment	

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
		1...		SJVLGDGC	"X'80'" USECATLG
		.1..		SJVLGDGL	"X'40'" LIFO
		..1.		SJVLGDGF	"X'20'" FIFO
0	(0)	BITSTRING	0	SJKYRACF	"X'8007'" SMS RACF FIELD
0	(0)	BITSTRING	0	SJKYSMSD	"X'8008'" SMS DATA FIELD
0	(0)	BITSTRING	0	SJKYSTRG	"X'8009'" STORAGE GROUP
0	(0)	BITSTRING	0	SJKYDADM	"X'800A'" DAADM
0	(0)	BITSTRING	0	SJKYDCLD	"X'8011'" DATACLAS Definition
0	(0)	BITSTRING	0	SJKYZSEG	"X'8015'" ZSEGID
0	(0)	BITSTRING	0	SJKYPOOL	"X'8016'" ZDEVPOOL KEYWORD
0	(0)	BITSTRING	0	SJKYDCL2	"X'801E'" DATACLAS Definition II
0	(0)	BITSTRING	0	SJKYSMS2	"X'8021'" SMS DATA FIELD II
0	(0)	BITSTRING	0	SJKYDCL3	"X'8027'" DATACLAS Definition III
0	(0)	BITSTRING	0	SJKYSMSB	"X'8500'" SMS FLAG BYTE
8	(8)	BITSTRING	1	SJBYSMSB	Byte to retrieve SMS flags into
				Comment	
				Bit masks for SMS flags field	
				End of Comment	
		1...		SJBISMSM	"X'80'" SMS MANAGED BIT ON INDICATOR
8	(8)	BITSTRING	0	SJKYVRDD	"X'8501'" VOL=REF=DDNAME
8	(8)	BITSTRING	0	SJKYVRDS	"X'8502'" VOL=REF=DSNAME
8	(8)	BITSTRING	0	SJKYDDNM	"X'8503'" DDNAME
8	(8)	BITSTRING	0	SJKYVSRN	"X'8504'" VOL=SER=
8	(8)	BITSTRING	0	SJKYSPAC	"X'8505'" SPACE
				Comment	
				Values for first parameter of SPACE field	
				End of Comment	
		11..		SJVLCYL	"X'C0'" Request for Cylinders
		1...		SJVLTRK	"X'80'" Request for Tracks
		.1..		SJVLAVR	"X'40'" Request for Average Block length
		..1.		SJVLMSGP	"X'20'" Request for MSVGP
		.... 1...		SJVLCONT	"X'08'" Request for Contiguous
		.... .1..		SJVLMXIG	"X'04'" Request for MXIG
		.... ..1.		SJVLALX	"X'02'" Request for ALX
		1		SJVLRND	"X'01'" Request for Round
		....		SJVLABS	"X'00'" Request for ABSTR
9	(9)	BITSTRING	1	SJBYSPC5	Byte to retrieve RLSE value into
				Comment	
				Bit masks for fifth parameter of SPACE (RLSE) field	
				End of Comment	
		11..		SJBIRLSE	"X'C0'" Release specified
9	(9)	BITSTRING	0	SJKYMSVG	"X'8506'" MSVGP
9	(9)	BITSTRING	0	SJKYDSRG	"X'8507'" DSORG
				Comment	
				Values for first parameter of DSORG field	
				End of Comment	
		1...		SJVLIS	"X'80'" Indexed Sequential
		.1..		SJVLPS	"X'40'" Physical Sequential
		..1.		SJVLDA	"X'20'" Direct Access
		...1		SJVLBQ	"X'10'" BTAM or QTAM line group
		.... 1...		SJVLQD	"X'08'" QTAM Direct Access Queue
		.... .1..		SJVLQP	"X'04'" QTAM problem prog. Queue
		.... ..1.		SJVLPO	"X'02'" Partitioned
		1		SJVLPU	"X'01'" Unmovable
		1... ...1		SJVLISU	"X'81'" Index Sequential Unmovable
		.1.. ...1		SJVLPSU	"X'41'" Physical Sequential Unmovable
		..1. ...1		SJVLDAU	"X'21'" Direct Access Unmovable
		11		SJVLPOU	"X'03'" Partitioned Unmovable

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
				Comment	
		Values for the second parameter of DSORG field			
				End of Comment	
		1...		SJVLGS	"X'80'" Graphics
		.1..		SJVLTL	"X'40'" TCAM line group
		..1.		SJVLTM	"X'20'" TCAM message queue
		.... 1...		SJVLVS	"X'08'" VSAM
		.... .1..		SJVLTR	"X'04'" TCAM 3705
9	(9)	BITSTRING	0	SJKYDISP	"X'8508'" DISP
10	(A)	BITSTRING	1	SJBYDISP	Byte to retrieve the Status value into
				Comment	
		Bit masks for DISP-Status field			
				End of Comment	
		.... .1..		SJBINEW	"X'04'" NEW
		.... ..1.		SJBIMOD	"X'02'" MOD
		1		SJBIOLD	"X'01'" OLD
11	(B)	BITSTRING	1	SJBYDSP2	Byte to retrieve the normal termination parameter into
				Comment	
		Bit masks for DISP-Normal termination field			
				End of Comment	
		...1		SJBIPASS	"X'10'" PASS
		.... 1...		SJBIKEEP	"X'08'" KEEP
		.... .1..		SJBIDELT	"X'04'" DELETE
		.... ..1.		SJBICATL	"X'02'" CATLG
		1		SJBIUCTL	"X'01'" UNCATLG
12	(C)	BITSTRING	1	SJBYDSP3	Byte to retrieve the abnormal termination parameter into
				Comment	
		Bit masks for DISP-Abnormal termination field			
				End of Comment	
		.... 1...		SJBIKEPA	"X'08'" KEEP
		.... .1..		SJBIDELA	"X'04'" DELETE
		.... ..1.		SJBICATA	"X'02'" CATLG
		1		SJBIUCTA	"X'01'" UNCATLG
12	(C)	BITSTRING	0	SJKYDSNM	"X'8509'" DSNAME
				Comment	
		Bit masks for quoted data set name indicator (second parameter)			
				End of Comment	
13	(D)	BITSTRING	1	SJBYDSQU	Byte to retrieve quoted DSNAME indicator
		.... .1..		SJBIDSQU	"X'04'" If this bit is on, then DSNAME was specified in quotes
13	(D)	BITSTRING	0	SJKYDUMY	"X'850A'" DUMMY
14	(E)	BITSTRING	1	SJBYDUMY	Byte to retrieve the DUMMY indicator
				Comment	
		Bit masks for DUMMY field			
				End of Comment	
		1...		SJBIDUMY	"X'80'" Dummy
14	(E)	BITSTRING	0	SJKYDSID	"X'850B'" DSID
15	(F)	BITSTRING	1	SJBYDSID	Byte to retrieve the DSID into
				Comment	
		Bit masks for DSID field			
				End of Comment	
		.... 1...		SJBIDSID	"X'08'" DSID
15	(F)	BITSTRING	0	SJKYUNIT	"X'850C'" UNIT

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
15	(F)	BITSTRING	0	SJKYSYSI	"X'850D'" SYSIN INDICATOR
16	(10)	BITSTRING	1	SJBYSYSI	Byte to retrieve the SYSIN indicator
				Comment	
Bit masks for SYSIN field					
				End of Comment	
		.1..		SJBISYSI	"X'40'" SYSIN indicator
16	(10)	BITSTRING	0	SJKYPROT	"X'850E'" PROTECT field
17	(11)	BITSTRING	1	SJBYPROT	Byte to retrieve PROTECT field
				Comment	
Bit masks for PROTECT field					
				End of Comment	
		1...		SJBIPROT	"X'80'" PROTECT=YES indicator
17	(11)	BITSTRING	0	SJKYDFUB	"X'850F'" Default unit indicator
18	(12)	BITSTRING	1	SJBYDFUB	Byte to retrieve default unit indicator
				Comment	
Bit mask for default unit indicator					
				End of Comment	
		...1		SJBIDFUM	"X'10'"
		.... 1...		SJBIDDFU	"X'08'" Dynamic allocation default unit indicator
18	(12)	BITSTRING	0	SJKYSYSO	"X'8510'" SYSOUT INDICATOR
19	(13)	BITSTRING	1	SJBYSYSO	Byte to retrieve the SYSOUT indicator
				Comment	
Bit masks for SYSOUT field					
				End of Comment	
		.... 1...		SJBISYSO	"X'08'" SYSOUT indicator
19	(13)	BITSTRING	0	SJKYSMS7	"X'8511'" SMS managed mountable indicator
20	(14)	BITSTRING	1	SJBYSMS7	Byte for SMS mountable flag
				Comment	
Bit masks for the SMS managed mountable flag					
				End of Comment	
		1		SJBISMS7	"X'01'" SMS managed mountable indicator
20	(14)	BITSTRING	0	SJKYTERM	"X'8512'" TERM INDICATOR
21	(15)	BITSTRING	1	SJBYTERM	Byte to retrieve the TERM indicator
				Comment	
Bit masks for TERM field					
				End of Comment	
		.1..		SJBITERM	"X'40'" TERM indicator
21	(15)	BITSTRING	0	SJKYSUBS	"X'8513'" SUBSYS
22	(16)	BITSTRING	1	SJBYSUBS	Byte to retrieve the SUBSYS indicator
				Comment	
Bit masks for SUBSYS field					
				End of Comment	
		1...		SJBISUBS	"X'80'" SUBSYS indicator
22	(16)	BITSTRING	0	SJKYQNME	"X'8514'" QNAME
23	(17)	BITSTRING	1	SJBYQNME	Byte to retrieve the QNAME indicator
				Comment	
Bit masks for QNAME field					
				End of Comment	
		1		SJBIQNME	"X'01'" QNAME indicator

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
23	(17)	BITSTRING	0	SJKYGDG	"X'8515'" GDG
		Comment			
		Bit masks for GDG field			
		End of Comment			
		.... ..1.		SJBIGDG	"X'02'" GDG indicator
23	(17)	BITSTRING	0	SJKYELNM	"X'8516'" ELEMENT NAME/RELATIVE GDG NUMBER
23	(17)	BITSTRING	0	SJKYRKP	"X'8517'" RKP
23	(17)	BITSTRING	0	SJKYDSEQ	"X'8518'" Dataset Sequence Number
23	(17)	BITSTRING	0	SJKYLTYP	"X'8519'" LABEL Parameter
24	(18)	BITSTRING	1	SJBYLTYP	Byte to retrieve LABEL indicator
		Comment			
		Bit masks for LABEL indicator			
		End of Comment			
		.1..		SJBIAL	"X'40'" AL
		.1.. 1...		SJBIAUL	"X'48'" AUL
		..1. ...1		SJBILTM	"X'21'" LTM
		...1		SJBIBLP	"X'10'" BLP
		.... 1.1.		SJBISUL	"X'0A'" SUL
		.... .1..		SJBINSL	"X'04'" NSL
		.... ..1.		SJBISL	"X'02'" SL
		1		SJBINL	"X'01'" NL 4
24	(18)	BITSTRING	0	SJKYOUTL	"X'851A'" OUTLIM
24	(18)	BITSTRING	0	SJKYDEN	"X'851B'" DENSITY
		Comment			
		Choice values for the first DENSITY parameter.			
		End of Comment			
		11		SJVL200	"X'03'" 7 track 200 BPI
		.1.. ..11		SJVL556	"X'43'" 7 track 556 BPI
		1... ..11		SJVL800	"X'83'" 7 and 9 track 800 BPI
		11.. ..11		SJVL1600	"X'C3'" 9 track 1600 BPI
		11.1 ..11		SJVL6250	"X'D3'" 9 track 6250 BPI
24	(18)	BITSTRING	0	SJKYRECF	"X'851C'" RECFM
25	(19)	BITSTRING	1	SJBYRECF	Byte to retrieve the RECFM field
		Comment			
		Bit masks for RECFM field			
		End of Comment			
		11..		SJBIUNDF	"X'C0'" Undefined format
		1...		SJBIFIXD	"X'80'" Fixed
		.1..		SJBIVARI	"X'40'" Variable
		..1.		SJBIASCI	"X'20'" Variable/track overflow
		...1		SJBIBLOK	"X'10'" Blocked
		.... 1...		SJBISPAN	"X'08'" Standard/spanned
		.... .1..		SJBIASA	"X'04'" ASA Control Characters
		.... ..1.		SJBIMACH	"X'02'" Machine Control Characters
25	(19)	BITSTRING	0	SJKYLREL	"X'851D'" LRECL
25	(19)	BITSTRING	0	SJKYKEYL	"X'851E'" KEYLEN
25	(19)	BITSTRING	0	SJKYEXPD	"X'851F'" EXPDT
28	(1C)	SIGNED	4	(0)	Full word alignment
28	(1C)	CHARACTER	1		Align the fields correctly
29	(1D)	CHARACTER	3	SJBYEXPD (0)	3 Bytes to retrieve the EXPDT into
29	(1D)	CHARACTER	1	SJBYEXYY	Year offset from 1900
30	(1E)	CHARACTER	2	SJBYEXDD	Julian days (0-366)
30	(1E)	BITSTRING	0	SJKYTEMP	"X'8520'" TEMP
32	(20)	BITSTRING	1	SJBYTEMP	Byte to retrieve the TEMP indicator
		Comment			
		Bit masks for TEMP keyword			
		End of Comment			
		1		SJBITEMP	"X'01'" TEMP indicator

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
32	(20)	BITSTRING	0	SJKYDCBR	"X'8521'" DCB=DSNAME
32	(20)	BITSTRING	0	SJKYVSCT	"X'8522'" VOLUME SERIAL COUNT
32	(20)	BITSTRING	0	SJKYVLC1	"X'8523'" VOLUME COUNT - SIOT
32	(20)	BITSTRING	0	SJKYVLC2	"X'8524'" VOLUME COUNT - JFCB
32	(20)	BITSTRING	0	SJKYUAFF	"X'8525'" UNIT=AFF
33	(21)	BITSTRING	1	SJBYDSAT	Dataset attributes byte
				Comment	
				Bit mask for UNIT=AFF keyword	
				End of Comment	
		.... ..1.		SJBIUAFF	"X'02'" UNIT=AFF indicator
33	(21)	BITSTRING	0	SJKYDSCB	"X'8526'" DSCB TTR
33	(21)	BITSTRING	0	SJKYSGDS	"X'8527'" SGDS (system generated data set name) indicator
34	(22)	BITSTRING	1	SJBYSGDS	Byte to retrieve SDGS indicator
				Comment	
				Bit masks for SGDS keyword	
				End of Comment	
		...1		SJBISGDS	"X'10'" SGDS indicator
34	(22)	BITSTRING	0	SJKYUNCT	"X'8528'" UNIT Count field
34	(22)	BITSTRING	0	SJKYCOMP	"X'8529'" COMPACTION indicator
35	(23)	BITSTRING	1	SJBYCOMP	Byte for COMPACTION indicator
				Comment	
				Constants for TRTCH data. Only the values for compaction and non-compaction are supplied because these are the only values that SMS needs to check for and make updates for.	
				End of Comment	
		.... 1...		SJBICOMP	"X'08'" COMPACTION
		.... .1..		SJBINCMP	"X'04'" No COMPACTION
35	(23)	BITSTRING	0	SJKYTDSI	"X'852A'" TDSI Information (JFCB)
				Comment	
				Constants for Track Recording Technique (first nibble JFCBTDSI)	
				End of Comment	
		1111		SJVLTRKR	"X'F0'" - Track recording technique
		....		SJVLOREC	"X'00'" - Recording technology unknown or not specified
		...1		SJVL18TK	"X'10'" - 18 track recording mode - (hex value)
		..1.		SJVL36TK	"X'20'" - 36 track recording mode - (hex value)
		..11		SJVL128T	"X'30'" - 128 track recording mode - (hex value)
		.1..		SJVL256T	"X'40'" - 256 track recording mode - (hex value)
		.1.1		SJVL384T	"X'50'" - 384 track recording mode - (hex value)
		.11.		SJVLEFM1	"X'60'" - Enterprise Format 1 recording mode - (hex value)
		.111		SJVLEFM2	"X'70'" - Enterprise Format 2 recording mode - (hex value)
		1...		SJVLEEFM2	"X'80'" - Encrypted Enterprise Format 2 recording mode - (hex value)
		1..1		SJVLEFM3	"X'90'" - Enterprise Format 3 recording mode (hex value)
		1.1.		SJVLEEFM3	"X'A0'" - Encrypted Enterprise Format 3 recording mode - (hex value)
		1.11		SJVLEFM4	"X'B0'" - Enterprise Format 4 recording mode - (hex value)
		11..		SJVLEEFM4	"X'C0'" - Encrypted Enterprise Format 4 recording mode - (hex value)
				Comment	
				Constants for Media Type (second nibble of JFCBTDSI)	
				End of Comment	
		 1111		SJVLMEDI	"X'0F'" - Media type
		....		SJVLOMED	"X'00'" - Media type unknown or not specified
		1		SJVLMED1	"X'01'" - Cartridge System Tape - (hex value)
		.... ..1.		SJVLMED2	"X'02'" - Enhanced Capacity Cartridge System Tape - (hex value)
		11		SJVLMED3	"X'03'" - High Performance Cartridge Tape - (hex value)
		.... .1..		SJVLMED4	"X'04'" - Extended High Performance Cartridge Tape - (hex value)
		.... .1.1		SJVLMED5	"X'05'" - Enterprise Cartridge Tape - (hex value)
		.... .11.		SJVLMED6	"X'06'" - Enterprise WORM Cartridge Tape - (hex value)

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
		.... .111		SJVLMED7	"X'07'" - Enterprise Economy Cartridge Tape - (hex value)
		.... 1...		SJVLMED8	"X'08'" - Enterprise Economy WORM Cartridge Tape - (hex value)
		.... 1..1		SJVLMED9	"X'09'" - Enterprise Extended Cartridge Tape - (hex value)
		.... 1.1.		SJVLME10	"X'0A'" - Enterprise Extended WORM Cartridge Tape - (hex value)
		.... 1.11		SJVLME11	"X'0B'" - Enterprise Advanced Cartridge Tape - (hex value)
		.... 11..		SJVLME12	"X'0C'" - Enterprise Advanced WORM Cartridge Tape - (hex value)
		.... 11.1		SJVLME13	"X'0D'" - Enterprise Advanced Economy Cartridge Tape - (hex value)
					Comment
					Constants for Compaction Type (third nibble of JFCBTDSI)
					End of Comment
		1111		SJVLCMPT	"X'F0'" - Compaction type
		....		SJVLMPNS	"X'00'" - Compaction type unknown or not specified
		...1		SJVLNOCP	"X'10'" - Compaction not used - (hex value)
		..1.		SJVLIDRC	"X'20'" - Compaction type=IDRC - (hex value)
					Comment
					Constants for Special Attributes (fourth nibble of JFCBTDSI)
					End of Comment
		 1111		SJVLSPEC	"X'0F'" - Special attributes
		....		SJVLOSPC	"X'00'" - Volume has no special attributes
		1		SJVLDCOM	"X'01'" - Read compatibility attribute. When set, it indicates that the volumes will be used for input only and read compatible devices can be added to the device eligibility - (hex value)
35	(23)	BITSTRING	0	SJKYAIND	"X'852B'" IN/OUT Indicator on LABEL keyword
36	(24)	BITSTRING	1	SJBYAIND	Byte to retrieve IN/OUT data into
					Comment
					Bit masks for IN/OUT indicator
					End of Comment
		1...		SJBIINSP	"X'80'" If this bit is on, then IN was specified
		.1..		SJBIOUTS	"X'40'" If this bit is on, then OUT was specified
36	(24)	BITSTRING	0	SJKYRETP	"X'852C'" Expiration date derived from retention period
36	(24)	BITSTRING	0	SJKYRESL	"X'852D'" SMS VOLREF "to be resolved" flag
37	(25)	BITSTRING	1	SJBYRESL	Byte for SMS to be resolved flag
					Comment
					Bit masks for the SMS to be resolved flag
					End of Comment
		.... .1..		SJBIRESL	"X'04'" SMS VOLREF to be resolved
37	(25)	BITSTRING	0	SJKYBLKS	"X'852E'" Blocksize (from SIOTX)
					Comment
					Doubleword to retrieve Blocksize from SIOTX into
					End of Comment
38	(26)	BITSTRING	8	SJBYBLKS	Blocksize field from SIOTX
38	(26)	BITSTRING	0	SJKYUNAF	"X'852F'" Affed-to DD token (UNIT=AFF=DDx)
38	(26)	BITSTRING	0	SJKYSMSV	"X'8530'" Duplicate volume bit in SIOT
					Comment
					Bit mask for Duplicate Volume indicator for JES3
					End of Comment
46	(2E)	BITSTRING	1	SJBYDUPV	Byte for Duplicate Volume bit
		.... ..1.		SJBIDUPV	"X'02'" Duplicate volume indicator
46	(2E)	BITSTRING	0	SJKYTDS2	"X'8531'" TDSI Information (SIOTX)

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
				Comment	
					Constants for Track Recording Technique (first byte of TDSI within the SIOTX_TDSI)
				End of Comment	
46	(2E)	X'0'	0	SJBYOREC	"0" Recording Technology Unknown or Unspecified
46	(2E)	X'1'	0	SJBY18TK	"1" Read/Write on 18-track device
46	(2E)	X'2'	0	SJBY36TK	"2" Read/Write on 36-track device
46	(2E)	X'3'	0	SJBY128T	"3" Read/Write on 128-track device
46	(2E)	X'4'	0	SJBY256T	"4" Read/Write on 256-track device
46	(2E)	X'5'	0	SJBY384T	"5" 384 track recording mode
46	(2E)	X'6'	0	SJBYEFM1	"6" Enterprise Format 1 recording mode
46	(2E)	X'7'	0	SJBYEFM2	"7" Enterprise Format 2 recording mode
46	(2E)	X'8'	0	SJBYEEFM2	"8" Encrypted Enterprise Format 2 recording mode
46	(2E)	X'9'	0	SJBYEFM3	"9" Enterprise Format 3 recording mode
46	(2E)	X'A'	0	SJBYEEFM3	"10" Encrypted Enterprise Format 3 recording mode
46	(2E)	X'B'	0	SJBYEFM4	"11" Enterprise Format 4 recording mode
46	(2E)	X'C'	0	SJBYEEFM4	"12" Encrypted Enterprise Format 4 recording mode
				Comment	
					Constants for Media Type (second byte of SIOTX_TDSI)
				End of Comment	
46	(2E)	X'0'	0	SJBYOMED	"0" Media Type Unknown or Unspecified
46	(2E)	X'1'	0	SJBYMED1	"1" Media1 - Cartridge System Tape
46	(2E)	X'2'	0	SJBYMED2	"2" Media2 - Enhanced Capacity Cartridge System Tape
46	(2E)	X'3'	0	SJBYMED3	"3" Media3 - High Performance Cartridge Tape
46	(2E)	X'4'	0	SJBYMED4	"4" Media4 - Extended High Performance Cartridge Tape
46	(2E)	X'5'	0	SJBYMED5	"5" Media5 - Enterprise Cartridge Tape
46	(2E)	X'6'	0	SJBYMED6	"6" Media6 - Enterprise WORM Cartridge Tape
46	(2E)	X'7'	0	SJBYMED7	"7" Media7 - Enterprise Economy Cartridge Tape
46	(2E)	X'8'	0	SJBYMED8	"8" Media8 - Enterprise Economy WORM Cartridge Tape
46	(2E)	X'9'	0	SJBYMED9	"9" Media9 - Enterprise Extended Cartridge Tape
46	(2E)	X'A'	0	SJBYME10	"10" Media10 - Enterprise Extended WORM Cartridge Tape
46	(2E)	X'B'	0	SJBYME11	"11" Media11 - Enterprise Advanced Cartridge Tape
46	(2E)	X'C'	0	SJBYME12	"12" Media12 - Enterprise Advanced WORM Cartridge Tape
46	(2E)	X'D'	0	SJBYME13	"13" Media13 - Enterprise Advanced Economy Cartridge Tape
				Comment	
					Constants for Compaction Type (third byte of SIOTX_TDSI)
				End of Comment	
46	(2E)	X'0'	0	SJBYMPNS	"0" Compaction Unknown or not set
46	(2E)	X'1'	0	SJBYNOCP	"1" No compaction
46	(2E)	X'2'	0	SJBYCMP	"2" Compaction
				Comment	
					Constants for Special Attributes (fourth byte of SIOTX_TDSI)
				End of Comment	
46	(2E)	X'0'	0	SJBYOSPC	"0" Volume has no special attribute
46	(2E)	X'1'	0	SJBYDCOM	"1" Volume will be mounted for read only - All read-compatible devices may be selected
				Comment	
					KEY CONSTANTS FOR 'EXEC'
				End of Comment	
47	(2F)	CHARACTER	8	SJVBEXEC	
47	(2F)	BITSTRING	0	SJKYPGMN	"X'8900'" PROGRAM NAME
47	(2F)	BITSTRING	0	SJKYSACT	"X'8901'" STEP ACCOUNTING
47	(2F)	BITSTRING	0	SJKYSTPN	"X'8902'" STEPNAME
47	(2F)	BITSTRING	0	SJKYSACS	"X'8903'" STEP ACCOUNTING SUB PARAMETERS
		1		SJKYSCCS	"X'0001'" STEP CCSID
		.... ..1.		SJKYSRTO	"X'0002'" STEP RLSTMOUT

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					KEY CONSTANTS FOR 'EXPORT'
					End of Comment
55	(37)	CHARACTER	8	SJVBXPRT	
55	(37)	BITSTRING	0	SJKYEXPL	"X'8800'" EXPORT SYMLIST
55	(37)	BITSTRING	0	SJKYEXPS	"X'8801'" EXPORT EXPSET
55	(37)	BITSTRING	0	SJKYXSTP	"X'8802'" EXPORT XSTP
55	(37)	BITSTRING	0	SJKYXTYP	"X'8803'" EXPORT XTYP
					Comment
					KEY CONSTANTS FOR 'JOB'
					End of Comment
63	(3F)	CHARACTER	8	SJVBJOB	
63	(3F)	BITSTRING	0	SJKYJNME	"X'8700'" JOBNAME
63	(3F)	BITSTRING	0	SJKYJACT	"X'8701'" JOB ACCOUNTING
63	(3F)	BITSTRING	0	SJKYJACS	"X'8702'" JOB ACCOUNTING SUB PARAMETERS
		.... ..1.		SJKYJBYT	"X'0002'" BYTES (Max Count)
		11		SJKYJBY2	"X'0003'" BYTES (Disposition)
		.... .1..		SJKYJCRD	"X'0004'" CARDS (Max Count)
		.... .1.1		SJKYJCR2	"X'0005'" CARDS (Disposition)
		.... .11.		SJKYJPAG	"X'0006'" PAGES (Max Count)
		.... .111		SJKYJPA2	"X'0007'" PAGES (Disposition)
		.... 1...		SJKYJLIN	"X'0008'" LINES (Max Count)
		.... 1..1		SJKYJLI2	"X'0009'" LINES (Disposition)
		.... 1.1.		SJKYJENV	"X'000A'" SCHENV
		.... 1.11		SJKYJCCS	"X'000B'" JOB CCSID
		.... 11..		SJKYJJLG	"X'000C'" JESLOG (Disposition)
		.... 11.1		SJKYJJL2	"X'000D'" JESLOG (Frequency)
					Comment
					Values for JESLOG disposition
					End of Comment
		...1		SJVJSPIN	"X'10'" SPIN
		..1.		SJVJSUPP	"X'20'" SUPPRESS
		.1..		SJVJNOSP	"X'40'" NOSPIN
					Comment
					Values for BYTES, CARDS, PAGES and LINES disposition
					End of Comment
		...1		SJVLCANC	"X'10'" CANCEL
		..1.		SJVLDUMP	"X'20'" DUMP
		.1..		SJVLWARN	"X'40'" WARNING
63	(3F)	BITSTRING	0	SJKYMSGC	"X'8703'" Job Msgclass Information
63	(3F)	BITSTRING	0	SJKYSPRC	"X'8704'" Proc name for started task when JOBNAME= was used. Otherwise zeroes
63	(3F)	BITSTRING	0	SJKYJJRC	"X'8705'" JOBRC (method)
					Comment
					Values for JOBRC setting
					End of Comment
		.1..		SJVJJMRC	"X'40'" MAXRC
		..1.		SJVJJLRC	"X'20'" LASTRC
		...1		SJVJJSRC	"X'10'" STEP (see SJKYJJR2 for stepname)
63	(3F)	BITSTRING	0	SJKYJJR2	"X'8706'" JOBRC (stepname when JOBRC=STEP)
63	(3F)	BITSTRING	0	SJKYDSES	"X'8707'" DSENQSHR keyword
		1...		SJVJDSEA	"X'80'" ALLOW value for DSENQSHR
		.1..		SJVJDSEU	"X'40'" USEJC value for DSENQSHR
		..1.		SJVJDSED	"X'20'" DISALLOW value for DSENQSHR
63	(3F)	BITSTRING	0	SJKYJSYS	"X'8708'" SYSTEM keyword
63	(3F)	BITSTRING	0	SJKYJSYA	"X'8709'" SYSAFF keyword
63	(3F)	BITSTRING	0	SJKYJUJC	"X'870A'" UJOBCORR keyword

SJKEY Cross Reference

Name	Hex Offset	Hex Value
SJBIAL	18	40
SJBIASA	19	4
SJBIASCI	19	20
SJBIAUL	18	48
SJBIBLOK	19	10
SJBIBLP	18	10
SJBICATA	C	2
SJBICATL	B	2
SJBICOMP	23	8
SJBIDDFU	12	8
SJBIDELA	C	4
SJBIDELT	B	4
SJBIDFUM	12	10
SJBIDSID	F	8
SJBIDSQU	D	4
SJBIDUMY	E	80
SJBIDUPV	2E	2
SJBIFIXD	19	80
SJBIGDG	17	2
SJBIINSP	24	80
SJBIKEEP	B	8
SJBIKEPA	C	8
SJBILTM	18	21
SJBIMACH	19	2
SJBIMOD	A	2
SJBINCMP	23	4
SJBINEW	A	4
SJBINL	18	1
SJBINSL	18	4
SJBIOLD	A	1
SJBIOUTS	24	40
SJBIPASS	B	10
SJBIPROT	11	80
SJBIQNME	17	1
SJBIRESL	25	4
SJBIRLSE	9	C0
SJBISGDS	22	10
SJBISL	18	2
SJBISMSM	8	80
SJBISMS7	14	1
SJBISPAN	19	8
SJBISUBS	16	80
SJBISUL	18	A
SJBISYSI	10	40
SJBISYSO	13	8
SJBITEMP	20	1
SJBITERM	15	40
SJBIUAFF	21	2
SJBIUCTA	C	1
SJBIUCTL	B	1
SJBIUNDF	19	C0
SJBIVARI	19	40
SJBYAIND	24	
SJBYBLKS	26	
SJBYCMP	2E	2
SJBYCOMP	23	
SJBYDCOM	2E	1
SJBYDFUB	12	
SJBYDISP	A	
SJBYDSAT	21	
SJBYDSID	F	
SJBYDSP2	B	
SJBYDSP3	C	
SJBYDSQU	D	
SJBYDUMY	E	
SJBYDUPV	2E	
SJBYEEFM2	2E	8
SJBYEEFM3	2E	A
SJBYEEFM4	2E	C
SJBYEFM1	2E	6
SJBYEFM2	2E	7
SJBYEFM3	2E	9
SJBYEFM4	2E	B
SJBYEXDD	1E	
SJBYEXPD	1D	
SJBYEXYY	1D	
SJBYLTYP	18	
SJBYMED1	2E	1
SJBYMED2	2E	2
SJBYMED3	2E	3
SJBYMED4	2E	4
SJBYMED5	2E	5
SJBYMED6	2E	6
SJBYMED7	2E	7
SJBYMED8	2E	8
SJBYMED9	2E	9
SJBYME10	2E	A
SJBYME11	2E	B
SJBYME12	2E	C
SJBYME13	2E	D
SJBYMPNS	2E	0
SJBYNOCP	2E	1
SJBYOMED	2E	0
SJBYOREC	2E	0
SJBYOSPC	2E	0
SJBYPROT	11	
SJBYQNME	17	
SJBYRECF	19	
SJBYRESL	25	
SJBYSGDS	22	
SJBYSMSB	8	
SJBYSMS7	14	
SJBYSPC5	9	
SJBYSUBS	16	
SJBYSYSI	10	
SJBYSYSO	13	
SJBYTEMP	20	
SJBYTERM	15	
SJBY128T	2E	3
SJBY18TK	2E	1
SJBY256T	2E	4
SJBY36TK	2E	2
SJBY384T	2E	5
SJKY	0	
SJKYACDE	0	8001
SJKYAIND	23	852B
SJKYAVGR	0	8010
SJKYBLKS	25	852E
SJKYBSLM	0	8022
SJKYCNTL	0	8003
SJKYCOMP	22	8529
SJKYDACL	0	8006
SJKYDADM	0	800A
SJKYDCBR	20	8521
SJKYDCCS	0	8020
SJKYDCLD	0	8011
SJKYDCL2	0	801E
SJKYDCL3	0	8027
SJKYDDNM	8	8503
SJKYDEN	18	851B
SJKYDFUB	11	850F
SJKYDISP	9	8508
SJKYDSCB	21	8526
SJKYDSEQ	17	8518
SJKYDSES	3F	8707
SJKYDSID	E	850B
SJKYDSNM	C	8509
SJKYDSNT	0	8012
SJKYDSNV	0	802C
SJKYDSRG	9	8507
SJKYDUMY	D	850A
SJKYEATT	0	8028
SJKYELNM	17	8516
SJKYEXPD	19	851F

SJKEY Cross Reference

Name	Hex Offset	Hex Value
SJKYEXPL	37	8800
SJKYEXPS	37	8801
SJKYFDAT	0	801D
SJKYFRVL	0	8029
SJKYGDG	17	8515
SJKYGDGO	0	802E
SJKYJACS	3F	8702
SJKYJACT	3F	8701
SJKYJBYT	3F	2
SJKYJBY2	3F	3
SJKYJCCS	3F	B
SJKYJCRD	3F	4
SJKYJCR2	3F	5
SJKYJENV	3F	A
SJKYJJLG	3F	C
SJKYJJL2	3F	D
SJKYJJRC	3F	8705
SJKYJJR2	3F	8706
SJKYJLIN	3F	8
SJKYJLI2	3F	9
SJKYJNME	3F	8700
SJKYJPAG	3F	6
SJKYJPA2	3F	7
SJKYJSYA	3F	8709
SJKYJSYS	3F	8708
SJKYJUJC	3F	870A
SJKYKEYL	19	851E
SJKYKEYO	0	800C
SJKYKYC1	0	8025
SJKYKYC2	0	8026
SJKYKYL1	0	8023
SJKYKYL2	0	8024
SJKYLGST	0	801F
SJKYLIKE	0	800F
SJKYLREL	19	851D
SJKYLTYP	17	8519
SJKYMAXG	0	802D
SJKYMGCL	0	8005
SJKYMSGC	3F	8703
SJKYMSVG	9	8506
SJKYOUTL	18	851A
SJKYOUTP	0	8002
SJKYPATH	0	8017
SJKYPCDS	0	801B
SJKYPGMN	2F	8900
SJKYPMDE	0	8019
SJKYPNDS	0	801A
SJKYPOOL	0	8016
SJKYPOPT	0	8018
SJKYPROT	10	850E
SJKYQNME	16	8514
SJKYRACF	0	8007
SJKYRECF	18	851C
SJKYRECO	0	800B
SJKYREFD	0	800D
SJKYRESL	24	852D
SJKYRETP	24	852C
SJKYRKP	17	8517
SJKYRLS	0	801C
SJKYSACS	2F	8903
SJKYSACT	2F	8901
SJKYSCCS	2F	1
SJKYSECM	0	800E
SJKYSEGM	0	8014
SJKYSGDS	21	8527
SJKYSMSB	0	8500
SJKYSMSD	0	8008
SJKYSMSV	26	8530
SJKYSMS2	0	8021
SJKYSMS7	13	8511
SJKYSPAC	8	8505
SJKYSPIN	0	8013
SJKYSPI2	0	802A
SJKYSPRC	3F	8704
SJKYSRTO	2F	2
SJKYSTCL	0	8004
SJKYSTPN	2F	8902
SJKYSTRG	0	8009
SJKYSUBS	15	8513
SJKYSYML	0	802B
SJKYSYSI	F	850D
SJKYSYSO	12	8510
SJKYTDSI	23	852A
SJKYTDS2	2E	8531
SJKYTEMP	1E	8520
SJKYTERM	14	8512
SJKYUAFF	20	8525
SJKYUNAF	26	852F
SJKYUNCT	22	8528
SJKYUNIT	F	850C
SJKYVLC1	20	8523
SJKYVLC2	20	8524
SJKYVRDD	8	8501
SJKYVRDS	8	8502
SJKYVSCT	20	8522
SJKYVSRN	8	8504
SJKYXSTP	37	8802
SJKYXTYP	37	8803
SJKYZSEG	0	8015
SJVBDD	0	C4C44040
SJVBEXEC	2F	C5E7C5C3
SJVBJOB	3F	D1D6C240
SJVBXPRT	37	C5E7D7D6
SJVJDSEA	3F	80
SJVJDSED	3F	20
SJVJDSEU	3F	40
SJVJJLRC	3F	20
SJVJJMRC	3F	40
SJVJJSRC	3F	10
SJVJNOSP	3F	40
SJVJSPIN	3F	10
SJVJSUPP	3F	20
SJVLABS	8	0
SJVLALX	8	2
SJVLAPPE	0	8
SJVLARKI	0	40
SJVLARME	0	20
SJVLARUN	0	80
SJVLAVR	8	40
SJVLBASC	0	2
SJVLBIN	0	80
SJVLBQ	9	10
SJVLCANC	3F	10
SJVLCEXL	0	C0
SJVLCMPT	23	F0
SJVLCONT	8	8
SJVLCR	0	40
SJVLCRE	0	20
SJVLCREA	0	80
SJVLCYL	8	C0
SJVLDA	9	20
SJVLDAU	9	21
SJVLDCOM	23	1
SJVLDELE	0	4
SJVLDTLI	0	80
SJVLDTPD	0	40
SJVLDUMP	3F	20
SJVLEATN	0	1
SJVLEATO	0	2
SJVLEEFM2	23	80
SJVLEEFM3	23	A0
SJVLEEFM4	23	C0
SJVLEFM1	23	60
SJVLEFM2	23	70
SJVLEFM3	23	90
SJVLEFM4	23	B0
SJVLEXCL	0	40
SJVLEXP	0	4

Name	Hex Offset	Hex Value
SJVLEXR	0	8
SJVLFRVE	0	1
SJVLFRVV	0	2
SJVLGDGC	0	80
SJVLGDGF	0	20
SJVLGDGL	0	40
SJVLGENR	0	80
SJVLGS	9	80
SJVLHFSI	0	10
SJVLIDRC	23	20
SJVLIS	9	80
SJVLISU	9	81
SJVLKEEP	0	8
SJVLKE1H	0	C8
SJVLKE1L	0	D3
SJVLKE2H	0	C8
SJVLKE2L	0	D3
SJVLLARG	0	1
SJVLMEDI	23	F
SJVLMED1	23	1
SJVLMED2	23	2
SJVLMED3	23	3
SJVLMED4	23	4
SJVLMED5	23	5
SJVLMED6	23	6
SJVLMED7	23	7
SJVLMED8	23	8
SJVLMED9	23	9
SJVLME10	23	A
SJVLME11	23	B
SJVLME12	23	C
SJVLME13	23	D
SJVLMPNS	23	0
SJVLMSGP	8	20
SJVLMXIG	8	4
SJVLNBLK	0	4
SJVLNOCP	23	10
SJVLNOCT	0	20
SJVLNRI	0	80
SJVLOMED	23	0
SJVLOREC	23	0
SJVLOSPC	23	0
SJVLPIPE	0	20
SJVLPO	9	2
SJVLPOU	9	3
SJVLPS	9	40
SJVLPSU	9	41
SJVLPU	9	1
SJVLQD	9	8
SJVLQP	9	4
SJVLRDON	0	2
SJVLRDWR	0	3
SJVLREC	0	20
SJVLRGRP	0	20
SJVLRND	8	1
SJVLROES	0	40
SJVLROKS	0	80
SJVLROLS	0	10
SJVLRORR	0	20
SJVLROTH	0	4
SJVLRUSR	0	100
SJVLRWXG	0	38
SJVLRWXO	0	7
SJVLRWXU	0	1C0
SJVLSGID	0	400
SJVLSPEC	23	F
SJVLSPNO	0	40
SJVLSPUN	0	80
SJVLSUID	0	800
SJVLSYNC	0	100
SJVLTEXT	0	40
SJVLTL	9	40
SJVLTM	9	20
SJVLTR	9	4
SJVLTRK	8	80
SJVLTRKR	23	F0
SJVLTRUN	0	10
SJVLVS	9	8
SJVLWARN	3F	40
SJVLWDON	0	1
SJVLWGRP	0	10
SJVLWOTH	0	2
SJVLWUSR	0	80
SJVLXGRP	0	8
SJVLXOTH	0	1
SJVLXUSR	0	40
SJVL128T	23	30
SJVL1600	18	C3
SJVL18TK	23	10
SJVL200	18	3
SJVL256T	23	40
SJVL36TK	23	20
SJVL384T	23	50
SJVL556	18	43
SJVL6250	18	D3
SJVL800	18	83

SJKLP Information

SJKLP Heading Information

Common Name: Scheduler JCL Facility Key List Service Parameter List
Macro ID: IEFSJKLP
DSECT Name: SJKLP
Owning Component: Scheduler JCL facility (BB131)
Eye-Catcher ID: SJKLP
Offset: 0
Length: 4
Storage Attributes: Subpool: Any
Key: Caller's key
Residency: Any
Size: 54 (decimal)
Created by: N/A
Pointed to by: N/A
Serialization: N/A
Function: Mapping for the Scheduler JCL Facility Key List Service Parameter List.

SJKLP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	54	SJKLP	SJF Keylist parameter list
0	(0)	CHARACTER	4	SJKLID	Identifier 'SJKL'
4	(4)	UNSIGNED	1	SJKLVERS	Version number
5	(5)	BITSTRING	1	SJKLFLAG	Control flag byte
		1...		SJKLNREC	No recovery
		.1..		SJKLNOCU	No cleanup
		..11 1111		*	Reserved
6	(6)	SIGNED	2	SJKLLEN	Length of parameter list
8	(8)	ADDRESS	4	SJKLSTOR	Local storage pointer or zero
12	(C)	SIGNED	4	SJKLREAS	Reason code
16	(10)	CHARACTER	8	SJKLJDVT	JDVT name
24	(18)	CHARACTER	8	SJKLVERB	Verb name
32	(20)	CHARACTER	8	SJKLCLAS	Class identifier
40	(28)	UNSIGNED	2	SJKLSUBP	Subpool for key list storage
42	(2A)	BITSTRING	1	SJKLFLG1	Flag field
		1...		SJKLNICF	Not in class flag
		.1..		SJKLRKWO	Return keyword only
43	(2B)	CHARACTER	1	SJKLRSV0	Reserved for IBM use
44	(2C)	ADDRESS	4	SJKLRETA	Key list return area address
48	(30)	UNSIGNED	2	SJKLARLN	Key list return area length
50	(32)	UNSIGNED	2	SJKLKIDN	Number of keys returned
52	(34)	CHARACTER	2	SJKLRSV1	Reserved for IBM use

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	10	SJKLKEYL (*)	Key list mapping
0	(0)	CHARACTER	10	SJKLKENT	Key list entry
0	(0)	CHARACTER	8	SJKLKYWD	Keyword name
8	(8)	UNSIGNED	2	SJKLKEY	Key number

SJKLP Constants

Len	Type	Value	Name	Description
		Comment		
		Additional data needed for parameter list		
		End of Comment		
4	CHARACTER	SJKL	SJKLCID	Parameter list identifier
1	DECIMAL	1	SJKLCVER	Version number
2	DECIMAL	230	SJKLSBPL	Typical subpool for keylist

SJKLP Cross Reference

Name	Hex Offset	Hex Value
SJKLARLN	30	
SJKLCLAS	20	
SJKLFLAG	5	
SJKLFLG1	2A	
SJKLID	0	
SJKLJDVT	10	
SJKLKENT	0	
SJKLKEY	8	
SJKLKEYL	0	
SJKLKIDN	32	
SJKLKYWD	0	
SJKLLEN	6	
SJKLNICF	2A	80
SJKLNOCU	5	40
SJKLNREC	5	80
SJKLP	0	
SJKLREAS	C	
SJKLRETA	2C	
SJKLRKWO	2A	40
SJKLRSV0	2B	
SJKLRSV1	34	
SJKLSTOR	8	
SJKLSUBP	28	
SJKLVERB	18	
SJKLVERS	4	

SJMRP Information

SJMRP Heading Information

Common Name: Scheduler JCL Facility Merge SWB Parameter list
Macro ID: IEFSJMRP
DSECT Name: SJMRP
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: SJMP
Offset: 0
Length: 4
Storage Attributes: Subpool: Any
Key: Caller's key
Residency: Any
Size: See assembler listing
Created by: Caller
Pointed to by: Standard linkage parameter list (Register 1 pointing to a word which points to SJMRP).
Serialization: N/A
Function: Mapping for the Scheduler JCL Facility Merge SWB Parameter list. The Merge SWB service will take two SWB chains (specified with the 'Merge' and 'Base' tokens), and create a new SWB chain (returned as the 'Resolve' token) that contains keywords from both input SWB chains. Specific keys in the 'Merge' SWB chain will override the same key in the 'Base' SWB chain.

SJMRP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	44	SJMRP	SJF Merge SWB parameter list
0	(0)	CHARACTER	4	SJMRID	Identifier 'SJMR'
4	(4)	UNSIGNED	1	SJMRVERS	Version number
5	(5)	CHARACTER	1	SJMRFLAG	Control flags
		1...		SJMRNREC	No recovery
		.1..		SJMRNOCU	No cleanup
		..11 1111		*	Reserved
6	(6)	SIGNED	2	SJMRLEN	Length of parameter list
8	(8)	ADDRESS	4	SJMRSTOR	Local storage pointer or zero
12	(C)	SIGNED	4	SJMRREAS	Reason code (returned) End of common header
16	(10)	CHARACTER	8	SJMRBAST	SJF Base token
24	(18)	CHARACTER	8	SJMRMRGT	SJF Merge token
32	(20)	CHARACTER	8	SJMRREST	SJF Resolve token (result)
40	(28)	CHARACTER	1	SJMRFLG1	Control flags
		1...		SJMRNSWA	Create result SWB in non-SWA storage
		.1..		SJMRNFB	New SWB label from Base SWB
		..1.		SJMRNSER	Do not serialize resultant SWA SWB
		...1		SJMRRTNB	De-serialize Base SWB
		.... 1...		SJMRRTNM	De-serialize Merge SWB
		.... .111		*	Reserved
41	(29)	CHARACTER	1	SJMRRETF	Return Flags
		1...		SJMRDESB	Base SWB Chain Deserialized
		.1..		SJMRDESM	Merge SWB Chain Deserialized
		..1.		SJMRDSEB	Error during Base SWB DeSer
		...1		SJMRDSEM	Error during Merge SWB DeSer
42	(2A)	CHARACTER	2	SJMRRSV0	Reserved

SJMRP Constants

Len	Type	Value		Name	Description
		Comment			
		ADDITIONAL DATA NEEDED FOR PARAMETER LIST			
		End of Comment			
4	CHARACTER	SJMR		SJMRCID	Parameter list acronym
1	DECIMAL		1	SJMRCVER	Parameter list version

SJMRP Cross Reference

Name	Hex Offset	Hex Value
SJMRBAST	10	
SJMRDESB	29	80
SJMRDESM	29	40
SJMRDSEB	29	20
SJMRDSEM	29	10
SJMRFLAG	5	
SJMRFLG1	28	
SJMRID	0	
SJMRLEN	6	
SJMRMRGT	18	
SJMRNFB	28	40
SJMRNOCU	5	40
SJMRNREC	5	80
SJMRNSER	28	20
SJMRNSWA	28	80
SJMRP	0	
SJMRREAS	C	
SJMRREST	20	
SJMRRETF	29	
SJMRRSV0	2A	
SJMRRTNB	28	10
SJMRRTNM	28	08
SJMRSTOR	8	
SJMRVERS	4	

SJPRFX Information

SJPRFX Heading Information

Common Name: NJE Prefix Mapping
Macro ID: IEFSJPFX
DSECT Name: SJPRFX
Owning Component: (BB131) SJF
Eye-Catcher ID: SJPF
Offset: +0
Length: 4 characters (bytes)
Storage Attributes: Subpool: N/A
Key: N/A
Residency: Any
Size: 28 ('1C'x)
FREQUENCY = N/A
Created by: N/A
Pointed to by: N/A
Serialization: None
Function: This mapping is used as the prefix section of a SWBTU (SWB in text unit format). A SWBTU is the transportable format of SWB (JCL) data owned by SJF. The mapping is also defined as the NJE Data Set Header OPTB prefix.

SJPRFX Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	28	SJPRFX	NJE PREFIX
0	(0)	CHARACTER	4	SJPRID	IDENTIFIER 'SJPF'
4	(4)	UNSIGNED	1	SJPRVERS	VERSION NUMBER
5	(5)	UNSIGNED	1	SJPRPLEN	LENGTH OF PREFIX
6	(6)	SIGNED	2	SJPRDLEN	LENGTH OF DATA
8	(8)	CHARACTER	8	SJPRVERB	VERB FOR SWB CHAIN
16	(10)	CHARACTER	8	SJPRVRBL	LABEL FOR SWB CHAIN
24	(18)	BITSTRING	1	SJPRFLG1	FLAGS
		1...		SJPRCONT	CONTINUATION TEXT UNIT FLAG
		.1..		SJPRDYNM	DYNAMICALLY CREATED SWB CHAIN
25	(19)	UNSIGNED	1	SJPRPARM	NUMBER OF PARAMETERS ALREADY PROCESSED IN THE FIRST TEXT UNIT
26	(1A)	UNSIGNED	2	SJPRRSV1	RESERVED

SJPRFX Constants

Len	Type	Value		Name	Description
Comment					
ADDITIONAL DATA NEEDED FOR NJE PREFIX MAPPING					
End of Comment					
4	CHARACTER	SJPF		SJPRCID	IDENTIFIER
1	DECIMAL		2	SJPRCVER	CURRENT VERSION NUMBER

SJPRFX Cross Reference

Name	Hex Offset	Hex Value
SJPRCONT	18	80
SJPRDLEN	6	
SJPRDYNM	18	40
SJPRFLG1	18	
SJPRFX	0	
SJPRID	0	
SJPRPARM	19	
SJPRPLEN	5	
SJPRRSV1	1A	
SJPRVERB	8	
SJPRVERS	4	
SJPRVRBL	10	

SJPUP Information

SJPUP Heading Information

Common Name: SCHEDULER JCL FACILITY PUT SWB PARAMETER LIST
Macro ID: IEFSJPUP
DSECT Name: SJPUP
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: SJPU
Offset: 0
Length: 4
Storage Attributes: Subpool: Any
Key: Caller's key
Residency: Any
Size: 56 bytes
Created by: Caller of SJFREQ REQUEST=PUTSWB
Pointed to by: On entry to SJF, register 1 points to a word that points to SJPUP
Serialization: None
Function: Mapping for the Scheduler JCL Facility Put SWB Service Parameter List.

SJPUP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	56	SJPUP	
0	(0)	CHARACTER	4	SJPUID	IDENTIFIER
4	(4)	UNSIGNED	1	SJPUVERS	VERSION NUMBER
5	(5)	BITSTRING	1	SJPUFLAG	CONTROL FLAGS
		1...		SJPUNREC	NO RECOVERY
		.1..		SJPUNOCU	NO CLEANUP
		..11 1111		*	RESERVED
6	(6)	SIGNED	2	SJPULEN	LENGTH OF PARAMETER LIST
8	(8)	ADDRESS	4	SJPUSTOR	LOCAL STORAGE POINTER
12	(C)	SIGNED	4	SJPUREAS	RESULT REASON CODE (RETURNED)
16	(10)	CHARACTER	8	SJPUTOKN	
16	(10)	ADDRESS	4	SJPUANBK	ADDRESS OF CONTROL BLOCK FOR A JCL STATEMENT (JCT, SCT, SIOT OR SWB) OR THE ADDRESS OF A SWB CHAIN
20	(14)	ADDRESS	4	SJPUANCA	ADDRESS OF WORD POINTING TO A A SWB CHAIN OR ZERO
24	(18)	ADDRESS	4	SJPUSWBA	ADDRESS OF AREA CONTAINING KEYWORD DATA
28	(1C)	UNSIGNED	2	SJPUALEN	LENGTH OF AREA CONTAINING KEYWORD DATA
30	(1E)	BITSTRING	1	SJPUFLG2	FLAGS
		1...		SJPUNSWA	SWBS TO BE BUILT IN NON-SWA SUBPOOL
		.1..		SJPUWARN	CONTINUE PROCESSING AFTER AN ERROR WHICH IS DUE TO CHANGES IN THE JDTS FROM RELEASE TO RELEASE IS ENCOUNTERED
		..1.		SJPUOSER	SERIALIZATION ON SWB USE COUNT IS NOT REQUIRED
		...1 1111		*	RESERVED
31	(1F)	BITSTRING	1	SJPURSV2	RESERVED
32	(20)	CHARACTER	8	SJPUJDVT	JDVT NAME
40	(28)	CHARACTER	0	SJPUV1ND	END OF VERSION 1 PARMLIST
40	(28)	CHARACTER	3	SJPUSVA	SVA TO BE REASSIGNED TO SWB (USED BY SWA RELOCATOR)
43	(2B)	CHARACTER	13	SJPURSV3	RESERVED

SJPUP Constants

Len	Type	Value		Name	Description
			Comment		
		ADDITIONAL DATA NEEDED FOR THE PUT SWB PARAMETER LIST			
			End of Comment		
4	CHARACTER	SJPU		SJPUCID	IDENTIFIER
1	DECIMAL		2	SJPUCVER	CURRENT VERSION NUMBER

SJPUP Cross Reference

Name	Hex Offset	Hex Value
SJPUALEN	1C	
SJPUANBK	10	
SJPUANCA	14	
SJPUFLAG	5	
SJPUFLG2	1E	
SJPUID	0	
SJPUJDVT	20	
SJPULEN	6	
SJPUNOCU	5	40
SJPUNREC	5	80
SJPUNSWA	1E	80
SJPUOSER	1E	20
SJPUP	0	
SJPUREAS	C	
SJPURSV2	1F	
SJPURSV3	2B	
SJPUSTOR	8	
SJPUSVA	28	
SJPUSWBA	18	
SJPUTOKN	10	
SJPUVERS	4	
SJPUV1ND	28	
SJPUWARN	1E	40

SJRC Information

SJRC Programming Interface information

Programming Interface information

SJRC

The following fields are **NOT** programming interface information:

- SJRC_BASE_SWBTU_CONFLICT
- SJRC_FREEMAIN_FAILURE
- SJRC_GETMAIN_FAILURE
- SJRC_KEYLIST_INVALID_PLIST
- SJRC_KEYLIST_NO_STORAGE
- SJRC_KEYLIST_VERB_NOT_DEF
- SJRC_MODIFY_SWBTU_ERROR
- SJRC_MODIFY_SWBTU_NO_TU
- SJRC_NO_KEYWORDS_TO_RETURN
- SJRC_NOT_FOUND
- SJRC_OUTAREA_OVERFLOW
- SJRC_SCAN_INVALID_KEY
- SJRC_SCAN_INVALID_PARM
- SJRC_SCAN_INVALID_PLIST
- SJRC_SCAN_VERB_DOESNT_MATCH
- SJRC_SPLICE_STORAGE_ERROR
- SJRC_SWBTU_WITH_NO_TUS
- SJRCALDL
- SJRCALLW
- SJRCBKK
- SJRCBUFL
- SJRCBVC
- SJRCCOPY
- SJRCDDNM
- SJRCDFTJ
- SJRCDLVL
- SJRCDUPJ
- SJRCDUPK
- SJRCDUPV
- SJRCEBIT
- SJRCGEGM
- SJRCGETJ
- SJRCGETS
- SJRCGLEN
- SJRCGSWB
- SJRCIFUN
- SJRCINAN
- SJRCINVJ
- SJRCIREF
- SJRCITKN
- SJRCIVAC
- SJRCIVCB
- SJRCIVDT
- SJRCIVID
- SJRCIVKY
- SJRCIVLB
- SJRCIVRB
- SJRCLDEL
- SJRCLSTG
- SJRCMORE
- SJRCMRDV
- SJRCNATH
- SJRCNBIT
- SJRCNDAT
- SJRCNDTA
- SJRCNDYN
- SJRCNGRP
- SJRCNJDT
- SJRCNOCB
- SJRCNOIP
- SJRCNOST
- SJRCNSCH
- SJRCNSDT
- SJRCNSLE
- SJRCNSTG
- SJRCNSWB
- SJRCNUPD
- SJRCPERR
- SJRCPLEN
- SJRCPLST
- SJRCPMOD
- SJRCPRMN
- SJRCPSWB
- SJRCPTUL
- SJRCPUGM
- SJRCSSTG
- SJRCSTEP
- SJRCSUHT
- SJRCSWAM
- SJRCUSEH
- SJRCUSEZ
- SJRCVAOC
- SJRCWSPC

End of Programming Interface information

SJRC Heading Information

Common Name: Scheduler JCL Facility Reason Codes
Macro ID: IEFSJRC
DSECT Name: n/a
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: none
Storage Attributes: Virtual Storage: included in module's dynamic area
Size: n/a
Created by: n/a
Pointed to by: n/a
Serialization: None
Function: Maps the reason codes used by all SJFREQ Functions

SJRC Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0		
0	(0)	SIGNED	4	SJRCNOER	X'000' PROCESSING SUCCESSFUL
4	(4)	SIGNED	4	SJRCIVID	X'001' INVALID SWB ID, OWNER NAME, BLOCK ID, VERB, OR LABEL
8	(8)	SIGNED	4	SJRCIVTK	X'002' INVALID SWB TOKEN
12	(C)	SIGNED	4	SJRCNSWB	X'003' SWB NOT FOUND ON SWB CHAIN
16	(10)	SIGNED	4	SJRCNJDV	X'004' JDVT NOT FOUND ON JDVT CHAIN
20	(14)	SIGNED	4	SJRCNJCH	X'005' JDVT CHAIN DOES NOT EXIST
				Comment	
				JDT REASON CODES - Returned BY SJF Extract and callers of SJF Extract	
				End of Comment	
24	(18)	SIGNED	4	SJRCNVRB	X'0C8' VERB NOT DEFINED IN JDT
28	(1C)	SIGNED	4	SJRCNKWD	X'0C9' KEYWORD NOT DEFINED IN JDT
32	(20)	SIGNED	4	SJRCNKEY	X'0CA' KEY NOT DEFINED IN JDT
36	(24)	SIGNED	4	SJRCNPRM	X'0CB' SUBPARAMETER NOT DEFINED IN JDT
40	(28)	SIGNED	4	SJRCBKK	X'0CC' BOTH A KEYWORD AND KEY ARE SPECIFIED
44	(2C)	SIGNED	4	SJRCNSLE	X'0CE' SUBLIST ELEMENT NOT DEFINED IN THE JDT
48	(30)	SIGNED	4	SJRCNCMD	X'0CF' COMMAND NOT DEFINED IN JDT
52	(34)	SIGNED	4	SJRCNOPE	X'0D0' OPERAND NOT DEFINED IN JDT
56	(38)	SIGNED	4	SJRCBVC	X'0D1' BOTH VERB AND COMMAND SPECIFIED
60	(3C)	SIGNED	4	SJRCNOIP	X'0D2' NO OPERAND INFORMATION POINTER
64	(40)	SIGNED	4	SJRCVAOC	X'0D3' VERB AND/OR COMMAND NOT SPECIFIED
68	(44)	SIGNED	4	SJRCDLVL	X'0D4' Special parameter information not supported by this parameter list version
				Comment	
				DEFINE JDVT REASON CODES	
				End of Comment	
72	(48)	SIGNED	4	SJRCNJDT	X'12C' JDT NOT FOUND
76	(4C)	SIGNED	4	SJRCDUPJ	X'12D' DUPLICATE JDVT NAME
80	(50)	SIGNED	4	SJRCDFTJ	X'12E' DEFAULT JDVT ALREADY EXISTS
84	(54)	SIGNED	4	SJRCGETJ	X'12F' GETMAIN FOR JDVT FAILED
88	(58)	SIGNED	4	SJRCSUHT	X'130' STORAGE UNAVAILABLE FOR HASH TABLE
92	(5C)	SIGNED	4	SJRCNSDT	X'132' SDT NOT FOUND
				Comment	
				FIND SWB REASON CODES	
				End of Comment	
96	(60)	SIGNED	4	SJRCNSCH	X'190' SPECIFIED SWB CHAIN NOT FOUND
100	(64)	SIGNED	4	SJRCSTEP	X'191' SPECIFIED STEP OR PROC NAME NOT FOUND
104	(68)	SIGNED	4	SJRCDDNM	X'192' SPECIFIED DD LABEL NOT FOUND
108	(6C)	SIGNED	4	SJRCNBIT	X'193' NO SEARCH BITS SPECIFIED
112	(70)	SIGNED	4	SJRCEBIT	X'194' UNDEFINED BITS IN PARM LIST
116	(74)	SIGNED	4	SJRCNGRP	X'195' CONTROL GROUP NOT FOUND
120	(78)	SIGNED	4	SJRCNOST	X'196' NO STEPNAME IN PARM LIST
124	(7C)	SIGNED	4	SJRCINAN	X'197' INVALID STARTING ADDRESS PASSED IN PARAMETER LIST
128	(80)	SIGNED	4	SJRCINVJ	X'198' INVALID JOB OR STEP TOKEN SUPPLIED

Offsets					
Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					UPDATE REASON CODES
					End of Comment
132	(84)	SIGNED	4	SJRCIVLN	X'1F4' INVALID LENGTH OF PARAMETER
136	(88)	SIGNED	4	SJRCIVCH	X'1F5' INVALID CHOICE SPECIFIED
140	(8C)	SIGNED	4	SJRCGMAX	X'1F6' NUMERIC PARAMETER EXCEEDS MAXIMUM
144	(90)	SIGNED	4	SJRCLMIN	X'1F7' NUMERIC PARAMETER LESS THAN MINIMUM
148	(94)	SIGNED	4	SJRCIVKY	X'1F8' INVALID KEY, SYSTEM SPECIFICATION ONLY
152	(98)	SIGNED	4	SJRCDUPK	X'1F9' DUPLICATE KEY SPECIFIED
156	(9C)	SIGNED	4	SJRCNNUM	X'1FA' NO PARAMETER SPECIFIED
160	(A0)	SIGNED	4	SJRCCOPY	X'1FB' NO STORAGE COULD BE OBTAINED IN WHICH TO UPDATE SWBS
164	(A4)	SIGNED	4	SJRCIVRB	X'1FC' INVALID VERB SPECIFIED
168	(A8)	SIGNED	4	SJRCIVLB	X'1FD' INVALID LABEL SPECIFIED
172	(AC)	SIGNED	4	SJRCNLLN	X'1FE' LENGTH OF LEVEL EXCEEDS MAXIMUM
176	(B0)	SIGNED	4	SJRCNLNM	X'1FF' NUMBER OF LEVELS EXCEEDS MAXIMUM
180	(B4)	SIGNED	4	SJRCNFCH	X'200' INVALID FIRST CHARACTER OF LEVEL IN PARAMETER
184	(B8)	SIGNED	4	SJRCNOCH	X'201' INVALID CHARACTER OTHER THAN THE FIRST IN LEVEL IN PARAMETER
188	(BC)	SIGNED	4	SJRCNLIV	X'202' INVALID SPECIFICATION OF LEVELS
192	(C0)	SIGNED	4	SJRCIHEX	X'203' NONHEX CHARACTERS SPECIFIED
196	(C4)	SIGNED	4	SJRCINUM	X'204' NONNUMERIC CHARACTERS SPECIFIED
200	(C8)	SIGNED	4	SJRCIREF	X'205' INVALID REFERENCE. THIS IS GENERATED WHEN A REFERENCE IS MADE TO A DYNAMIC SWB IN A STEP OTHER THAN THE CURRENT STEP. (RESTART CANNOT HANDLE THIS CASE)
204	(CC)	SIGNED	4	SJRCZKEY	X'206' Text unit key value of hex zeroes found in input text unit
					Comment
					The range of 550 to 579 is reserved for reason codes that may be generated as a warning reason code. That is, if the update service encounters the error and the warning processing was requested, then a return code of zero will be taken.
					End of Comment
208	(D0)	SIGNED	4	SJRCIVTX	X'226' INVALID TEXT CHARACTER
212	(D4)	SIGNED	4	SJRCISEQ	X'227' Invalid sequence of characters found in parameter
216	(D8)	SIGNED	4	SJRCIBIT	X'228' Undefined bits are on in this bistring parameter
					Comment
					RETRIEVE REASON CODES
					End of Comment
220	(DC)	SIGNED	4	SJRCSTRS	X'258' NOT ENOUGH SPACE IN STORAGE AREA
224	(E0)	SIGNED	4	SJRCWSPC	X'259' NOT ENOUGH WORK SPACE FOR TEXT UNIT POINTER LIST
228	(E4)	SIGNED	4	SJRCSTRA	X'25B' NO ADDRESS SPECIFIED FOR THE STORAGE AREA
232	(E8)	SIGNED	4	SJRCIVKN	X'25C' ZERO SPECIFIED FOR NUMBER OF KEYWORDS
236	(EC)	SIGNED	4	SJRCIVKL	X'25D' ZERO KEYWORD LIST ADDRESS SPECIFIED
240	(F0)	SIGNED	4	SJRCIVKW	X'25F' KEYWORD NOT SPECIFIED
					Comment
					DELETE REASON CODES
					End of Comment
244	(F4)	SIGNED	4	SJRCALDL	X'2BC' SWB CHAIN ALREADY LOGICALLY DELETED
248	(F8)	SIGNED	4	SJRCNDYN	X'2BD' STATIC SWB CHAINS CANNOT BE DYNAMICALLY DELETED
					Comment
					WRITE REASON CODES
					End of Comment
252	(FC)	SIGNED	4	SJRCIVDT	X'320' DATA EXCEEDS LENGTH OF SWB DATA PORTION
256	(100)	SIGNED	4	SJRCDUPV	X'321' SWB CHAIN ALREADY EXISTS WITH SPECIFIED VERB AND LABEL

SJRC Map

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					Comment
					PUT REASON CODES
					End of Comment
260	(104)	SIGNED	4	SJRCPSWB	X'384' PUT SWB ADDRESS NOT SPECIFIED
264	(108)	SIGNED	4	SJRCPUGM	X'385' UNABLE TO GETMAIN STORAGE FOR TEXT UNIT POINTER LIST
268	(10C)	SIGNED	4	SJRCPLEN	X'386' LENGTH OF AREA TO BE PUT NOT SPECIFIED
272	(110)	SIGNED	4	SJRCPTUL	X'387' TEXT UNIT COUNT OR LENGTH IS INVALID
					Comment
					GET REASON CODES
					End of Comment
276	(114)	SIGNED	4	SJRCGSWB	X'3E8' AREA TO CONTAIN KEYWORD DATA NOT SPECIFIED
280	(118)	SIGNED	4	SJRCMORE	X'3E9' MORE KEYWORD DATA TO BE OBTAINED
284	(11C)	SIGNED	4	SJRCGLEN	X'3EA' INVALID LENGTH SPECIFIED FOR KEYWORD DATA AREA
288	(120)	SIGNED	4	SJRCGEGM	X'3EB' UNABLE TO OBTAIN STORAGE FOR KEYWORD LIST OR TEXT UNIT AREA
292	(124)	SIGNED	4	SJRCNDTA	X'3EC' NO KEYWORD DATA IN SWB CHAIN WHICH MATCHES QUALIFIER SPECIFIED
					Comment
					BUILD REASON CODES
					End of Comment
296	(128)	SIGNED	4	SJRCGETS	X'44C' GETMAIN FOR SWB FAILED
					Comment
					VERIFY REASON CODES
					End of Comment
300	(12C)	SIGNED	4	SJRCIVCM	X'4B0' COMMAND NOT SPECIFIED
304	(130)	SIGNED	4	SJRCIVTP	X'4B1' NO ADDRESS SPECIFIED FOR THE TEXT UNIT BUFFER
308	(134)	SIGNED	4	SJRCIVTL	X'4B2' NOT ENOUGH STORAGE IN THE TEXT UNIT BUFFER
312	(138)	SIGNED	4	SJRCIVQU	X'4B3' SUBPARAMETER MAY NOT BE SPECIFIED IN QUOTES
316	(13C)	SIGNED	4	SJRCIVBD	X'4B4' OUTPUT TEXT UNIT BUFFER IS DIFFERENT THAT OUTPUT TEXT UNIT BUFFER PASSED ON FIRST CALL
					Comment
					ACCESS REASON CODES
					End of Comment
320	(140)	SIGNED	4	SJRCNDAT	X'514' NO DATA EXISTS FOR THIS PARAMETER
324	(144)	SIGNED	4	SJRCNATH	X'515' NOT AUTHORIZED TO ACCESS THIS INFORMATION
328	(148)	SIGNED	4	SJRCNSTG	X'516' UNABLE TO OBTAIN STORAGE FOR INTERNAL RETRIEVE TABLE OR TEMPORARY SWA BLOCKS
332	(14C)	SIGNED	4	SJRCNUPD	X'517' UNABLE TO UPDATE THIS KEY
336	(150)	SIGNED	4	SJRCALLW	X'518' ALLOWABLE ERRORS OCCURRED. PROCESSING CONTINUED WITH NEXT REQUEST
340	(154)	SIGNED	4	SJRCPLST	X'519' ERROR IN PARAMETER LIST FIELD
344	(158)	SIGNED	4	SJRCNOCB	X'51A' CONTROL BLOCK WHICH CONTAINS INFORMATION FOR THIS PARAMETER DOES NOT EXIST
348	(15C)	SIGNED	4	SJRCLSTG	X'51B' STORAGE AREA LARGER THAN THE REQUIRED LENGTH
352	(160)	SIGNED	4	SJRCSSTG	X'51C' STORAGE AREA SMALLER THAN THE REQUIRED LENGTH
356	(164)	SIGNED	4	SJRCNRET	X'51D' Key not retrievable
					Comment
					ERASE REASON CODES
					End of Comment
360	(168)	SIGNED	4	SJRCPRMN	X'578' SUBPARAMETER INFORMATION does not exist in the SWB
364	(16C)	SIGNED	4	SJRCPERR	X'579' Conflicting requests found in input parameter list

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
				Comment	
				SERIALIZE REASON CODES	
				End of Comment	
368	(170)	SIGNED	4	SJRCUSEH	X'5DC' Use count high limit exceeded
372	(174)	SIGNED	4	SJRCUSEZ	X'5DD' Use count is zero
376	(178)	SIGNED	4	SJRCLDEL	X'5DE' SWB has been logically deleted
380	(17C)	SIGNED	4	SJRCIFUN	X'5DF' Invalid function requested
				Comment	
				SCAN REASON CODES	
				End of Comment	
384	(180)	SIGNED	4	SJRC_NOT_FOUND	X'640' No matching SWB chain found
388	(184)	SIGNED	4	SJRC_SCAN_VERB_DOESNT_MATCH	X'641' Verb in passed SWB chain does not match verb in parm list
392	(188)	SIGNED	4	SJRC_SCAN_INVALID_PLIST	X'642' Invalid parm list
396	(18C)	SIGNED	4	SJRC_SCAN_INVALID_KEY	X'643' Key of zero passed
400	(190)	SIGNED	4	SJRC_SCAN_INVALID_PARM	X'644' Parm number zero passed
				Comment	
				Keylist service reason codes	
				End of Comment	
404	(194)	SIGNED	4	SJRC_KEYLIST_INVALID_PLIST	X'708' Invalid parameter list detected
408	(198)	SIGNED	4	SJRC_KEYLIST_VERB_NOT_DEF	X'709' Verb passed is not defined to the given JDVT
412	(19C)	SIGNED	4	SJRC_KEYLIST_NO_STORAGE	X'70A' Getmain could not obtain requested storage
416	(1A0)	SIGNED	4	SJRC_NO_KEYWORDS_TO_RETURN	X'70B' No keywords are associated with the given class specification
				Comment	
				SWBTU Merge service reason codes	
				End of Comment	
420	(1A4)	SIGNED	4	SJRC_SWBTU_WITH_NO_TUS	X'76C' The SWBTU returned on output contains no text units, only a SWBTU prefix (IEFSJPFX)
424	(1A8)	SIGNED	4	SJRC_OUTAREA_OVERFLOW	X'76D' The output area supplied by the caller was not large enough for the resulting SWBTU
428	(1AC)	SIGNED	4	SJRC_BASE_SWBTU_CONFLICT	X'76E' The base SWBTU pointer list address and the number of SWBTUs are inconsistent, one is zero and the other is non-zero
432	(1B0)	SIGNED	4	SJRC_MODIFY_SWBTU_CONFLICT	X'76F' The modify SWBTU pointer list address and the number of SWBTUs are inconsistent, one is zero and the other is non-zero
436	(1B4)	SIGNED	4	SJRC_ERASE_KEYS_CONFLICT	X'770' The erase key list address and size fields are inconsistent, one is zero and the other is non-zero
440	(1B8)	SIGNED	4	SJRC_OUTPUT_AREA_ERROR	X'771' The output area address is zero or the output area size field is not greater than zero
444	(1BC)	SIGNED	4	SJRC_INCORRECT_INPUT_COMBO	X'772' Neither a base SWBTU nor a modify SWBTU was provided, or a base SWBTU was provided but no modify SWBTU and no erase list were provided
448	(1C0)	SIGNED	4	SJRC_VERB_MISMATCH	X'773' The verb name in the modify SWBTU does not match the verb name in the base SWBTU
452	(1C4)	SIGNED	4	SJRC_SJSMP_PARM_ERROR	X'774' The version and length values are either inconsistent or undefined
456	(1C8)	SIGNED	4	SJRC_MODIFY_SWBTU_NO_TU	

Offsets Dec	Hex	Type/Value	Len	Name (Dim)	Description
					X'775' The modify SWBTU has no text units
				Comment	
					Reason codes from 1950 to 1999 are used to indicate an error occurred in a service that uses return and reason codes that are not defined by IEFSJRC.
				End of Comment	
460	(1CC)	SIGNED	4	SJRC_GETMAIN_FAILURE	X'79E' An unexpected return from a Getmain request was encountered. The return code from Getmain is in SJSMRETC and field SJSMERRS will contain a number representing which Getmain failed.
464	(1D0)	SIGNED	4	SJRC_FREEMAIN_FAILURE	X'79F' An unexpected return from a Freemain request was encountered The return code from Freemain is in SJSMRETC and field SJSMERRS will contain a number representing which Freemain failed.
468	(1D4)	SIGNED	4	SJRC_SPLICE_STORAGE_ERROR	X'7A0' An unexpected return from the SWBTUREQ SPLICE service to obtain the working storage size required for the service was encountered. The return code from SPLICE is in SJSMRETC and the reason code is in SJSMERRS.
472	(1D8)	SIGNED	4	SJRC_BASE_SWBTU_ERROR	X'7A1' An error return from the SWBTUREQ SPLICE service to validate the base SWBTUs was encountered. The return code from SPLICE is in SJSMRETC and the reason code is in SJSMERRS.
476	(1DC)	SIGNED	4	SJRC_MODIFY_SWBTU_ERROR	X'7A2' An error return from the SWBTUREQ SPLICE service to validate the modify SWBTUs was encountered. The return code from SPLICE is in SJSMRETC and the reason code is in SJSMERRS.
				Comment	
					MERGE SWB REASON CODES
				End of Comment	
480	(1E0)	SIGNED	4	SJRCMRDV	X'7D1' Merge attempted for SWBs of different verbs
				Comment	
					SJF Validate Reason Codes
				End of Comment	
484	(1E4)	SIGNED	4	SJRCPMOD	X'835' Parameter modified during validation
488	(1E8)	SIGNED	4	SJRCBUFL	X'836' Output buffer length too small
				Comment	
					IEFSJSWA REASON CODES
				End of Comment	
492	(1EC)	SIGNED	4	SJRCSWAM	X'1F40' SWA MANAGER RETURNED A NON-ZERO RETURN CODE
496	(1F0)	SIGNED	4	SJRCIVCB	X'1F41' AN INVALID CONTROL BLOCK CHAIN POINTER WAS FOUND
500	(1F4)	SIGNED	4	SJRCIVAC	X'1F42' AN INVALID CONTROL BLOCK CHAIN ACRONYM WAS FOUND
				Comment	
					IEFSJTOK REASON CODES
				End of Comment	
504	(1F8)	SIGNED	4	SJRCITKN	X'1FA4' INVALID TOKEN TYPE REQUESTED

SJRC Cross Reference

Name	Hex Offset	Hex Value
SJRC_BASE_SWBTU_CONFLICT	1AC	76E
SJRC_BASE_SWBTU_ERROR	1D8	7A1
SJRC_ERASE_KEYS_CONFLICT	1B4	770
SJRC_FREEMAIN_FAILURE	1D0	79F
SJRC_GETMAIN_FAILURE	1CC	79E
SJRC_INCORRECT_INPUT_COMBO	1BC	772
SJRC_KEYLIST_INVALID_PLIST	194	708
SJRC_KEYLIST_NO_STORAGE	19C	70A
SJRC_KEYLIST_VERB_NOT_DEF	198	709
SJRC_MODIFY_SWBTU_CONFLICT	1B0	76F
SJRC_MODIFY_SWBTU_ERROR	1DC	7A2
SJRC_MODIFY_SWBTU_NO_TU	1C8	775
SJRC_NO_KEYWORDS_TO_RETURN	1A0	70B
SJRC_NOT_FOUND	180	640
SJRC_OUTAREA_OVERFLOW	1A8	76D
SJRC_OUTPUT_AREA_ERROR	1B8	771
SJRC_SCAN_INVALID_KEY	18C	643
SJRC_SCAN_INVALID_PARM	190	644
SJRC_SCAN_INVALID_PLIST	188	642
SJRC_SCAN_VERB_DOESNT_MATCH	184	641
SJRC_SJSMP_PARM_ERROR	1C4	774
SJRC_SPLICE_STORAGE_ERROR	1D4	7A0
SJRC_SWBTU_WITH_NO_TUS	1A4	76C
SJRC_VERB_MISMATCH	1C0	773
SJRCALDL	F4	2BC
SJRCALLW	150	518
SJRCBKK	28	CC
SJRCBUFL	1E8	836
SJRCBVC	38	D1
SJRCCOPY	A0	1FB
SJRCDDNM	68	192
SJRCDFTJ	50	12E
SJRCDLVL	44	D4
SJRCDUPJ	4C	12D
SJRCDUPK	98	1F9
SJRCDUPV	100	321
SJRCEBIT	70	194
SJRCGEGM	120	3EB
SJRCGETJ	54	12F
SJRCGETS	128	44C
SJRCGLEN	11C	3EA
SJRCGMAX	8C	1F6
SJRCGSWB	114	3E8
SJRCIBIT	D8	228
SJRCIFUN	17C	5DF
SJRCIHEX	C0	203
SJRCINAN	7C	197
SJRCINUM	C4	204
SJRCINVJ	80	198
SJRCIREF	C8	205
SJRCISEQ	D4	227
SJRCITKN	1F8	1FA4
SJRCIVAC	1F4	1F42
SJRCIVBD	13C	4B4
SJRCIVCB	1F0	1F41
SJRCIVCH	88	1F5
SJRCIVCM	12C	4B0
SJRCIVDT	FC	320
SJRCIVID	4	1
SJRCIVKL	EC	25D
SJRCIVKN	E8	25C
SJRCIVKW	F0	25F
SJRCIVKY	94	1F8
SJRCIVLB	A8	1FD
SJRCIVLN	84	1F4
SJRCIVQU	138	4B3
SJRCIVRB	A4	1FC
SJRCIVTK	8	2
SJRCIVTL	134	4B2
SJRCIVTP	130	4B1
SJRCIVTX	D0	226
SJRCLDEL	178	5DE
SJRCLMIN	90	1F7
SJRCLSTG	15C	51B
SJRCMORE	118	3E9
SJRCMRDV	1E0	7D1
SJRCNATH	144	515
SJRCNBIT	6C	193
SJRCNCMD	30	CF
SJRCNDAT	140	514
SJRCNDTA	124	3EC
SJRCNDYN	F8	2BD
SJRCNFCH	B4	200
SJRCNGRP	74	195
SJRCNJCH	14	5
SJRCNJDT	48	12C
SJRCNJDV	10	4
SJRCNKEY	20	CA
SJRCNKWD	1C	C9
SJRCNLIV	BC	202
SJRCNLLN	AC	1FE
SJRCNLNM	B0	1FF
SJRCNNUM	9C	1FA
SJRCNOCB	158	51A
SJRCNOCH	B8	201
SJRCNOER	0	0
SJRCNOIP	3C	D2
SJRCNOPE	34	D0
SJRCNOST	78	196
SJRCNPRM	24	CB
SJRCNRET	164	51D
SJRCNSCH	60	190
SJRCNSDT	5C	132
SJRCNSLE	2C	CE
SJRCNSTG	148	516
SJRCNSWB	C	3
SJRCNUPD	14C	517
SJRCNVRB	18	C8
SJRCPERR	16C	579
SJRCPLEN	10C	386
SJRCPLST	154	519
SJRCPMOD	1E4	835
SJRCPRMN	168	578
SJRCPSWB	104	384
SJRCPTUL	110	387
SJRCPUGM	108	385
SJRCSSTG	160	51C
SJRCSTEP	64	191
SJRCSTRA	E4	25B
SJRCSTRS	DC	258

SJRC Cross Reference

Name	Hex Offset	Hex Value
SJRCSUHT	58	130
SJRCSWAM	1EC	1F40
SJRCUSEH	170	5DC
SJRCUSEZ	174	5DD
SJRCVAOC	40	D3
SJRCWSPC	E0	259
SJRCZKEY	CC	206

SJREP Information

SJREP Programming Interface information

Programming Interface information

SJREP

The following fields are **NOT** programming interface information:

- SJREANBK
- SJREANCA
- SJRENREC

End of Programming Interface information

SJREP Heading Information

Common Name: SJF Retrieve Parameter List
Macro ID: IEFSJREP
DSECT Name: SJREP
Owning Component: Scheduler JCL Facility (BB131)
Eye-Catcher ID: SJRE
Offset: 0
Length: 4
Storage Attributes: Key: Key of caller
Size: 48 bytes
Created by: Caller of SJFREQ REQUEST=RETRIEVE
Pointed to by: On entry to SJF, register 1 points to a word that points to SJREP.
Serialization: None
Function: Maps the input and output to the SJF Retrieve Routine

SJREP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SJREP	
0	(0)	CHARACTER	4	SJREID	IDENTIFIER 'SJRE'
4	(4)	BITSTRING	1	SJREVERS	VERSION NUMBER
5	(5)	BITSTRING	1	SJREFLAG	CONTROL FLAGS
		1...		SJRENREC	"X'80'" NO RECOVERY
		.1..		SJRENOCU	"X'40'" NO CLEANUP
6	(6)	SIGNED	2	SJRELEN	LENGTH OF PARAMETER LIST
8	(8)	SIGNED	4	SJRESTOR	LOCAL STORAGE POINTER OR ZERO
12	(C)	SIGNED	4	SJREREAS	REASON CODE
16	(10)	CHARACTER	8	SJREJDVT	NAME OF JDVT OR ZEROES
24	(18)	DBL WORD	8	SJRETOKN (0)	SWB CHAIN TOKEN
24	(18)	SIGNED	4	SJREANBK	ADDRESS OF CONTROL BLOCK FOR A JCL STATEMENT (JCT, SCT, SIOT OR SWB) OR THE ADDRESS OF A SWB CHAIN
28	(1C)	SIGNED	4	SJREANCA	ADDRESS OF A WORD POINTING TO A SWB CHAIN OR ZERO
32	(20)	SIGNED	4	SJREAREA	STORAGE AREA ADDRESS
36	(24)	SIGNED	2	SJRESIZE	SIZE OF STORAGE AREA
38	(26)	SIGNED	2	SJRENKWD	NUMBER OF KEYWORDS PASSED
40	(28)	SIGNED	4	SJREKWDL	KEYWORD LIST ADDRESS
44	(2C)	SIGNED	4	SJREKERR	ADDRESS OF KEYWORD CAUSING ERROR
44	(2C)	X'30'	0	SJRELGTH	"*-SJREP" LENGTH OF THE SJF RETRIEVE PARAMETER LIST
44	(2C)	X'1'	0	SJRECVER	"01" CURRENT VERSION NUMBER

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	0	SJRELIST	KEYWORD LIST
0	(0)	CHARACTER	8	SJREKEYW	KEYWORD FOR RETRIEVE
8	(8)	SIGNED	4	SJRETPAD	ADDRESS OF A LIST OF TEXT UNIT POINTERS
8	(8)	X'C'	0	SJREKLEN	"*-SJRELIST" LENGTH OF ONE KEYWORD LIST ENTRY

SJREP Cross Reference

Name	Hex Offset	Hex Value	Name	Hex Offset	Hex Value
SJREANBK	18		SJREP	0	
SJREANCA	1C		SJREREAS	C	
SJREAREA	20		SJRESIZE	24	
SJRECVER	2C	1	SJRESTOR	8	
SJREFLAG	5		SJRETOKN	18	
SJREID	0		SJRETPAD	8	
SJREJDVT	10		SJREVERS	4	
SJREKERR	2C				
SJREKEYW	0				
SJREKLEN	8	C			
SJREKWDL	28				
SJRELEN	6				
SJRELGTH	2C	30			
SJRELIST	0				
SJRENKWD	26				
SJRENOCU	5	40			
SJRENREC	5	80			

SJRSP Information

SJRSP Heading Information

Common Name: SCHEDULER JCL FACILITY RETURN SWB PARAMETER LIST
Macro ID: IEFSJRSP
DSECT Name: SJRSP
Owning Component: Scheduler JCL facility (BB131)
Eye-Catcher ID: SJRSP
Offset: 0
Length: 4
Storage Attributes: Subpool: Any
Key: Caller's key
Residency: Any
Size: 24 (decimal)
Created by: Caller of SJFREQ REQUEST=RETURNSWB
Pointed to by: On entry to SJF, register 1 points to a word that points to SJRSP
Serialization: None
Function: MAPS THE INPUT TO THE SJF RETURN SWB ROUTINE.

SJRSP Map

Offsets					
Dec	**Hex**	**Type/Value**	**Len**	**Name (Dim)**	**Description**
0	(0)	STRUCTURE	24	SJRSP	RETURN SWB PARAMETER LIST
0	(0)	CHARACTER	4	SJRSID	IDENTIFIER 'SJRS'
4	(4)	UNSIGNED	1	SJRSVERS	VERSION NUMBER
5	(5)	BITSTRING	1	SJRSFLAG	CONTROL FLAGS
		1...		SJRSNREC	NO RECOVERY
		.1..		SJRSNOCU	NO CLEANUP
		..11 1111		*	RESERVED
6	(6)	SIGNED	2	SJRSLEN	LENGTH OF PARAMETER LIST
8	(8)	ADDRESS	4	SJRSSTOR	LOCAL STORAGE POINTER OR ZERO
12	(C)	SIGNED	4	SJRSREAS	REASON CODE
16	(10)	CHARACTER	8	SJRSTOKN	SWB STRUCTURE TOKEN
24	(18)	CHARACTER	0	*	WORD ALIGNMENT

SJRSP Constants

Len	Type	Value		Name	Description
Comment					
ADDITIONAL DATA NEEDED FOR RETURN SWB PARAMETER LIST					
End of Comment					
4	CHARACTER	SJRS		SJRSCID	IDENTIFIER
1	DECIMAL		1	SJRSCVER	CURRENT VERSION NUMBER

SJRSP Cross Reference

Name	Hex Offset	Hex Value
SJRSFLAG	5	
SJRSID	0	
SJRSLEN	6	
SJRSNOCU	5	40
SJRSNREC	5	80
SJRSP	0	
SJRSREAS	C	
SJRSSTOR	8	
SJRSTOKN	10	
SJRSVERS	4	

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