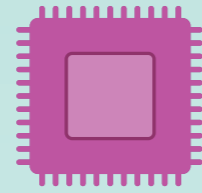


# How can semiconductor firms cut design costs and shorten time-to-market?

## IBM EDA



designs state-of-the-art  
IBM POWER processors

Aimed to deliver higher-quality  
chip designs to fabrication  
partners faster, cutting costs  
on both sides



Deployed IBM Platform  
LSF grid scheduling  
software to run  
complex simulations  
more efficiently



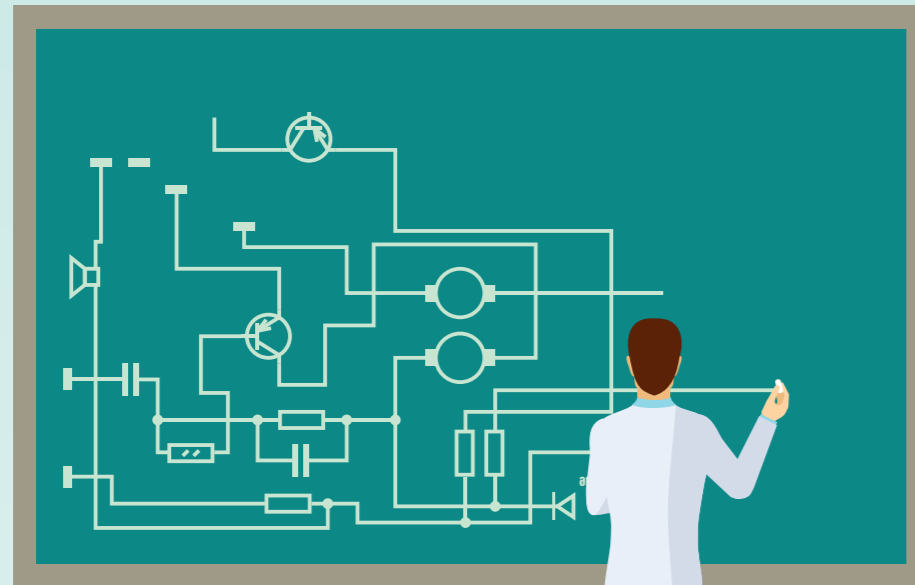
Achieves **multi-million  
dollar savings** annually  
by finding and fixing bugs  
before sending chip  
designs to fabrication

# 10%

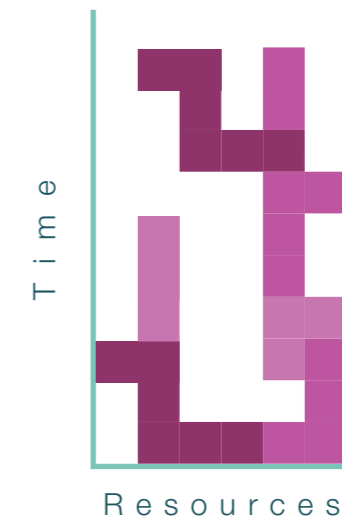
**10% higher utilization**  
of compute resources  
enables EDA to **do more  
with less** hardware  
investment

# 10x

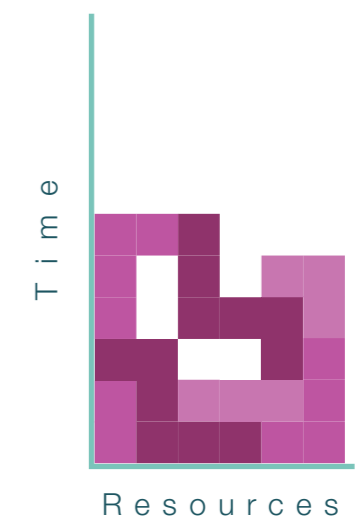
**10x faster scheduling**  
saves staff time and  
**accelerates** the delivery  
of high-quality processors  
to the market



### Original Schedule



### Smarter Schedule



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Source: <http://ibm.co/1X8ReMN>